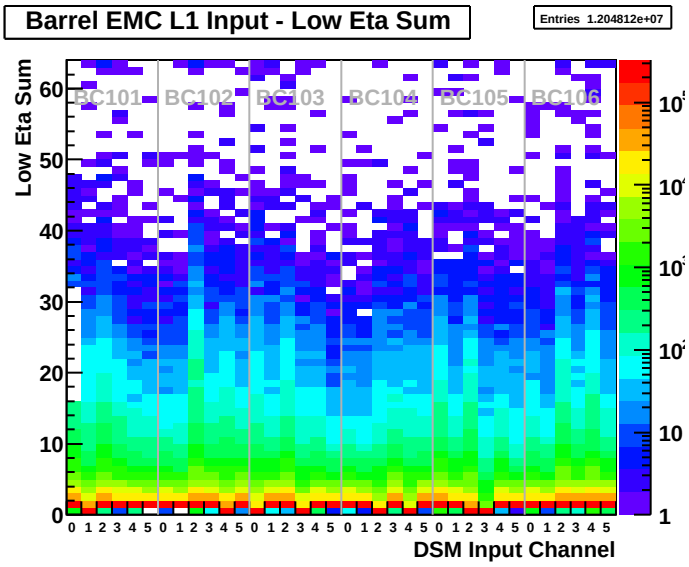
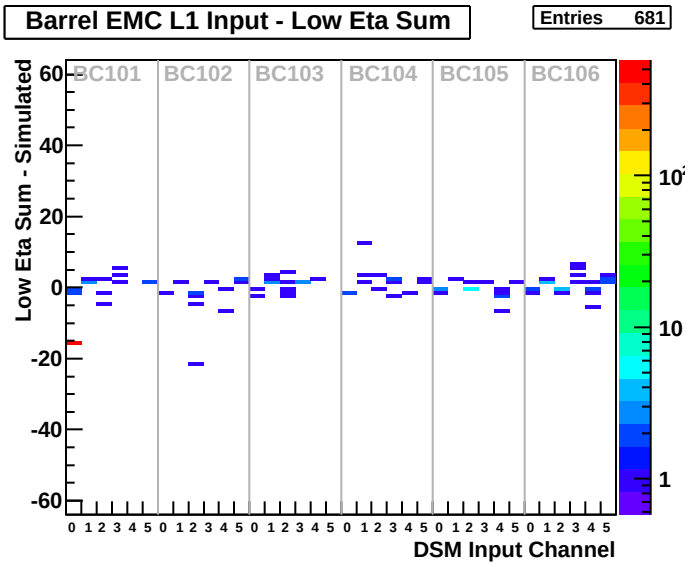
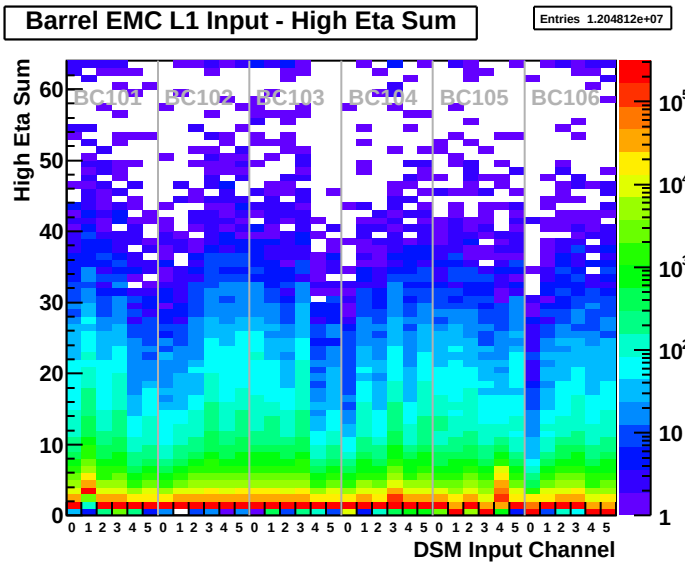
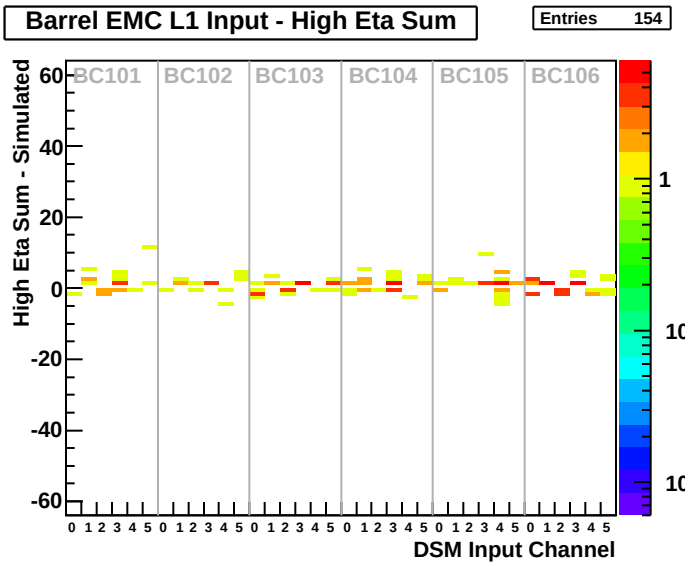
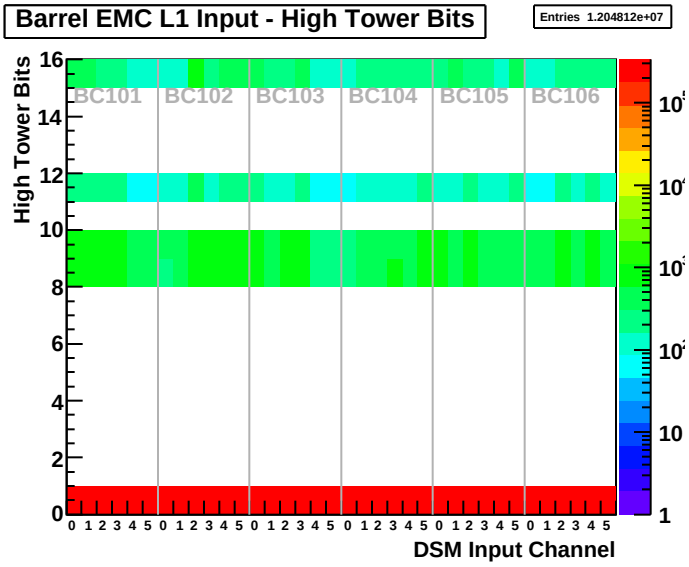
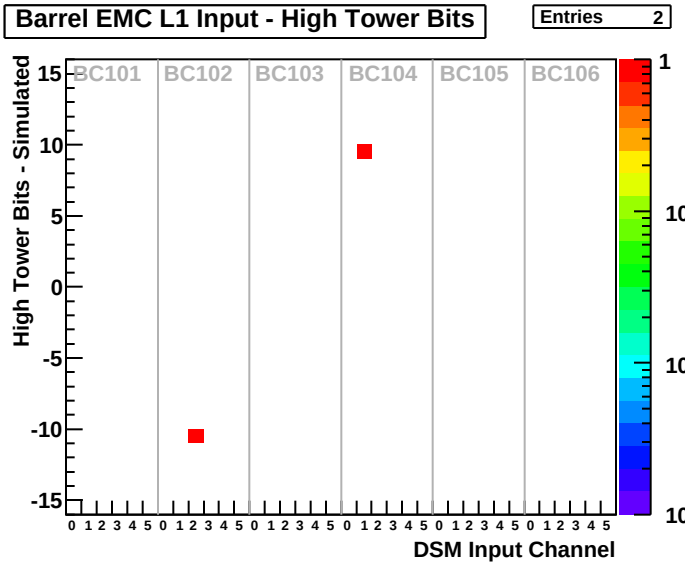

Barrel EMC L1 Input - Low Eta Sum
Entries 1.204812e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

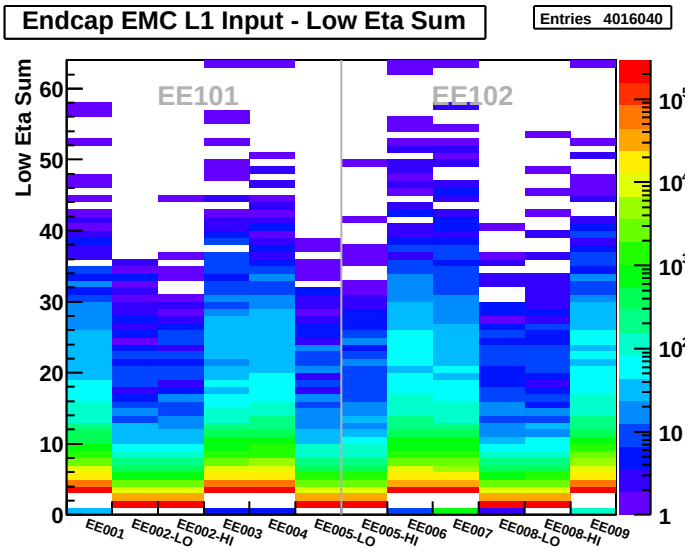

Barrel EMC L1 Input - Low Eta Sum
Entries 681
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

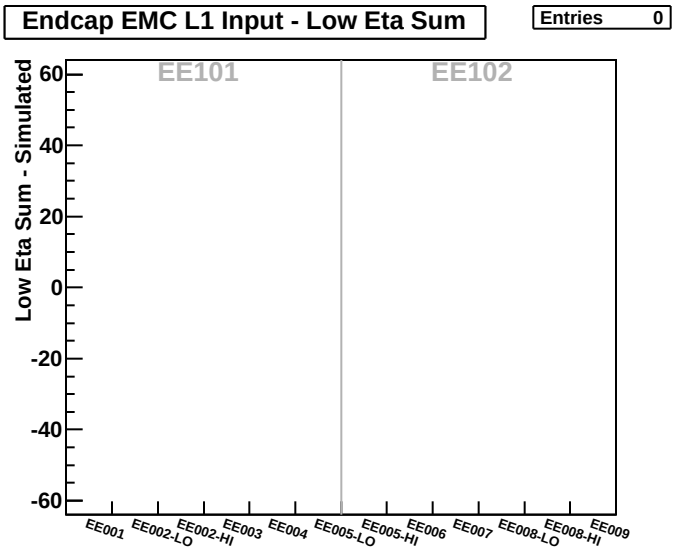

Barrel EMC L1 Input - High Eta Sum
Entries 1.204812e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

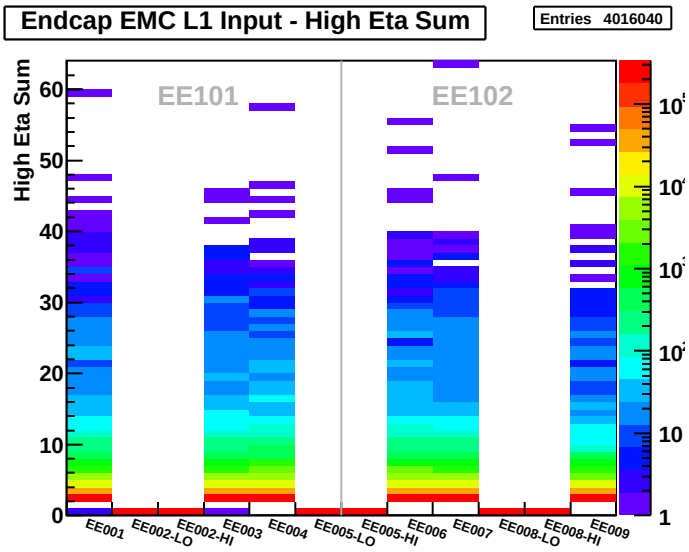

Barrel EMC L1 Input - High Eta Sum
Entries 154
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

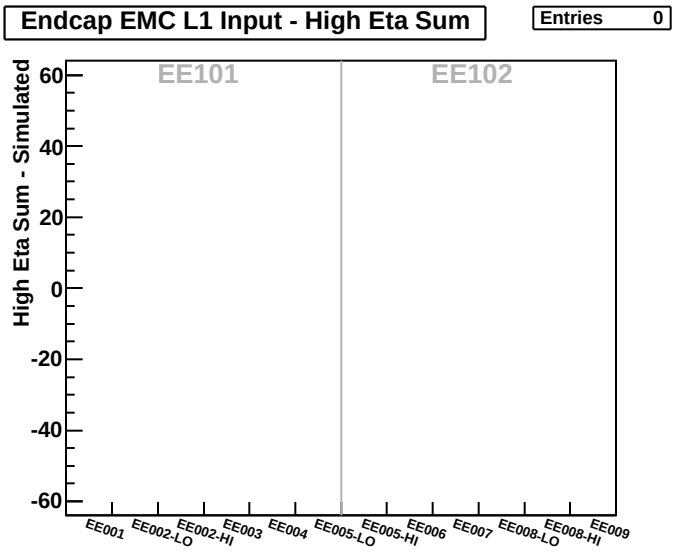

Barrel EMC L1 Input - High Tower Bits
Entries 1.204812e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

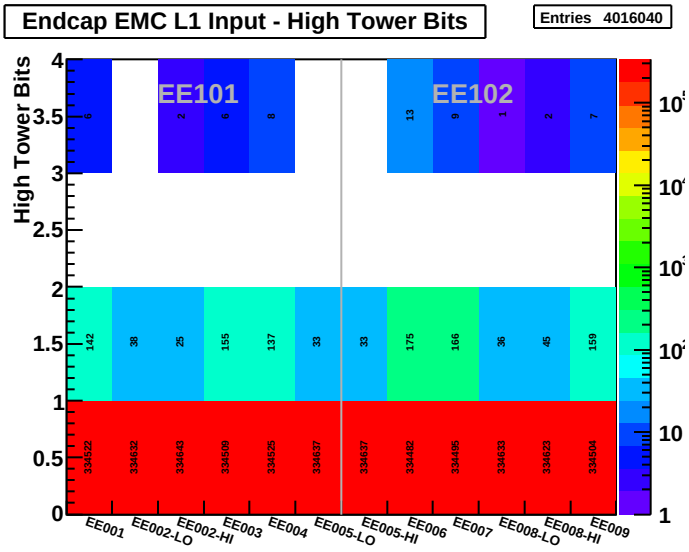

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

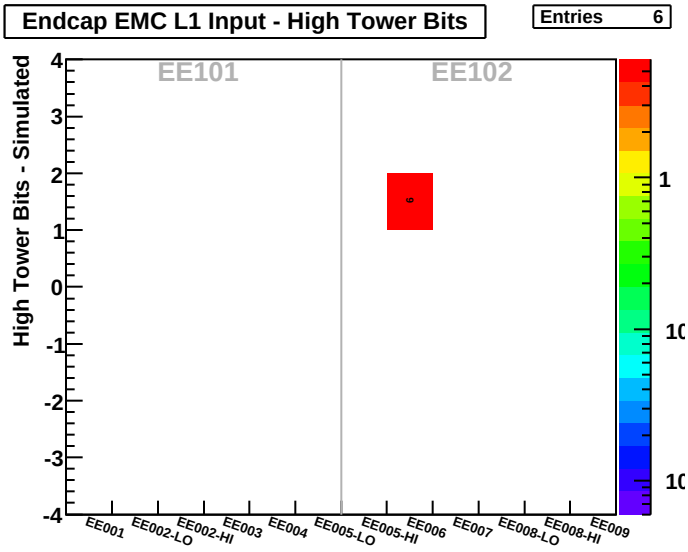

Endcap EMC L1 Input - Low Eta Sum
Entries 4016040
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

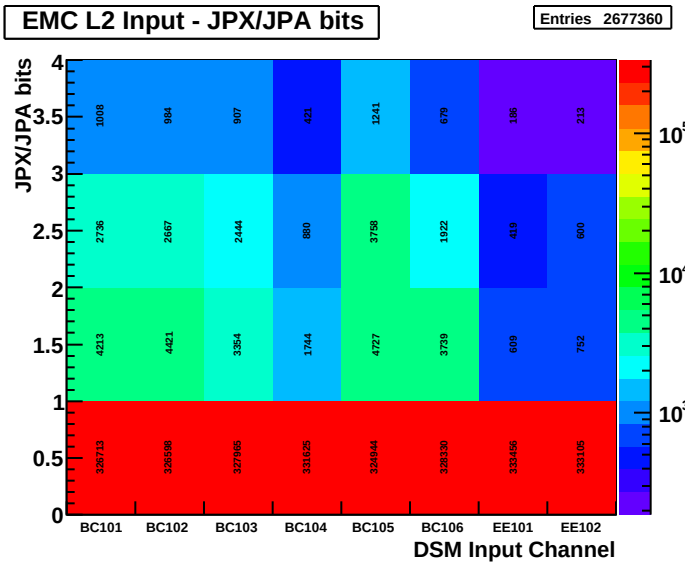

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

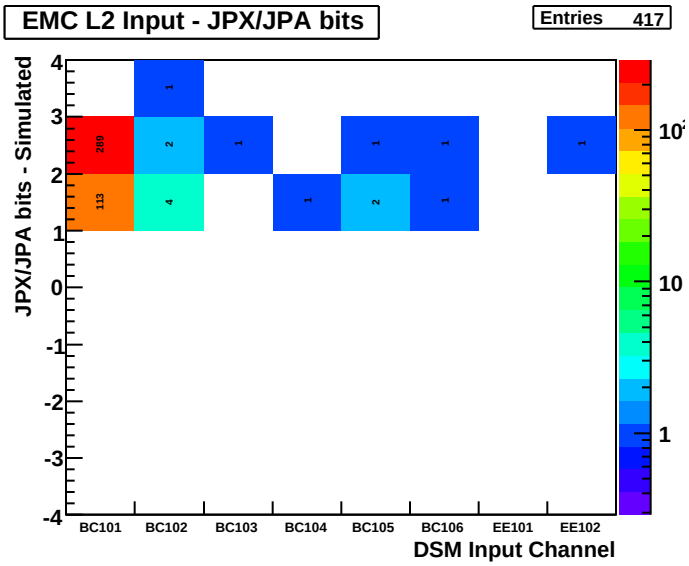

Endcap EMC L1 Input - High Eta Sum
Entries 4016040
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

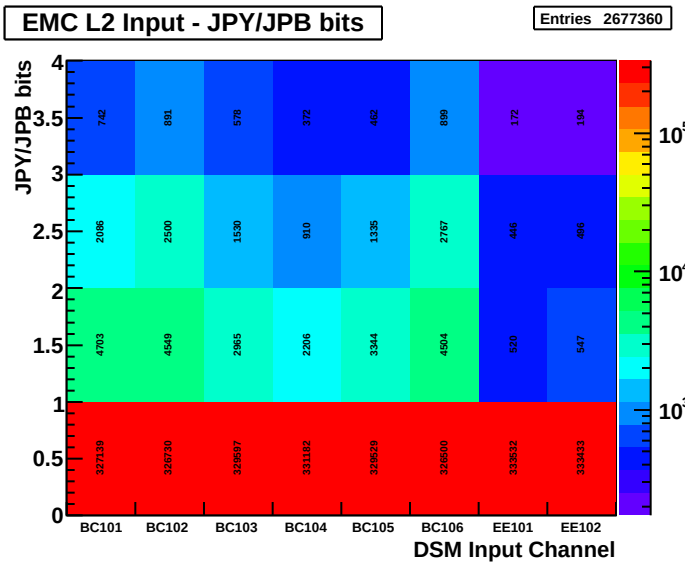

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

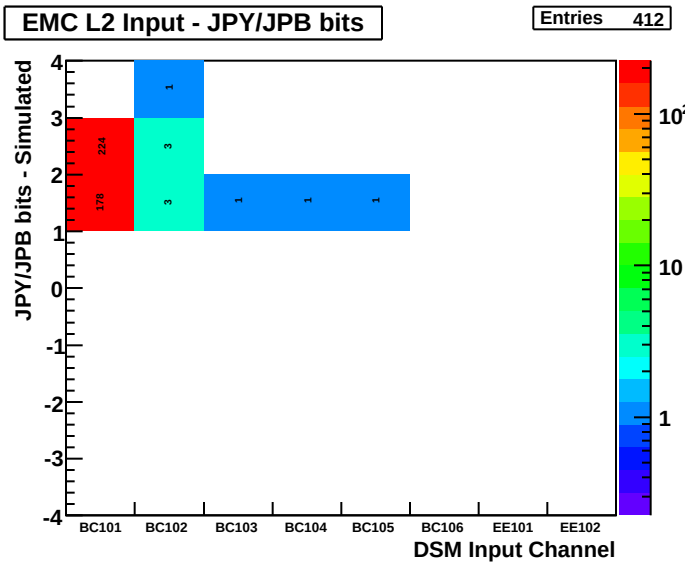

Endcap EMC L1 Input - High Tower Bits
Entries 4016040
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

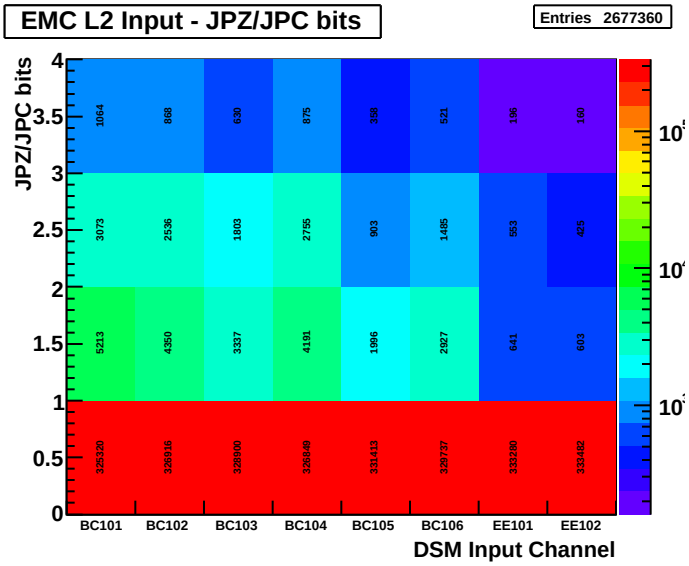

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

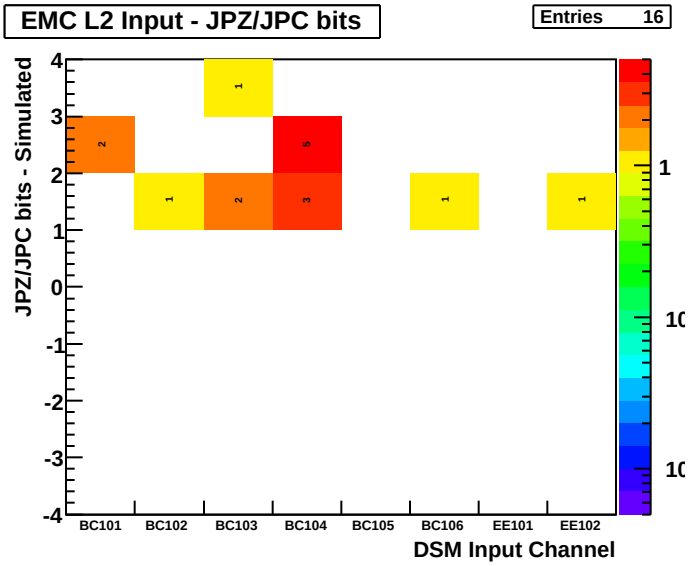

EMC L2 Input - JPX/JPA bits
Entries 2677360
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1008 984 907 421 1241 679 186 213
2736 2667 2444 880 3758 1922 419 600
4213 4421 3354 1744 4727 3739 609 752
326713 326598 327965 331625 324944 328330 333456 333105

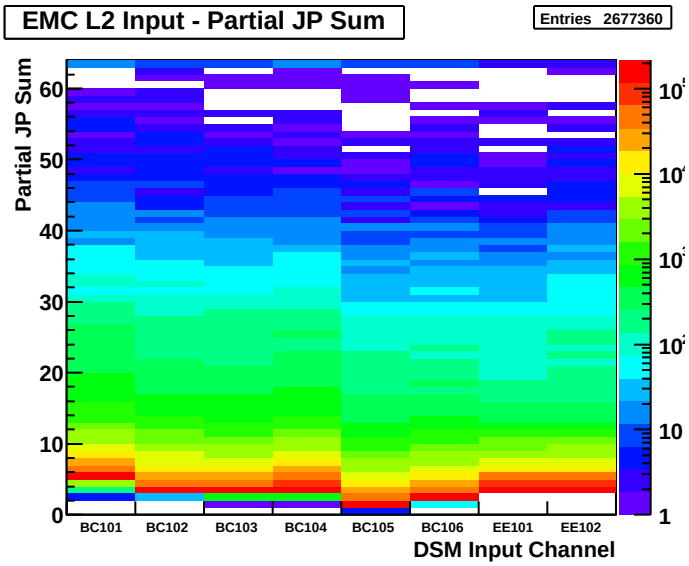

EMC L2 Input - JPX/JPA bits
Entries 417
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
289 1 1 1 1 1
113 4 2 1 2 1

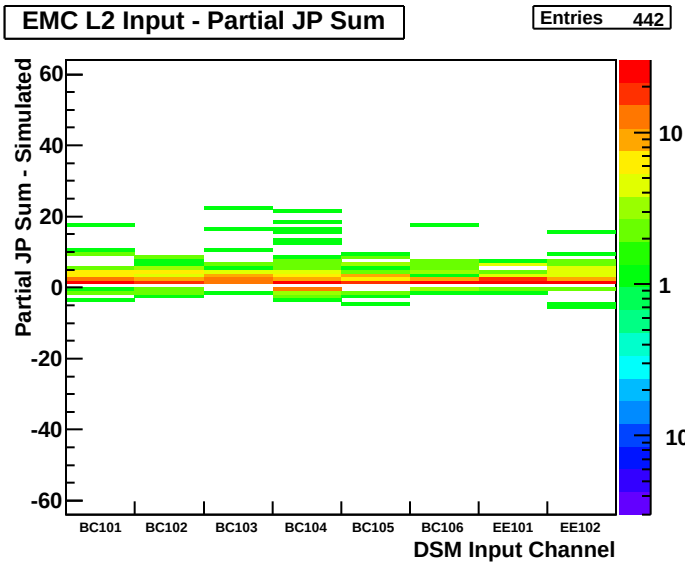

EMC L2 Input - JPY/JPB bits
Entries 2677360
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
742 891 578 372 462 899 172 194
2086 2500 1530 910 1335 2767 446 496
4703 4549 2965 2206 3344 4504 520 547
327139 326730 328597 331182 329529 326590 333532 333433

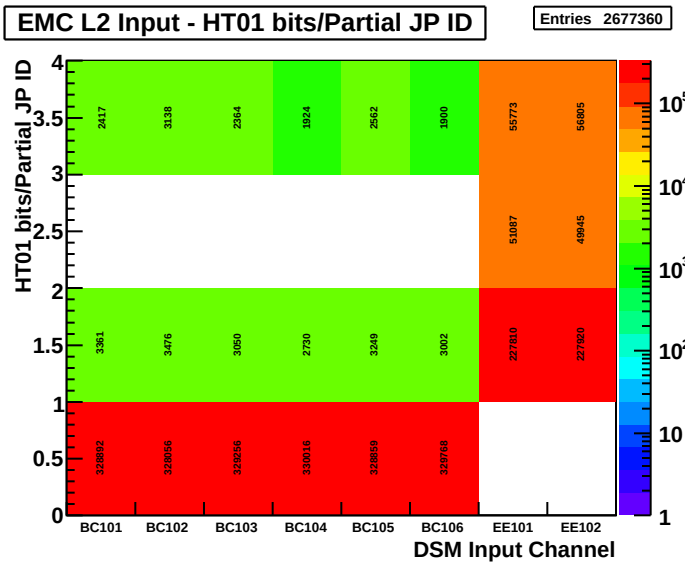

EMC L2 Input - JPY/JPB bits
Entries 412
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
224 1
178 3 3 1 1 1

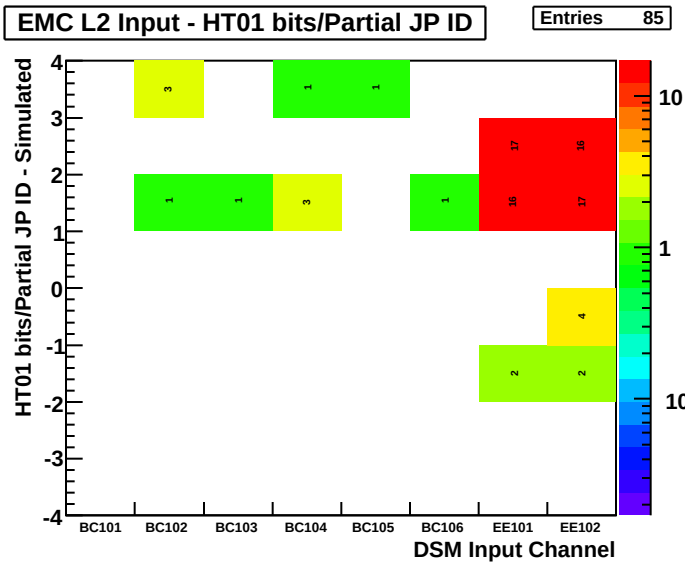

EMC L2 Input - JPZ/JPC bits
Entries 2677360
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1064 868 630 875 358 521 136 160
3073 2536 1803 2755 903 1485 553 425
5213 4350 3337 4191 1996 2927 641 603
325320 326916 328900 326840 331413 329737 333280 333482

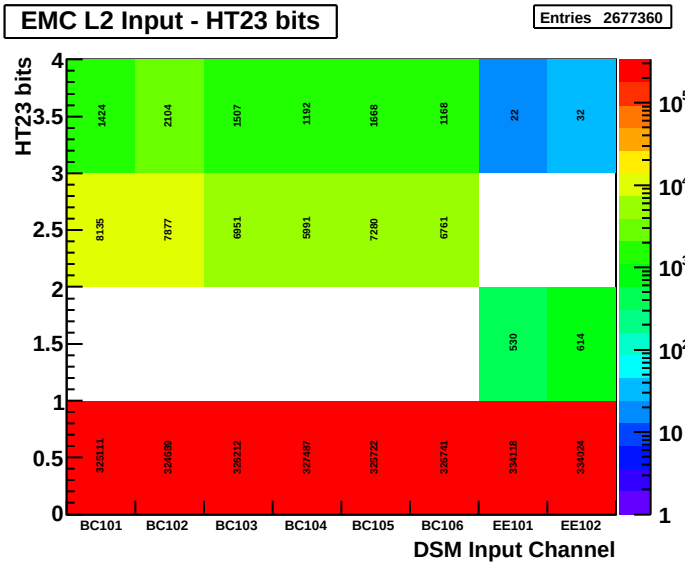

EMC L2 Input - JPZ/JPC bits
Entries 16
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
2 5
1 2 3 1 1

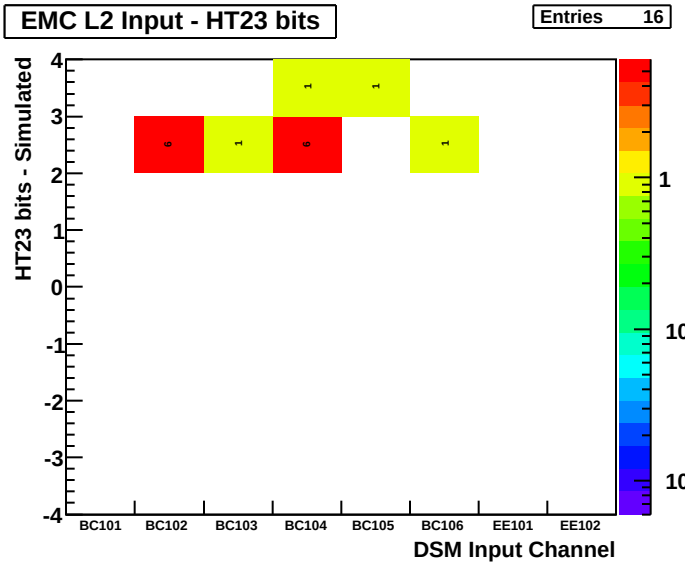

EMC L2 Input - Partial JP Sum
Entries 2677360
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

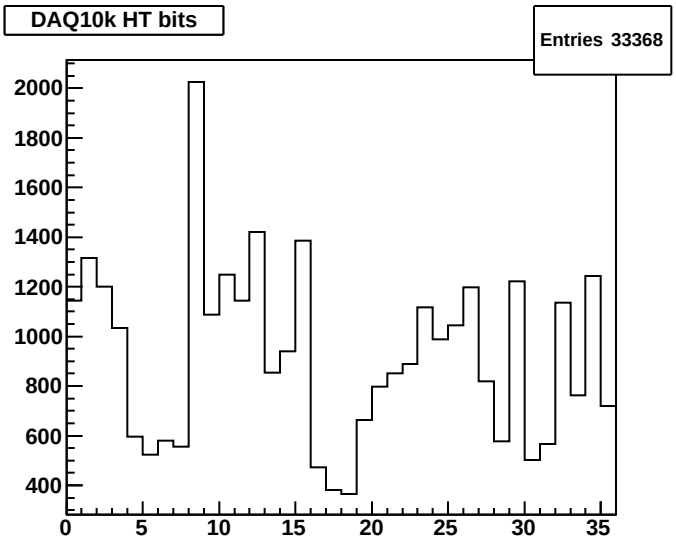

EMC L2 Input - Partial JP Sum
Entries 442
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

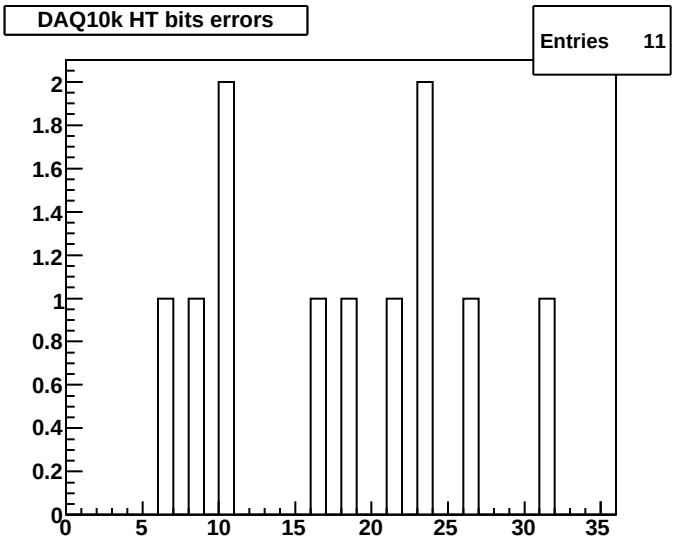

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2677360
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

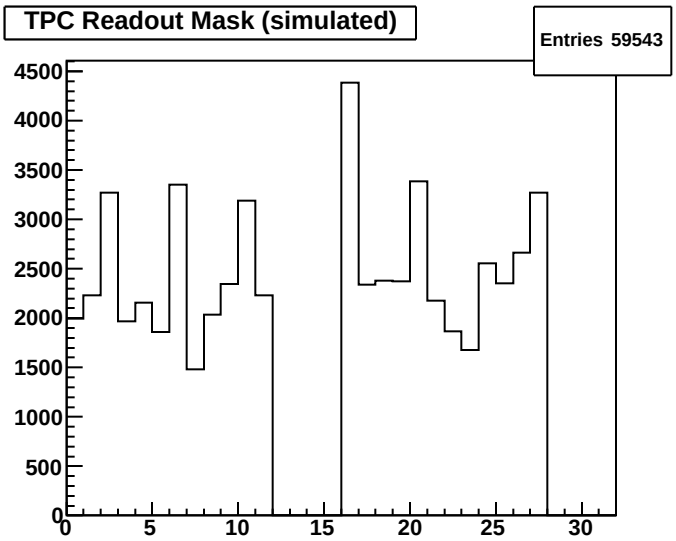

EMC L2 Input - HT01 bits/Partial JP ID
Entries 85
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

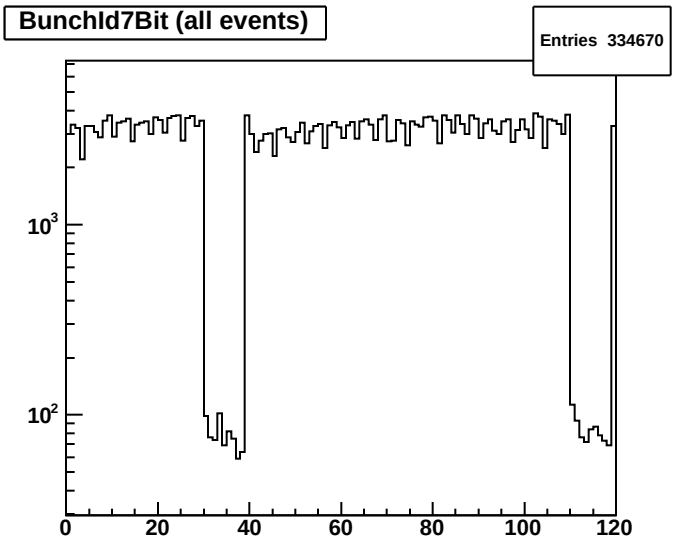

EMC L2 Input - HT23 bits
Entries 2677360
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

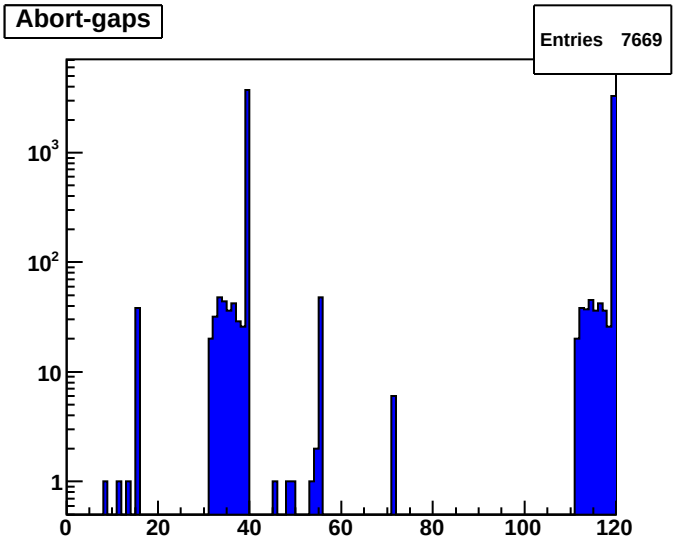

EMC L2 Input - HT23 bits
Entries 16
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

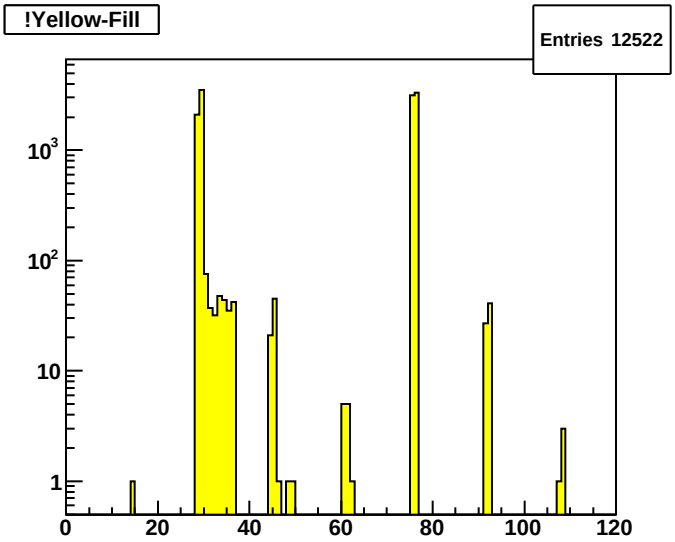

DAQ10k HT bits
Entries 33368

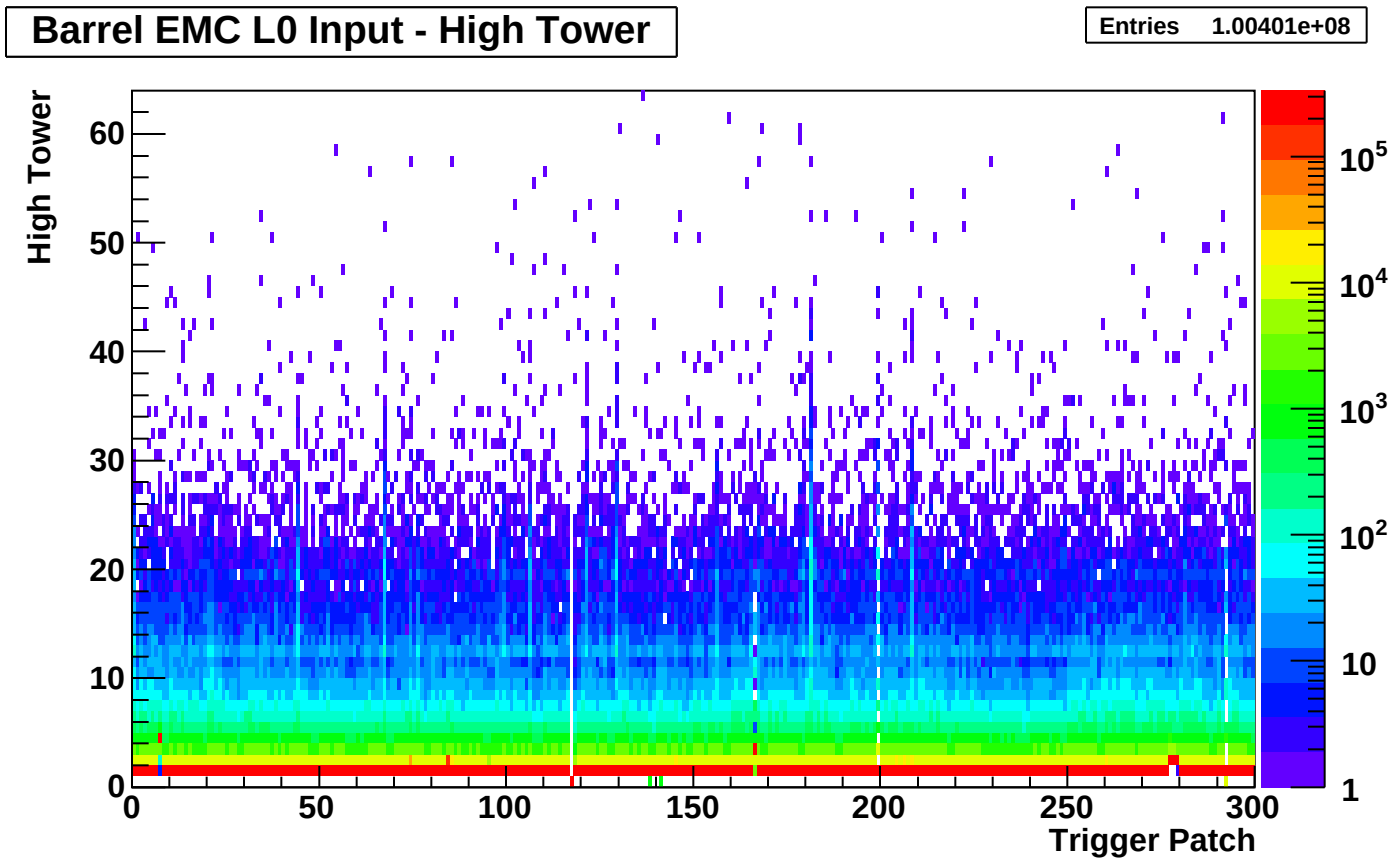

DAQ10k HT bits errors
Entries 11

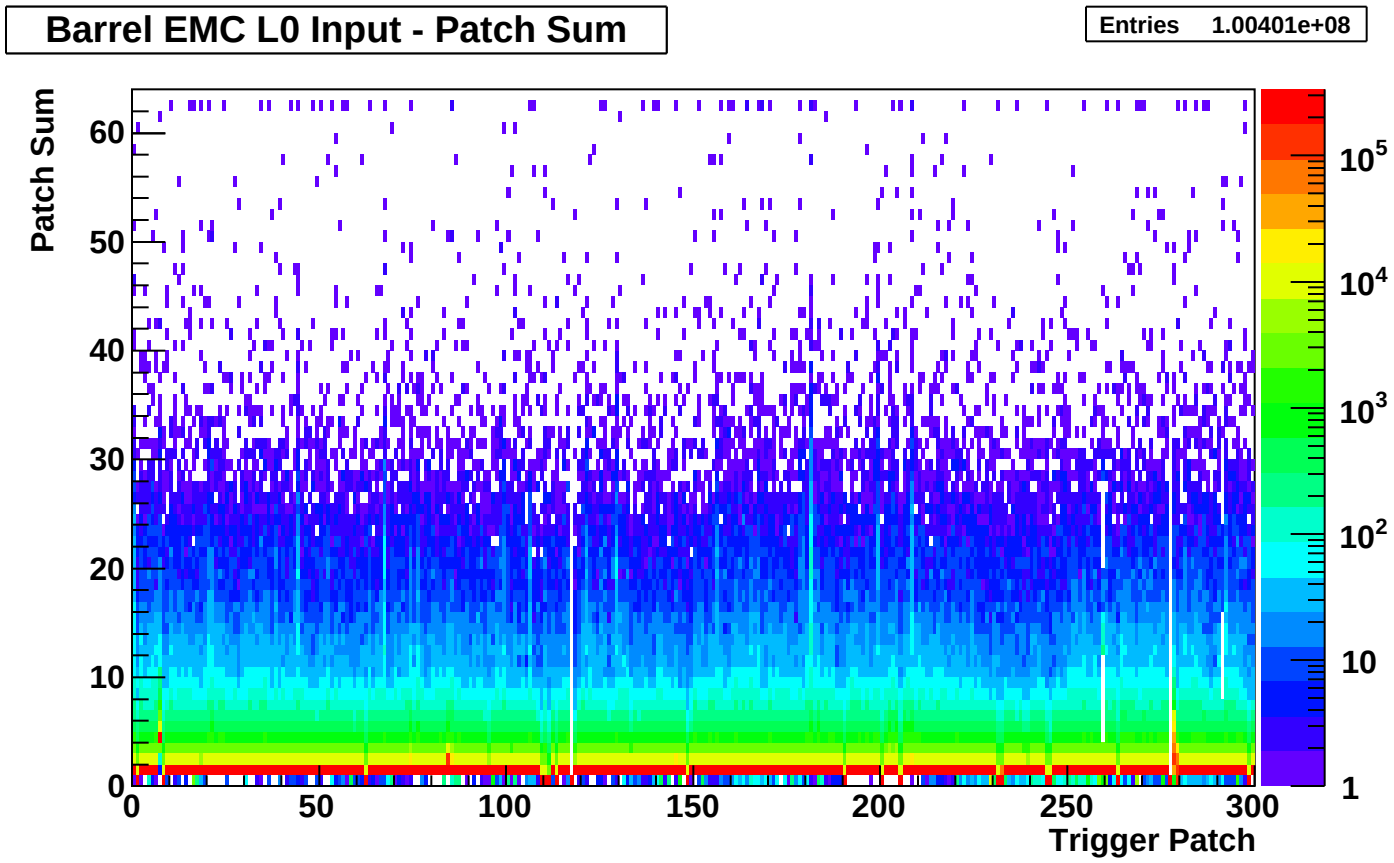

TPC Readout Mask (simulated)
Entries 59543

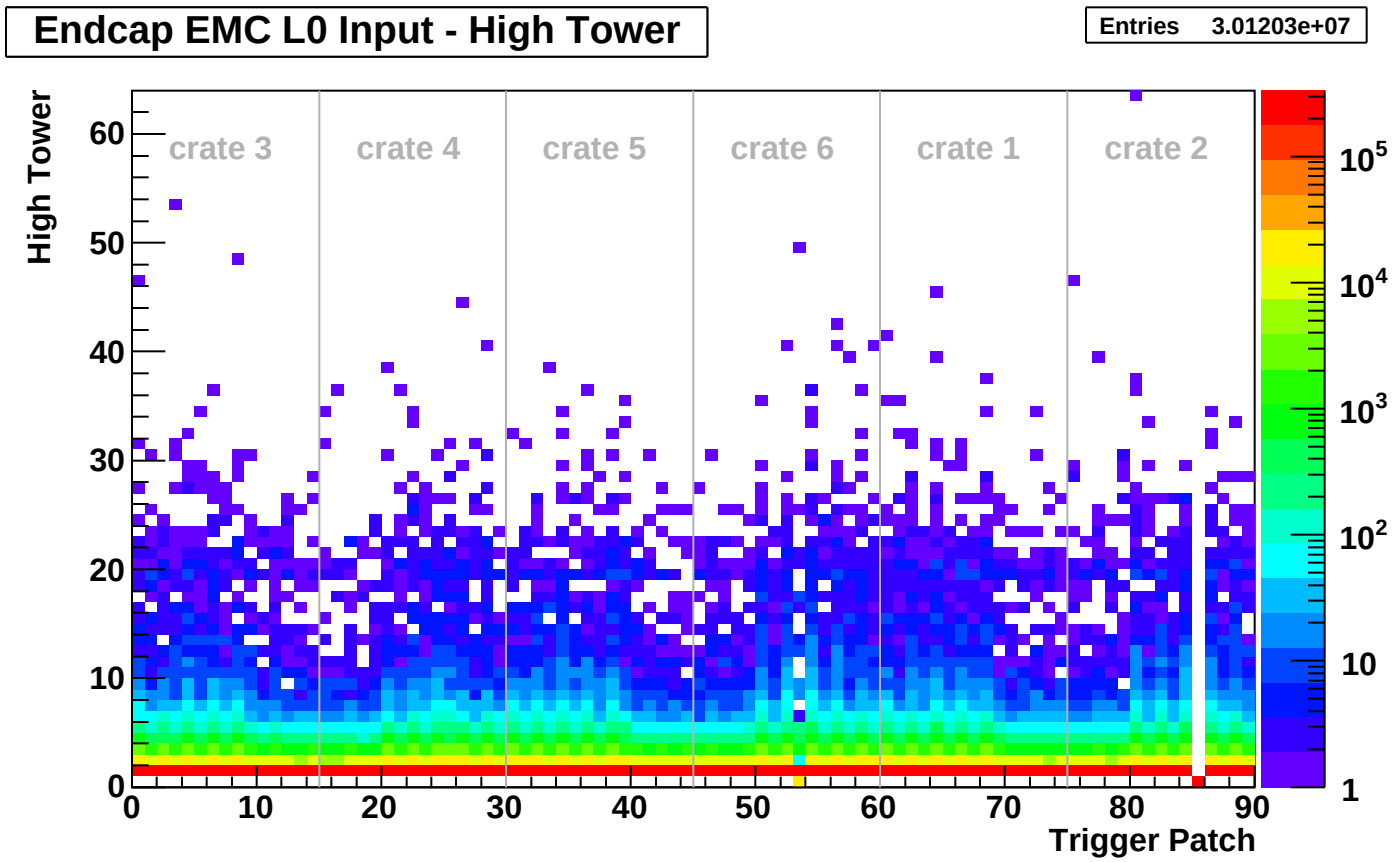

BunchId7Bit (all events)
Entries 334670

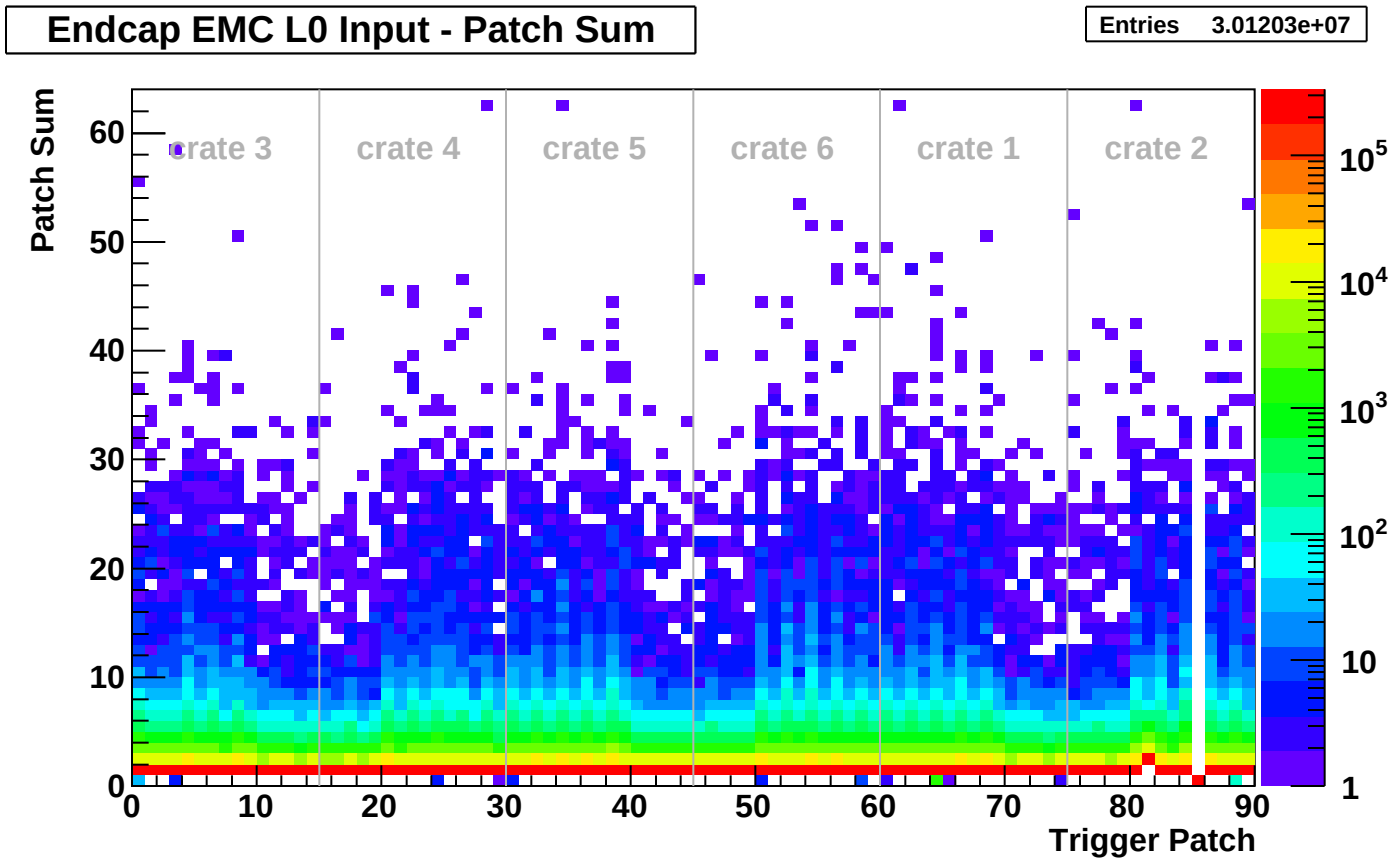

Abort-gaps
Entries 7669

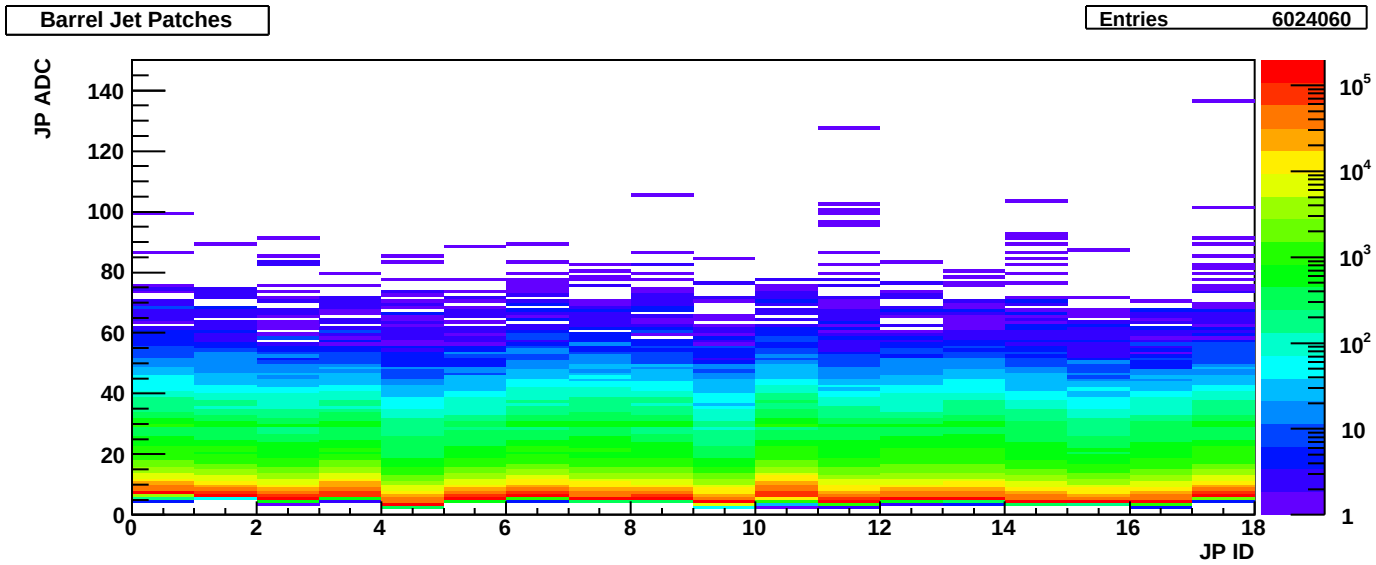

!Yellow-Fill
Entries 12522

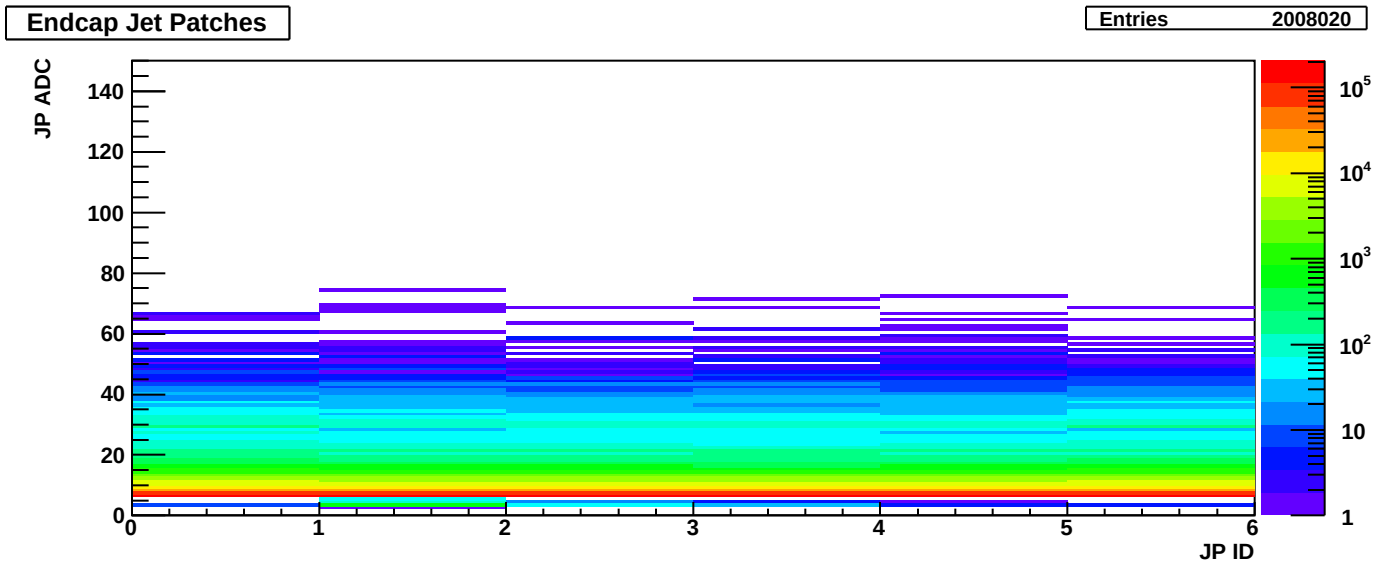

Barrel EMC L0 Input - High Tower
Entries 1.00401e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

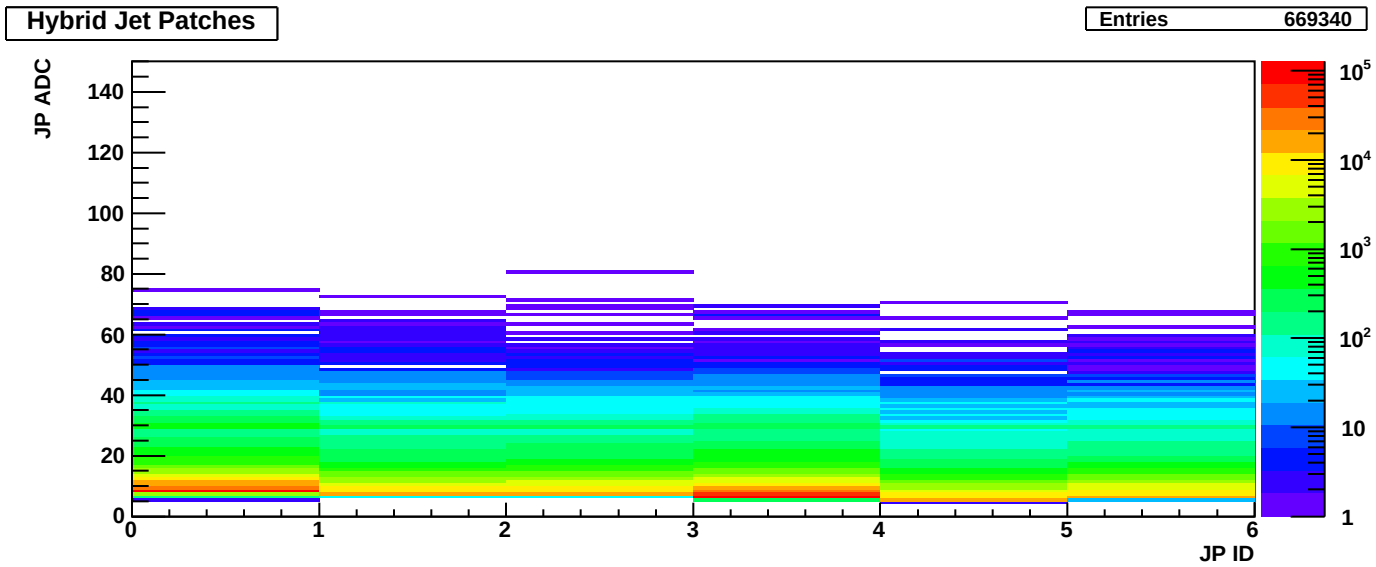

Barrel EMC L0 Input - Patch Sum
Entries 1.00401e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

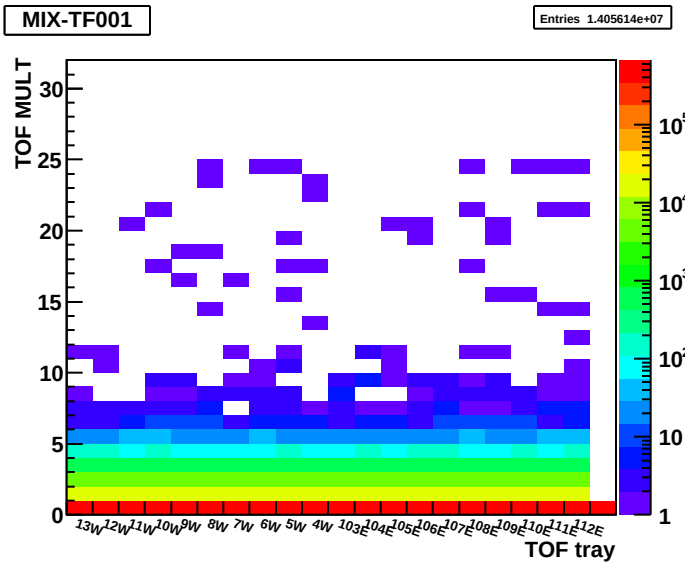

Endcap EMC L0 Input - High Tower
Entries 3.01203e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

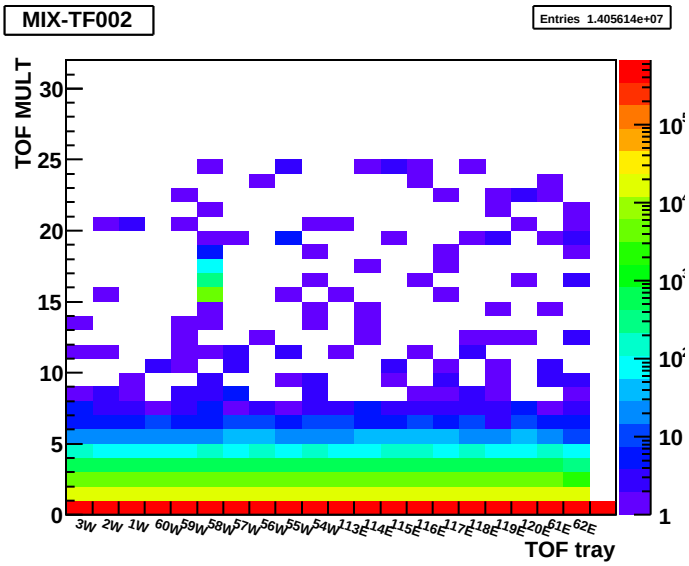

Endcap EMC L0 Input - Patch Sum
Entries 3.01203e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

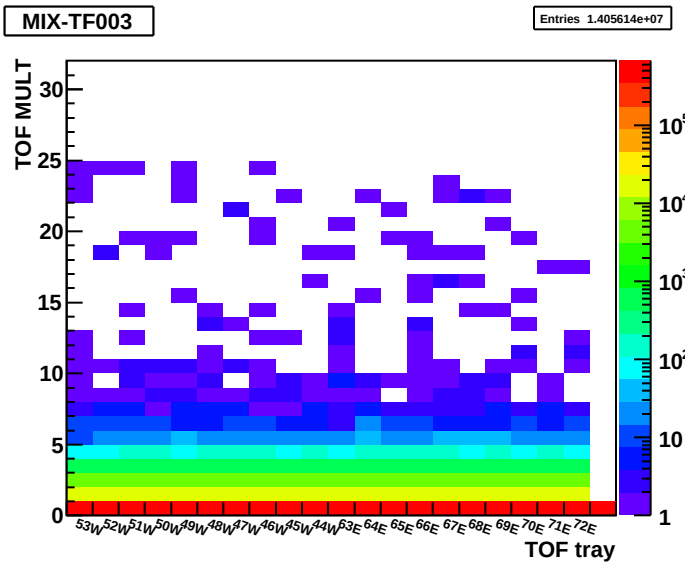

Barrel Jet Patches
Entries 6024060
JP ADC
JP ID

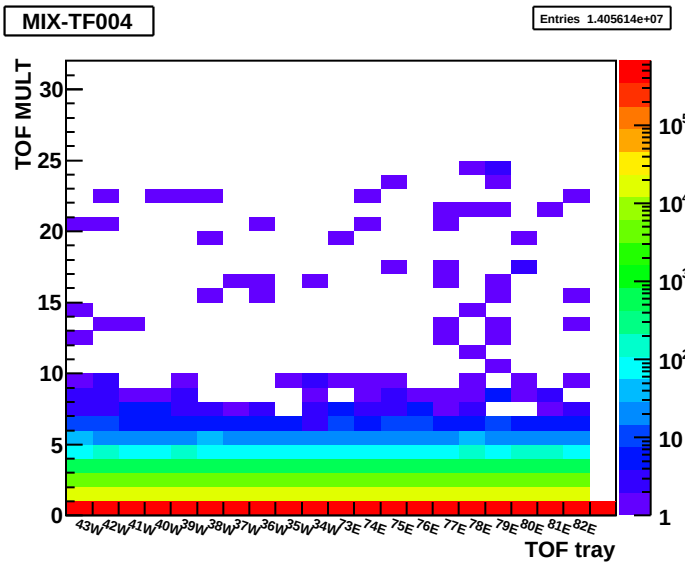

Endcap Jet Patches
Entries 2008020
JP ADC
JP ID

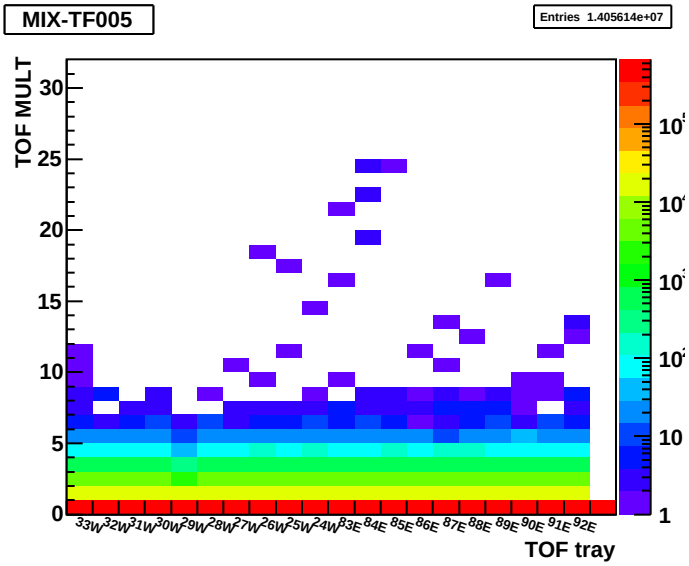

Hybrid Jet Patches
Entries 669340
JP ADC
JP ID

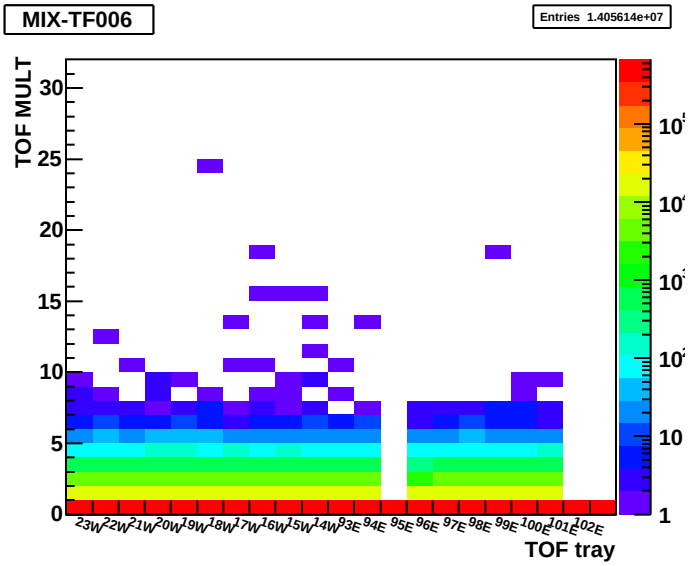

MIX-TF001
Entries 1.405614e+07
TOF MULT
TOF tray

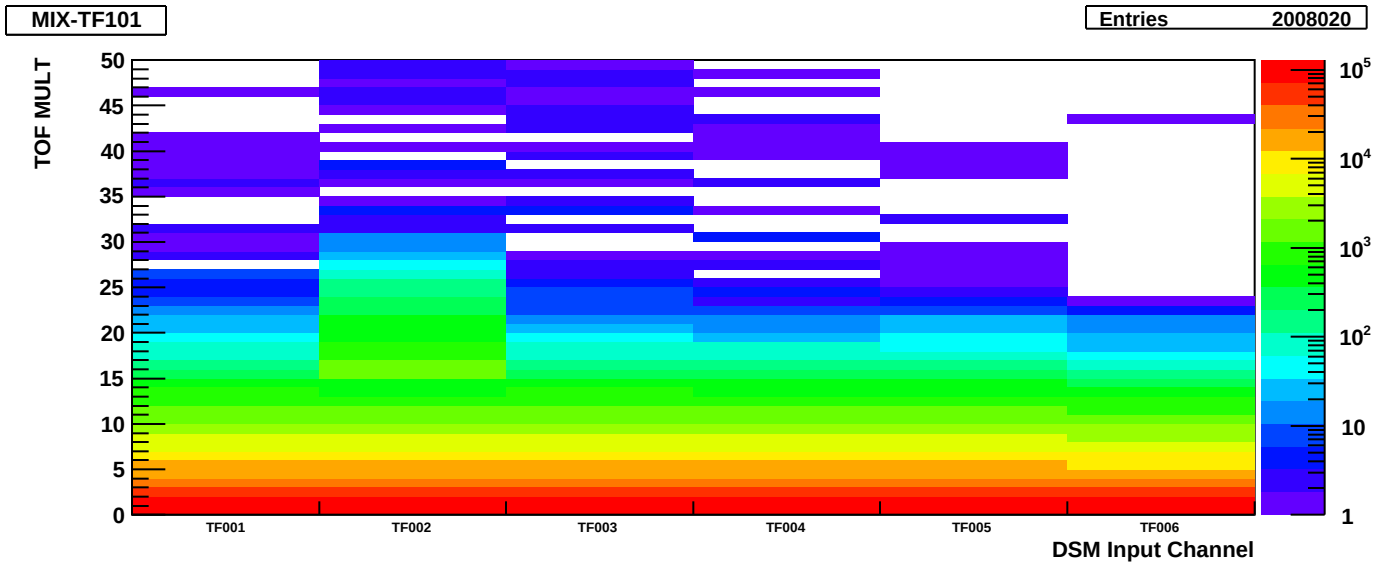

MIX-TF002
Entries 1.405614e+07
TOF MULT
TOF tray

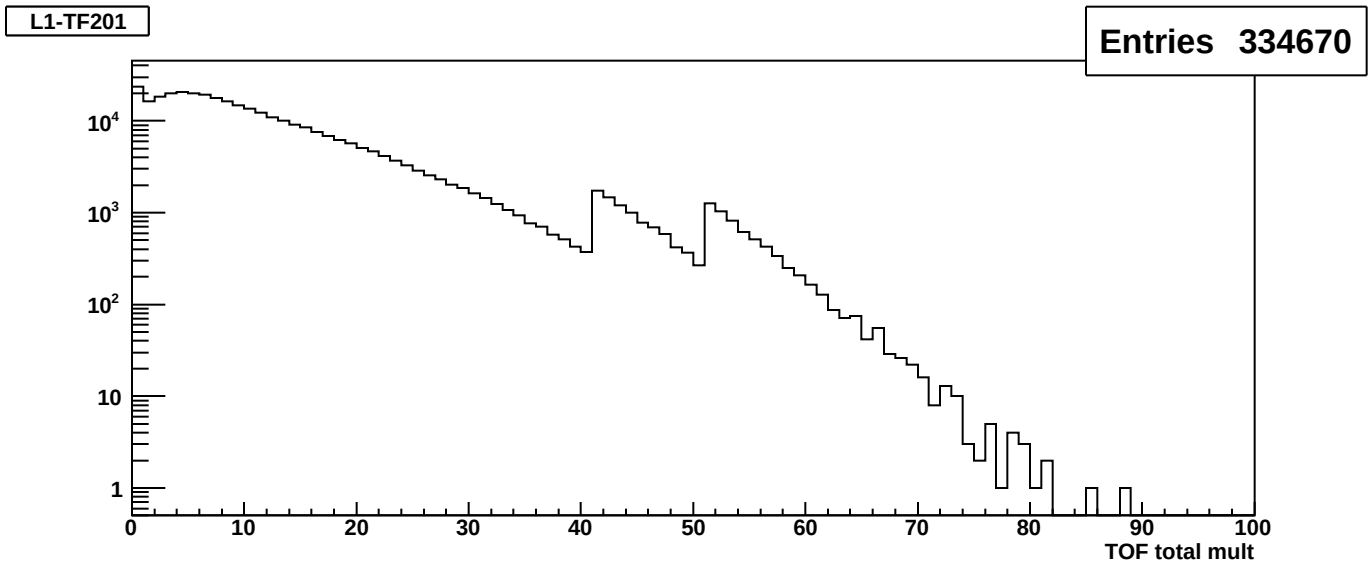

MIX-TF003
Entries 1.405614e+07
TOF MULT
TOF tray

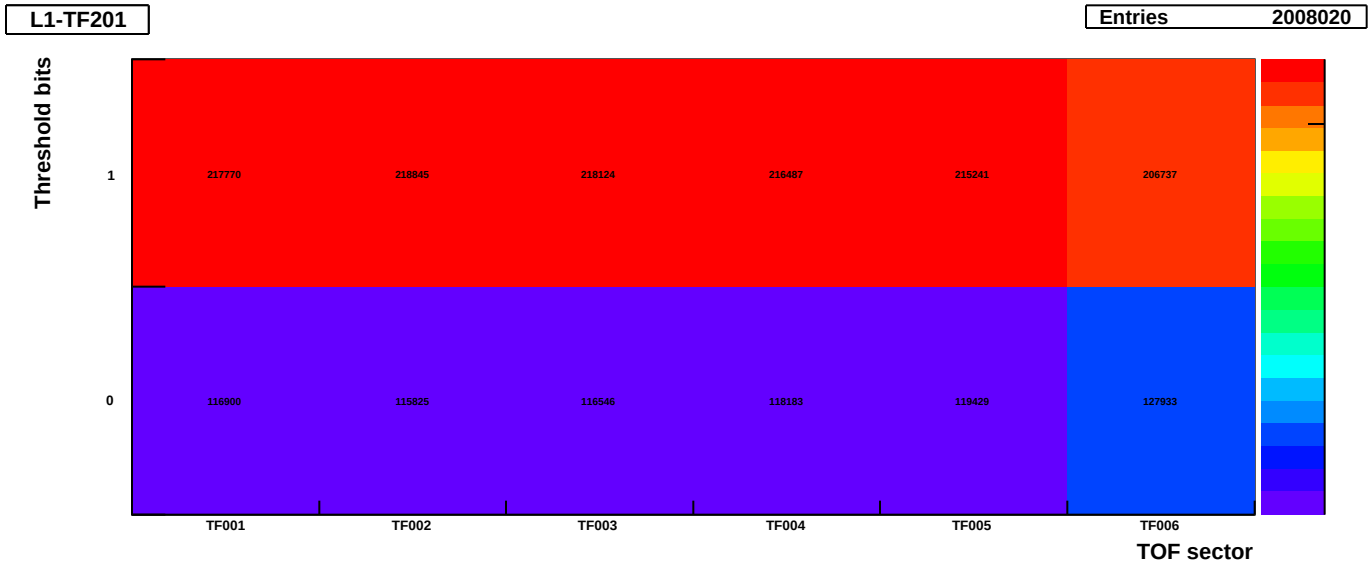

MIX-TF004
Entries 1.405614e+07
TOF MULT
TOF tray

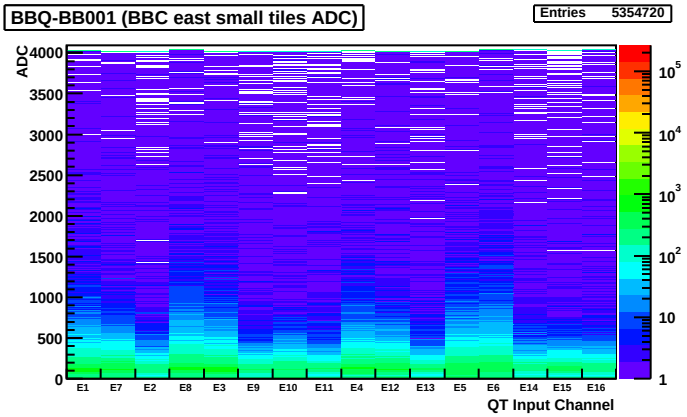

MIX-TF005
Entries 1.405614e+07
TOF MULT
TOF tray

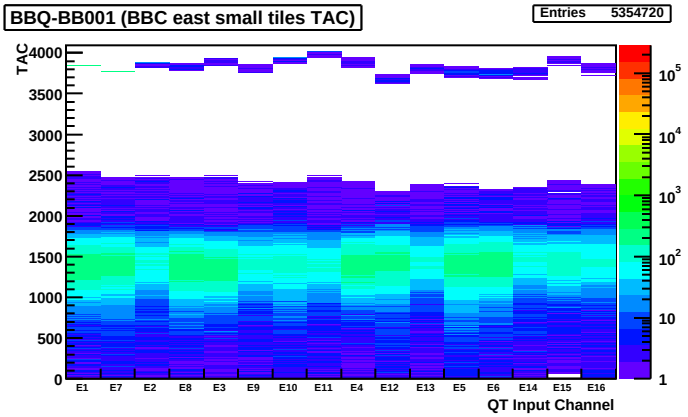

MIX-TF006
Entries 1.405614e+07
TOF MULT
TOF tray

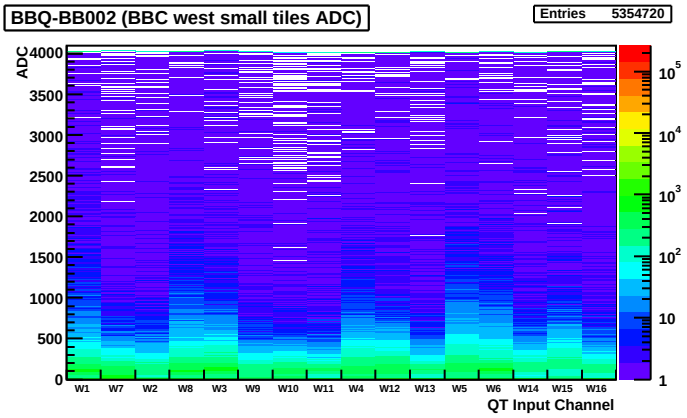

MIX-TF101
Entries 2008020
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

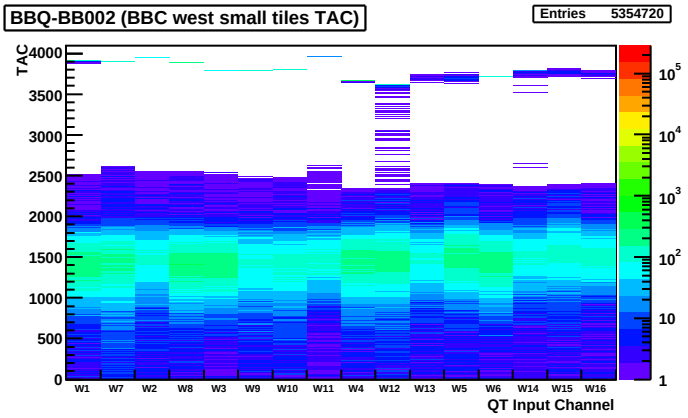

L1-TF201
Entries 334670
TOF total mult

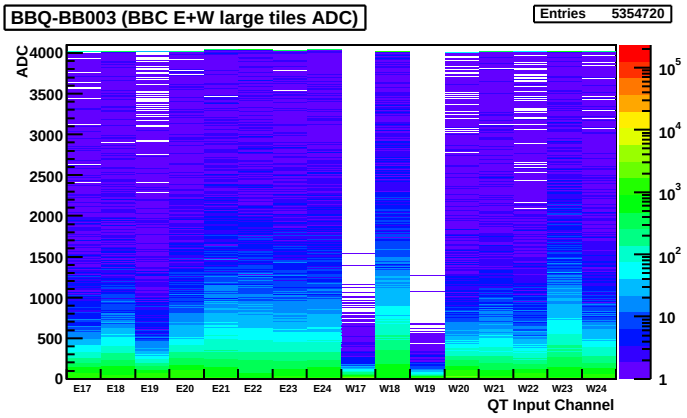

L1-TF201
Entries 2008020
Threshold bits
217770
218845
218124
216487
215241
206737
116900
115825
116546
118183
119429
127933
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

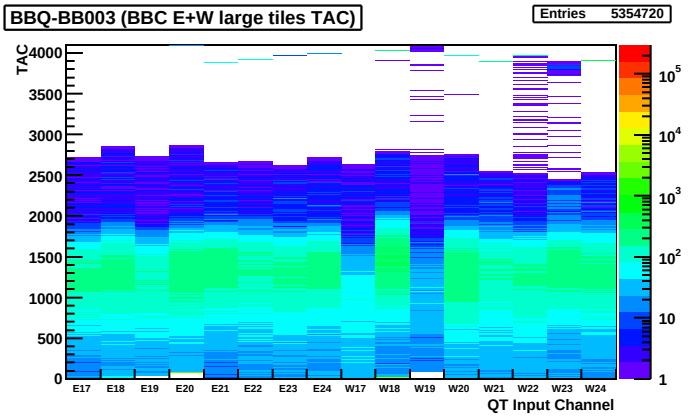

BBQ-BB001 (BBC east small tiles ADC)
Entries 5354720
ADC
QT Input Channel

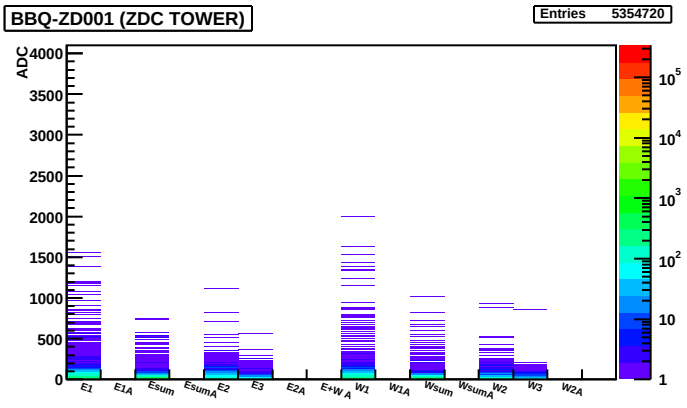

BBQ-BB001 (BBC east small tiles TAC)
Entries 5354720
TAC
QT Input Channel

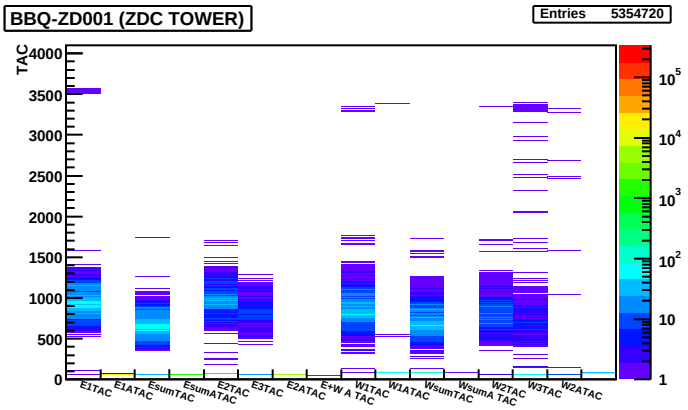

BBQ-BB002 (BBC west small tiles ADC)
Entries 5354720
ADC
QT Input Channel

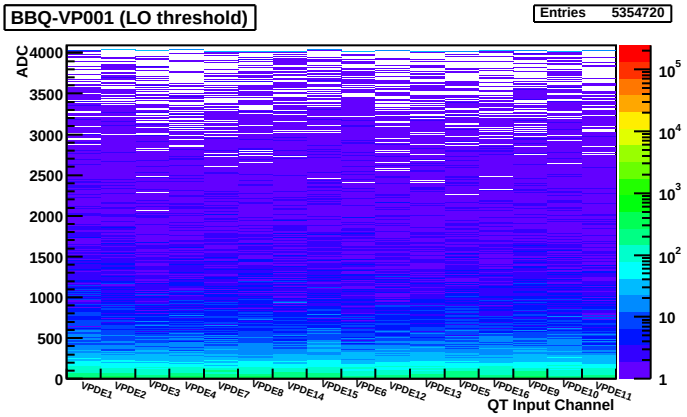

BBQ-BB002 (BBC west small tiles TAC)
Entries 5354720
TAC
QT Input Channel

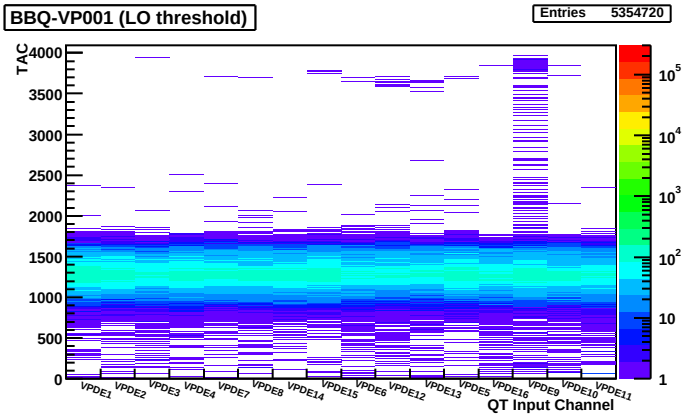

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5354720
ADC
QT Input Channel

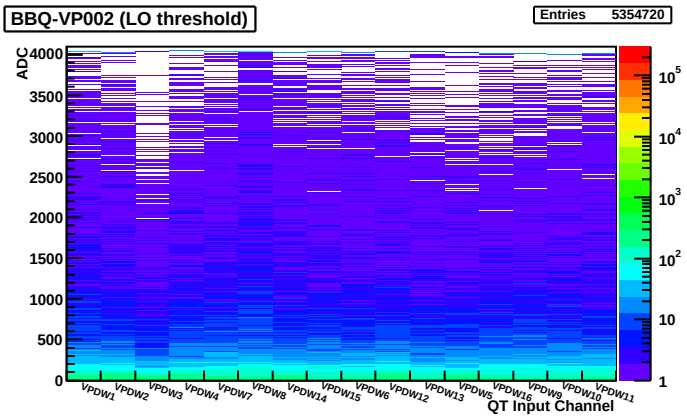

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5354720
TAC
QT Input Channel

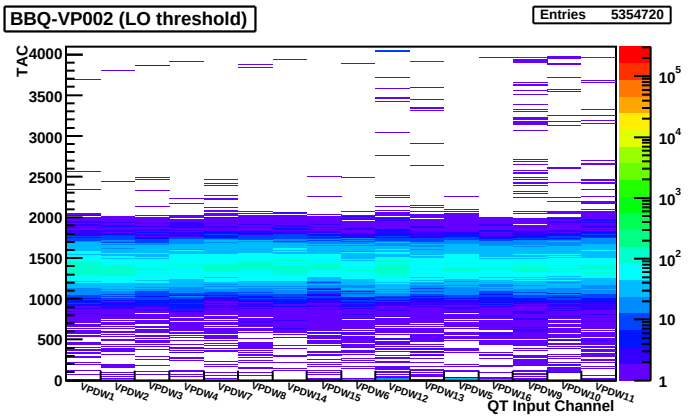

BBQ-ZD001 (ZDC TOWER)
Entries 5354720
ADC

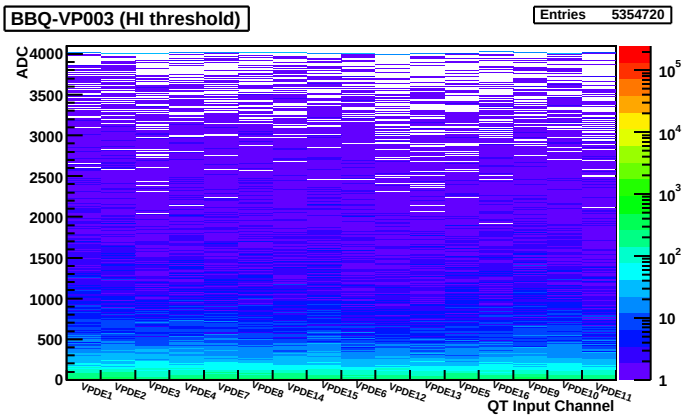

BBQ-ZD001 (ZDC TOWER)
Entries 5354720
TAC

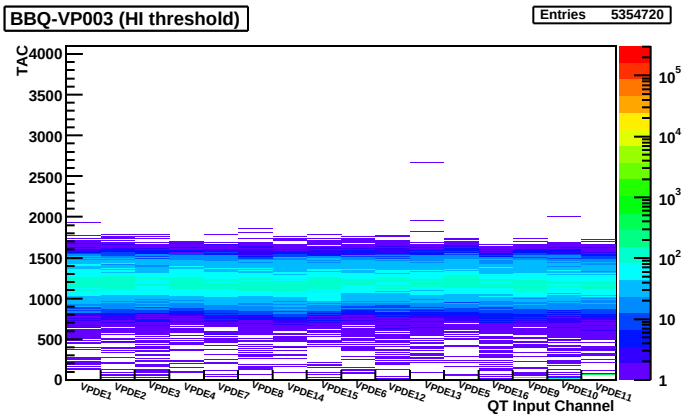

BBQ-VP001 (LO threshold)
Entries 5354720
ADC
QT Input Channel

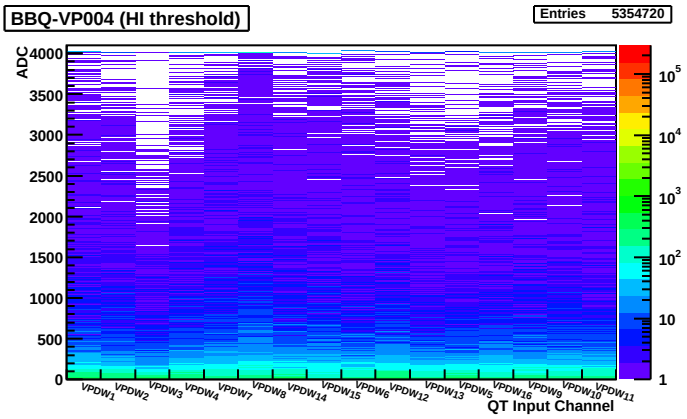

BBQ-VP001 (LO threshold)
Entries 5354720
TAC
QT Input Channel

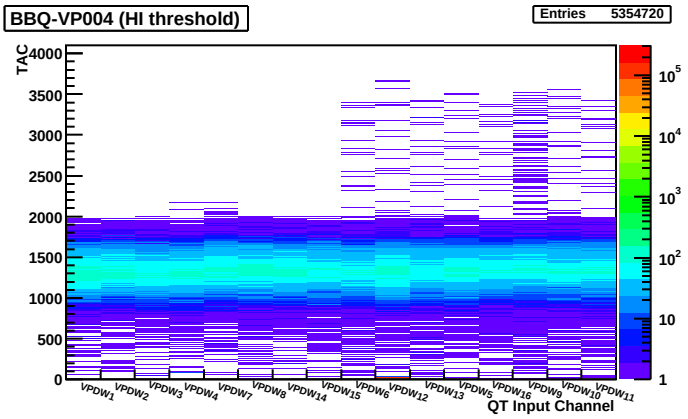

BBQ-VP002 (LO threshold)
Entries 5354720
ADC
QT Input Channel

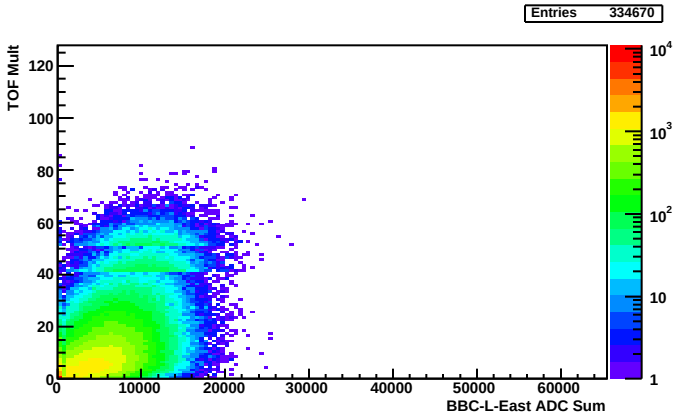

BBQ-VP002 (LO threshold)
Entries 5354720
TAC
QT Input Channel

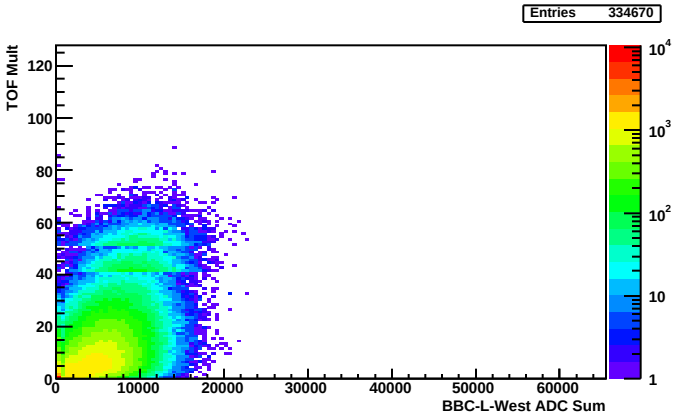

BBQ-VP003 (HI threshold)
Entries 5354720
ADC
QT Input Channel

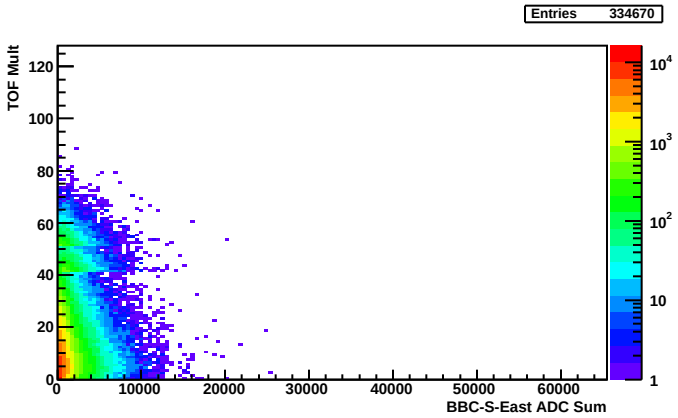

BBQ-VP003 (HI threshold)
Entries 5354720
TAC
QT Input Channel

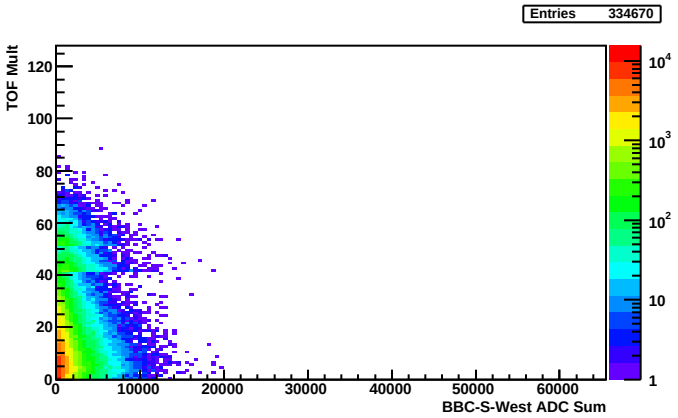

BBQ-VP004 (HI threshold)
Entries 5354720
ADC
QT Input Channel

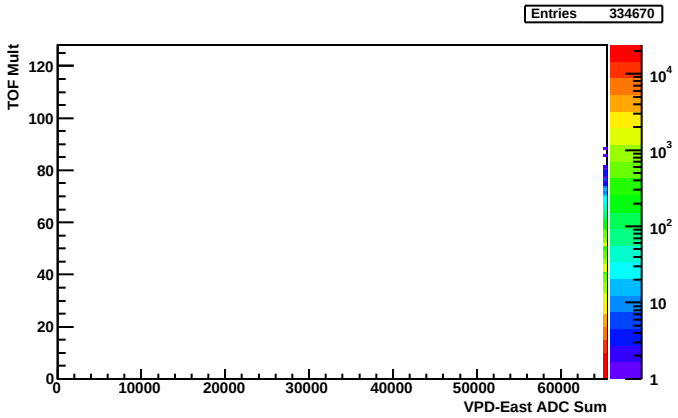

BBQ-VP004 (HI threshold)
Entries 5354720
TAC
QT Input Channel

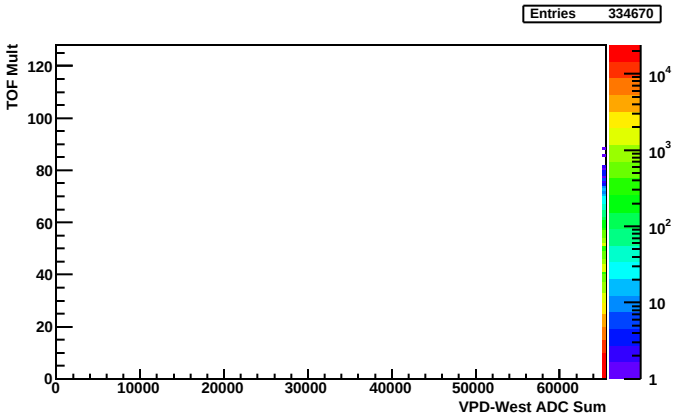

Entries 334670
TOF Mult
BBC-L-East ADC Sum

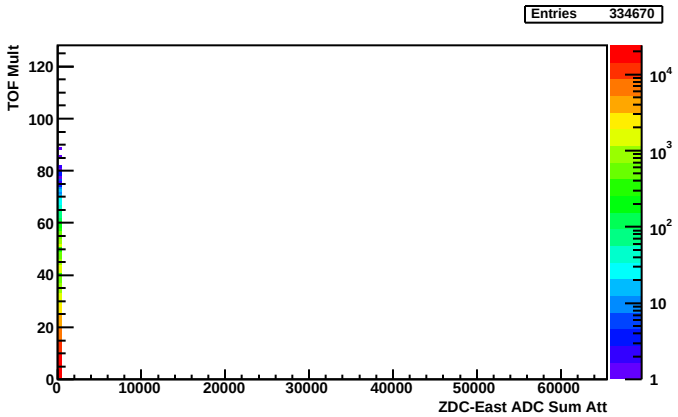

Entries 334670
TOF Mult
BBC-L-West ADC Sum

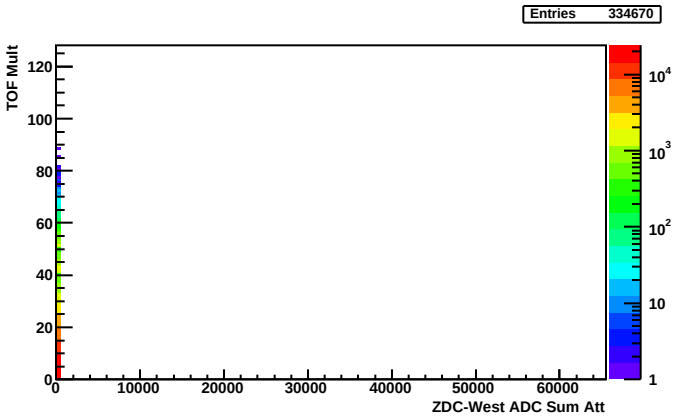

Entries 334670
TOF Mult
BBC-S-East ADC Sum

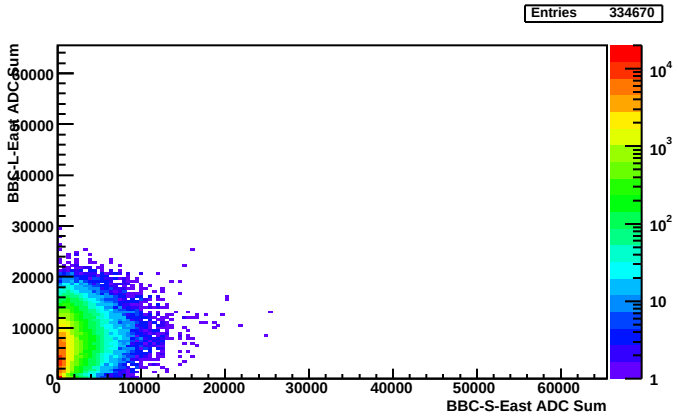

Entries 334670
TOF Mult
BBC-S-West ADC Sum

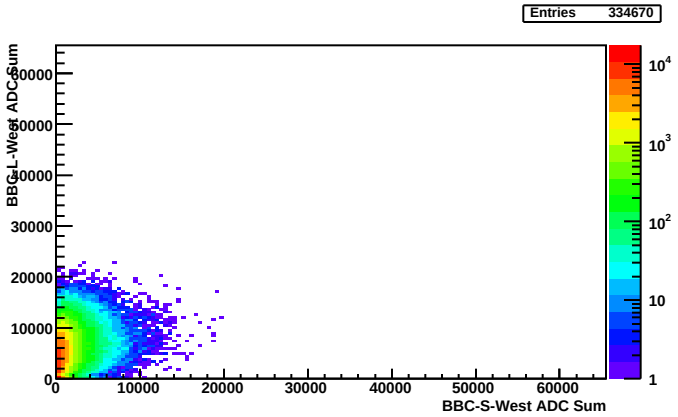

Entries 334670
TOF Mult
VPD-East ADC Sum

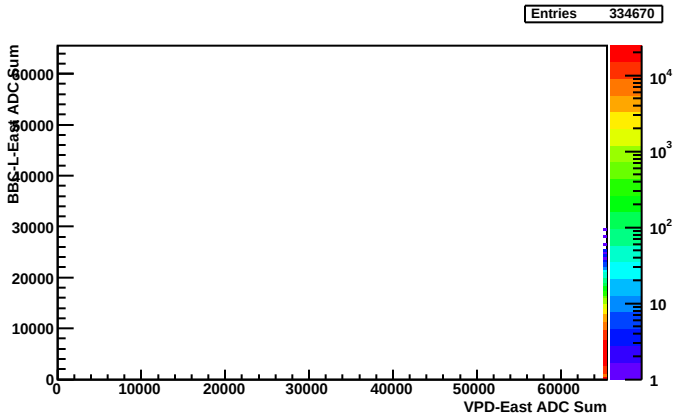

Entries 334670
TOF Mult
VPD-West ADC Sum

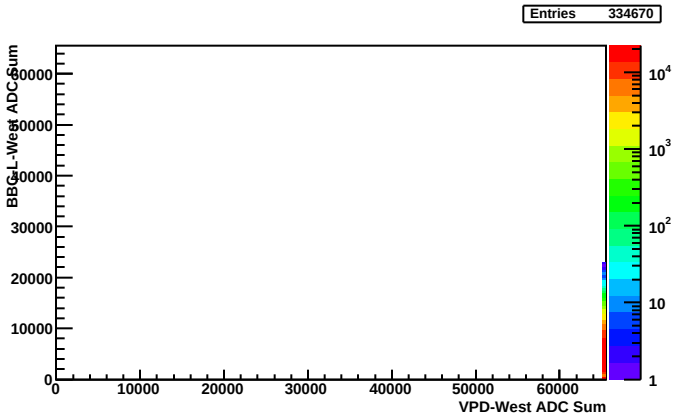

Entries 334670
TOF Mult
ZDC-East ADC Sum Att

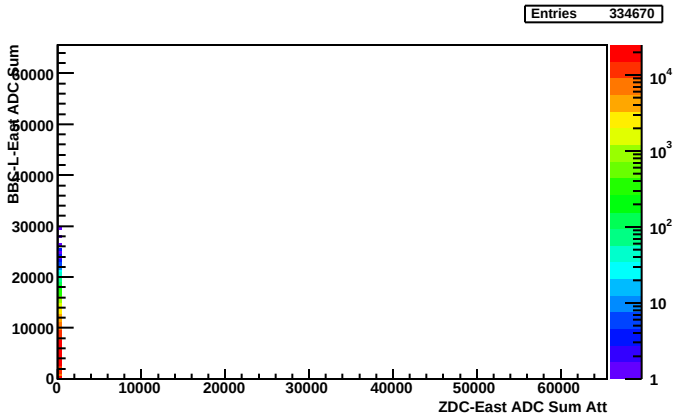

Entries 334670
TOF Mult
ZDC-West ADC Sum Att

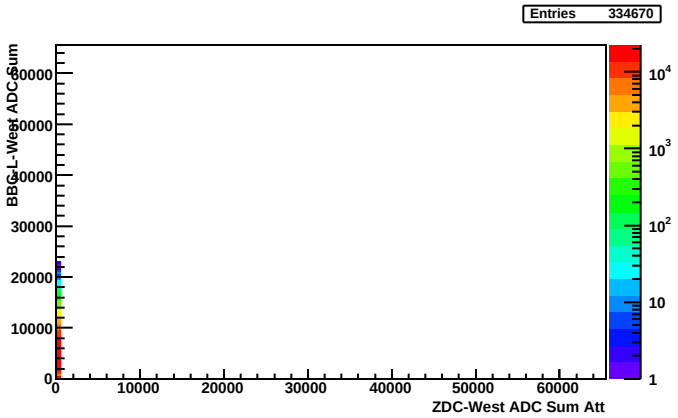

Entries 334670
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 334670
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 334670
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 334670
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 334670
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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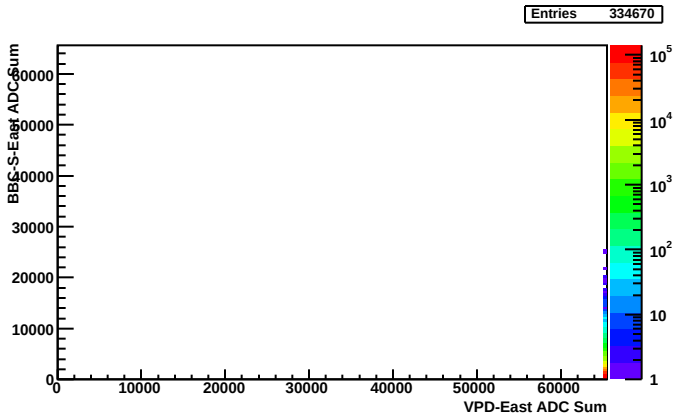
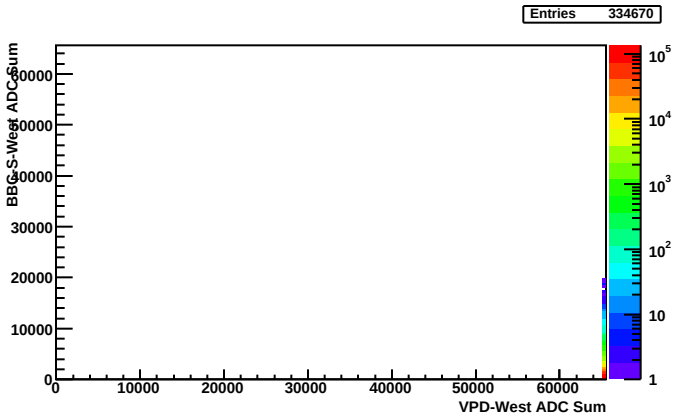
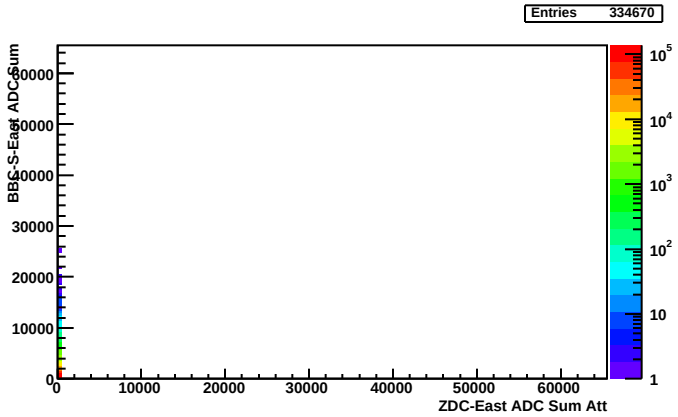
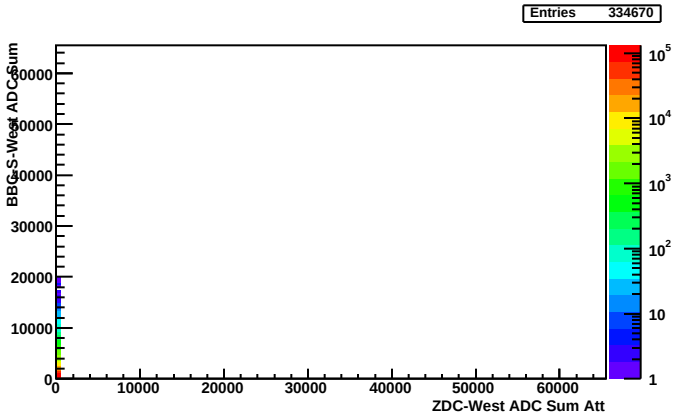
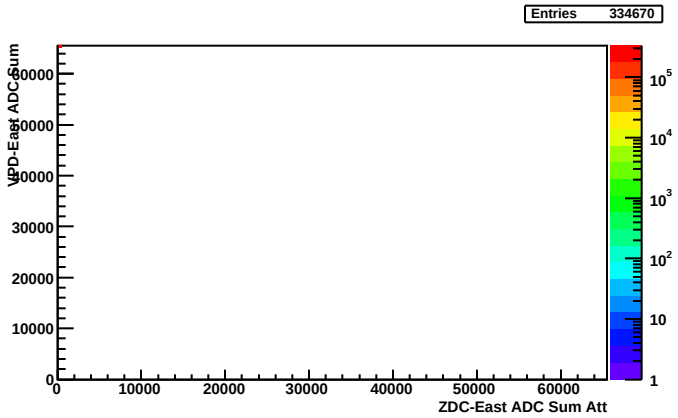
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

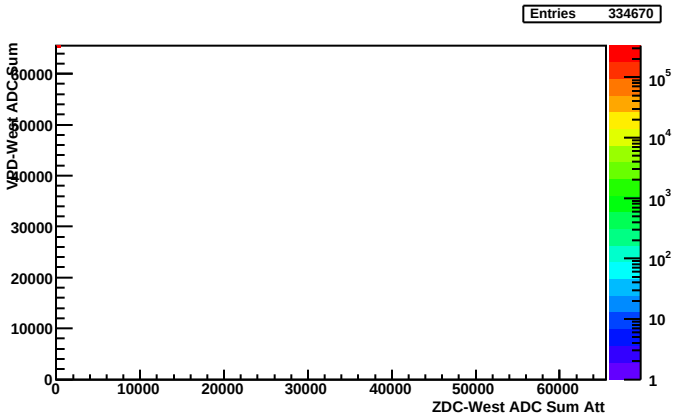

Entries 334670
BBC-S-East ADC Sum
VPD-East ADC Sum

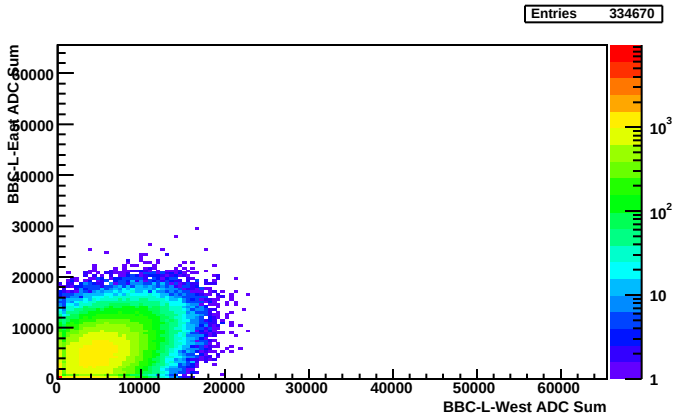

Entries 334670
BBC-S-West ADC Sum
VPD-West ADC Sum

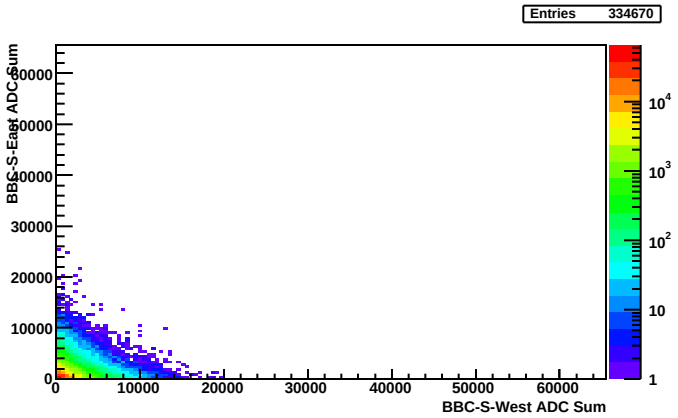

Entries 334670
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

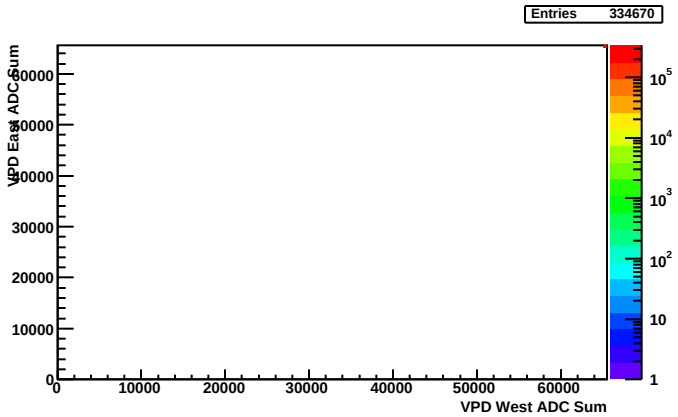

Entries 334670
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

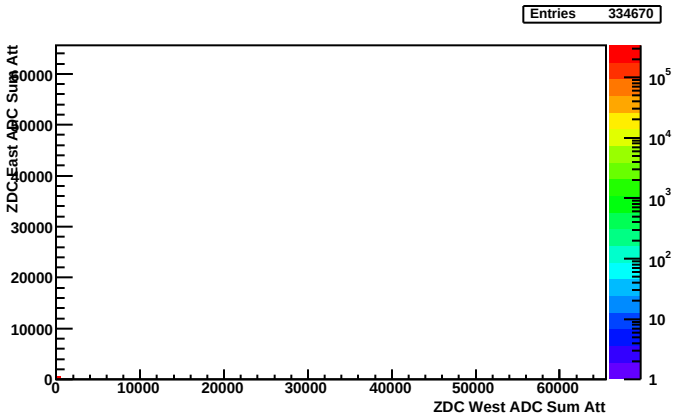

Entries 334670
VPD-East ADC Sum
ZDC-East ADC Sum Att

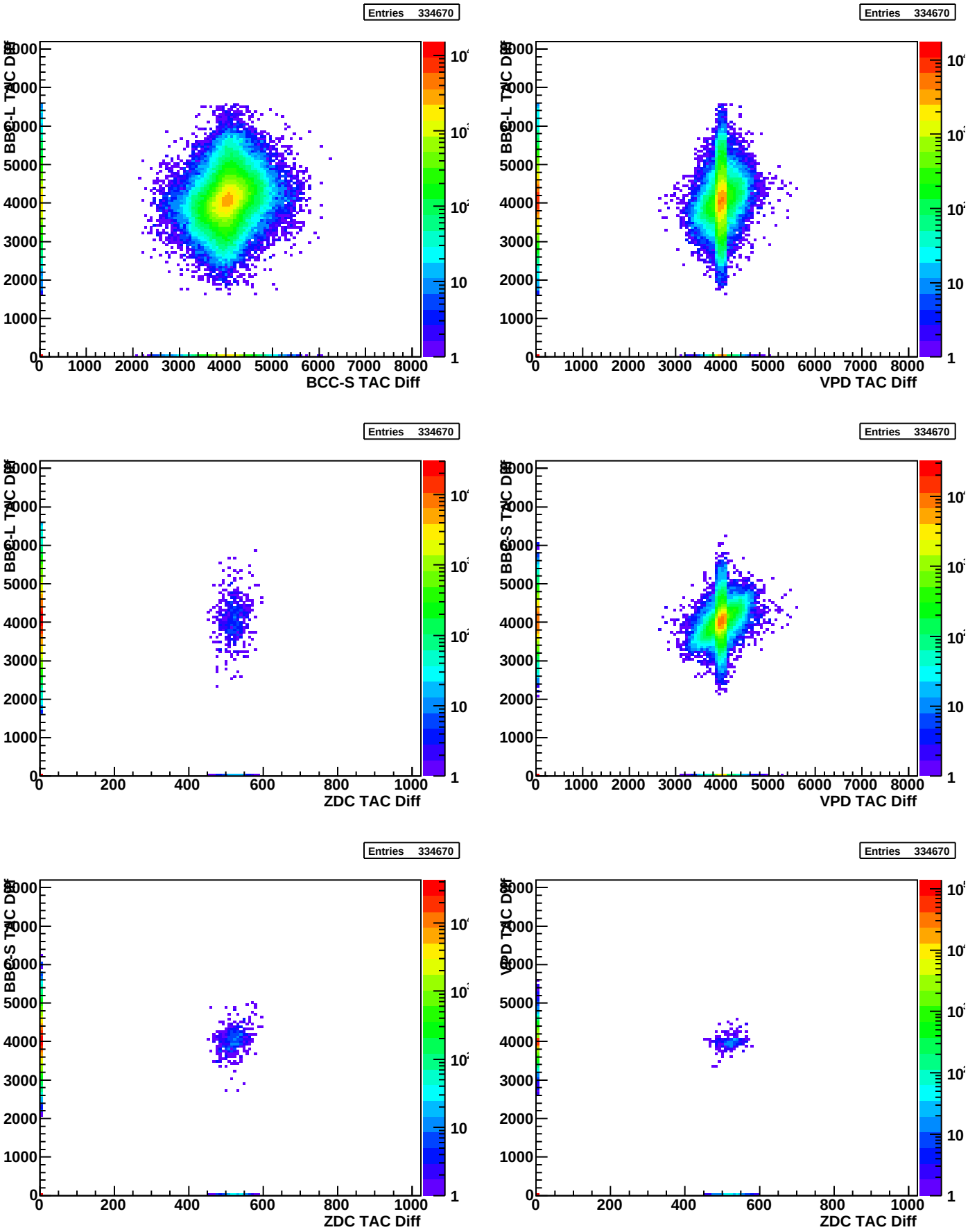

Entries 334670
VPD-West ADC Sum
ZDC-West ADC Sum Att

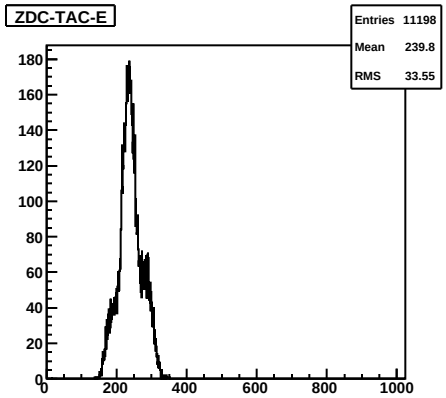

Entries 334670
BBC-L-East ADC Sum
BBC-L-West ADC Sum

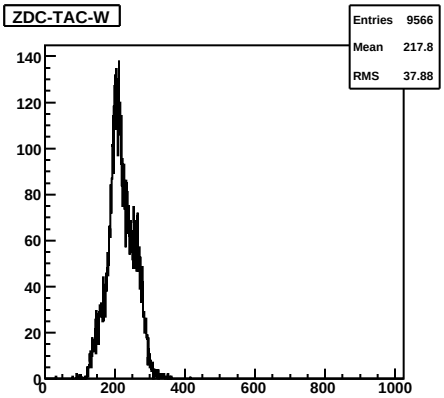

Entries 334670
BBC-S-East ADC Sum
BBC-S-West ADC Sum

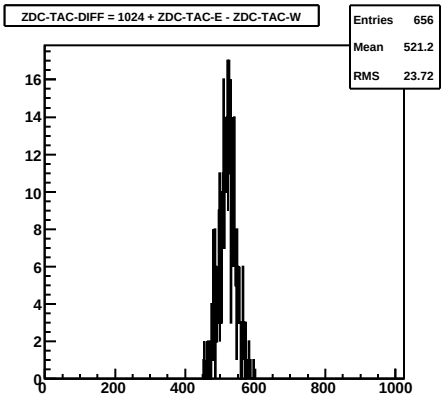

Entries 334670
VPD East ADC Sum
VPD West ADC Sum

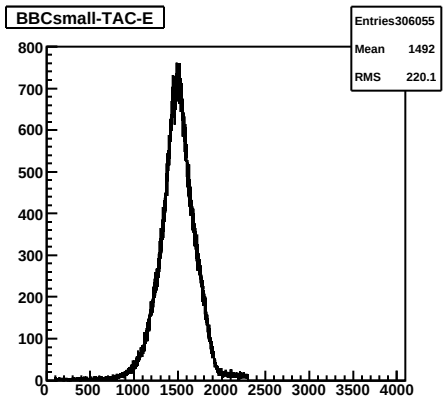

Entries 334670
ZDC East ADC Sum Att
ZDC West ADC Sum Att

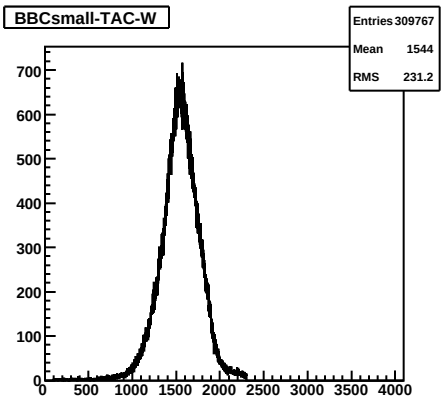

Entries 334670
BBC-L TAC Diff
BCC-S TAC Diff
Entries 334670
BBC-L TAC Diff
VPD TAC Diff
Entries 334670
BBC-L TAC Diff
ZDC TAC Diff
Entries 334670
BBC-S TAC Diff
VPD TAC Diff
Entries 334670
BBC-S TAC Diff
ZDC TAC Diff
Entries 334670
VPD TAC Diff
ZDC TAC Diff

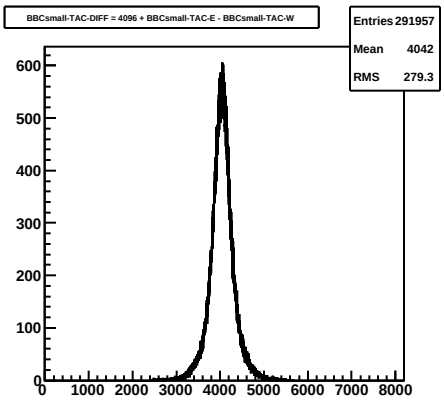

ZDC-TAC-E
Entries 11198
Mean 239.8
RMS 33.55

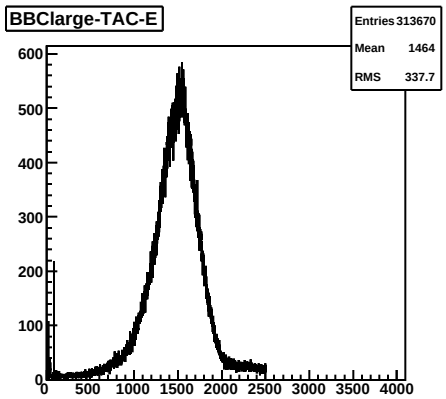

ZDC-TAC-W
Entries 9566
Mean 217.8
RMS 37.88

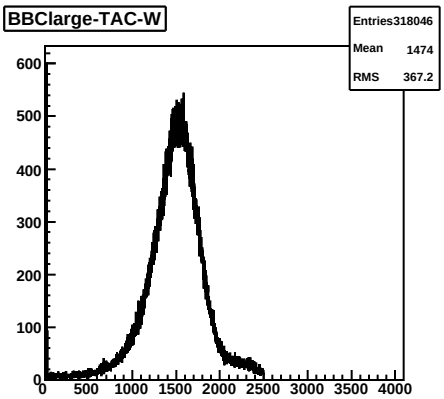

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 656
Mean 521.2
RMS 23.72

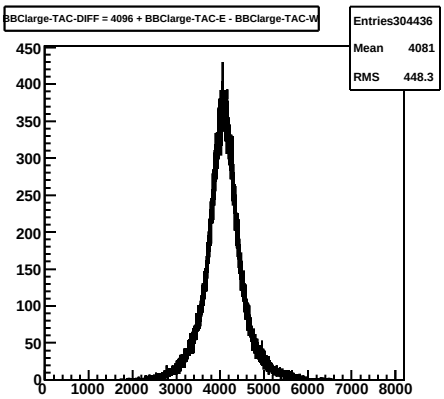

BBCsmall-TAC-E
Entries 306055
Mean 1492
RMS 220.1

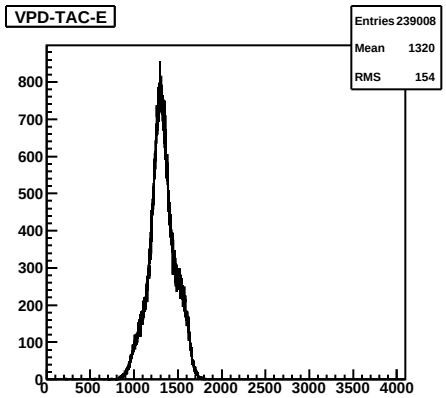

BBCsmall-TAC-W
Entries 309767
Mean 1544
RMS 231.2

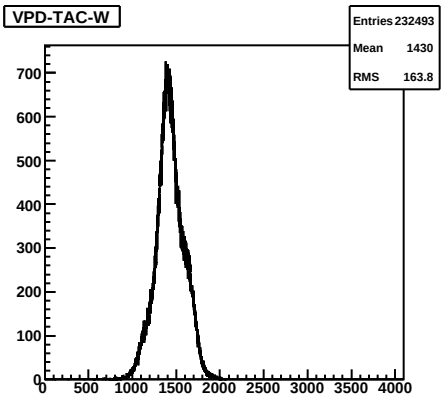

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 291957
Mean 4042
RMS 279.3

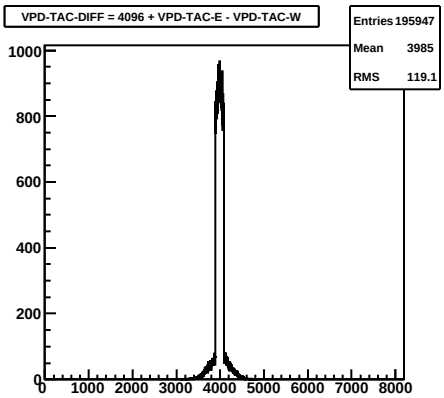

BBClarge-TAC-E
Entries 313670
Mean 1464
RMS 337.7

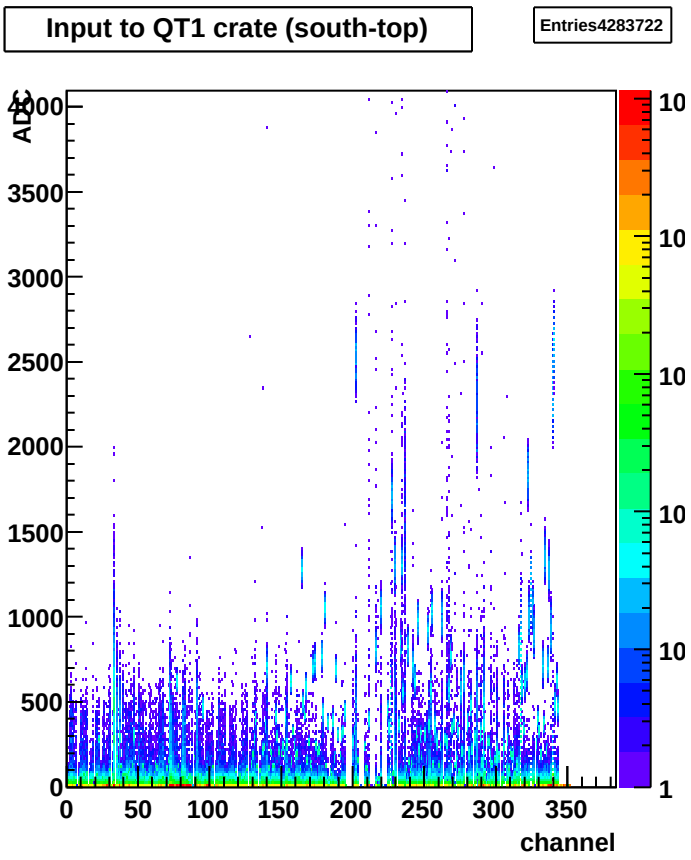

BBClarge-TAC-W
Entries 318046
Mean 1474
RMS 367.2

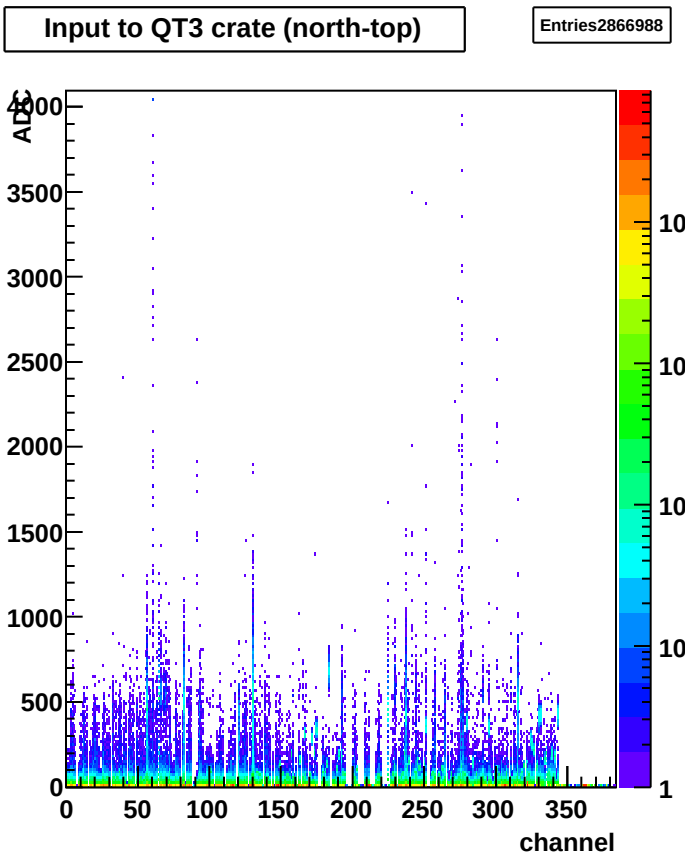

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 304436
Mean 4081
RMS 448.3

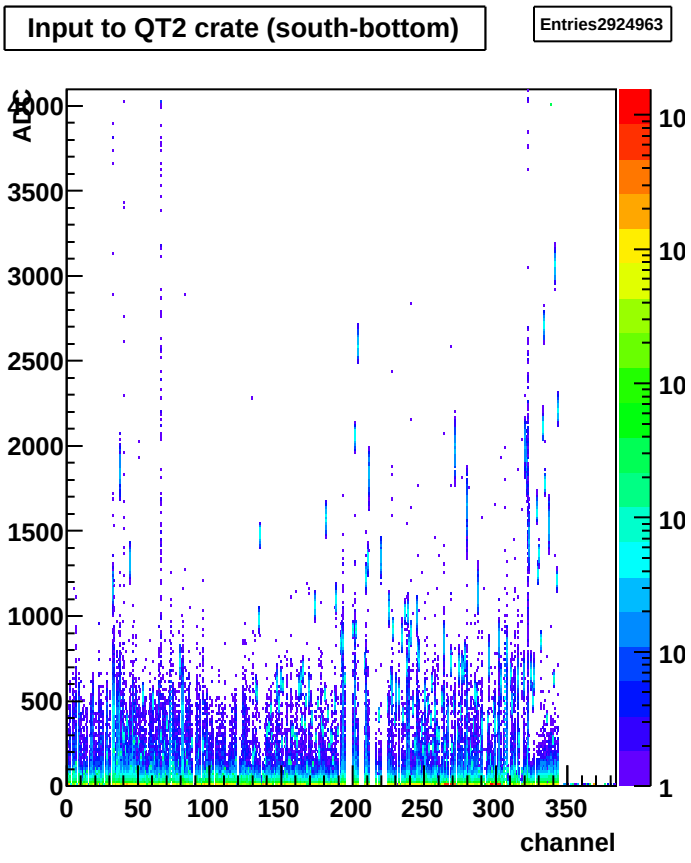

VPD-TAC-E
Entries 239008
Mean 1320
RMS 154

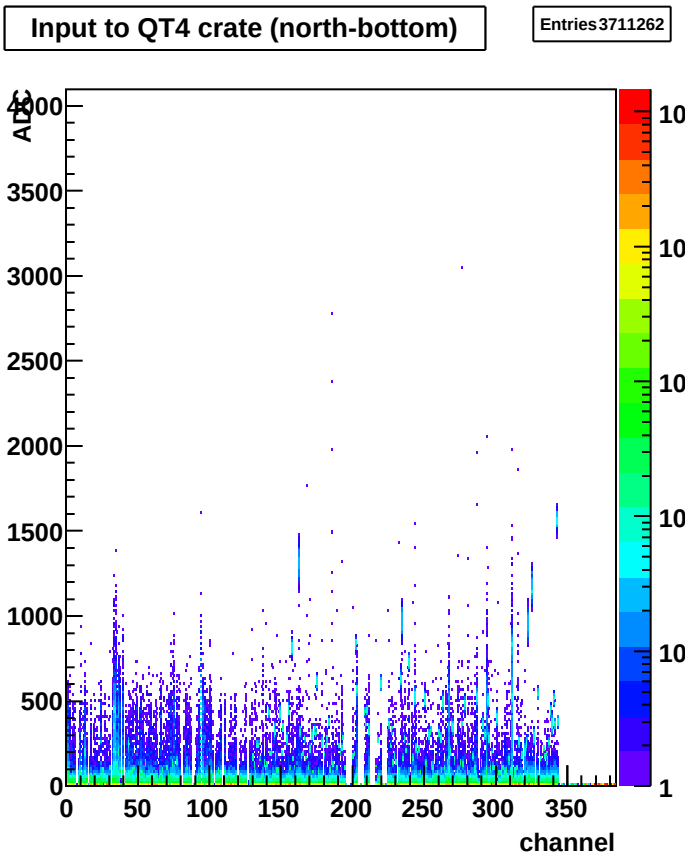

VPD-TAC-W
Entries 232493
Mean 1430
RMS 163.8

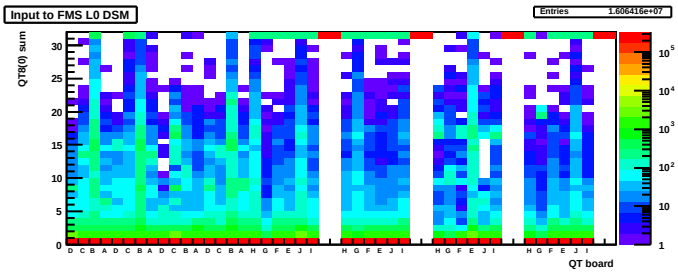

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 195947
Mean 3985
RMS 119.1

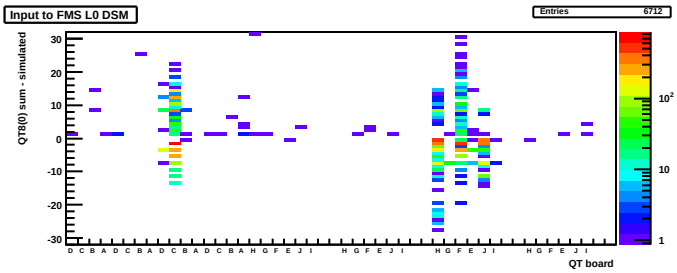

Input to QT1 crate (south-top)
Entries4283722
ADC
channel

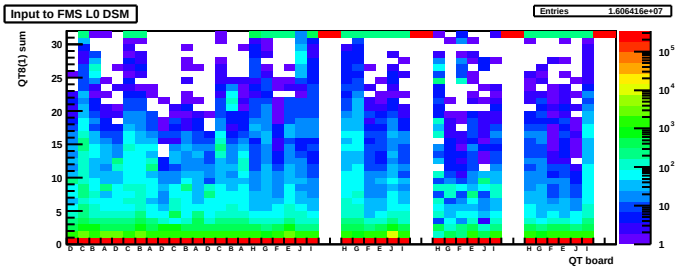

Input to QT3 crate (north-top)
Entries2866988
ADC
channel

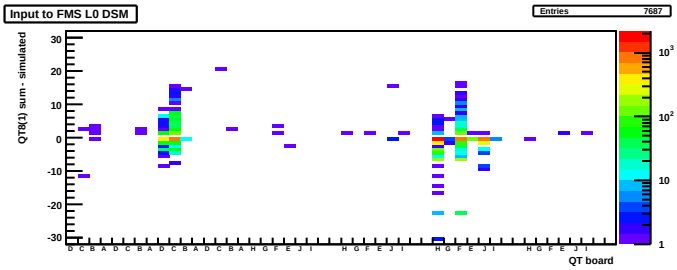

Input to QT2 crate (south-bottom)
Entries2924963
ADC
channel

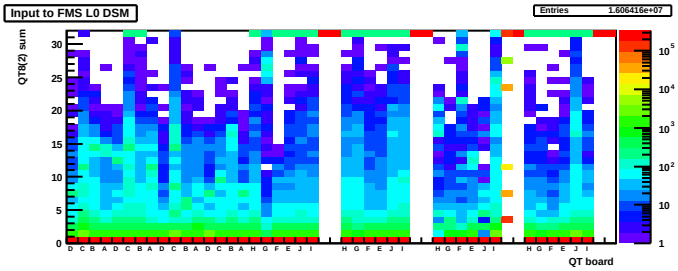

Input to QT4 crate (north-bottom)
Entries3711262
ADC
channel

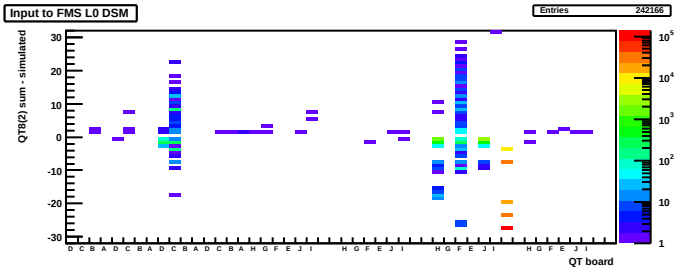

Input to FMS L0 DSM
Entries 1.606416e+07
QT8(0) sum
QT board

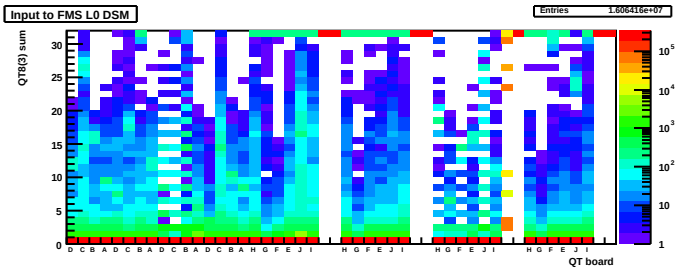

Input to FMS L0 DSM
Entries 6712
QT8(0) sum - simulated
QT board

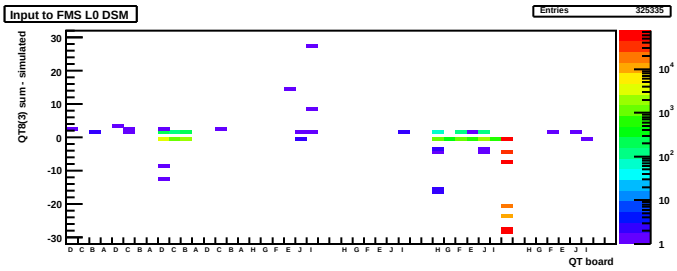

Input to FMS L0 DSM
Entries 1.606416e+07
QT8(1) sum
QT board

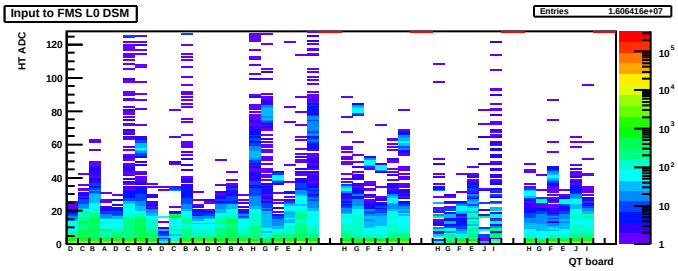

Input to FMS L0 DSM
Entries 7687
QT8(1) sum - simulated
QT board

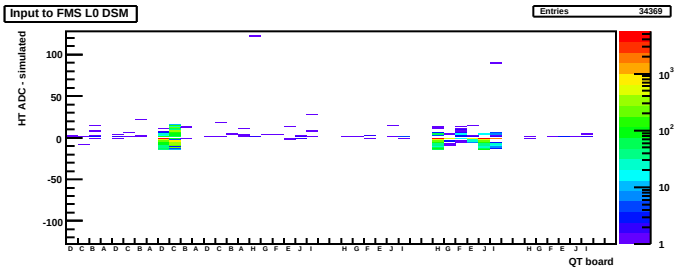

Input to FMS L0 DSM
Entries 1.606416e+07
QT8(2) sum
QT board

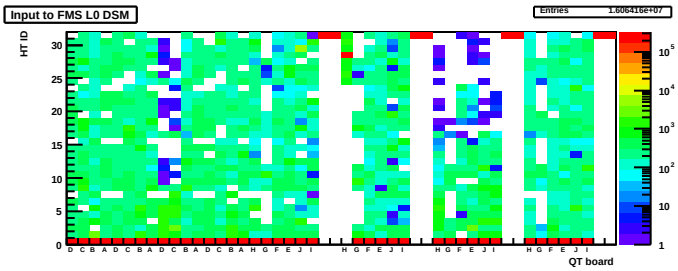

Input to FMS L0 DSM
Entries 242166
QT8(2) sum - simulated
QT board

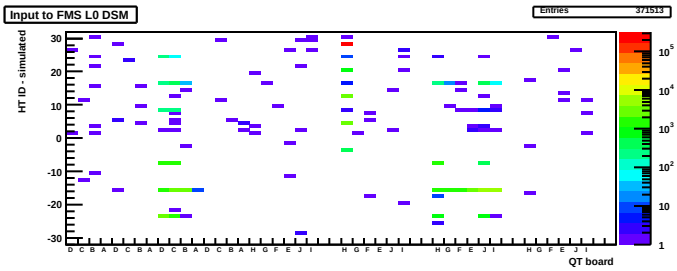

Input to FMS L0 DSM
Entries 1.606416e+07
QT8(3) sum
QT board

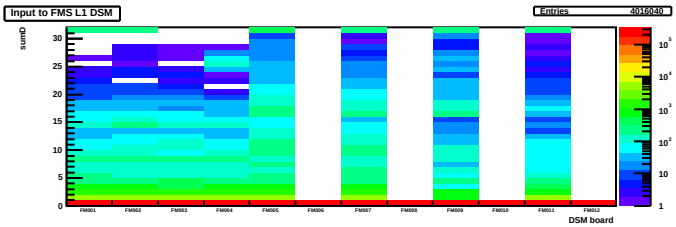

Input to FMS L0 DSM
Entries 325335
QT8(3) sum - simulated
QT board

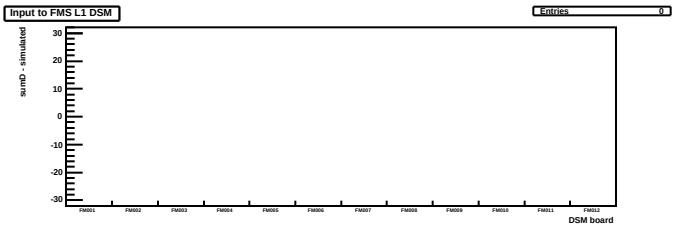

Input to FMS L0 DSM
Entries 1.606416e+07
HT ADC
QT board

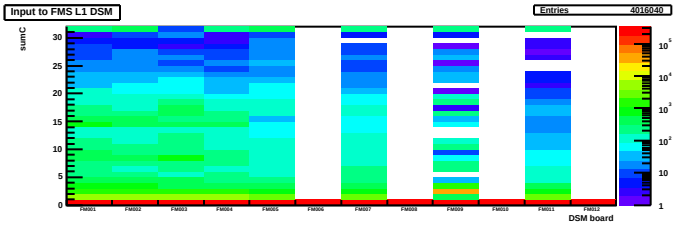

Input to FMS L0 DSM
Entries 34369
HT ADC - simulated
QT board

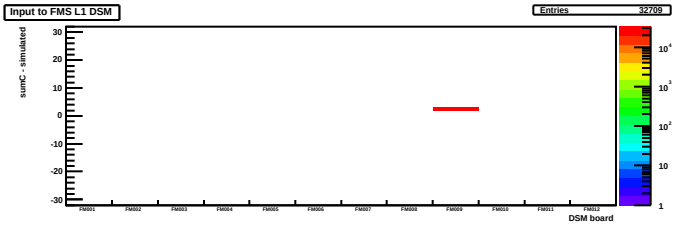

Input to FMS L0 DSM
Entries 1.606416e+07
HT ID
QT board

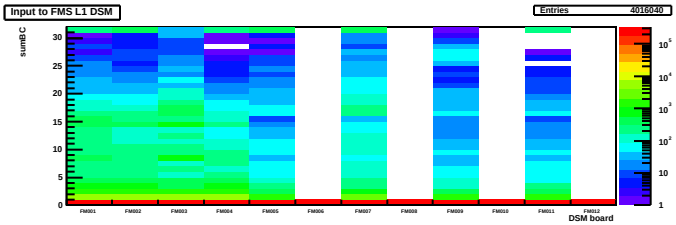

Input to FMS L0 DSM
Entries 371513
HT ID - simulated
QT board

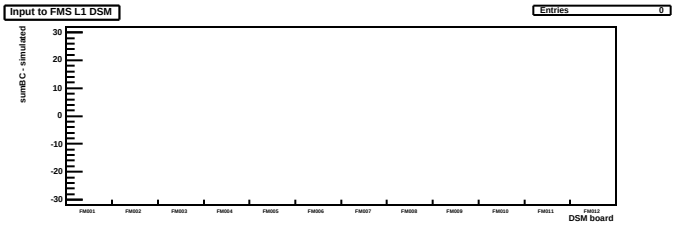

Input to FMS L1 DSM
Entries 4016040
sumD
DSM board

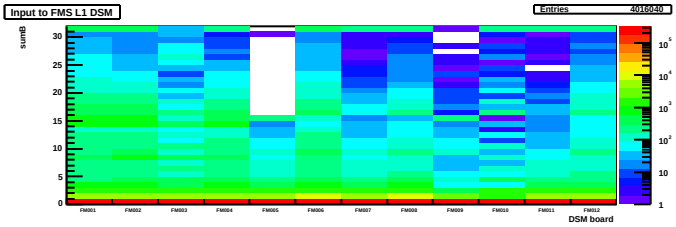

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

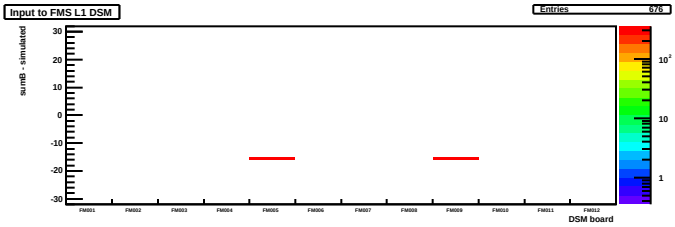

Input to FMS L1 DSM
Entries 4016040
sumC
DSM board

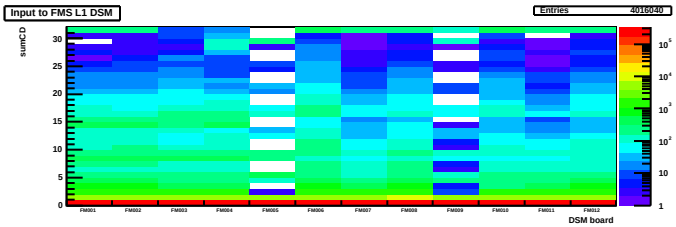

Input to FMS L1 DSM
Entries 32709
sumC - simulated
DSM board

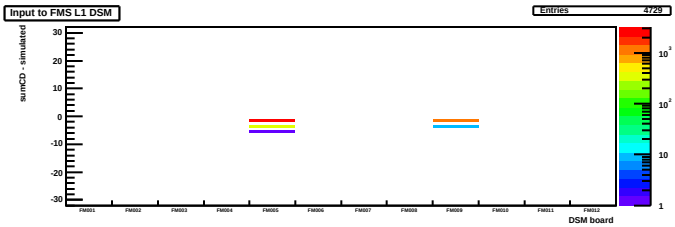

Input to FMS L1 DSM
Entries 4016040
sumBC
DSM board

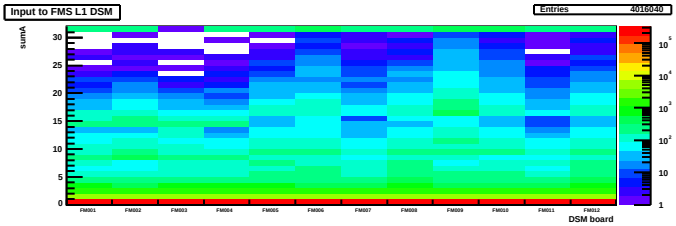

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

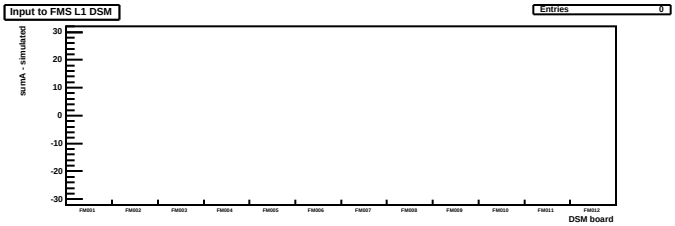

Input to FMS L1 DSM
Entries 4016040
sumB
DSM board

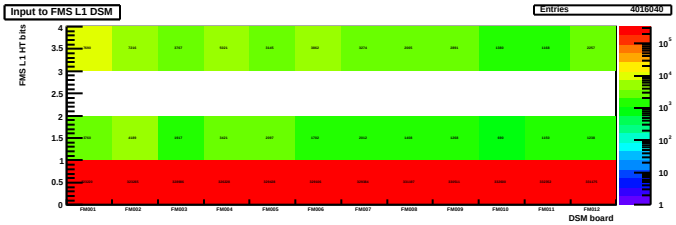

Input to FMS L1 DSM
Entries 676
sumB - simulated
DSM board

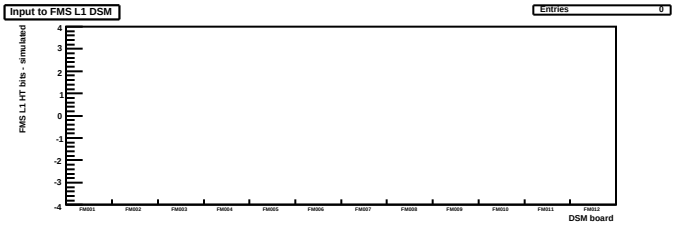

Input to FMS L1 DSM
Entries 4016040
sumCD
DSM board

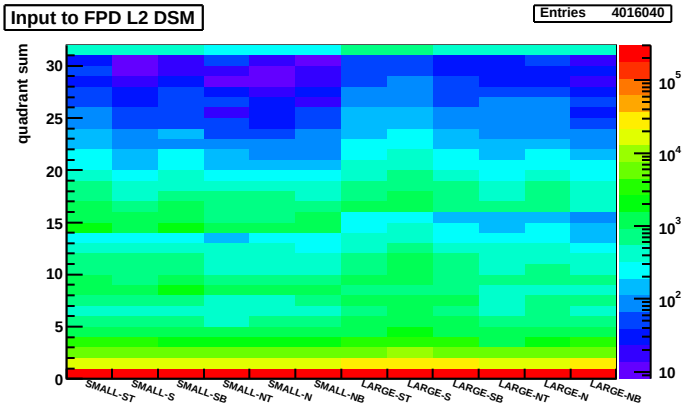

Input to FMS L1 DSM
Entries 4729
sumCD - simulated
DSM board

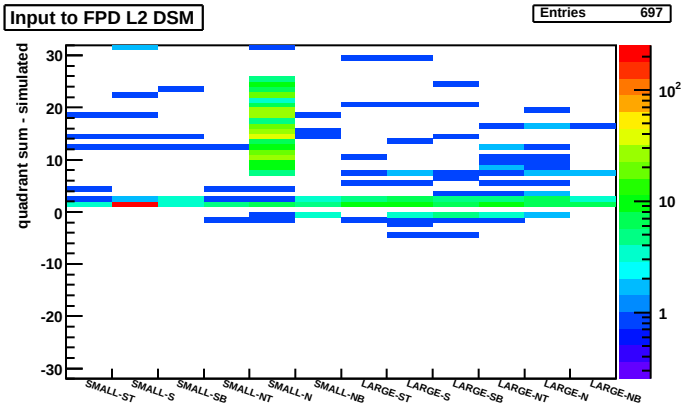

Input to FMS L1 DSM
Entries 4016040
sumA
DSM board

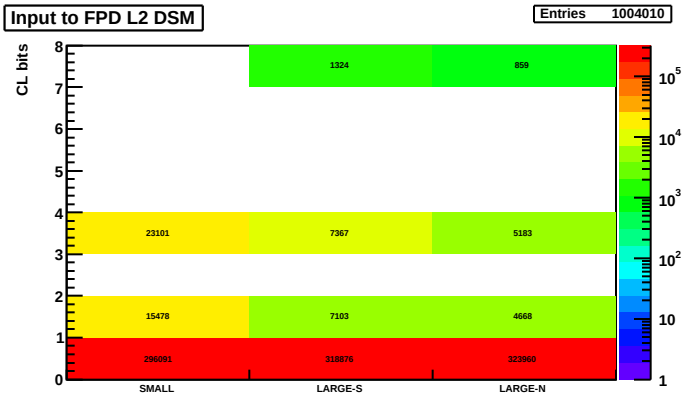

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

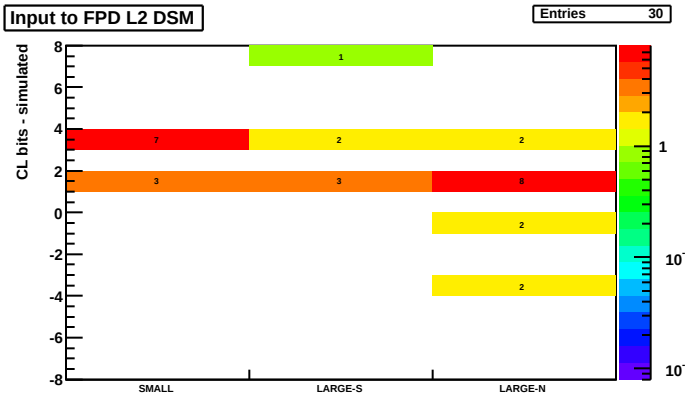

Input to FMS L1 DSM
Entries 4016040
FMS L1 HT bits
DSM board

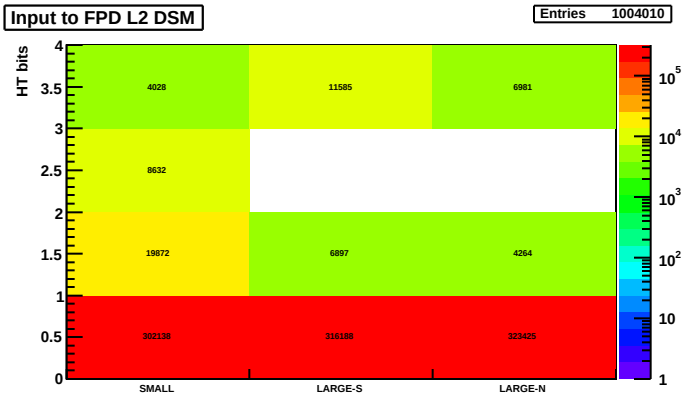

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

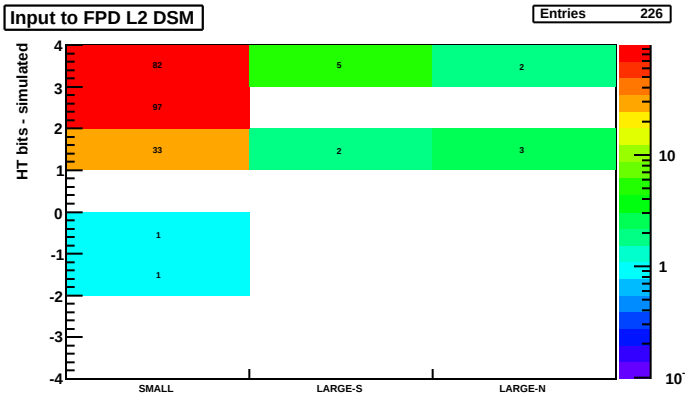

Input to FPD L2 DSM
Entries 4016040
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

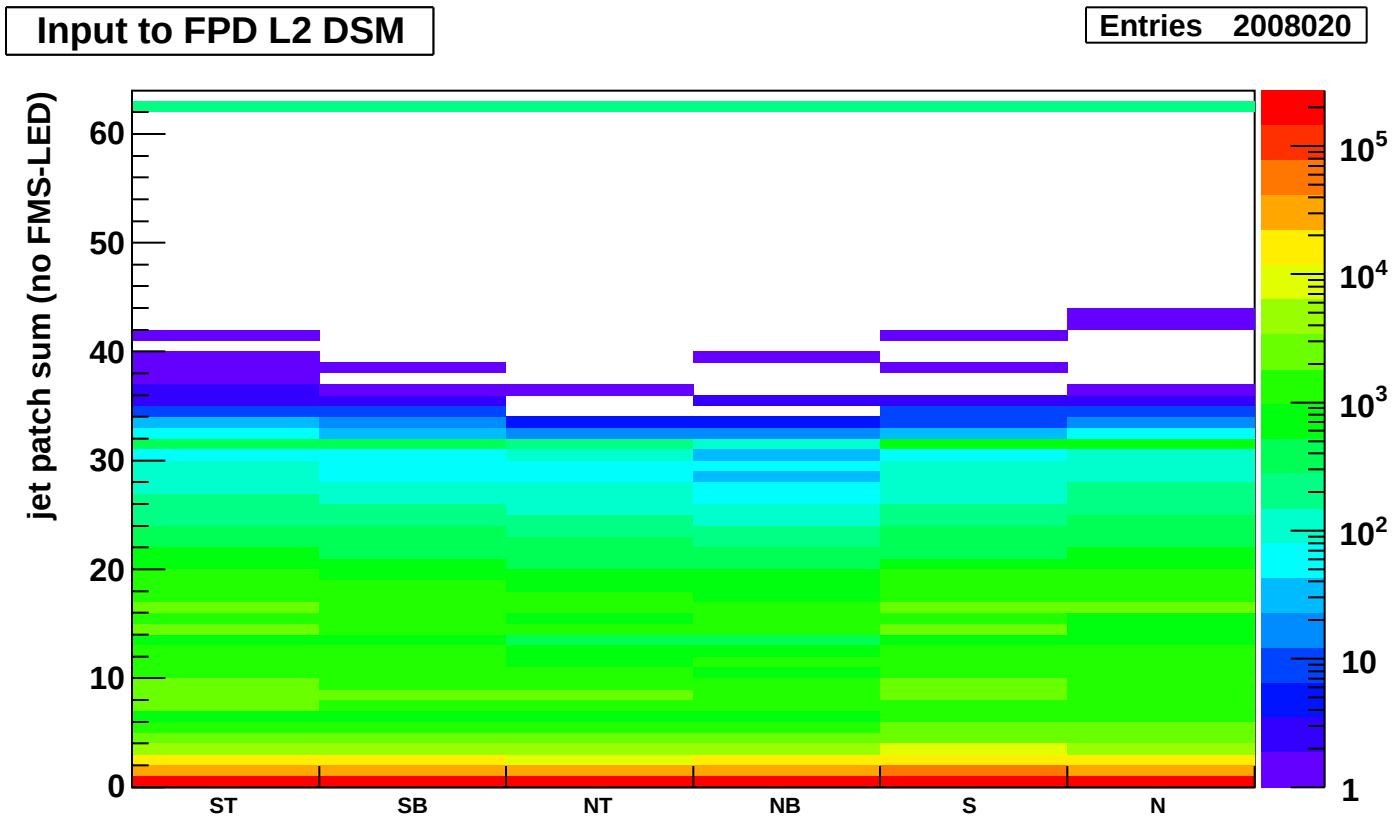

Input to FPD L2 DSM
Entries 697
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

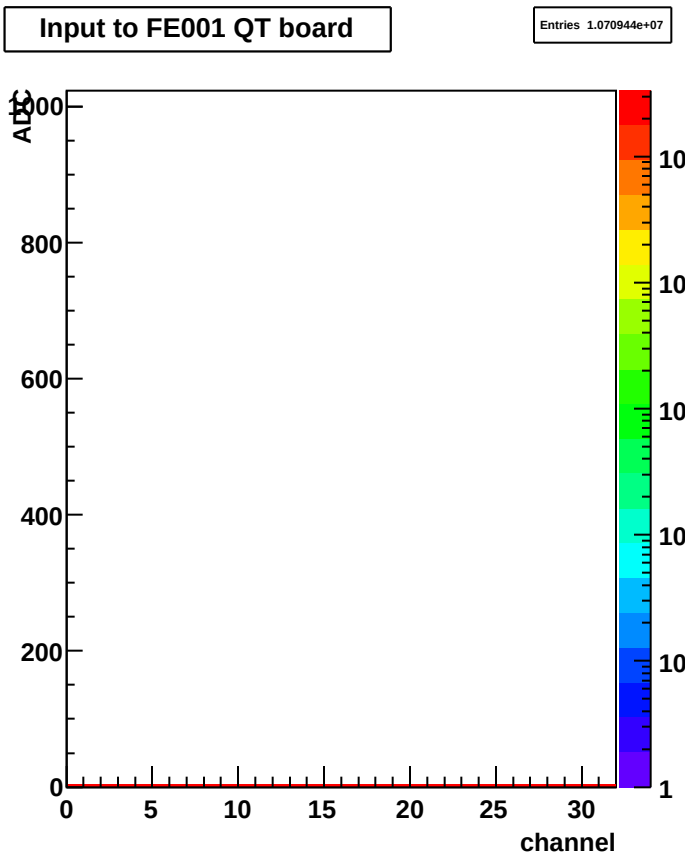

Input to FPD L2 DSM
Entries 1004010
CL bits
1324
859
23101
7367
5183
15478
7103
4668
296091
318876
323960
SMALL
LARGE-S
LARGE-N

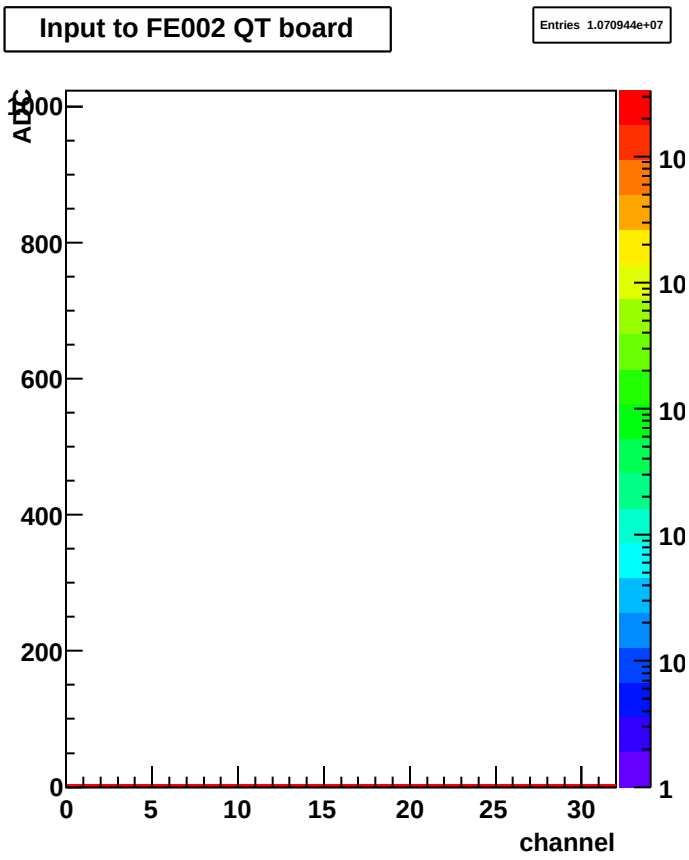

Input to FPD L2 DSM
Entries 30
CL bits - simulated
1
7
2
2
3
3
8
2
2
SMALL
LARGE-S
LARGE-N

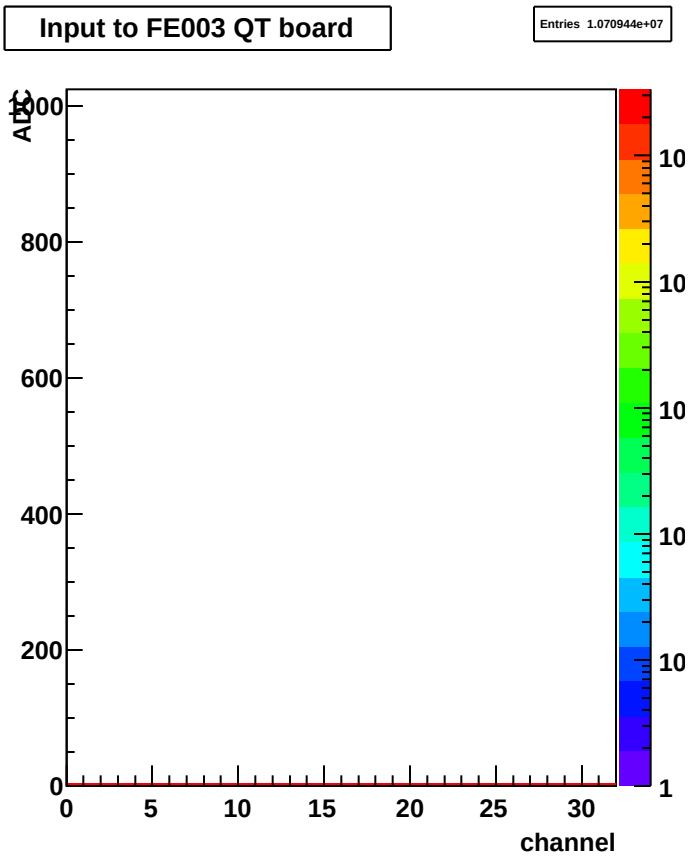

Input to FPD L2 DSM
Entries 1004010
HT bits
4028
11585
6981
8632
19872
6897
4264
302138
316188
323425
SMALL
LARGE-S
LARGE-N

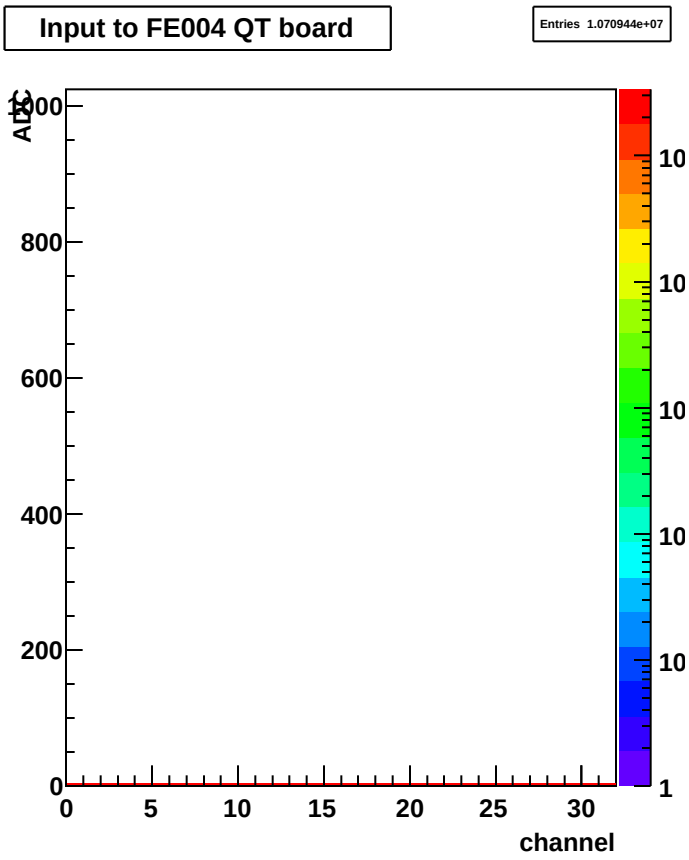

Input to FPD L2 DSM
Entries 226
HT bits - simulated
82
5
2
97
33
2
3
1
1
SMALL
LARGE-S
LARGE-N

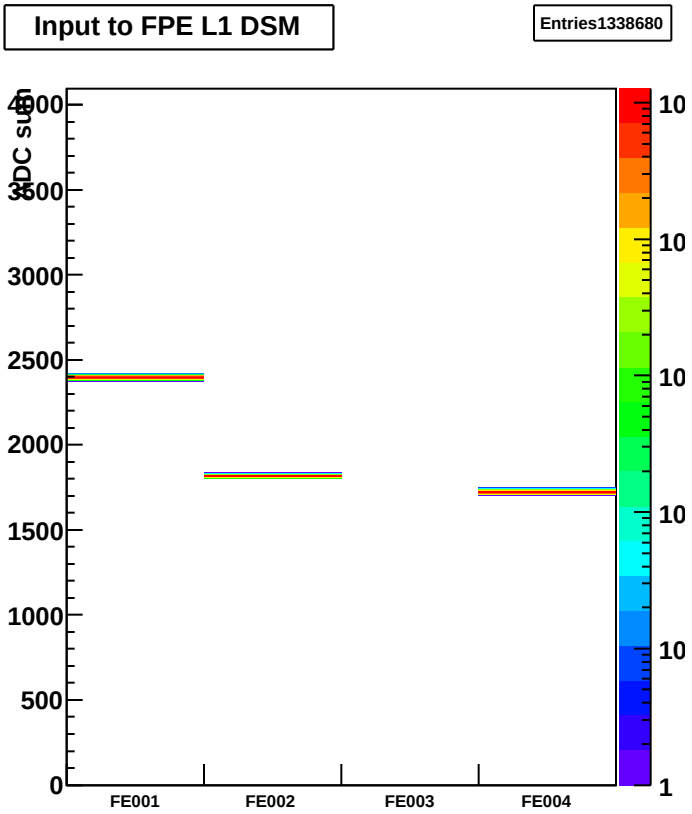

Input to FPD L2 DSM
Entries 2008020
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.070944e+07
ADC
channel

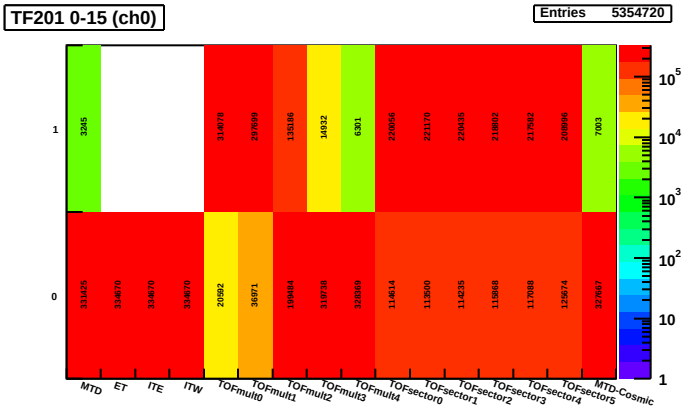

Input to FE002 QT board
Entries 1.070944e+07
ADC
channel


Input to FE003 QT board
Entries 1.070944e+07
ADC
channel

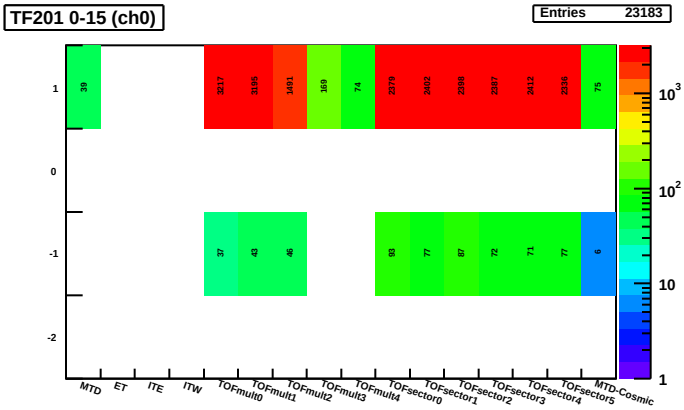

Input to FE004 QT board
Entries 1.070944e+07
ADC
channel


Input to FPE L1 DSM
Entries1338680
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

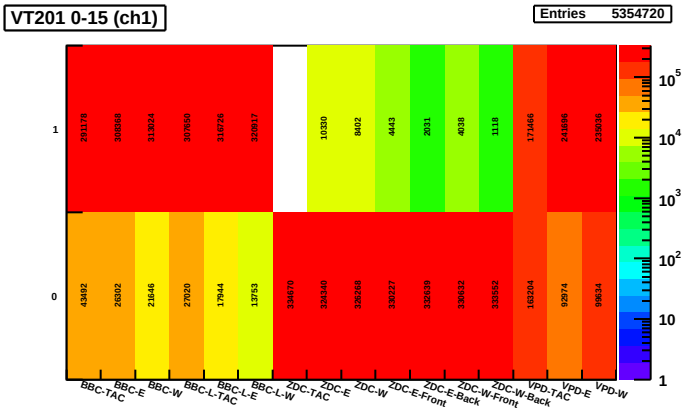
TF201 0-15 (ch0)



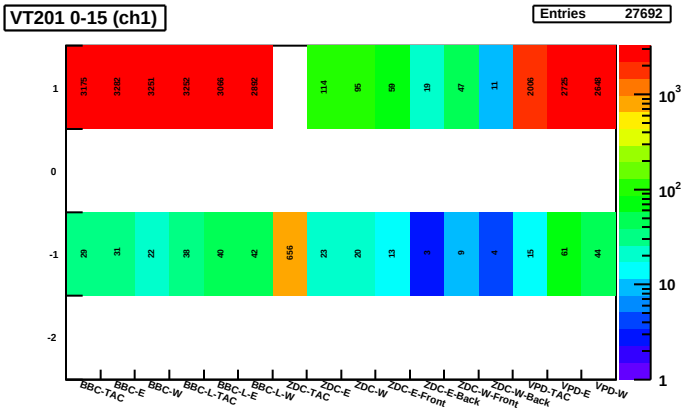
TF201 0-15 (ch0)



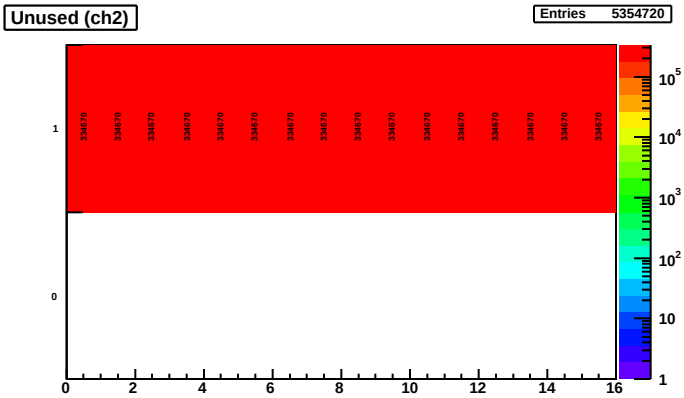
VT201 0-15 (ch1)

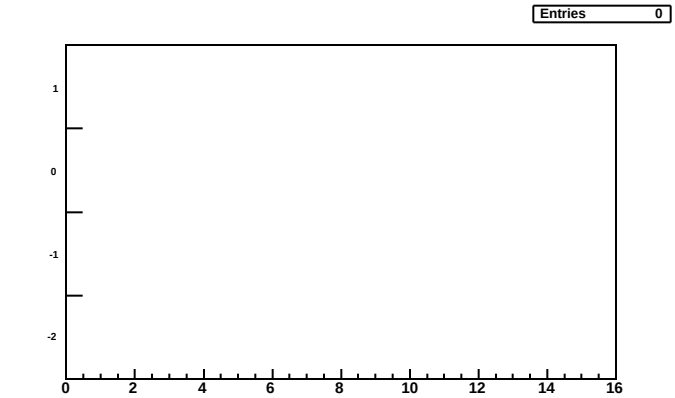


VT201 0-15 (ch1)

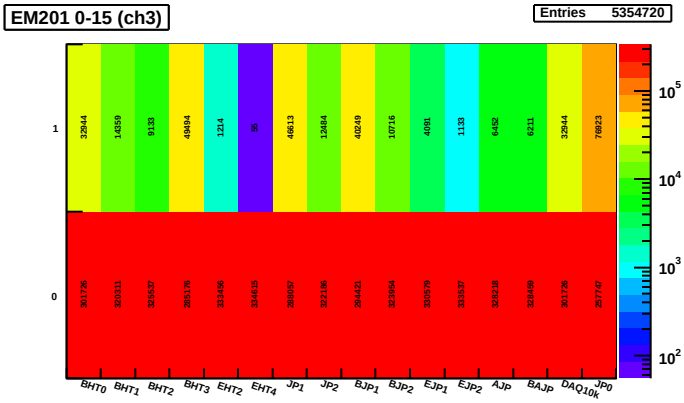


Unused (ch2)





EM201 0-15 (ch3)



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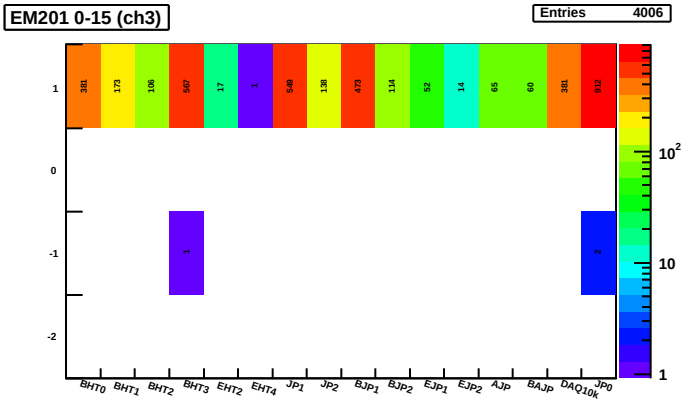
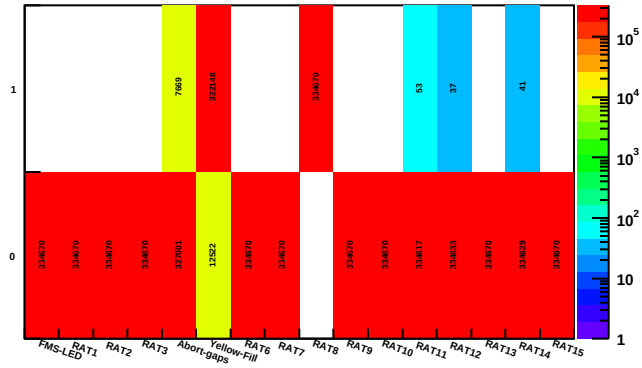
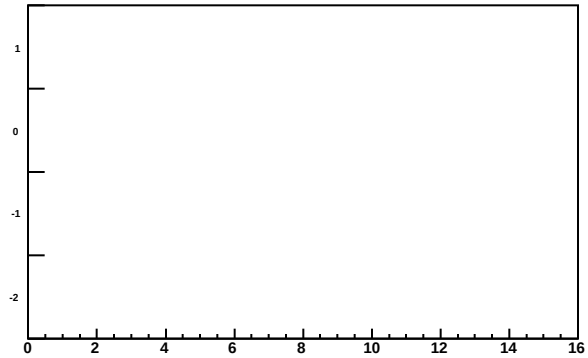


RAT board (ch4)





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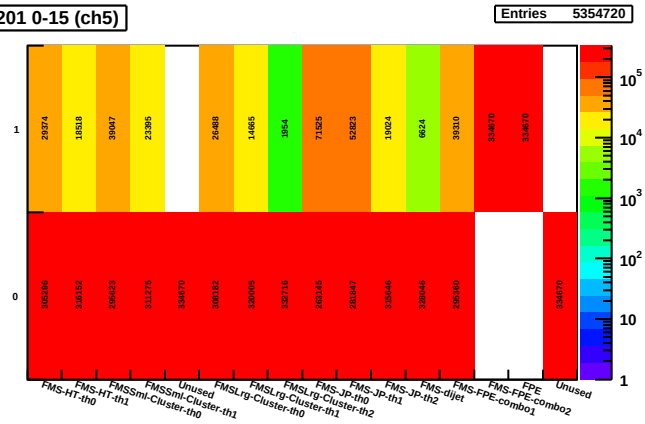

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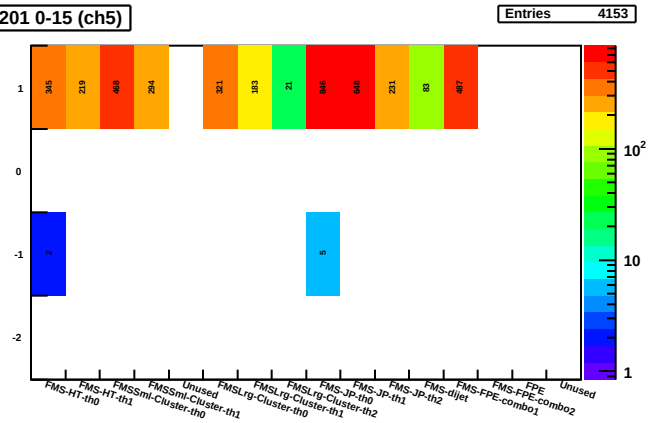

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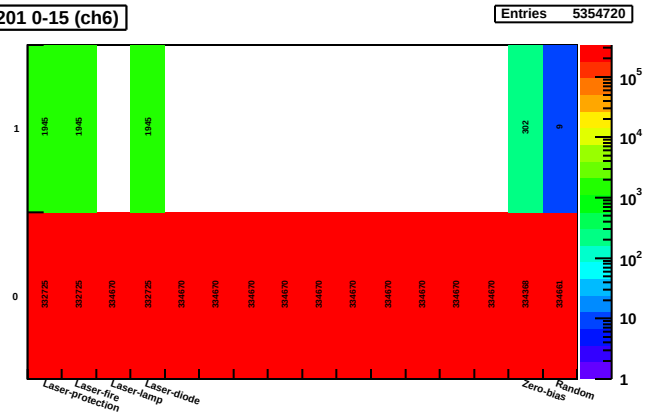

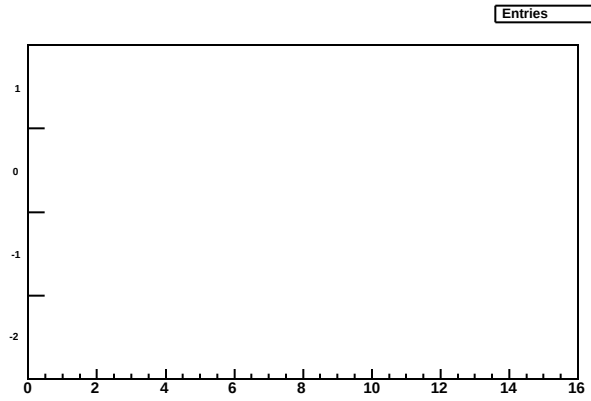
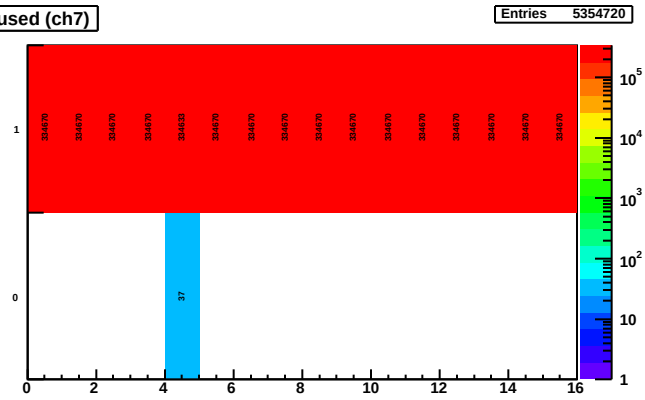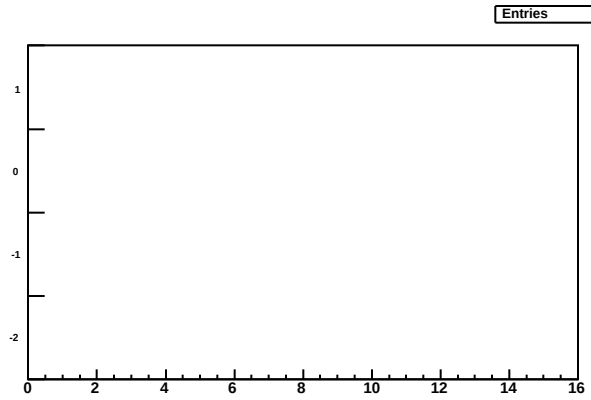
Unused (ch7)