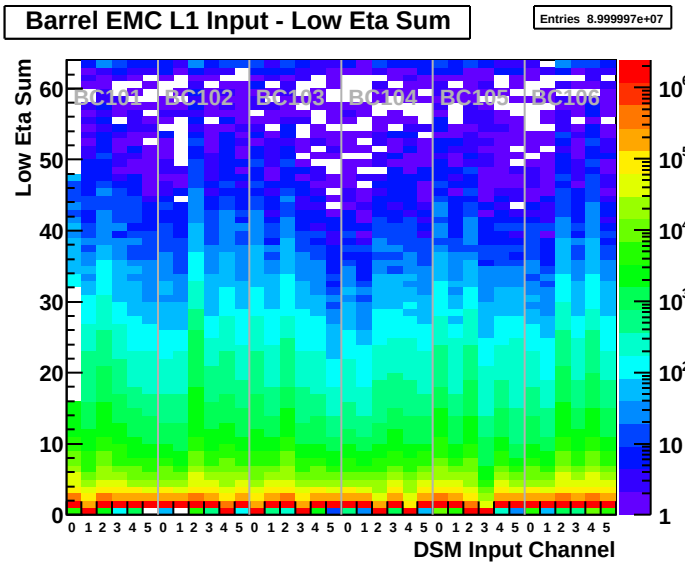
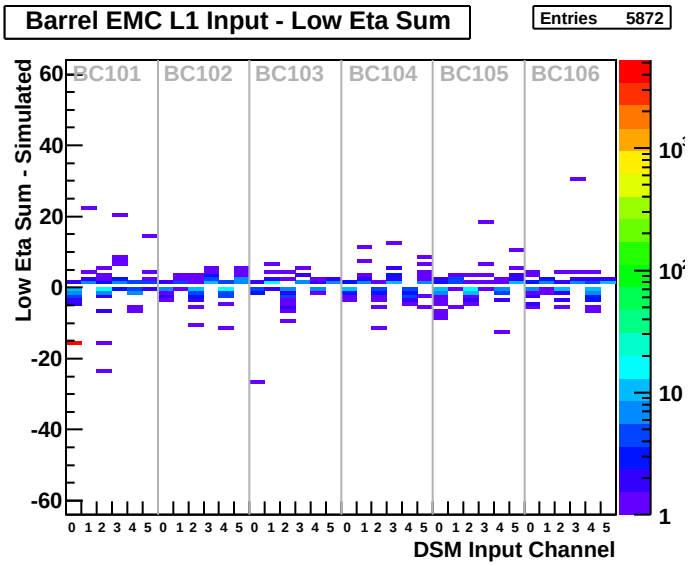
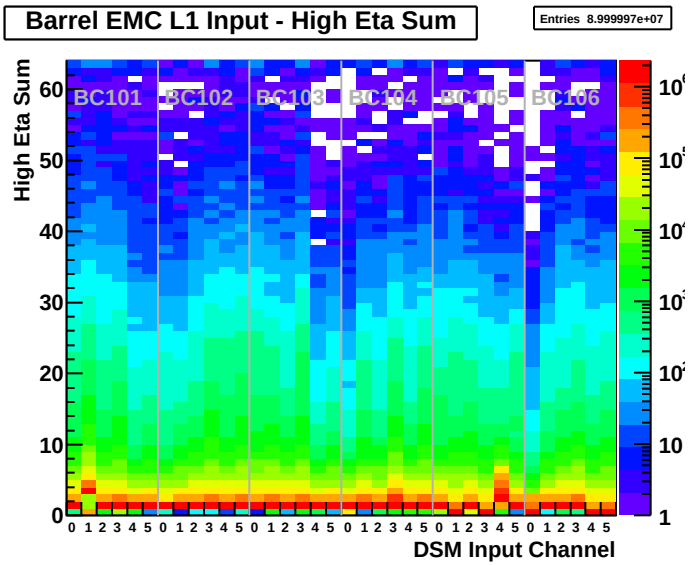
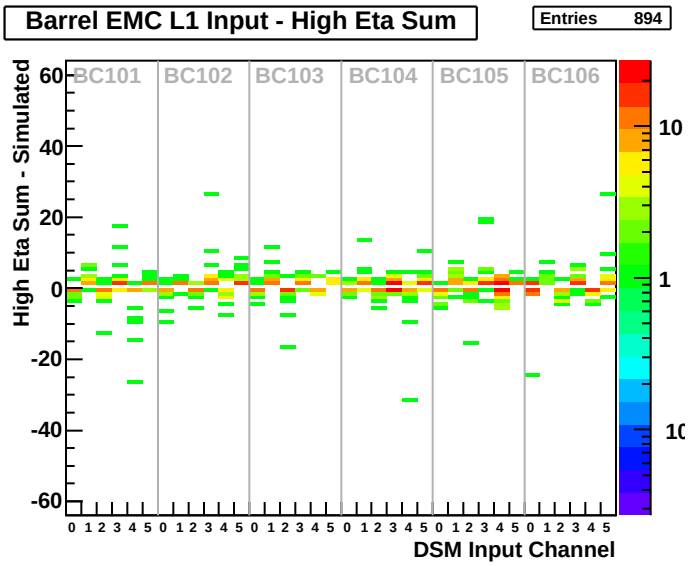
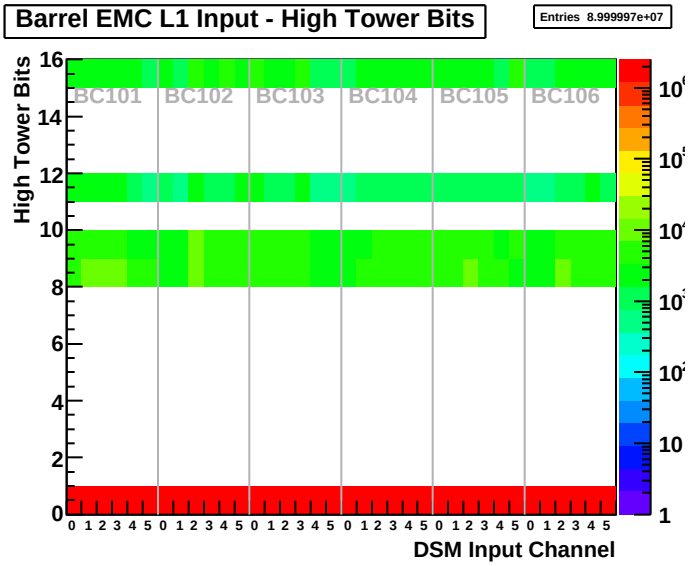
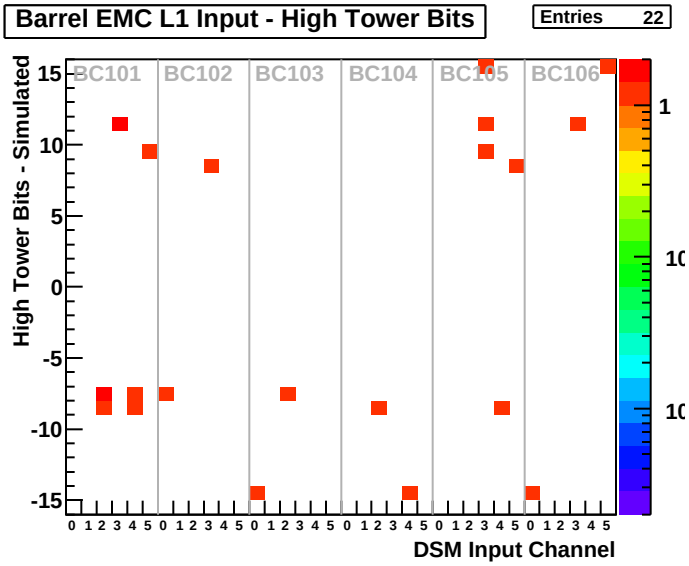

Barrel EMC L1 Input - Low Eta Sum
Entries 8.999997e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

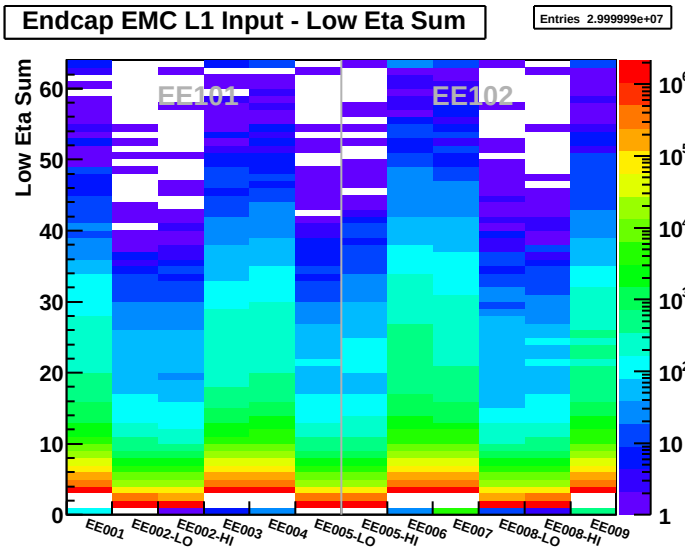

Barrel EMC L1 Input - Low Eta Sum
Entries 5872
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

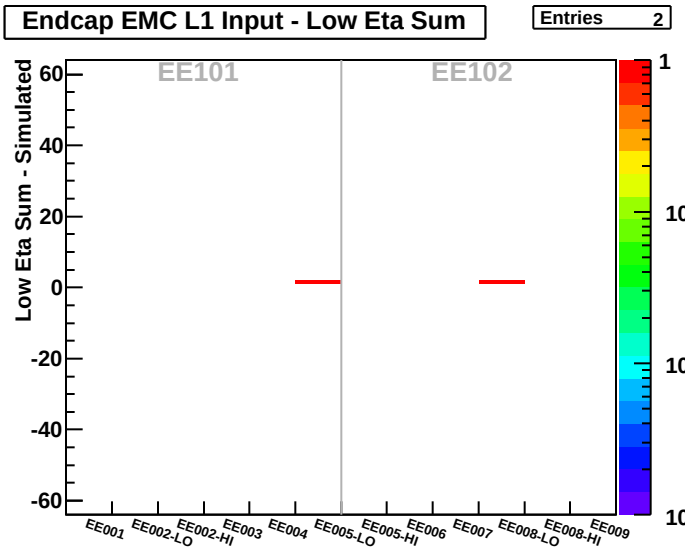

Barrel EMC L1 Input - High Eta Sum
Entries 8.999997e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

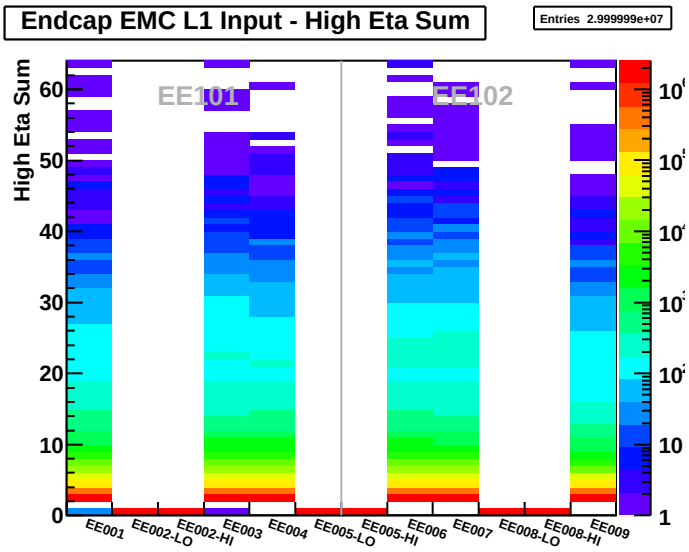

Barrel EMC L1 Input - High Eta Sum
Entries 894
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

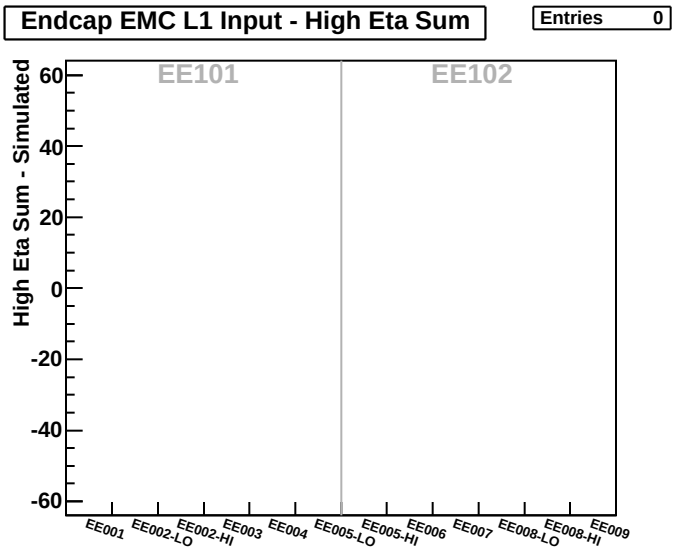

Barrel EMC L1 Input - High Tower Bits
Entries 8.999997e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

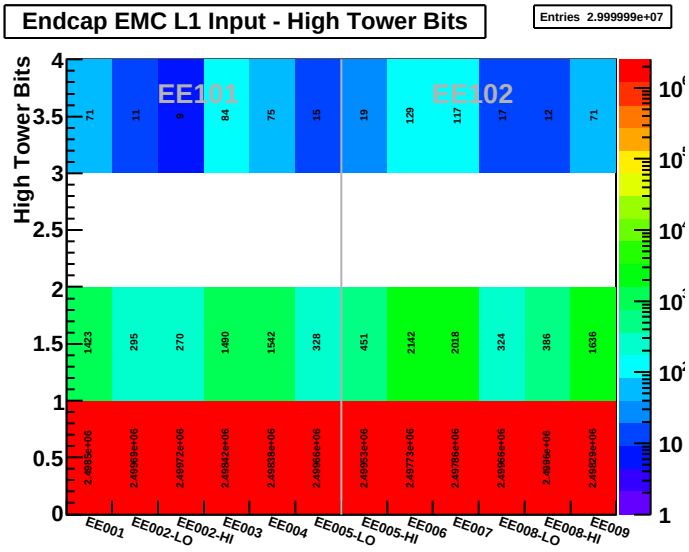

Barrel EMC L1 Input - High Tower Bits
Entries 22
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

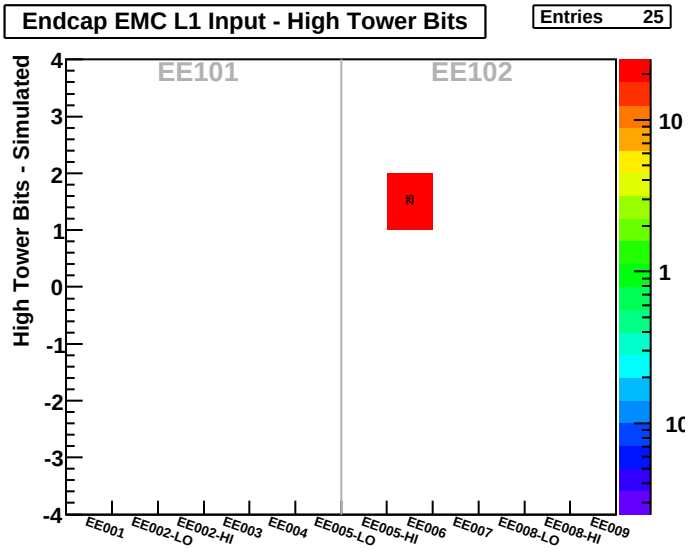

Endcap EMC L1 Input - Low Eta Sum
Entries 2.999999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

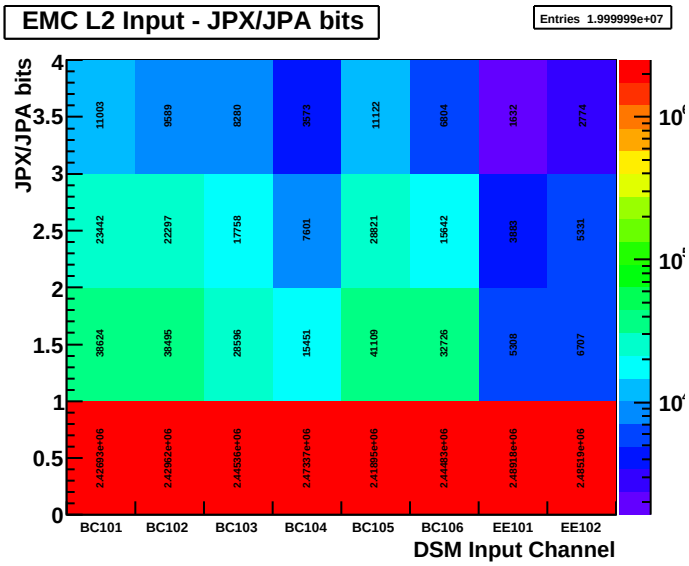

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

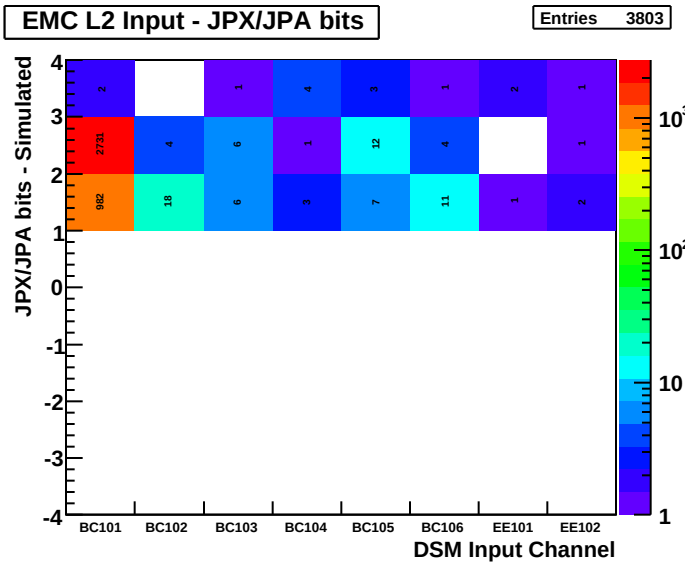

Endcap EMC L1 Input - High Eta Sum
Entries 2.999999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

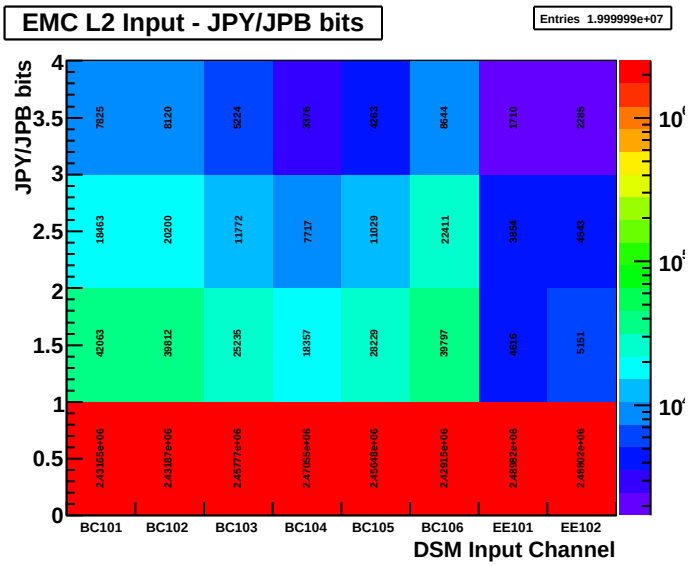

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

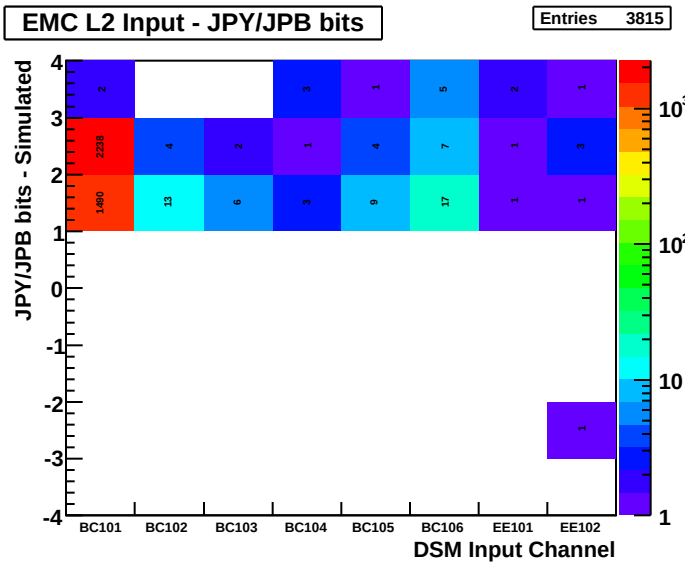

Endcap EMC L1 Input - High Tower Bits
Entries 2.999999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

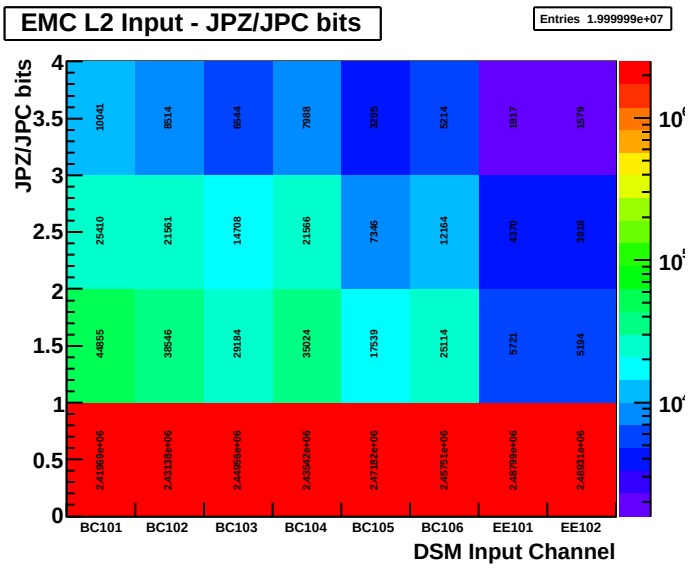

Endcap EMC L1 Input - High Tower Bits
Entries 25
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

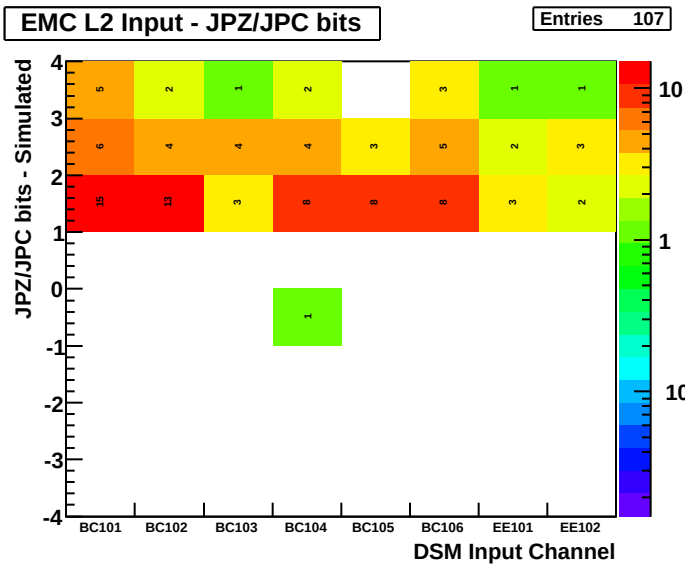

EMC L2 Input - JPX/JPA bits
Entries 1.999999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
11003 9589 8280 3573 11122 6804 1632 2774
23442 22297 17758 7601 28821 15642 3883 5331
38624 38495 28596 15451 41109 32726 5308 6707
2.42693e+06 2.42962e+06 2.44536e+06 2.47337e+06 2.41895e+06 2.44483e+06 2.48918e+06 2.48519e+06

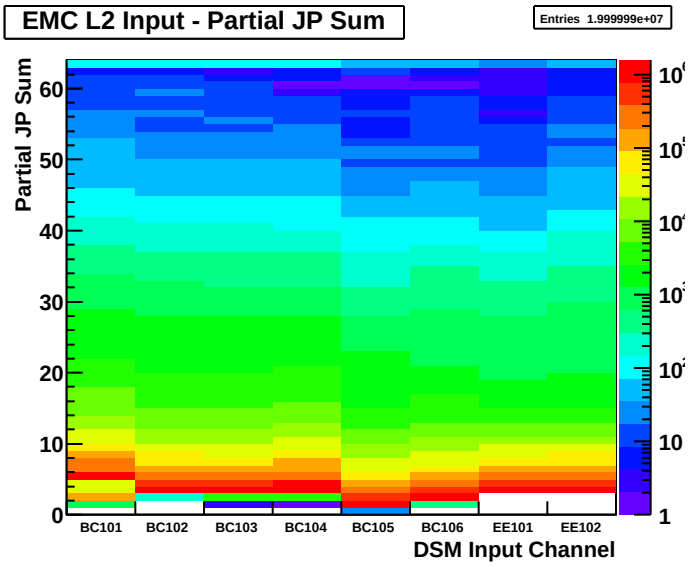

EMC L2 Input - JPX/JPA bits
Entries 3803
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 4 3 1 2 1
2731 4 6 1 12 4 1
982 18 6 3 7 11 1 2

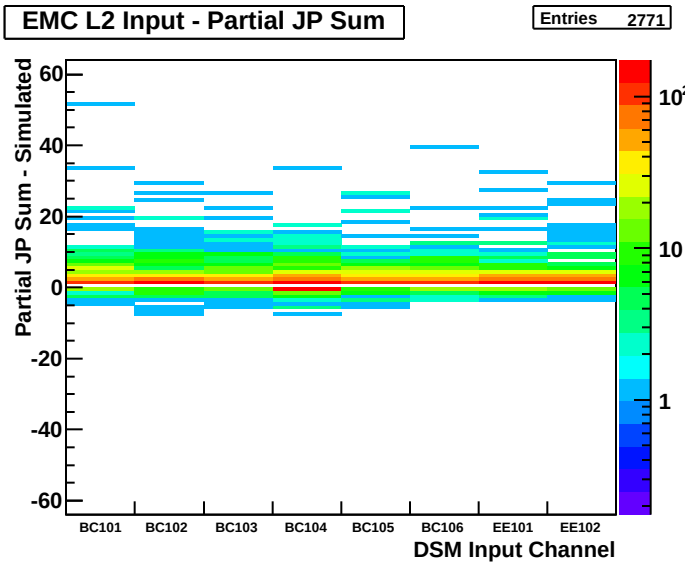

EMC L2 Input - JPY/JPB bits
Entries 1.999999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7825 8120 5224 3376 4263 8644 1710 2285
19463 20200 11772 7717 11029 22411 3854 4543
42063 39812 25235 18357 28229 39797 4616 5151
2.43165e+06 2.43137e+06 2.45777e+06 2.47055e+06 2.45649e+06 2.42015e+06 2.48992e+06 2.48802e+06

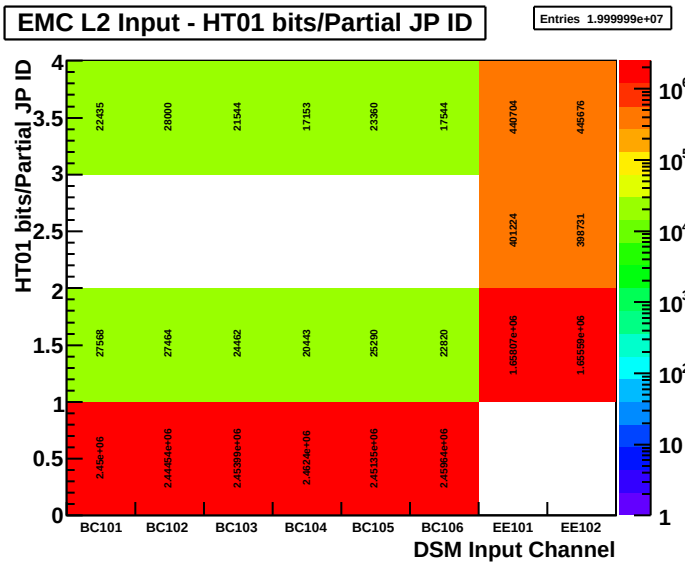

EMC L2 Input - JPY/JPB bits
Entries 3815
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 3 1 5 2 1
2238 4 2 1 4 7 1 3
1490 13 6 3 9 17 1 1
1

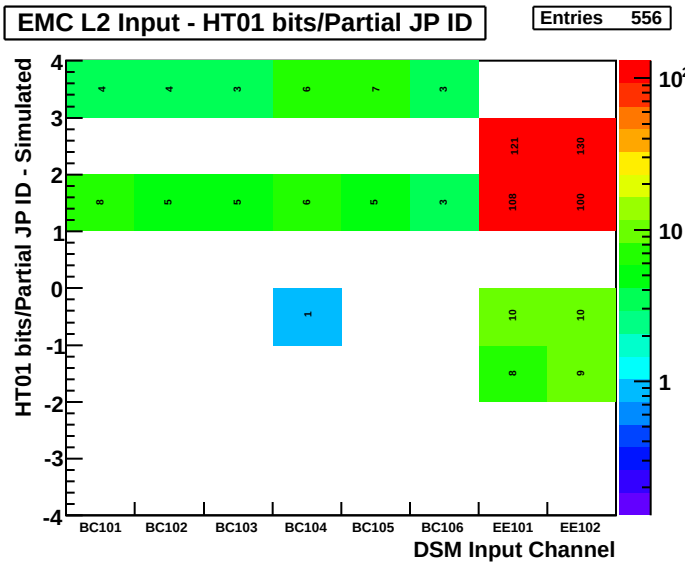

EMC L2 Input - JPZ/JPC bits
Entries 1.999999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10041 8514 6544 7988 3295 5214 1917 1579
25410 21561 14708 21566 7346 12164 4370 3918
44855 38546 29184 35024 17539 25114 5721 5194
2.41969e+06 2.43138e+06 2.44956e+06 2.43542e+06 2.47182e+06 2.45751e+06 2.48799e+06 2.48931e+06

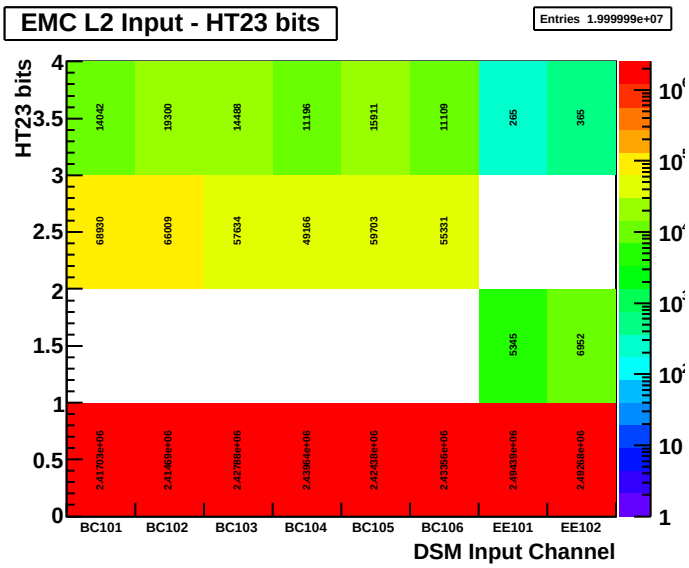

EMC L2 Input - JPZ/JPC bits
Entries 107
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5 2 1 2 3 1 1
6 4 4 4 3 5 2 3
15 13 3 8 8 8 3 2
1

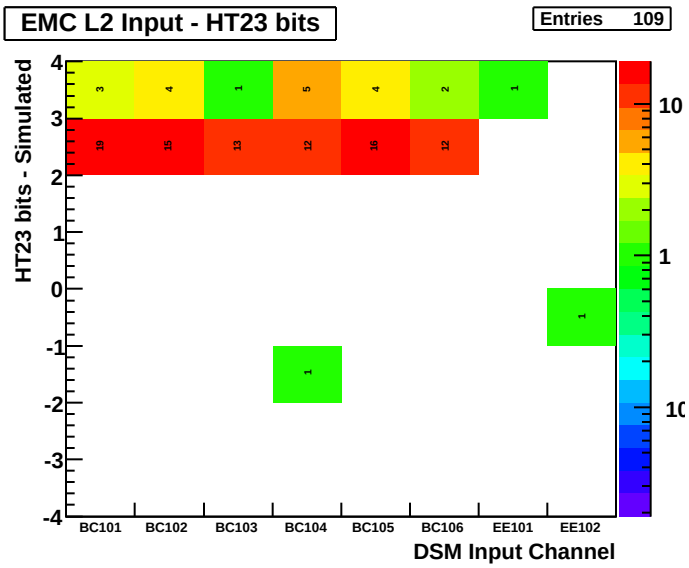

EMC L2 Input - Partial JP Sum
Entries 1.999999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

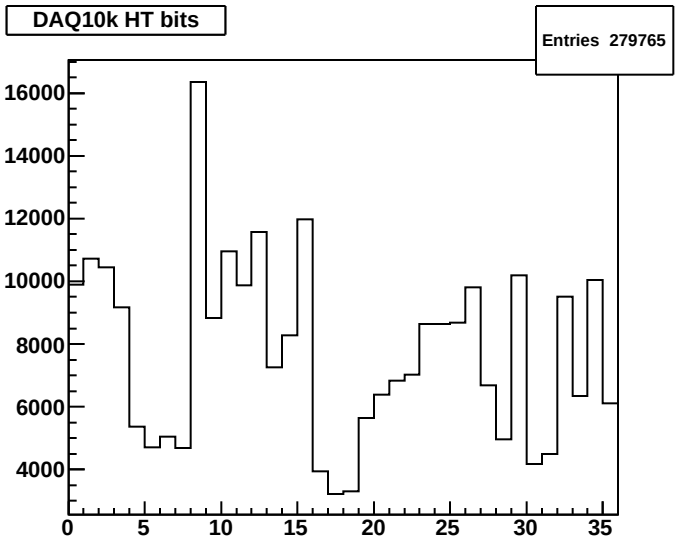

EMC L2 Input - Partial JP Sum
Entries 2771
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

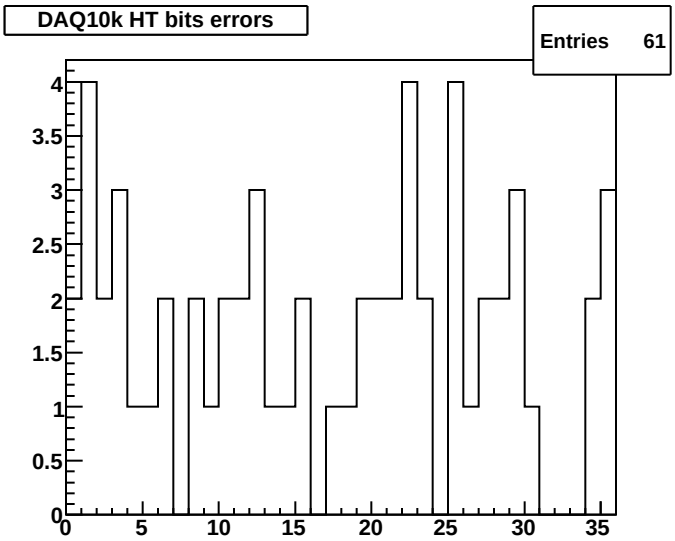

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.999999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

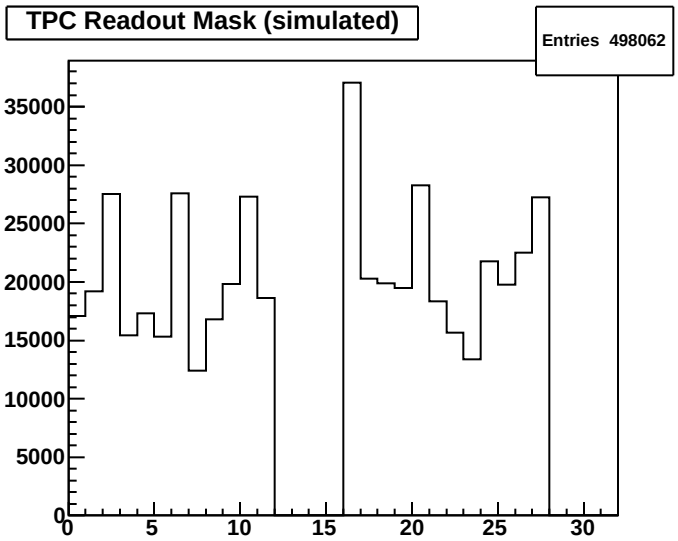

EMC L2 Input - HT01 bits/Partial JP ID
Entries 556
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

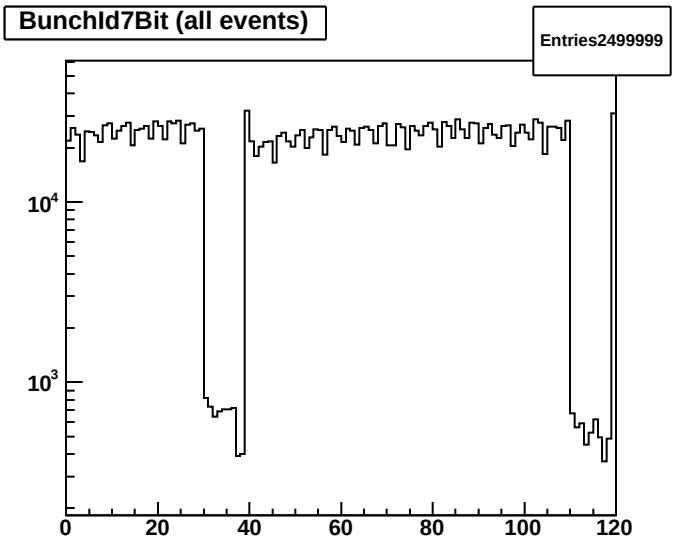

EMC L2 Input - HT23 bits
Entries 1.999999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

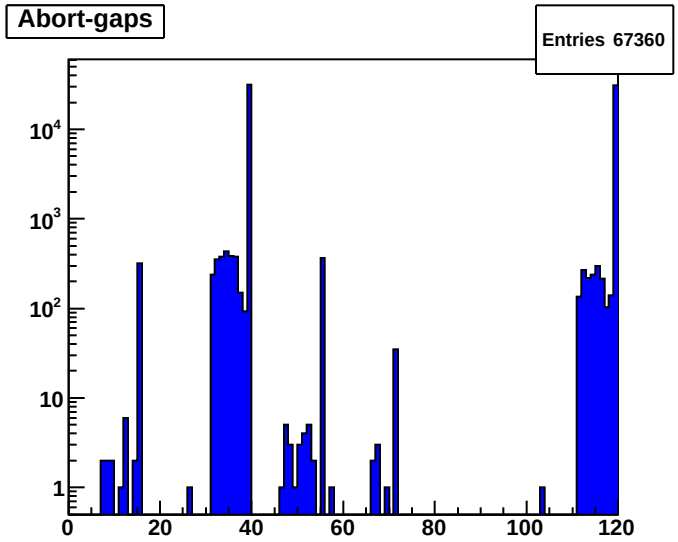

EMC L2 Input - HT23 bits
Entries 109
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

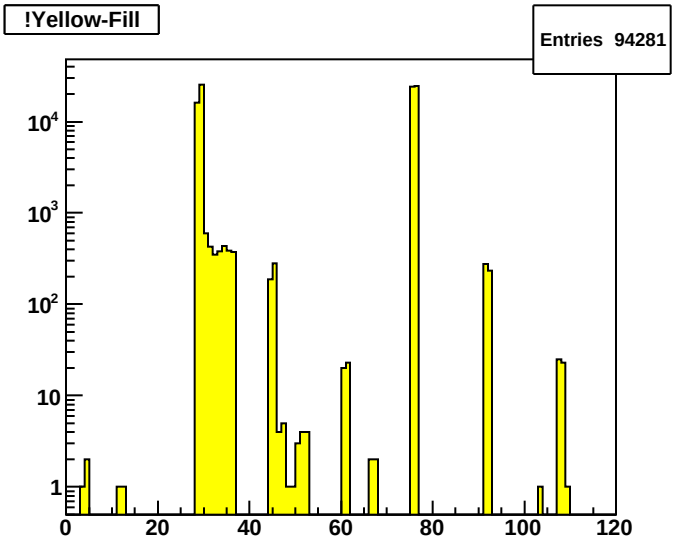

DAQ10k HT bits
Entries 279765

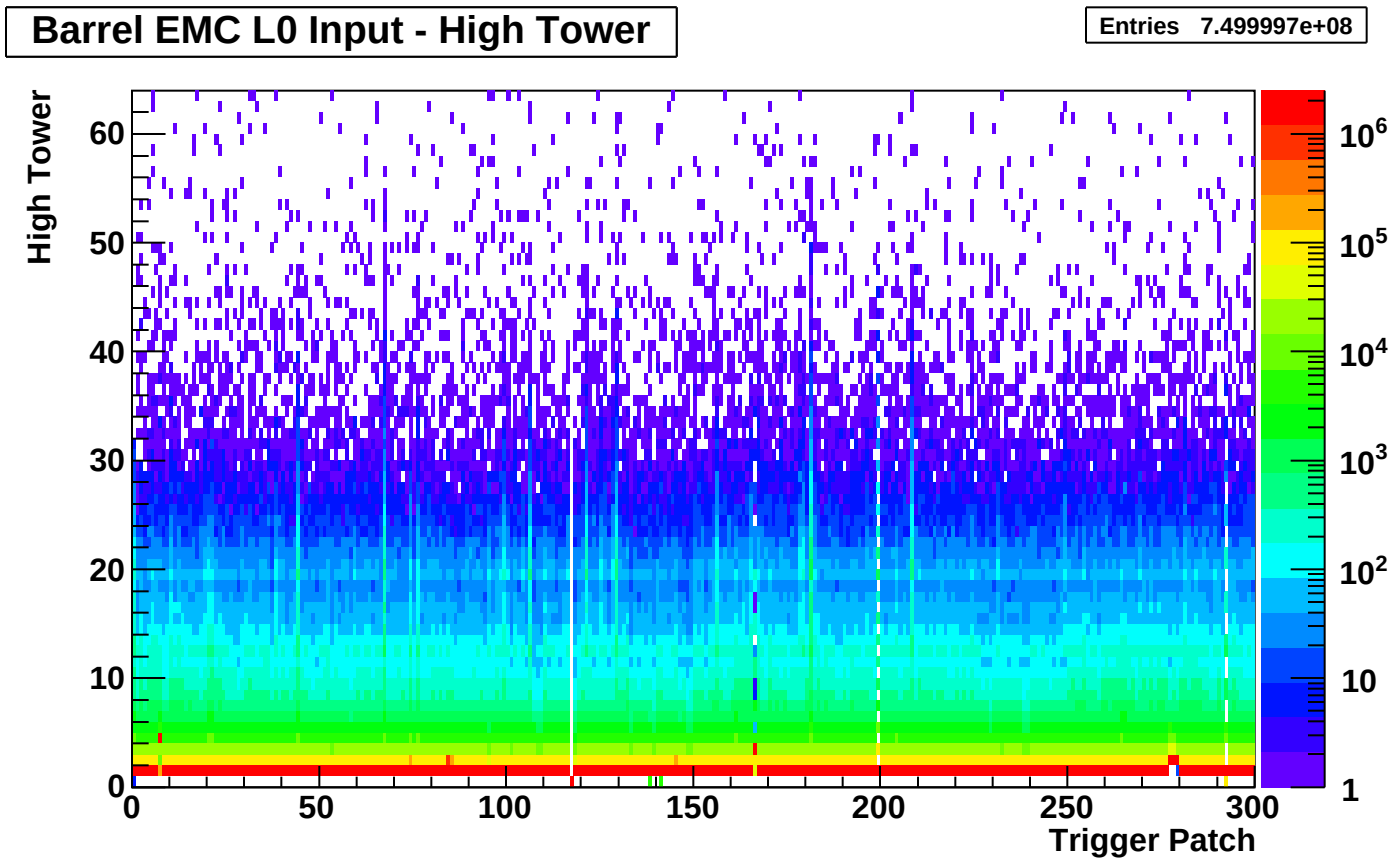

DAQ10k HT bits errors
Entries 61

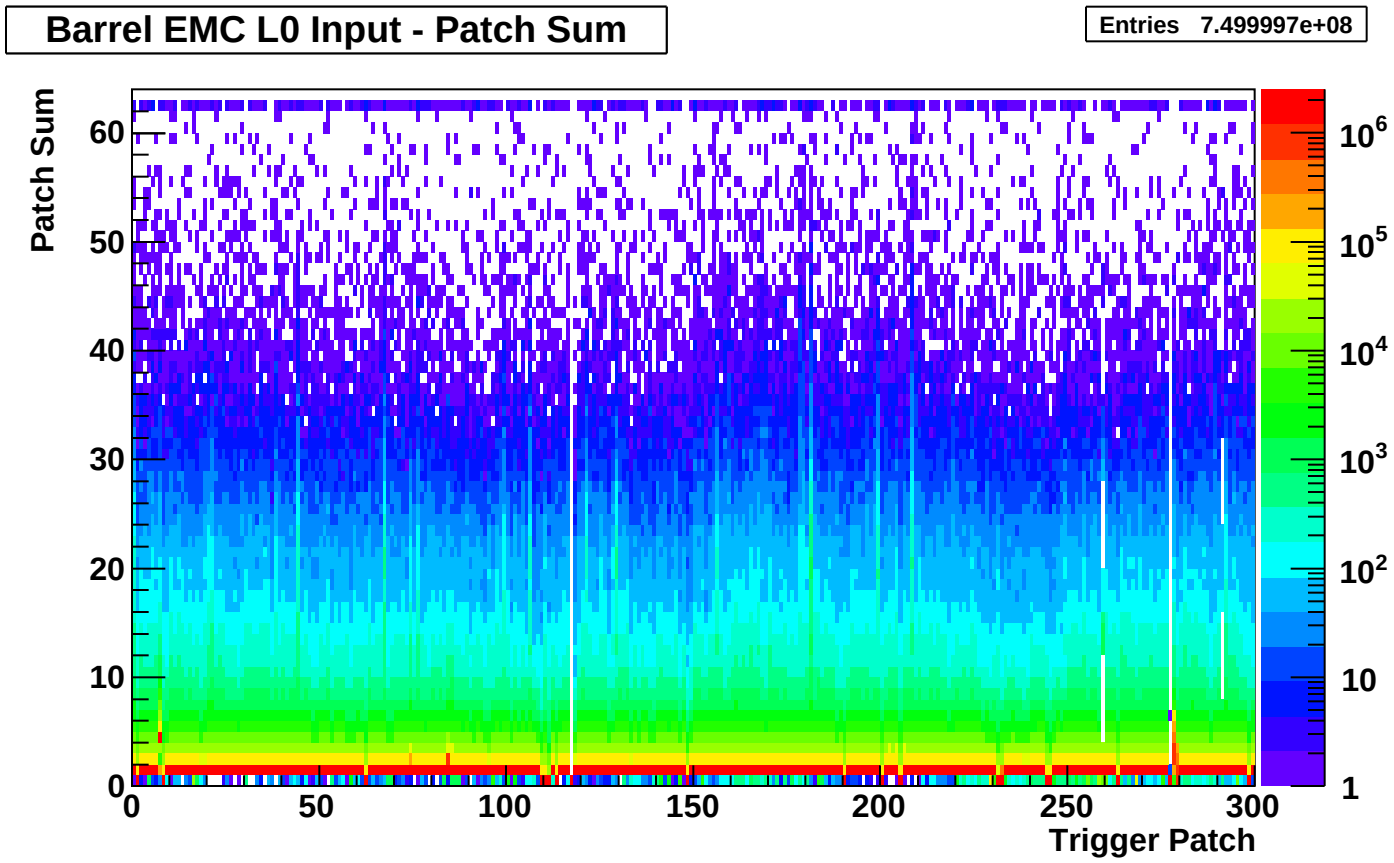

TPC Readout Mask (simulated)
Entries 498062

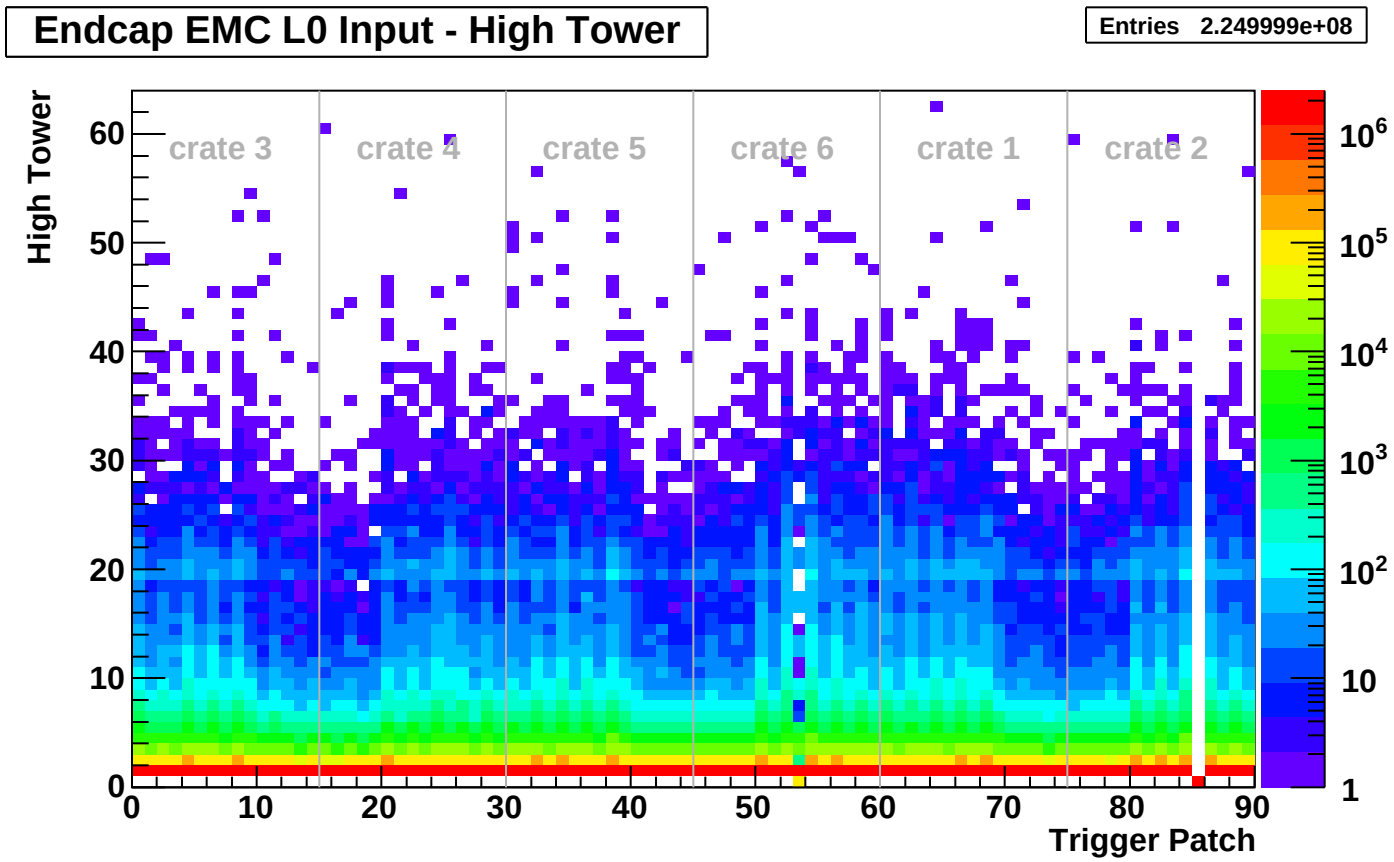

BunchId7Bit (all events)
Entries2499999

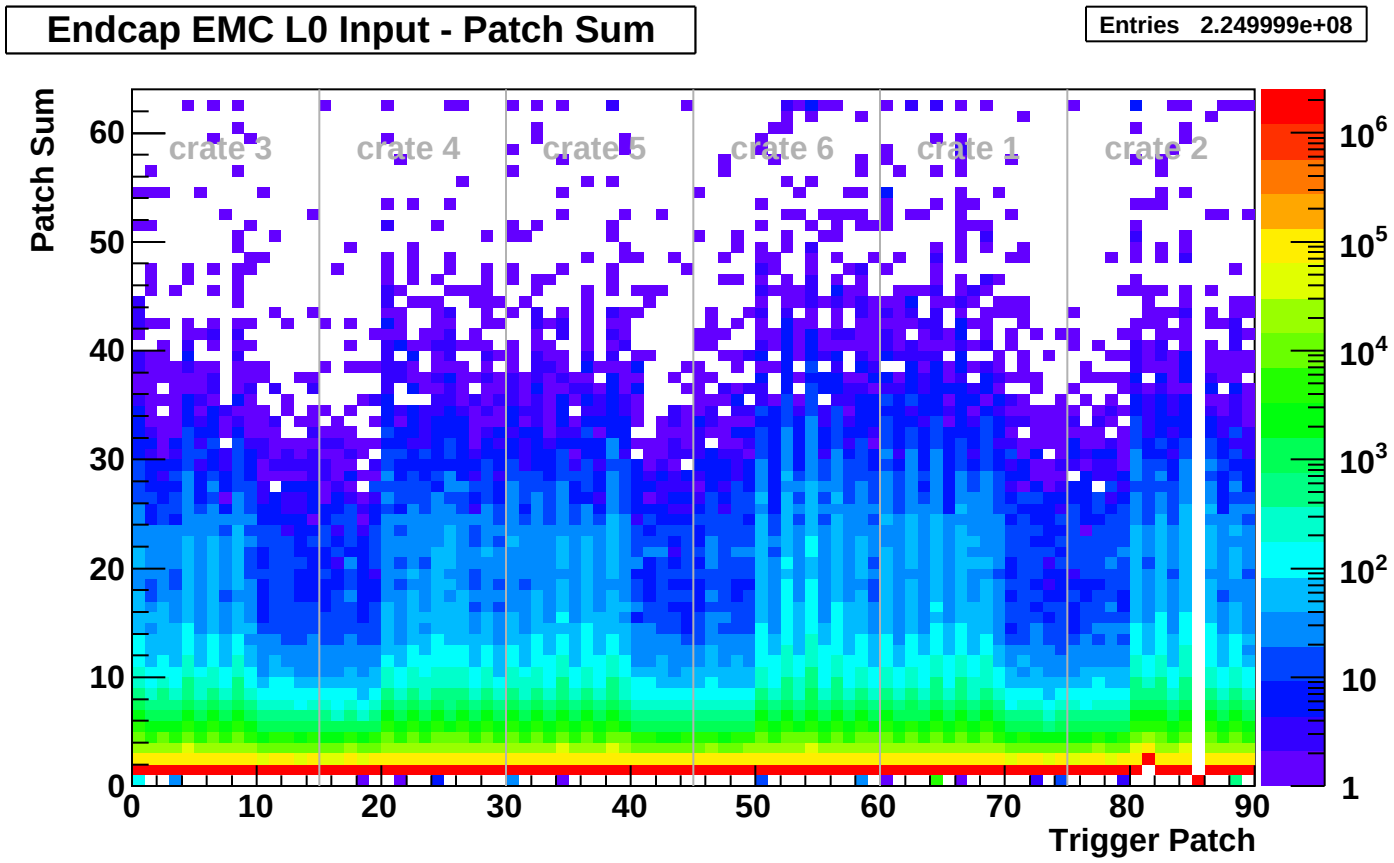

Abort-gaps
Entries 67360

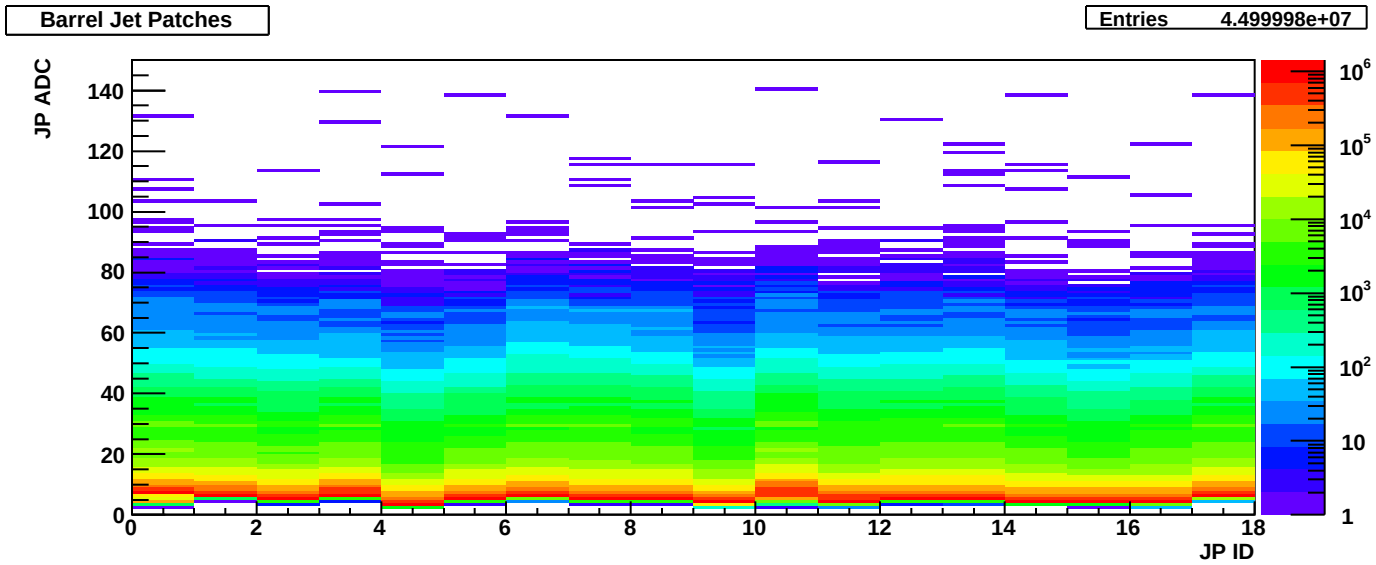

!Yellow-Fill
Entries 94281

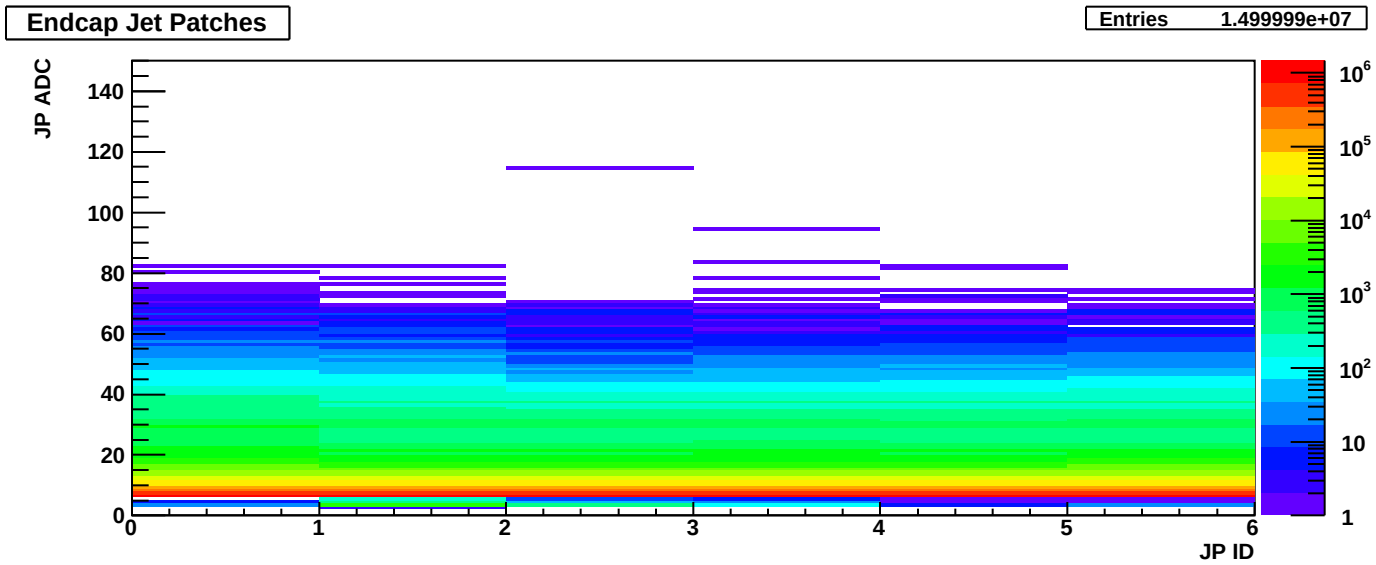

Barrel EMC L0 Input - High Tower
Entries 7.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

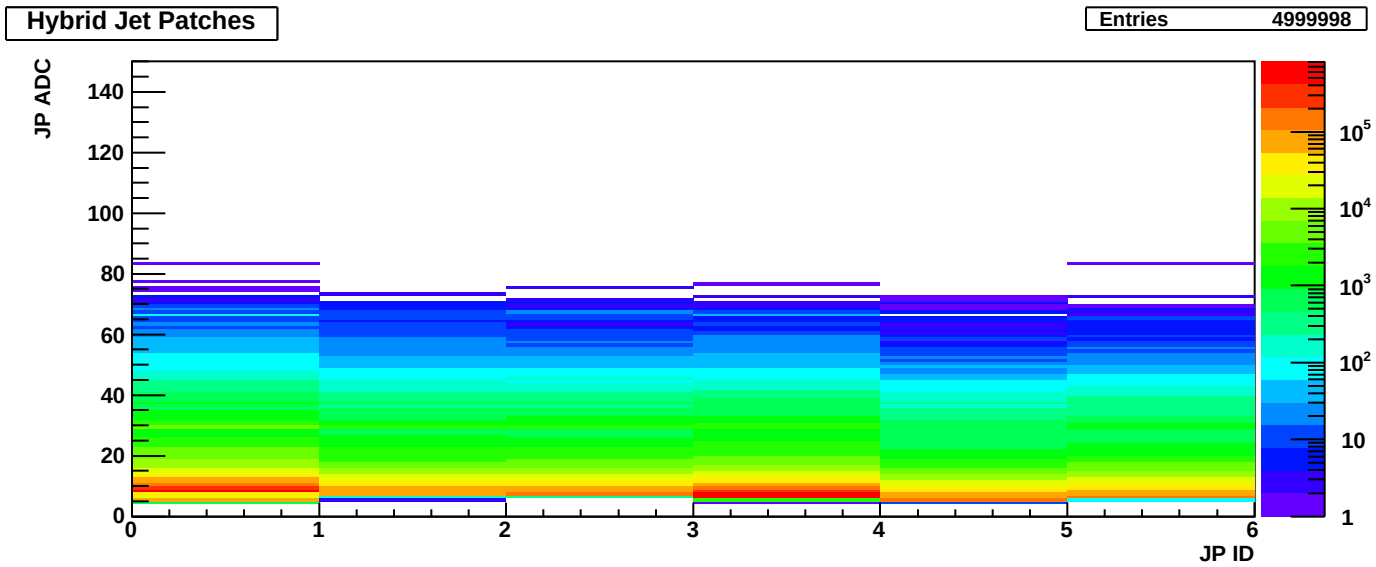

Barrel EMC L0 Input - Patch Sum
Entries 7.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

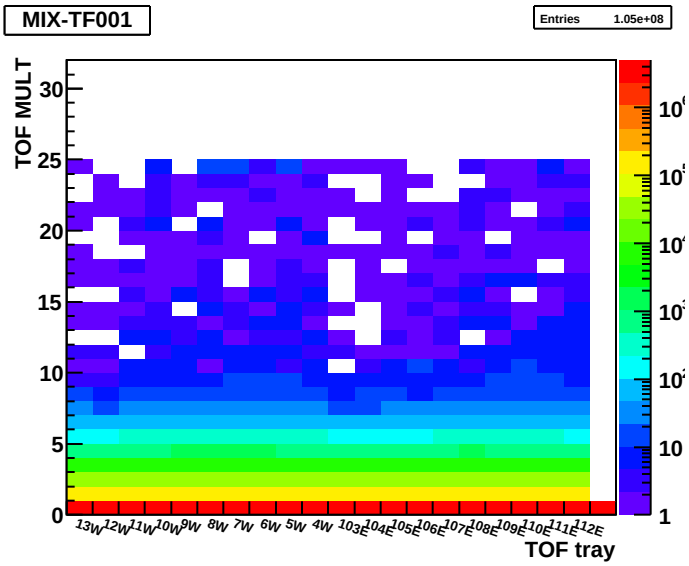

Endcap EMC L0 Input - High Tower
Entries 2.249999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

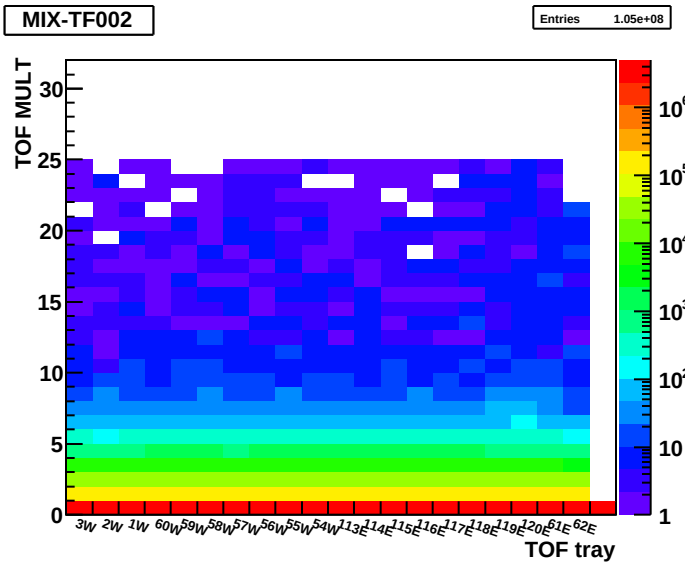

Endcap EMC L0 Input - Patch Sum
Entries 2.249999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

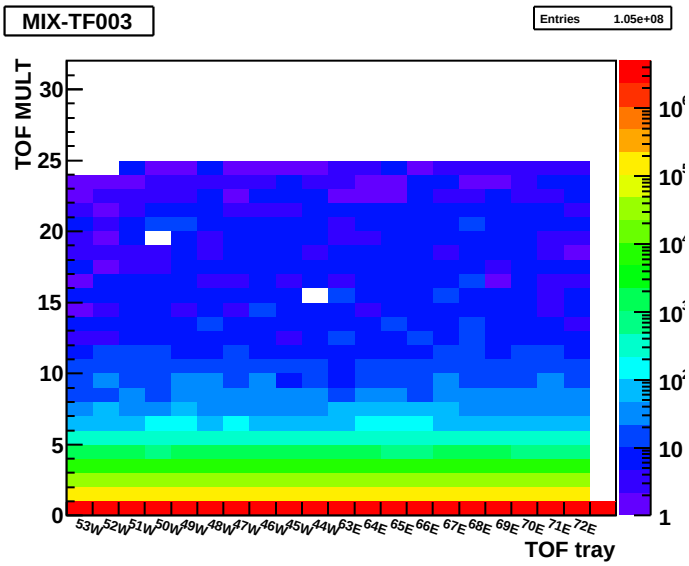

Barrel Jet Patches
Entries 4.499998e+07
JP ADC
JP ID

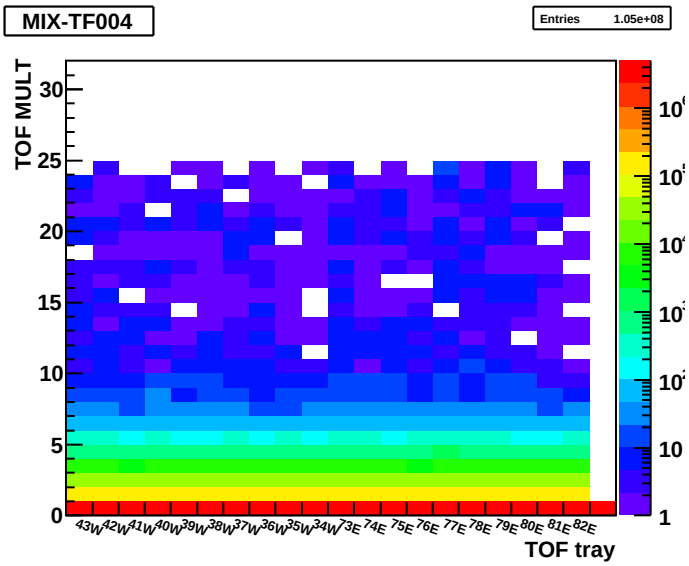

Endcap Jet Patches
Entries 1.499999e+07
JP ADC
JP ID

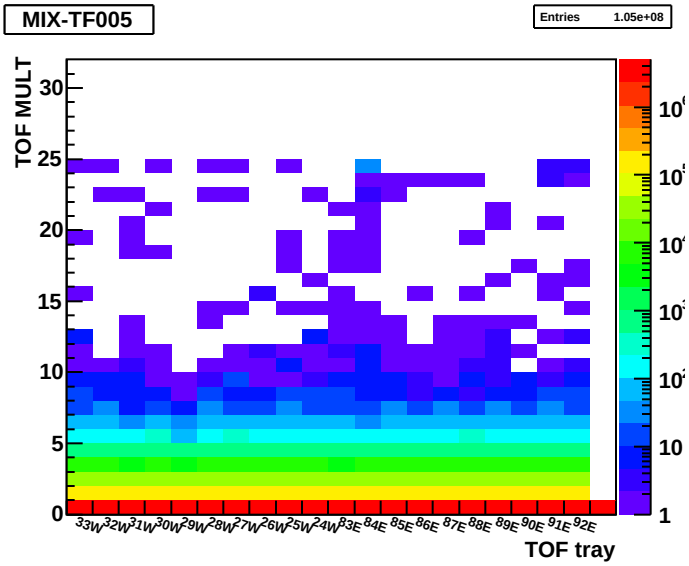

Hybrid Jet Patches
Entries 4999998
JP ADC
JP ID

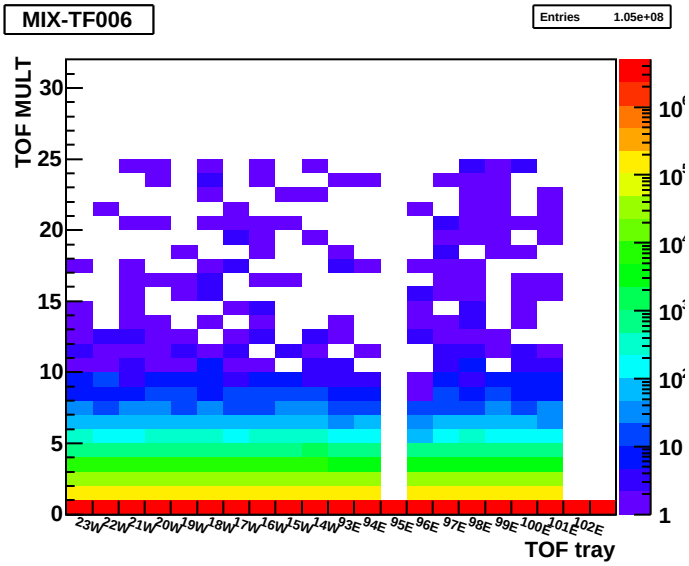

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

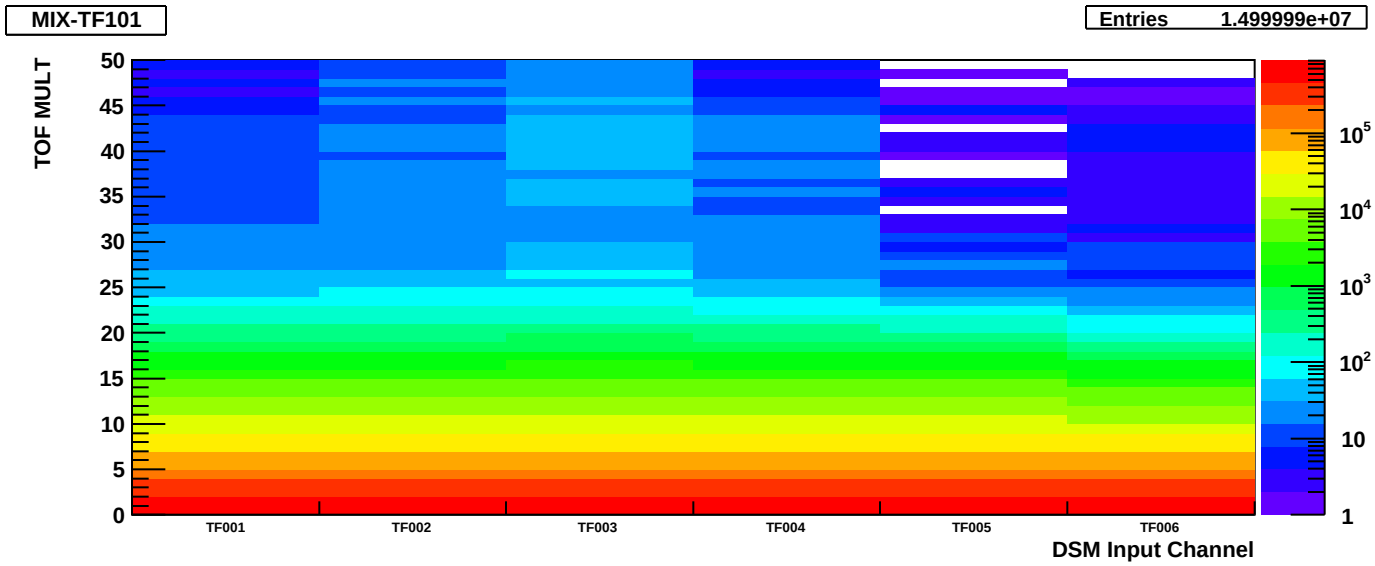

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

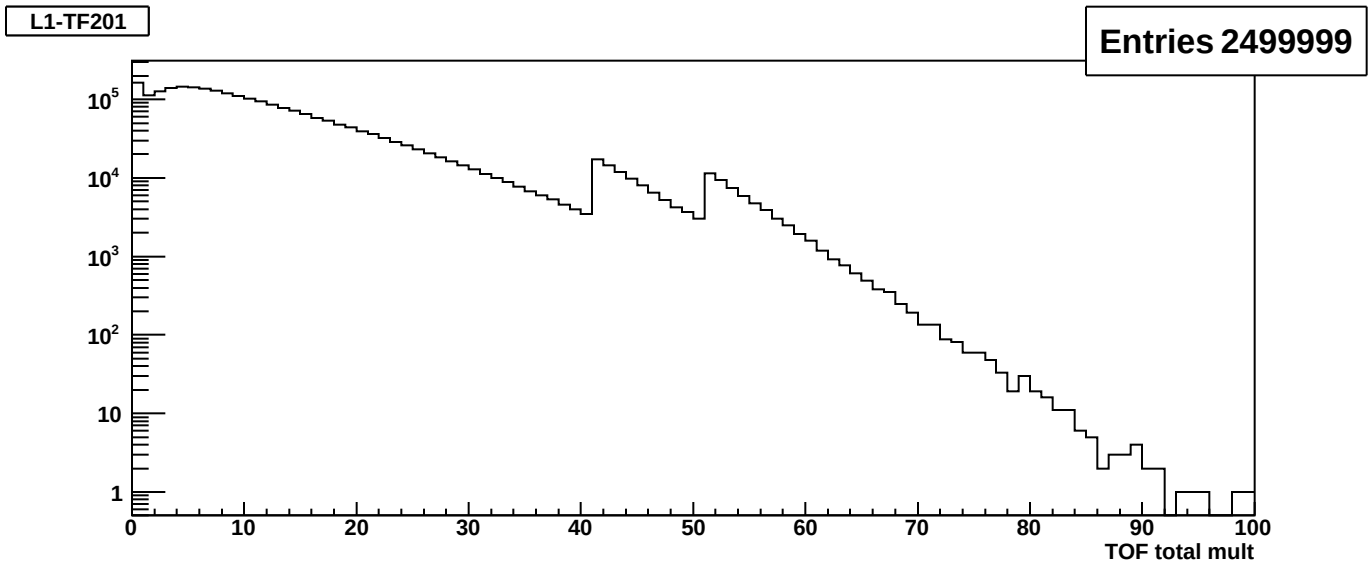

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

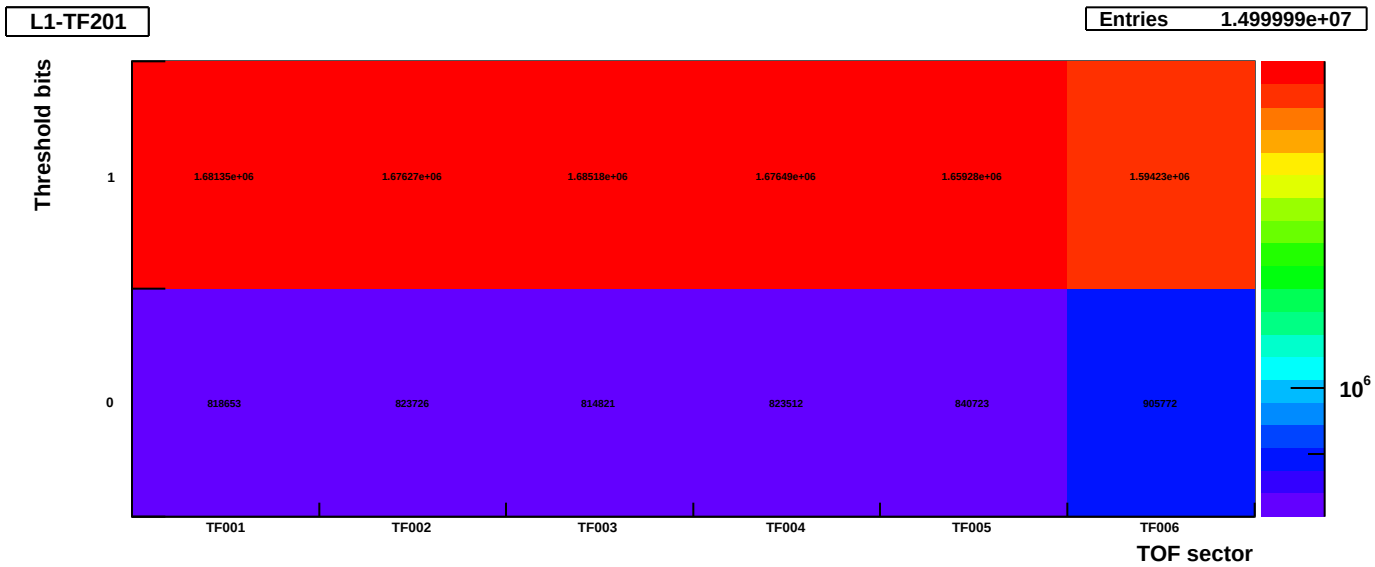

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

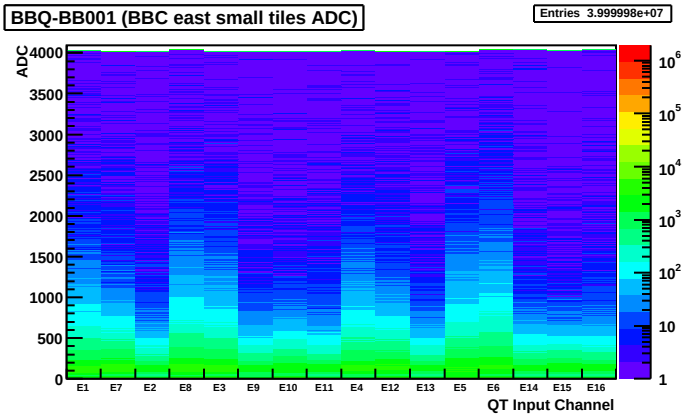

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

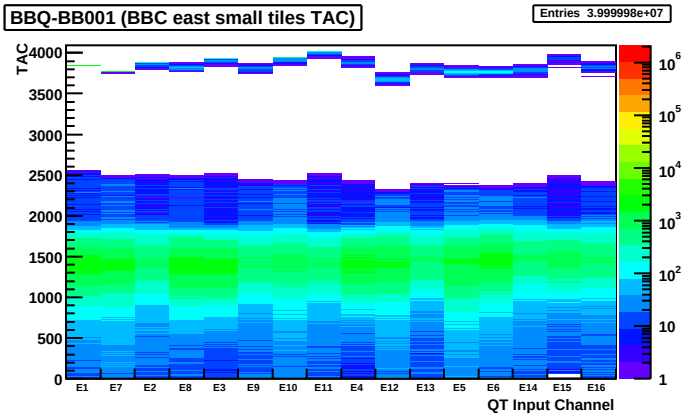

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

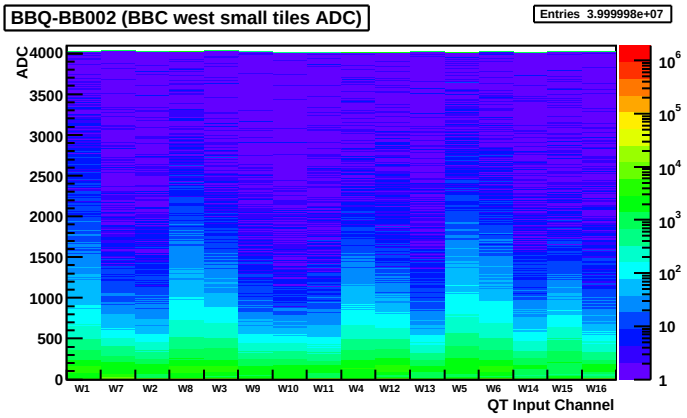

MIX-TF101
Entries 1.499999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

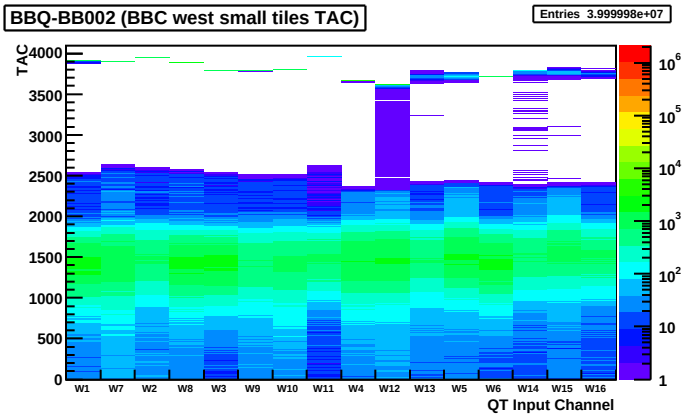

L1-TF201
Entries 2499999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

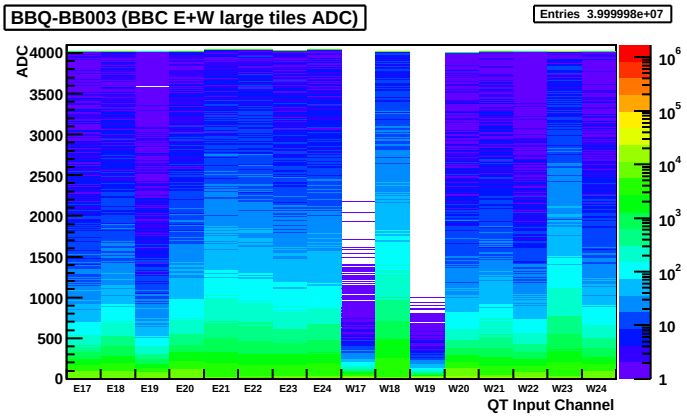

L1-TF201
Entries 1.499999e+07
Threshold bits
1
0
1.68135e+06
1.67627e+06
1.68518e+06
1.67649e+06
1.65928e+06
1.59423e+06
818653
823726
814821
823512
840723
905772
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

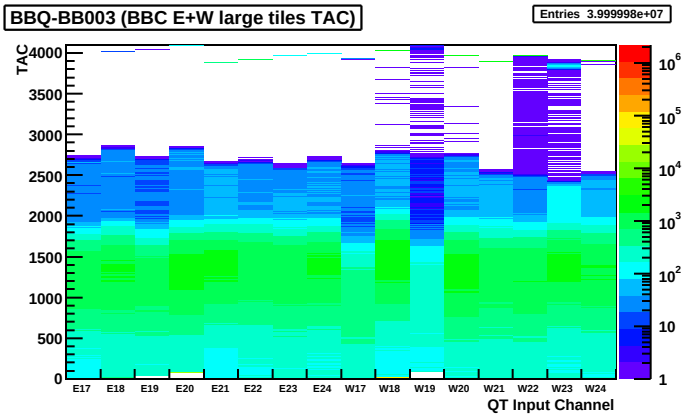

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

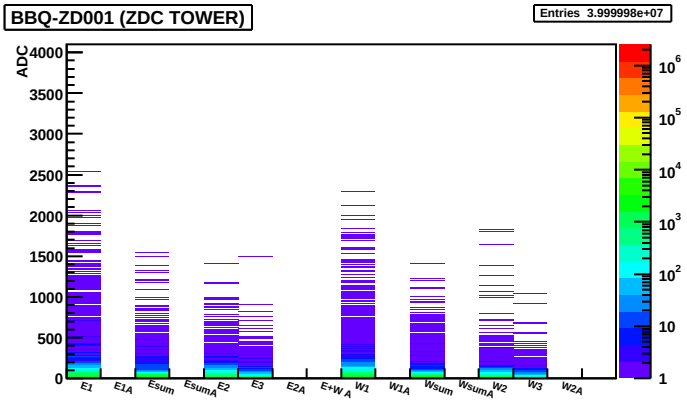

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

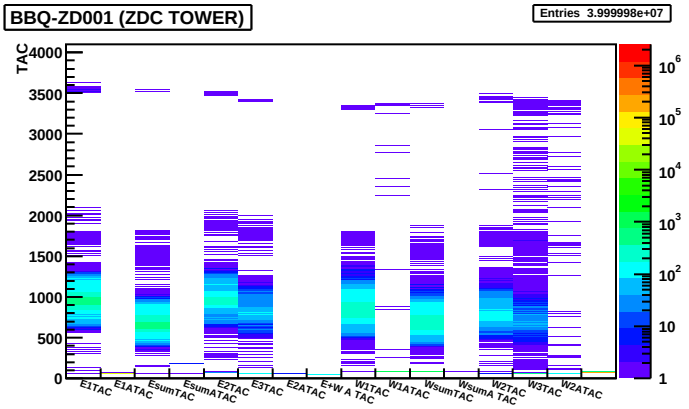

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

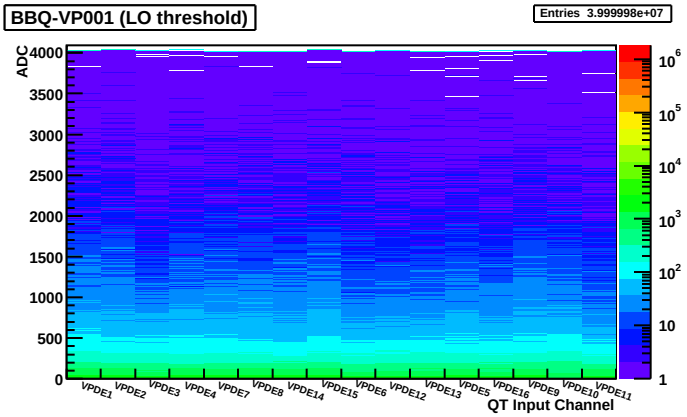

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

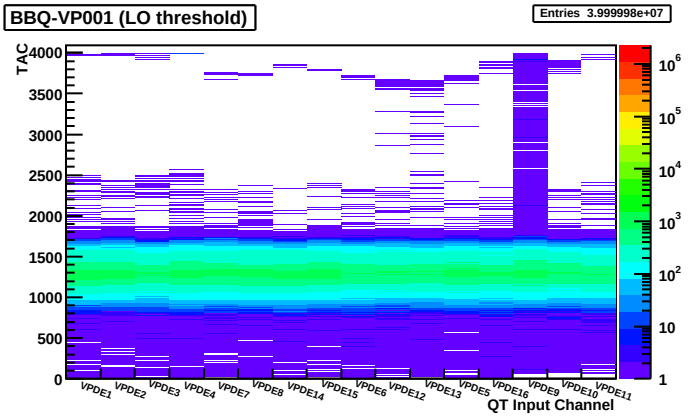

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

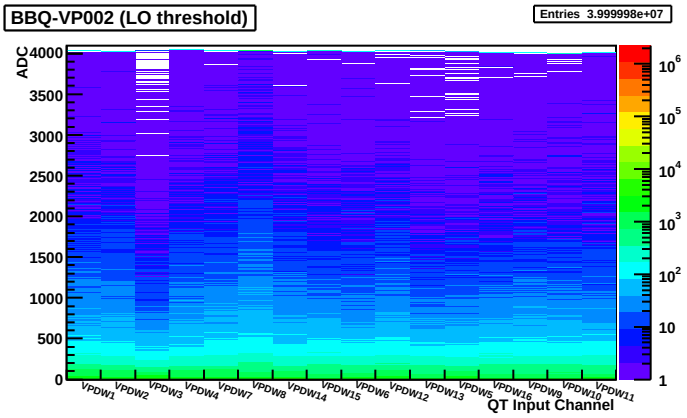

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

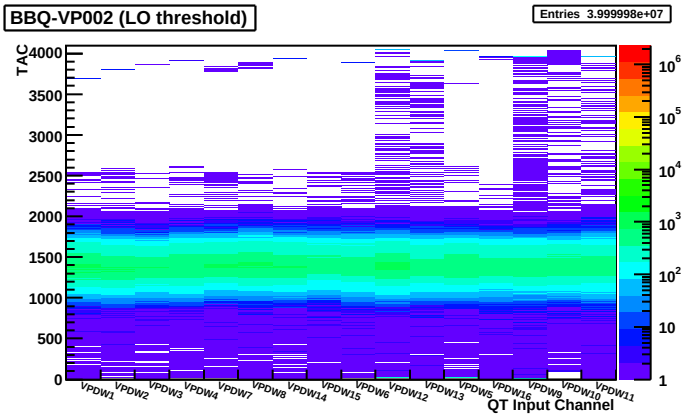

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
ADC

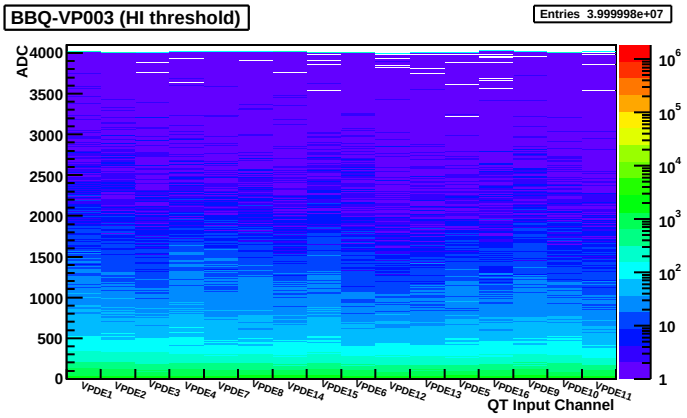

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
TAC

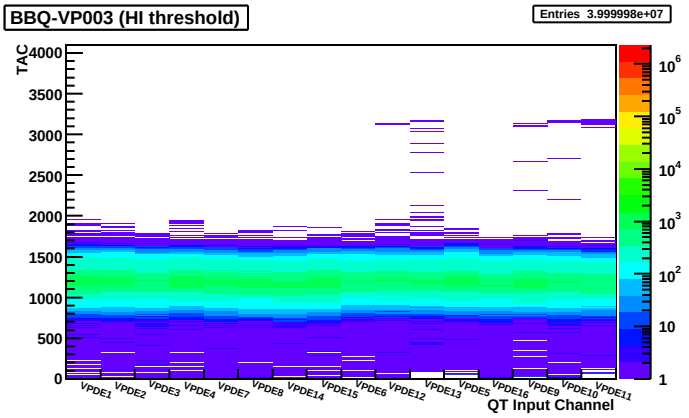

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

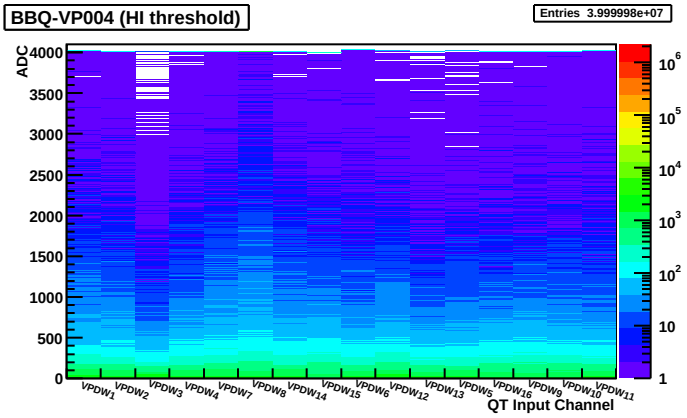

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

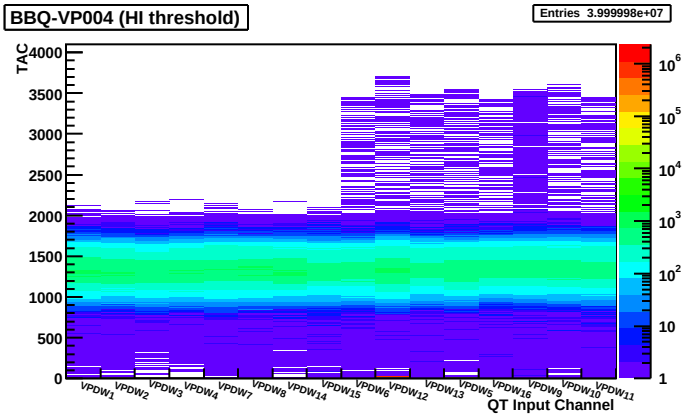

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

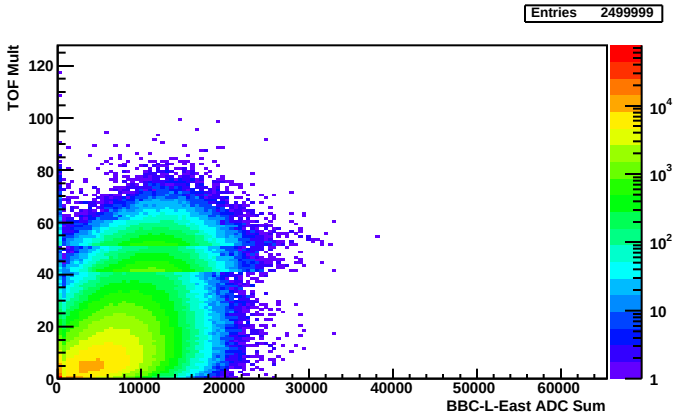

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

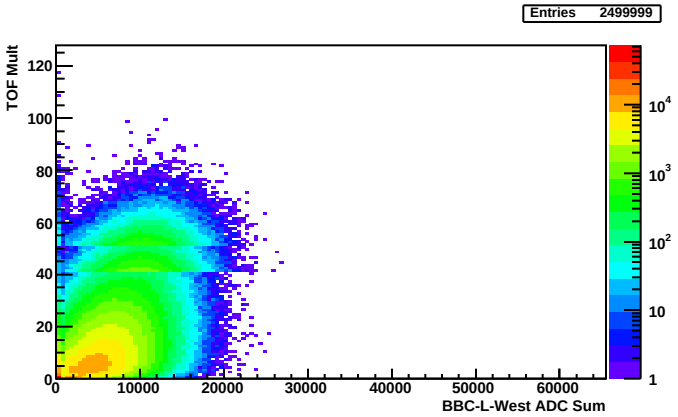

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

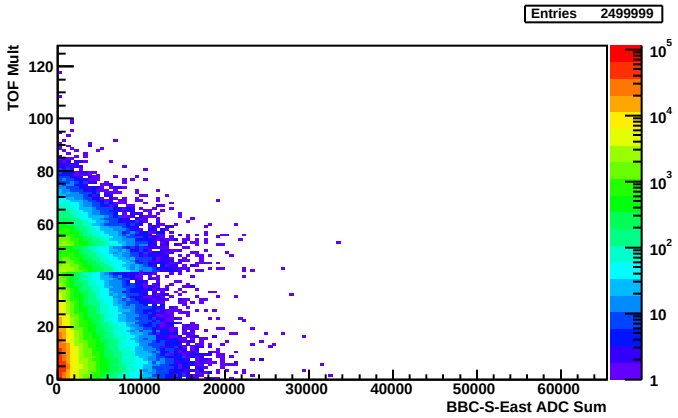

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

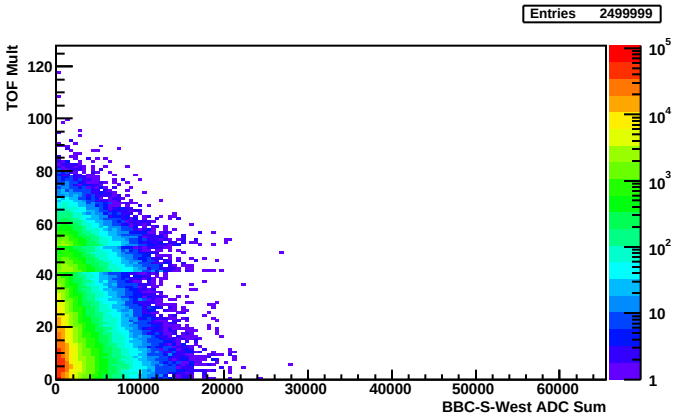

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

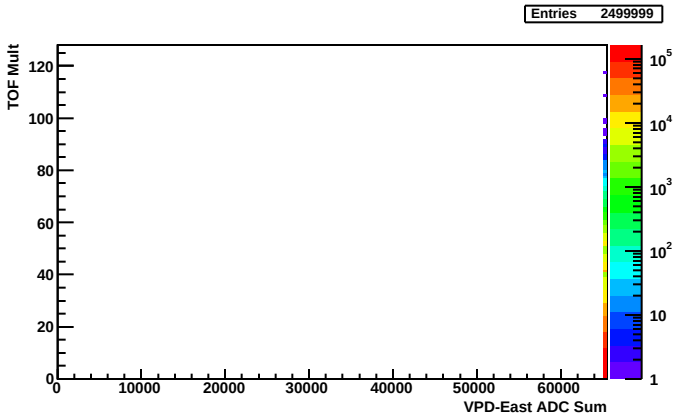

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

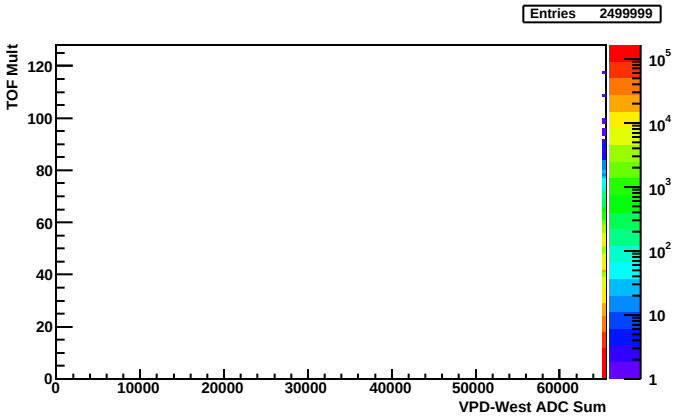

Entries 2499999
TOF Mult
BBC-L-East ADC Sum

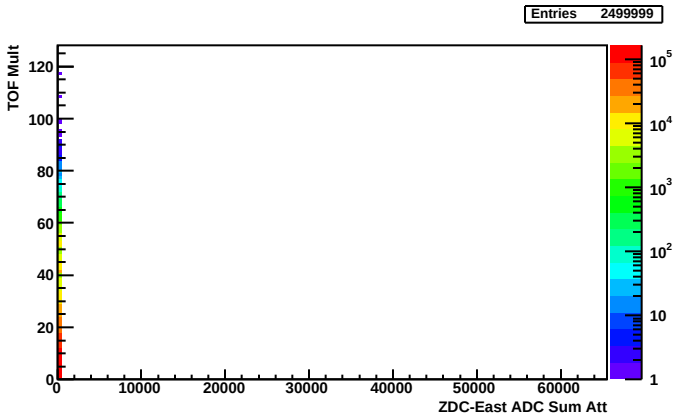

Entries 2499999
TOF Mult
BBC-L-West ADC Sum

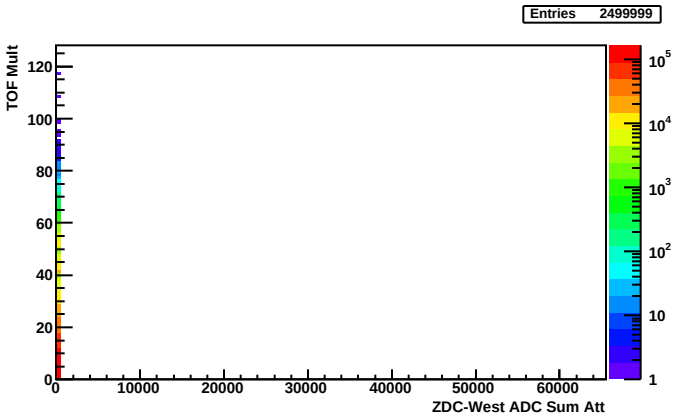

Entries 2499999
TOF Mult
BBC-S-East ADC Sum

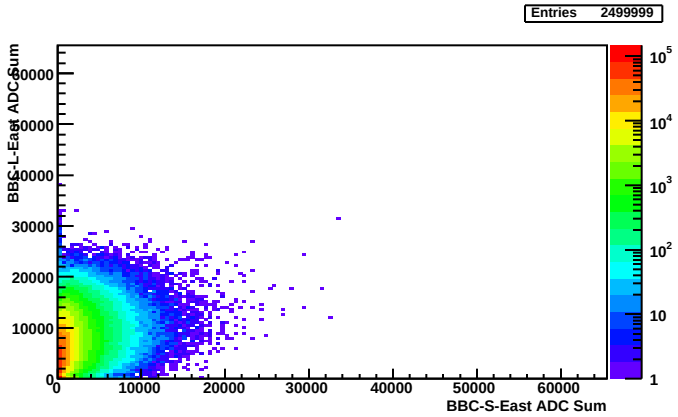

Entries 2499999
TOF Mult
BBC-S-West ADC Sum

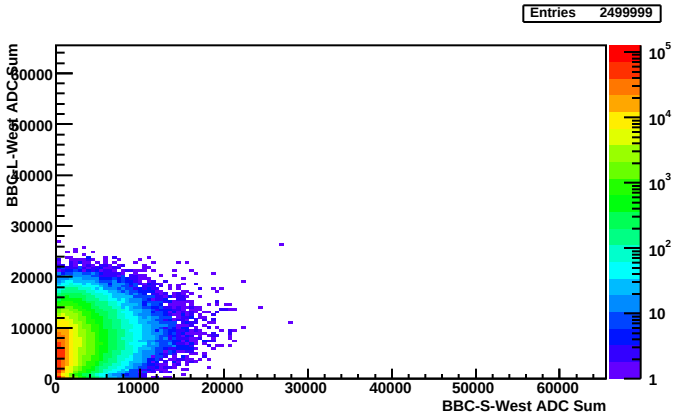

Entries 2499999
TOF Mult
VPD-East ADC Sum

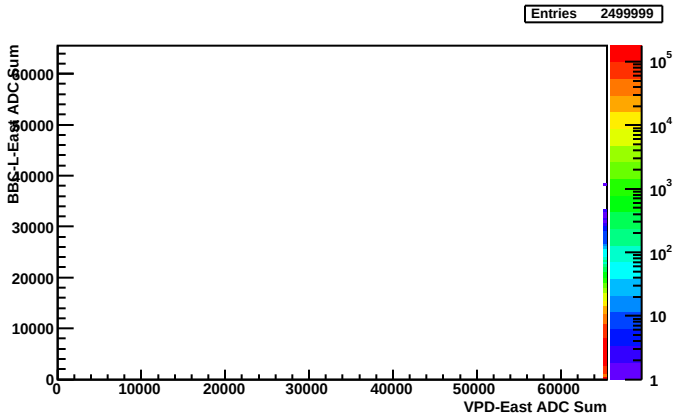

Entries 2499999
TOF Mult
VPD-West ADC Sum

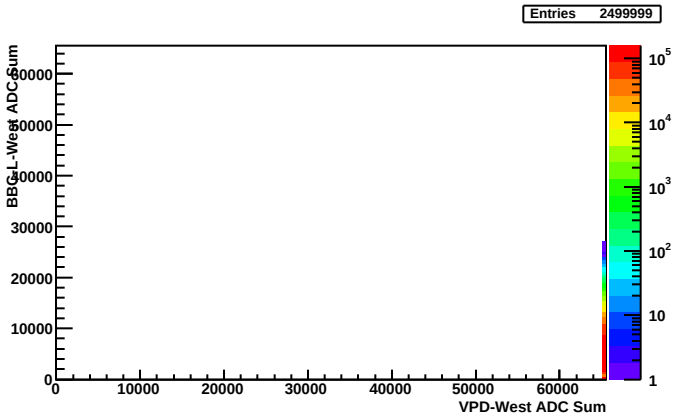

Entries 2499999
TOF Mult
ZDC-East ADC Sum Att

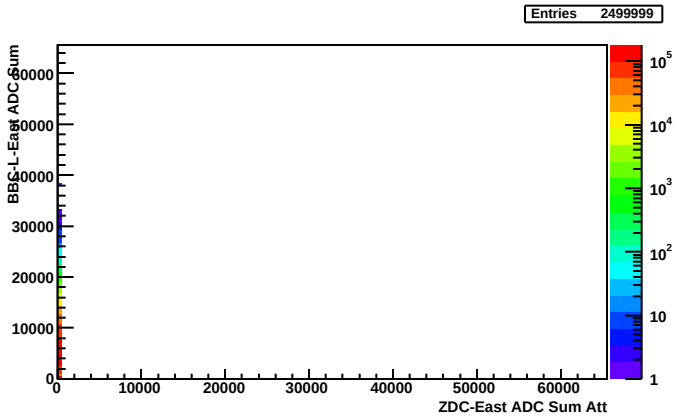

Entries 2499999
TOF Mult
ZDC-West ADC Sum Att

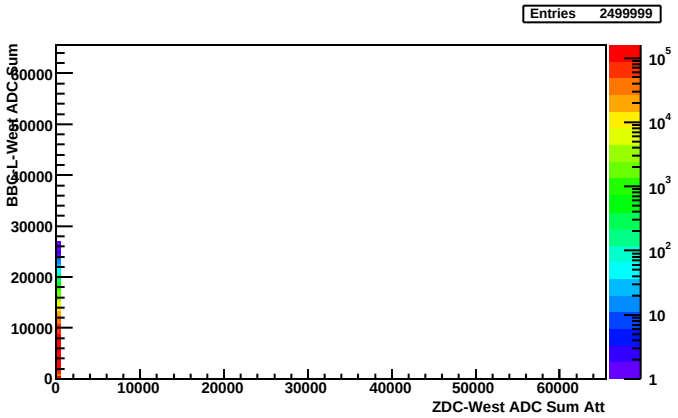

Entries 2499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

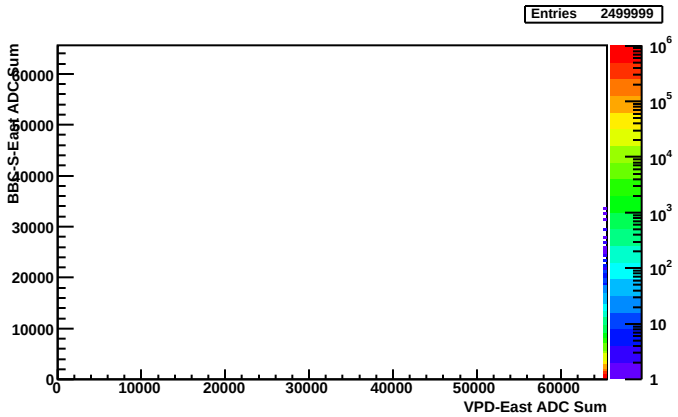

Entries 2499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

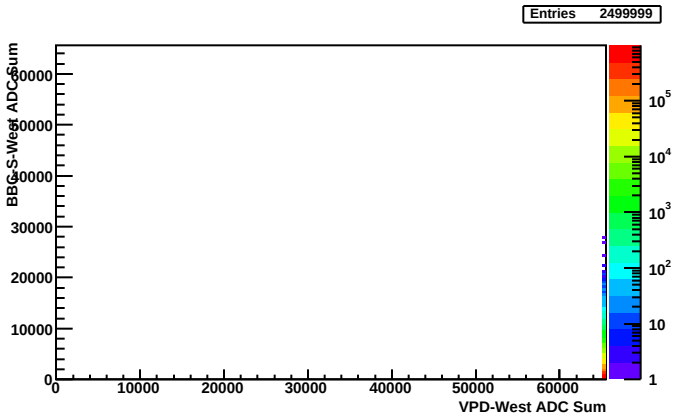

Entries 2499999
BBC-L-East ADC Sum
VPD-East ADC Sum

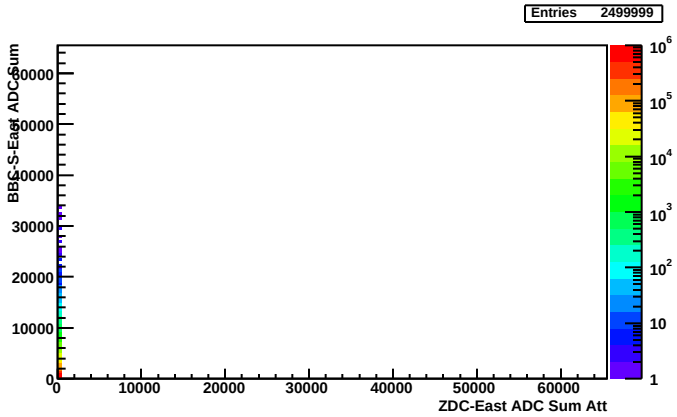

Entries 2499999
BBC-L-West ADC Sum
VPD-West ADC Sum

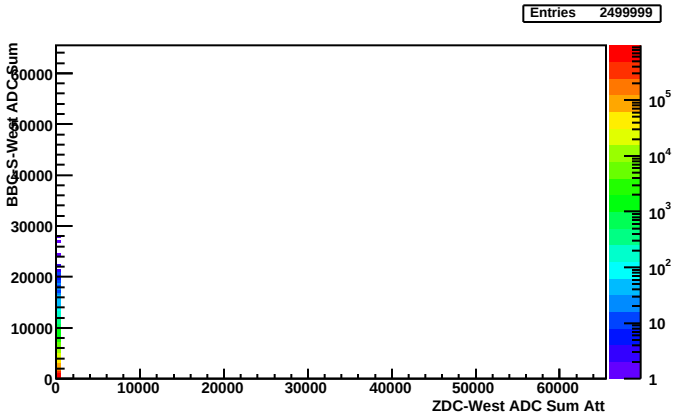

Entries 2499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

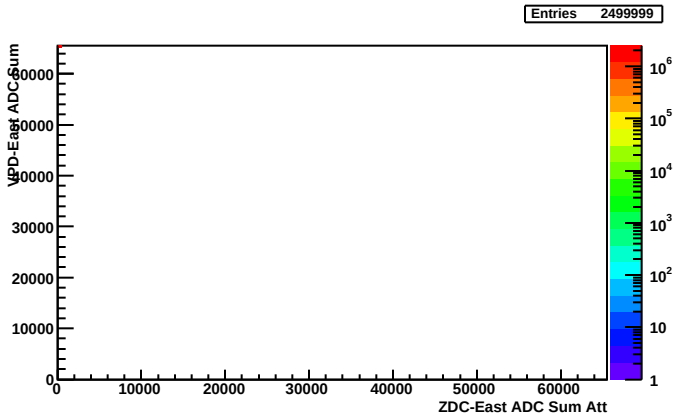

Entries 2499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

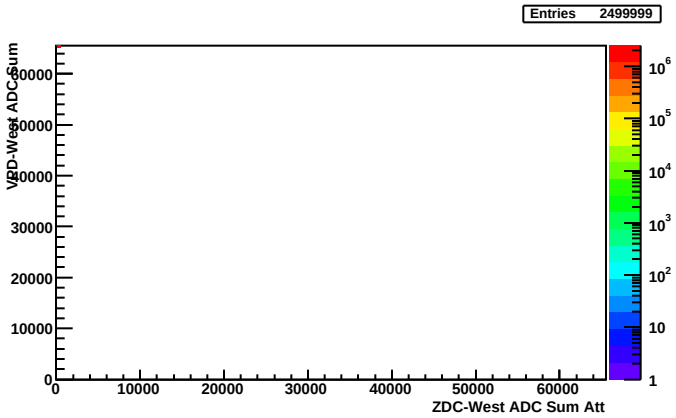

Entries 2499999
BBC-S-East ADC Sum
VPD-East ADC Sum

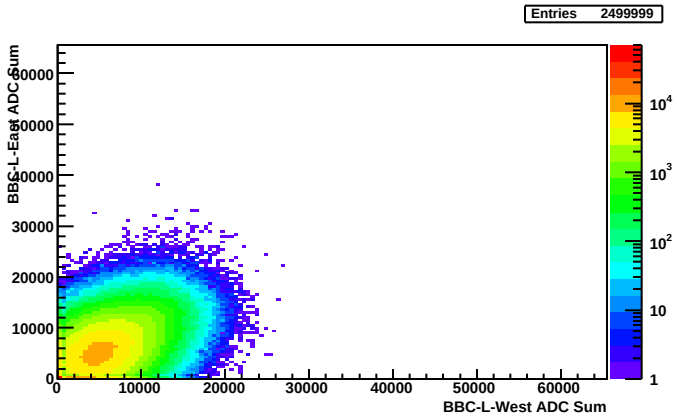

Entries 2499999
BBC-S-West ADC Sum
VPD-West ADC Sum

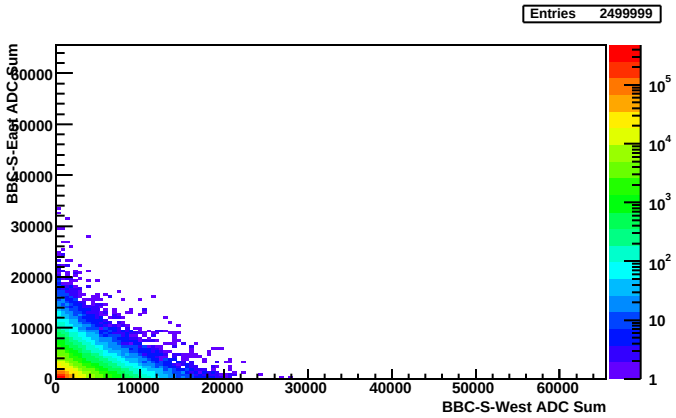

Entries 2499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

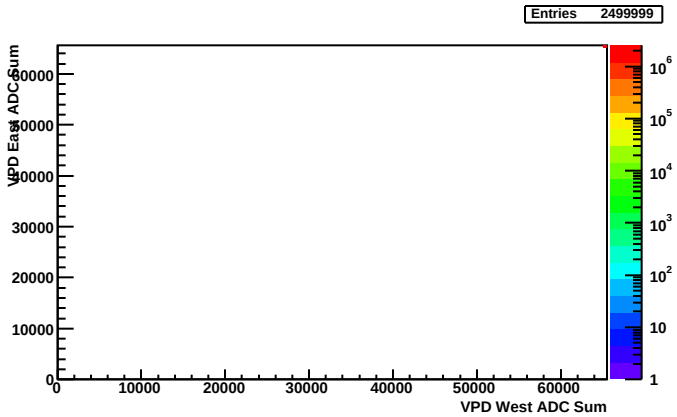

Entries 2499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

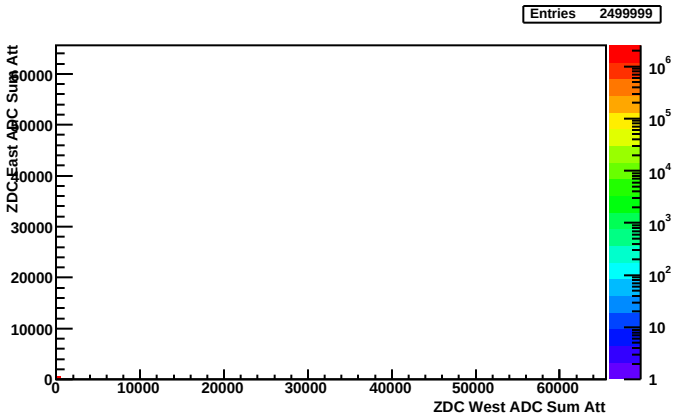

Entries 2499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

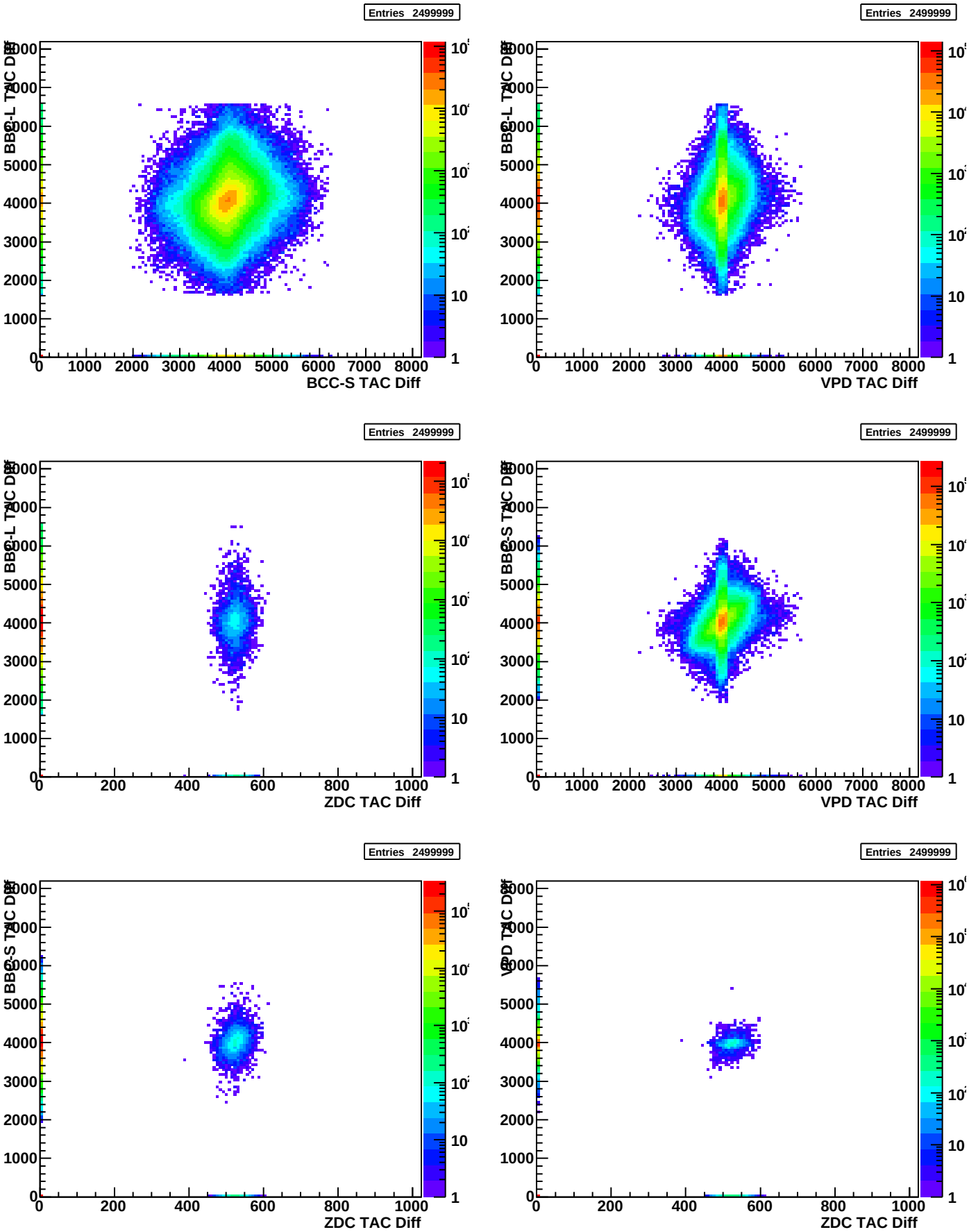

Entries 2499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

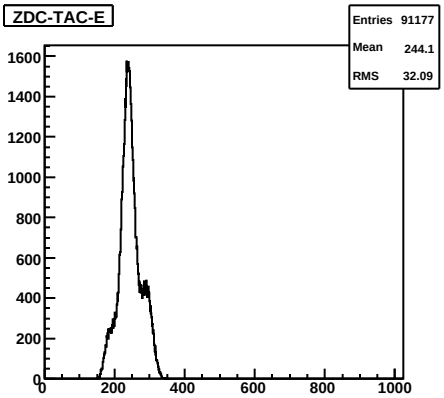

Entries 2499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

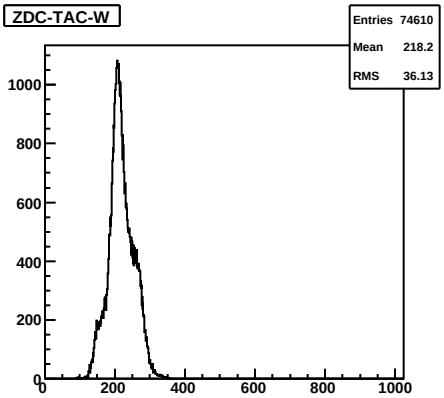

Entries 2499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

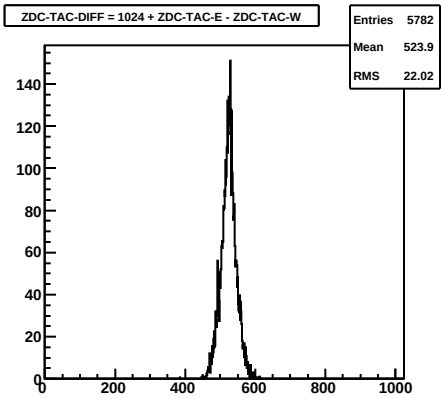

Entries 2499999
VPD East ADC Sum
VPD West ADC Sum

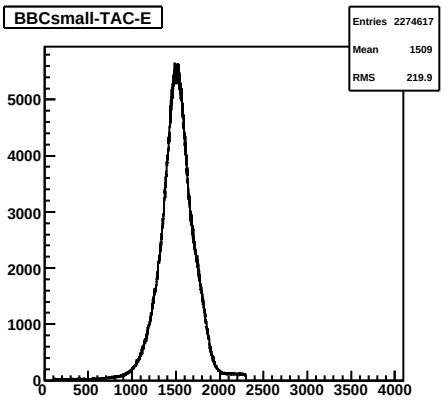

Entries 2499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

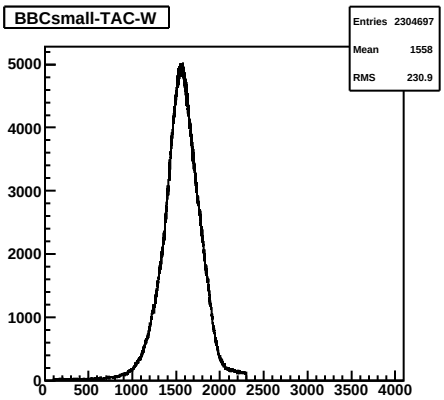

Entries 2499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2499999
BBC-L TAC Diff
VPD TAC Diff
Entries 2499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2499999
BBC-S TAC Diff
VPD TAC Diff
Entries 2499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2499999
VPD TAC Diff
ZDC TAC Diff

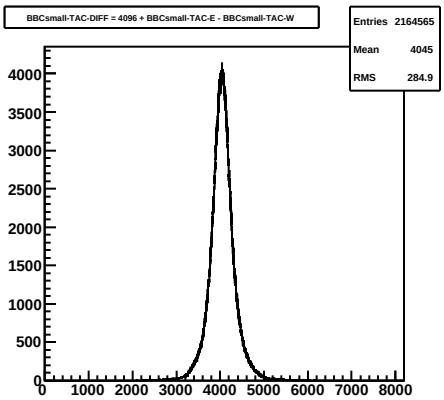

ZDC-TAC-E
Entries 91177
Mean 244.1
RMS 32.09

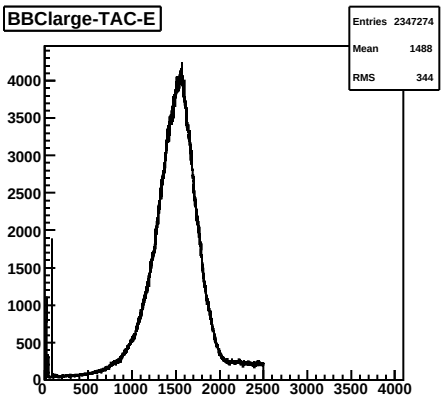

ZDC-TAC-W
Entries 74610
Mean 218.2
RMS 36.13

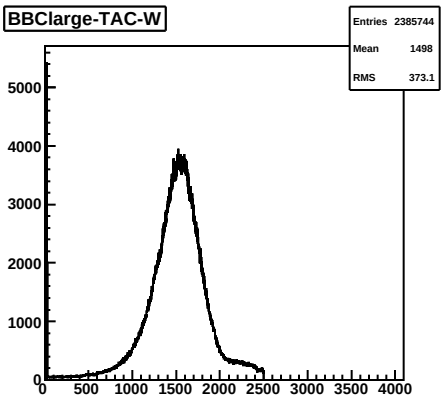

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5782
Mean 523.9
RMS 22.02

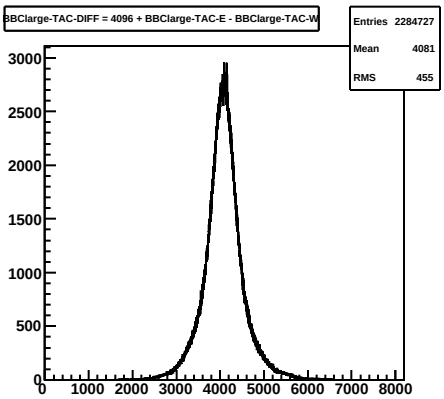

BBCsmall-TAC-E
Entries 2274617
Mean 1509
RMS 219.9

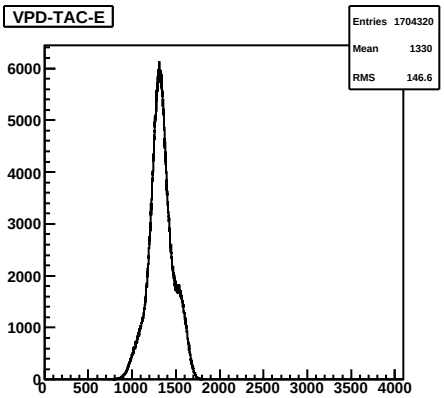

BBCsmall-TAC-W
Entries 2304697
Mean 1558
RMS 230.9

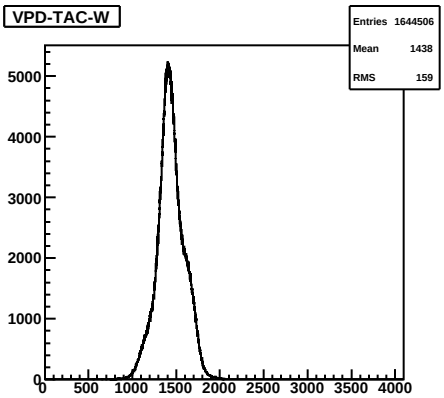

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2164565
Mean 4045
RMS 284.9

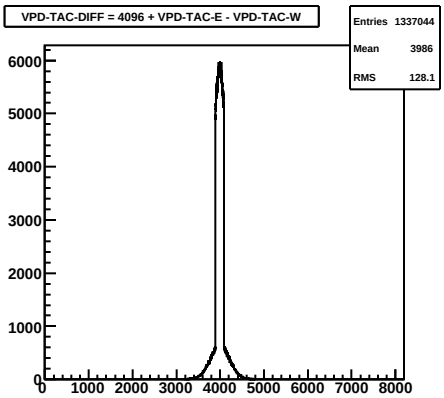

BBClarge-TAC-E
Entries 2347274
Mean 1488
RMS 344

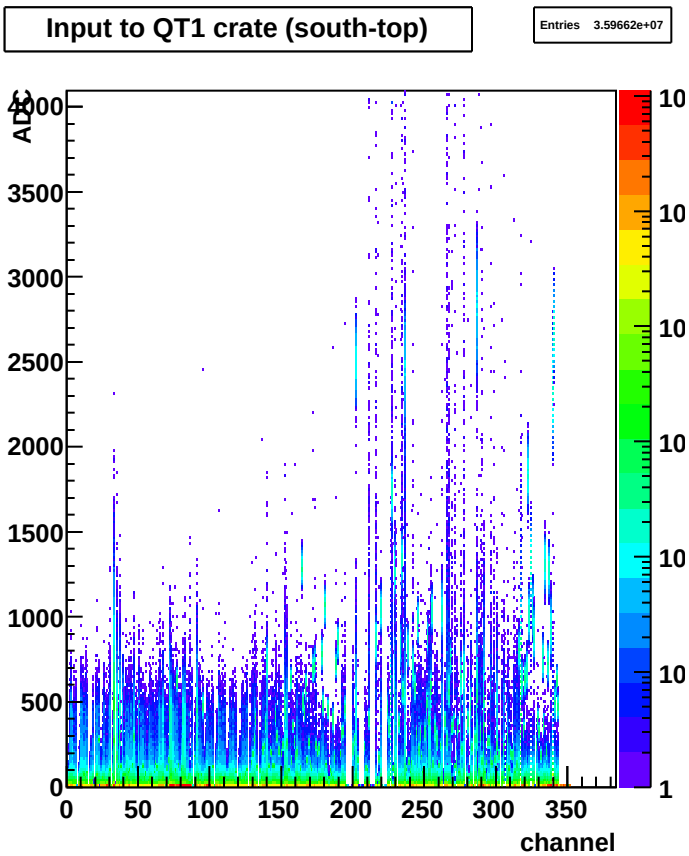

BBClarge-TAC-W
Entries 2385744
Mean 1498
RMS 373.1

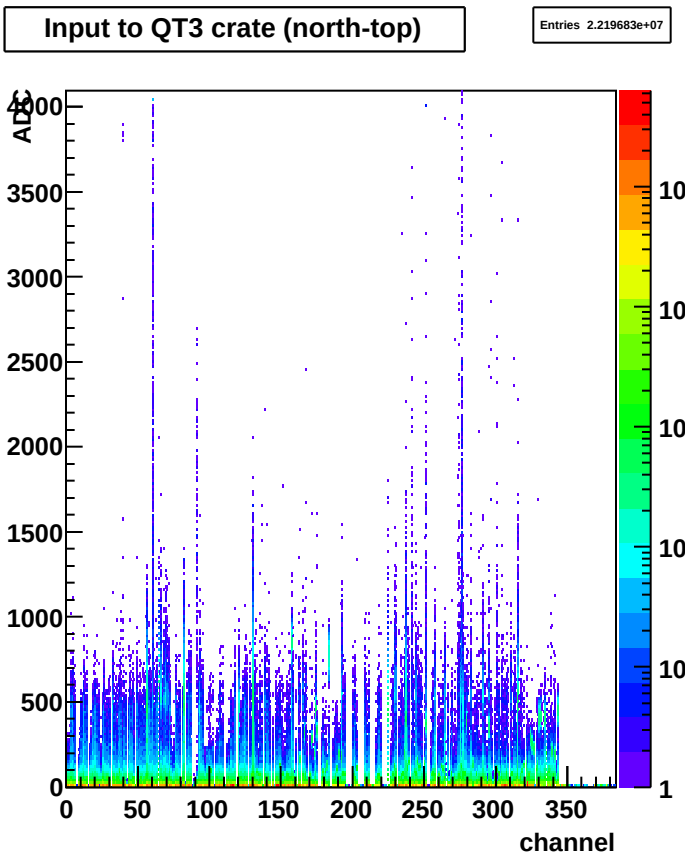

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2284727
Mean 4081
RMS 455

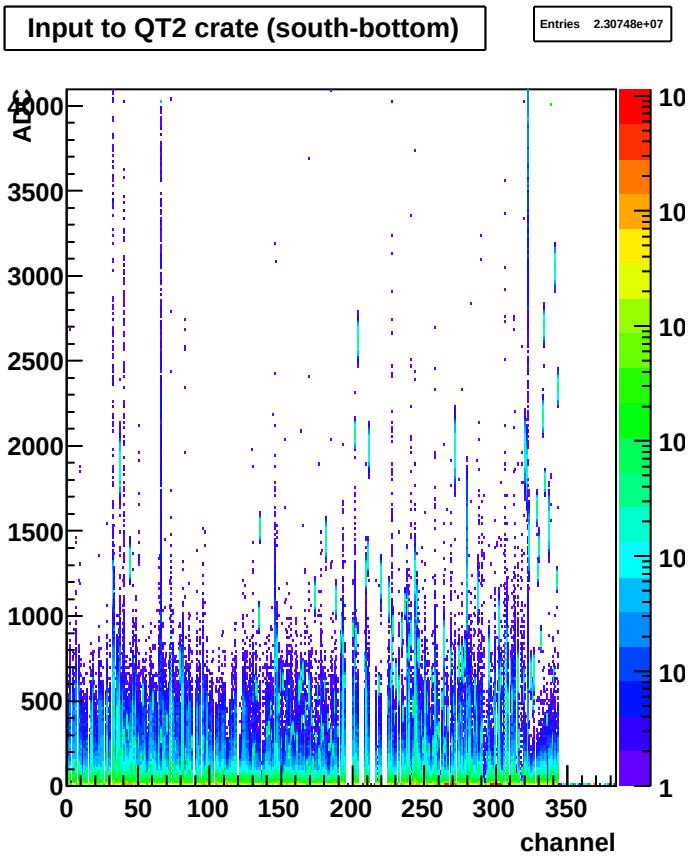

VPD-TAC-E
Entries 1704320
Mean 1330
RMS 146.6

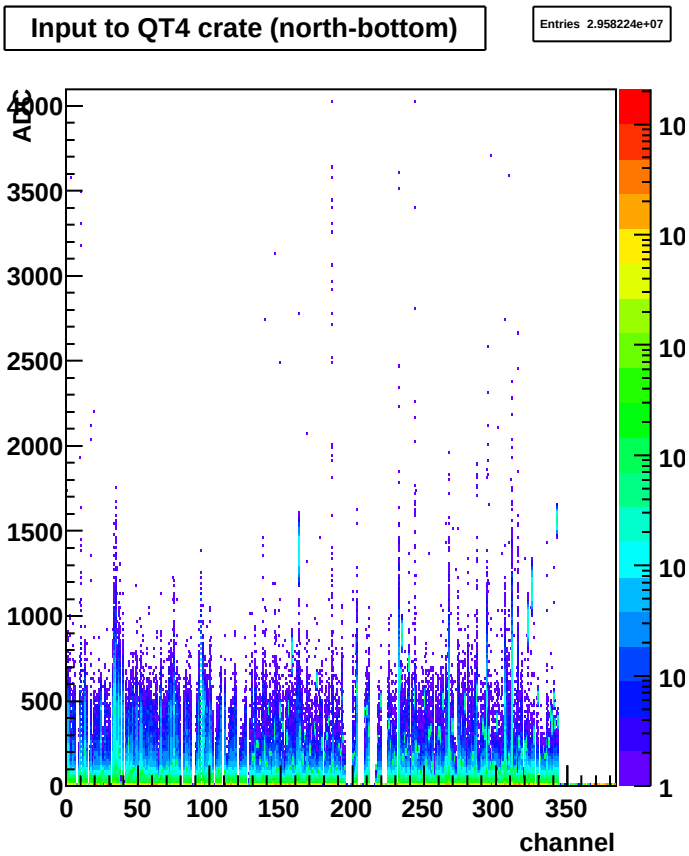

VPD-TAC-W
Entries 1644506
Mean 1438
RMS 159

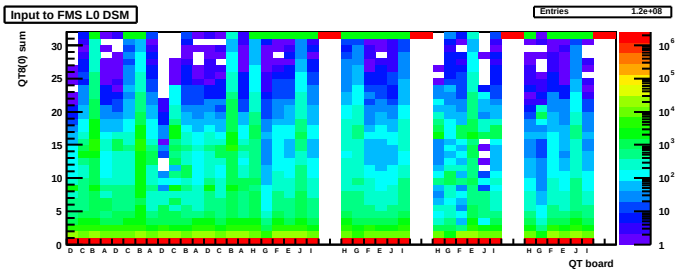

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1337044
Mean 3986
RMS 128.1

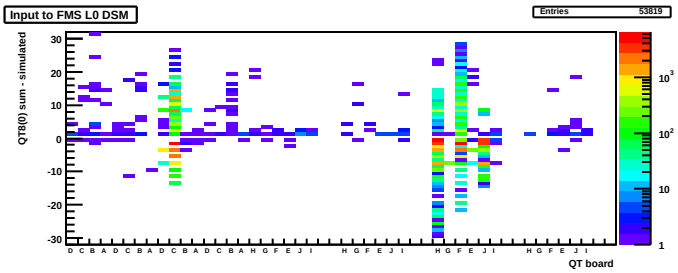

Input to QT1 crate (south-top)
Entries 3.59662e+07
ADC
channel

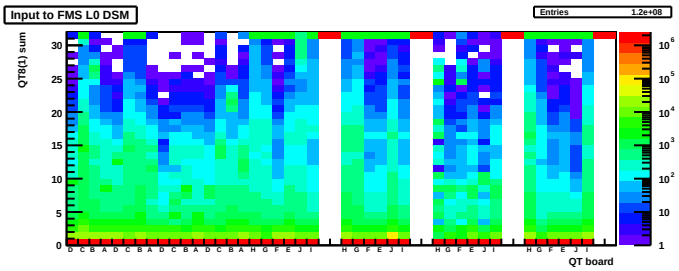

Input to QT3 crate (north-top)
Entries 2.219683e+07
ADC
channel

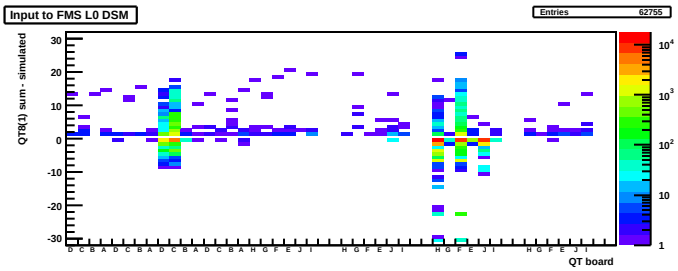

Input to QT2 crate (south-bottom)
Entries 2.30748e+07
ADC
channel

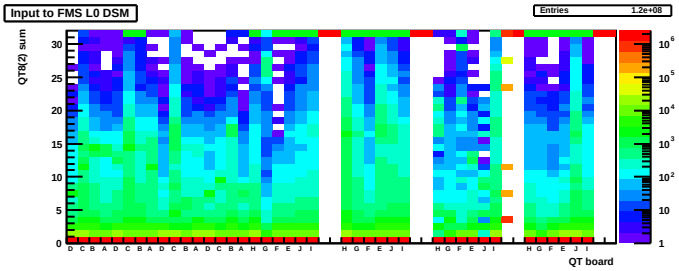

Input to QT4 crate (north-bottom)
Entries 2.958224e+07
ADC
channel

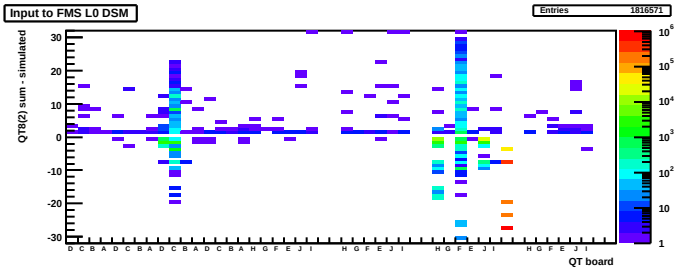

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

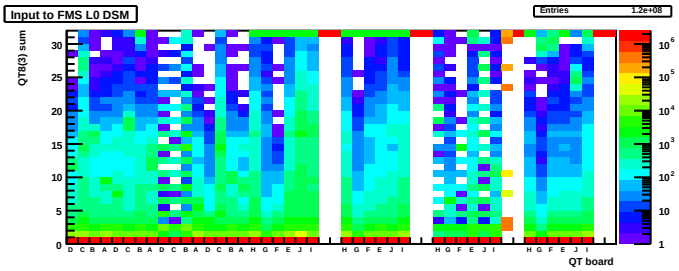

Input to FMS L0 DSM
Entries 53819
QT8(0) sum - simulated
QT board

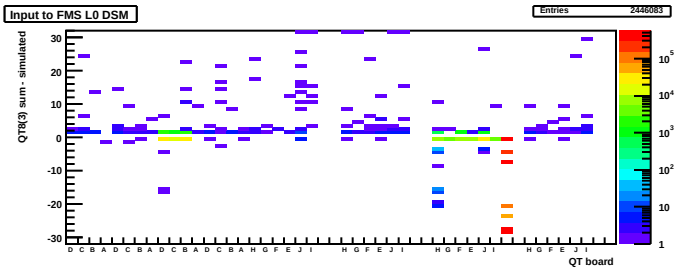

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

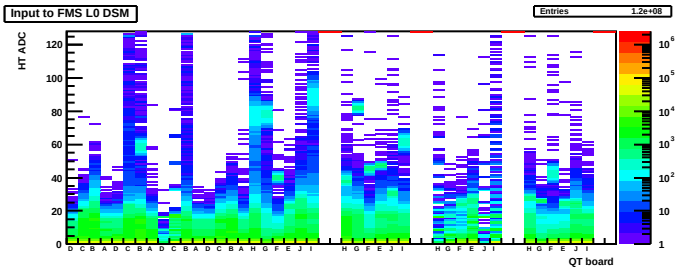

Input to FMS L0 DSM
Entries 62755
QT8(1) sum - simulated
QT board

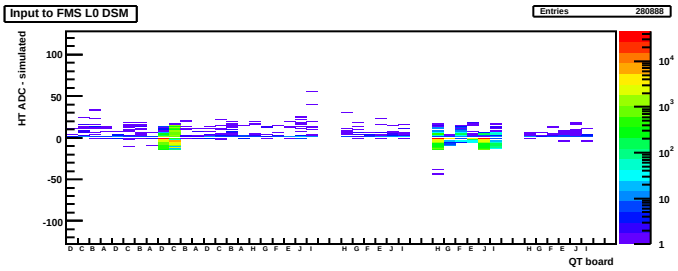

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

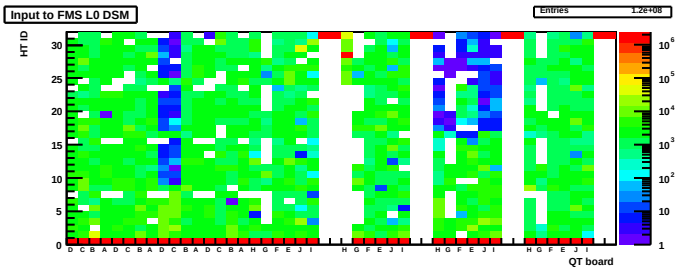

Input to FMS L0 DSM
Entries 1816571
QT8(2) sum - simulated
QT board

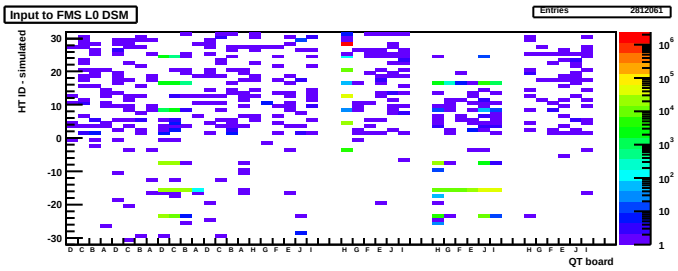

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

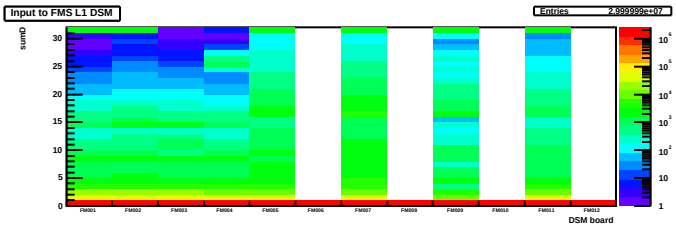

Input to FMS L0 DSM
Entries 2446083
QT8(3) sum - simulated
QT board

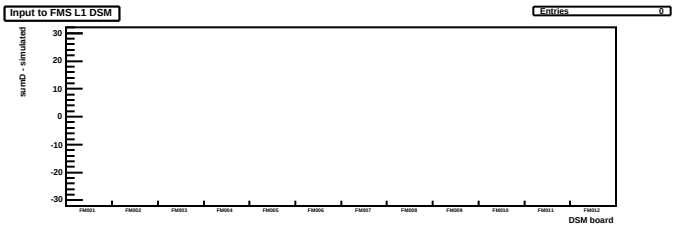

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

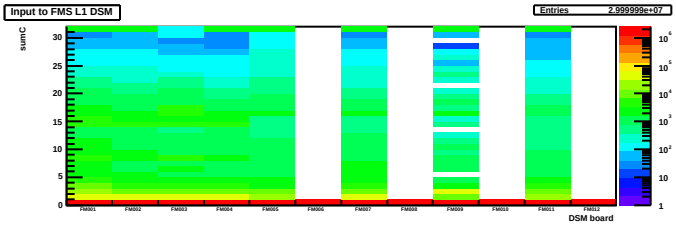

Input to FMS L0 DSM
Entries 280888
HT ADC - simulated
QT board

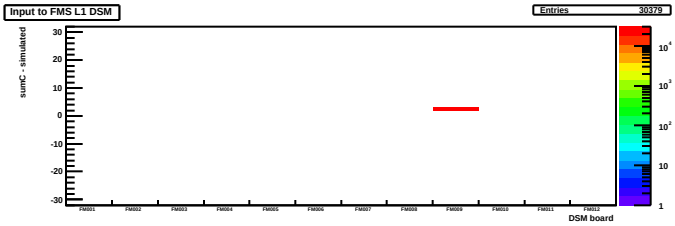

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

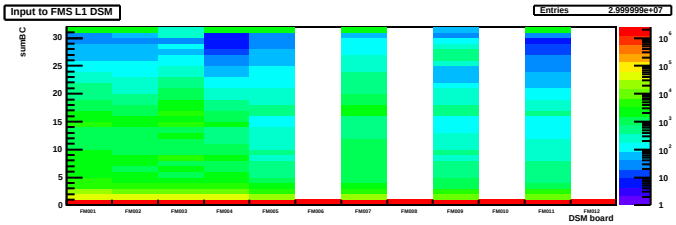

Input to FMS L0 DSM
Entries 2812061
HT ID - simulated
QT board

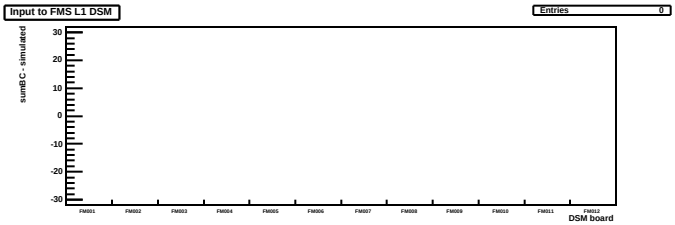

Input to FMS L1 DSM
Entries 2.999999e+07
sumD
DSM board

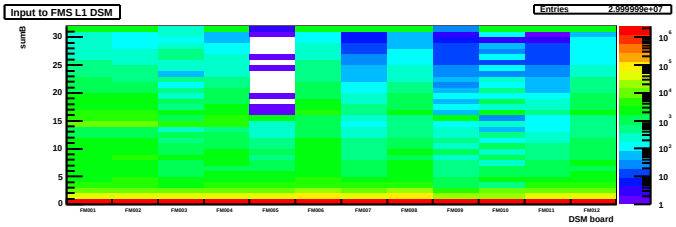

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

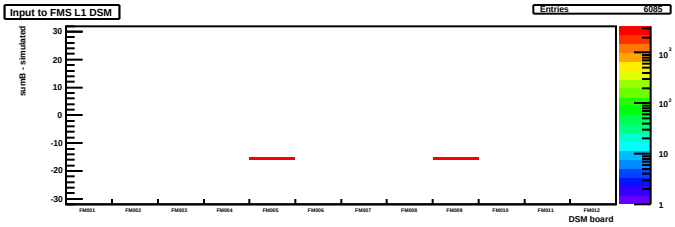

Input to FMS L1 DSM
Entries 2.999999e+07
sumC
DSM board

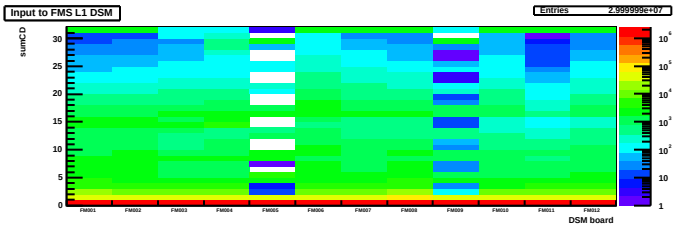

Input to FMS L1 DSM
Entries 30379
sumC - simulated
DSM board

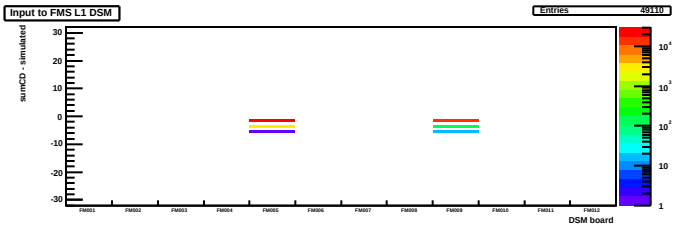

Input to FMS L1 DSM
Entries 2.999999e+07
sumBC
DSM board

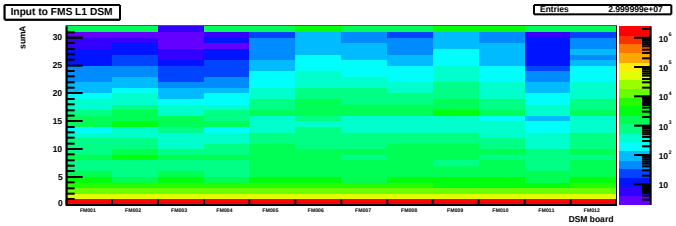

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

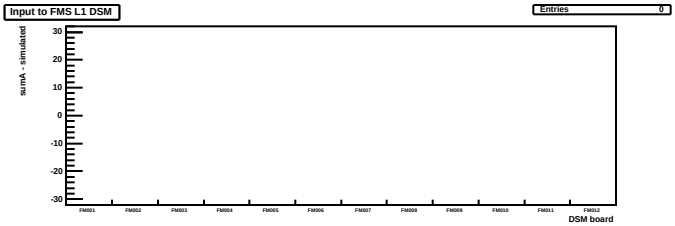

Input to FMS L1 DSM
Entries 2.999999e+07
sumB
DSM board

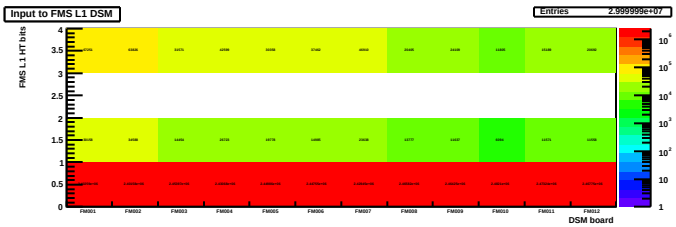

Input to FMS L1 DSM
Entries 6085
sumB - simulated
DSM board

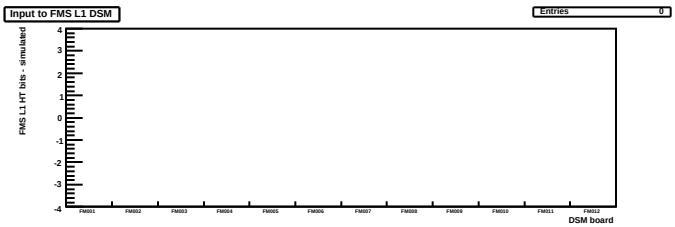

Input to FMS L1 DSM
Entries 2.999999e+07
sumCD
DSM board

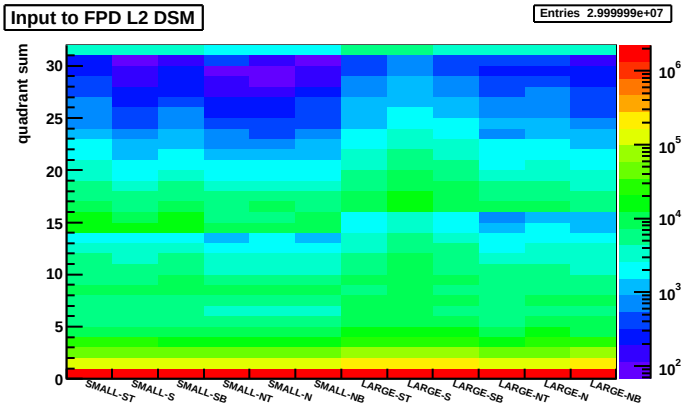

Input to FMS L1 DSM
Entries 49110
sumCD - simulated
DSM board

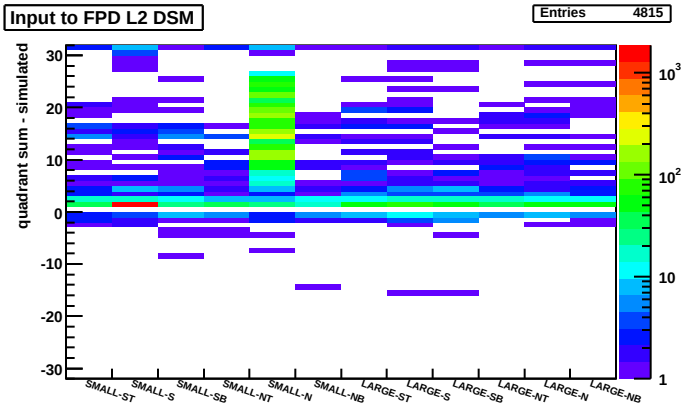

Input to FMS L1 DSM
Entries 2.999999e+07
sumA
DSM board

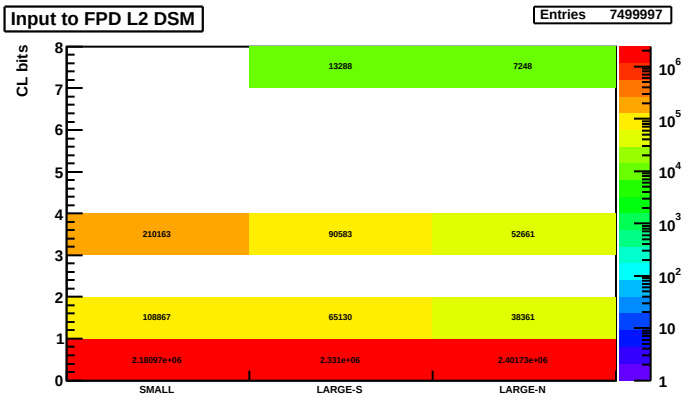

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

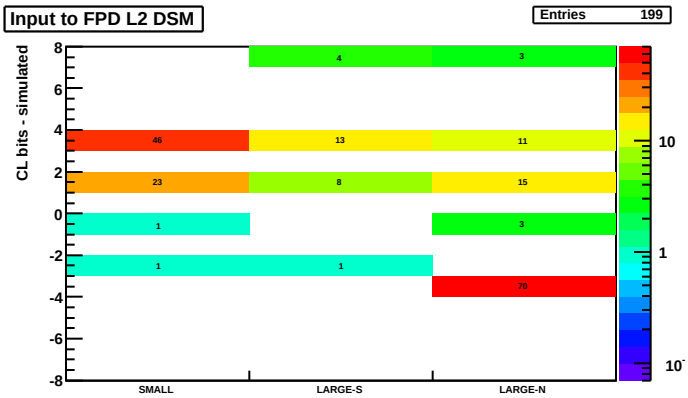

Input to FMS L1 DSM
Entries 2.999999e+07
FMS L1 HT bits
DSM board

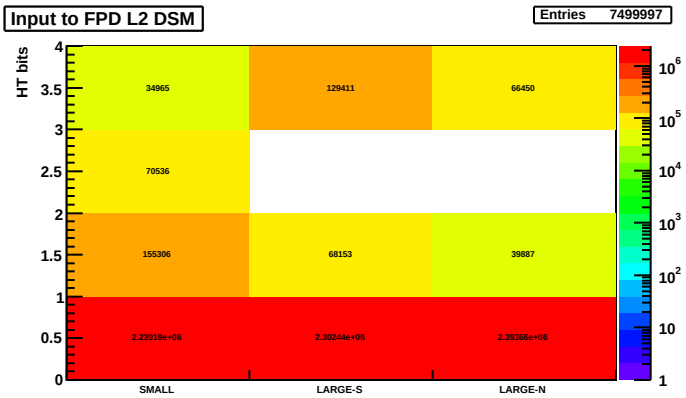

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

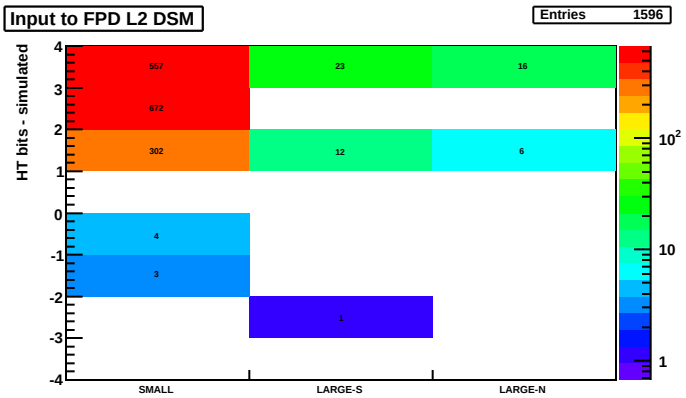

Input to FPD L2 DSM
Entries 2.999999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

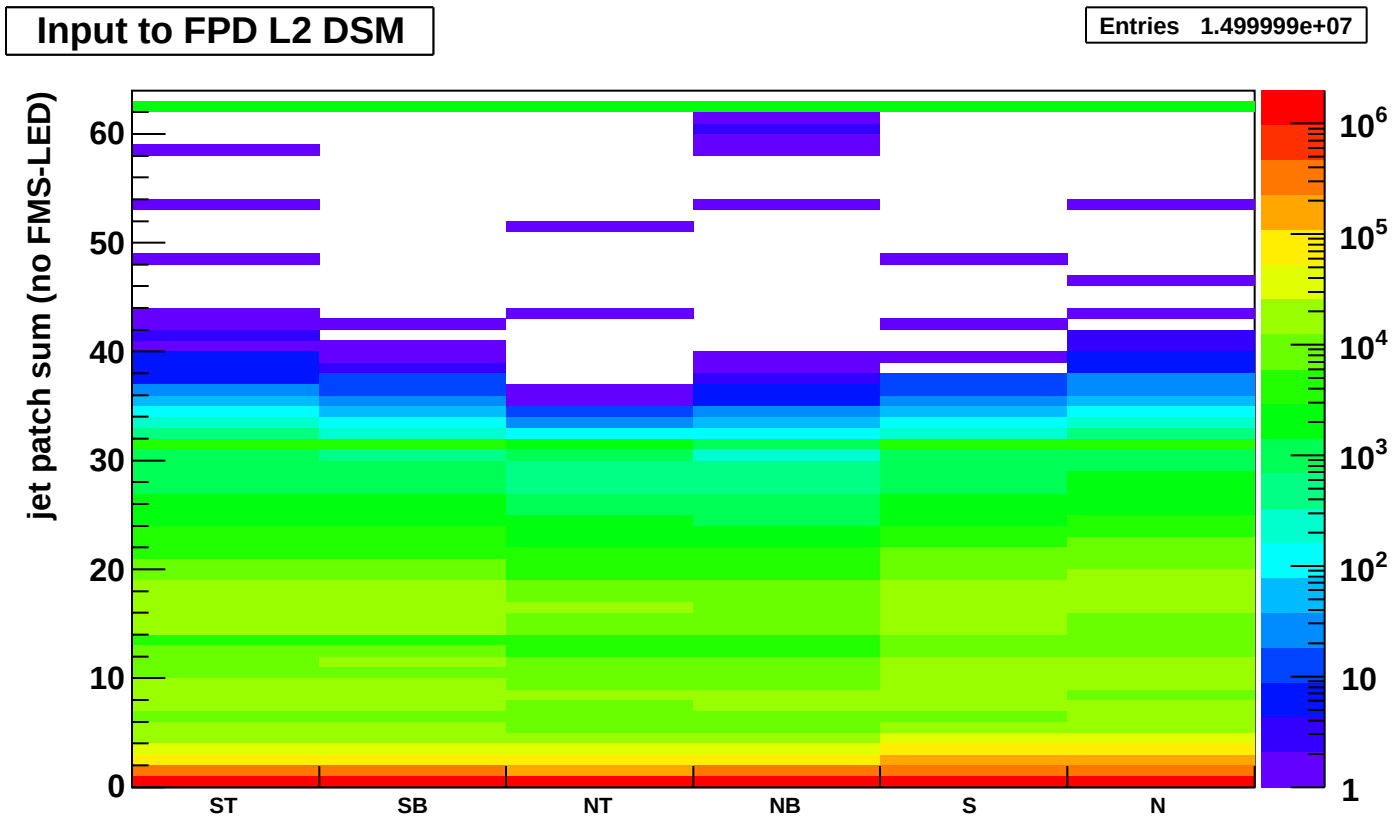

Input to FPD L2 DSM
Entries 4815
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

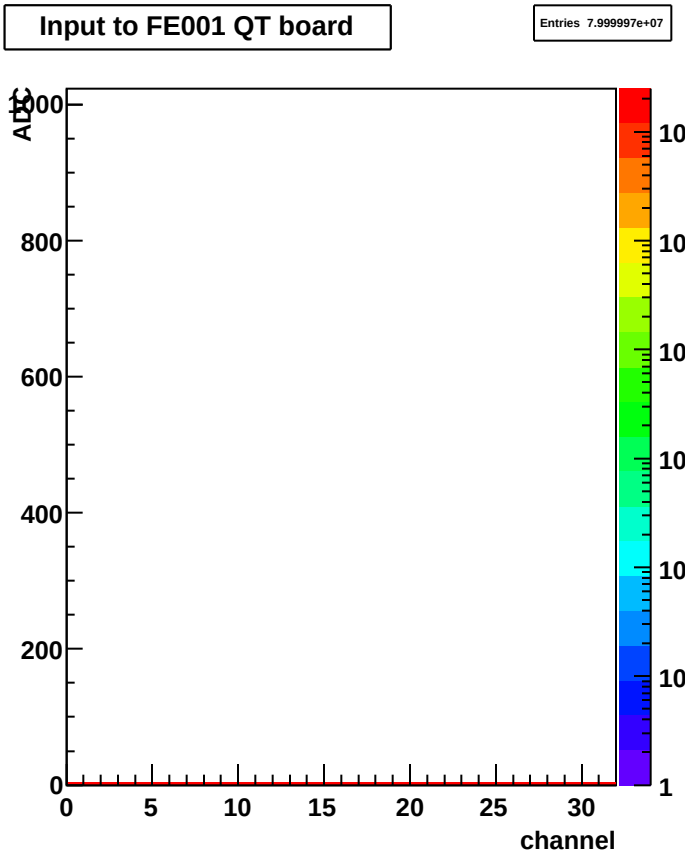

Input to FPD L2 DSM
Entries 7499997
CL bits
13288
7248
210163
90583
52661
108867
65130
38361
2.18097e+06
2.331e+06
2.40173e+06
SMALL
LARGE-S
LARGE-N

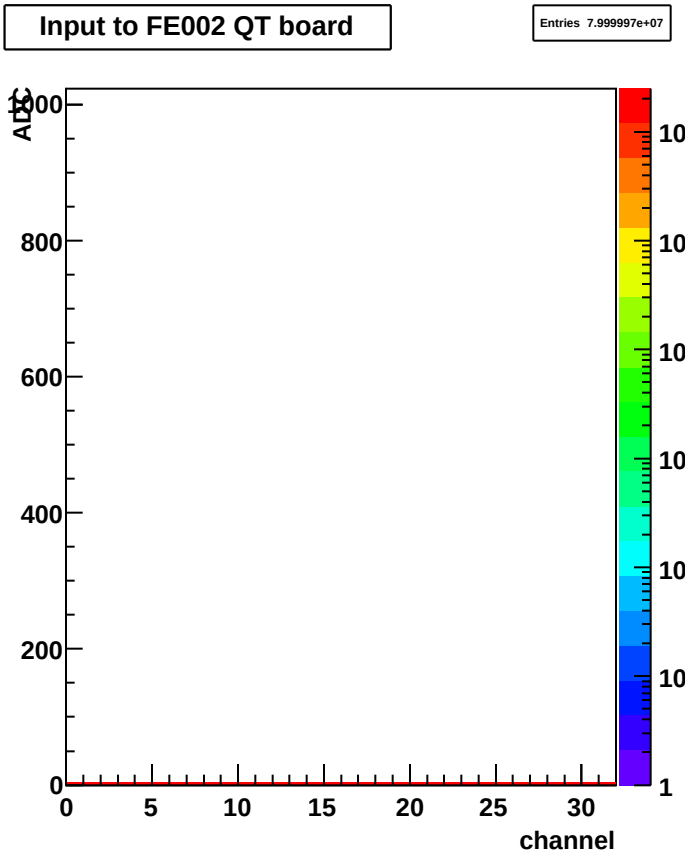

Input to FPD L2 DSM
Entries 199
CL bits - simulated
4
3
46
13
11
23
8
15
1
3
1
1
70
SMALL
LARGE-S
LARGE-N

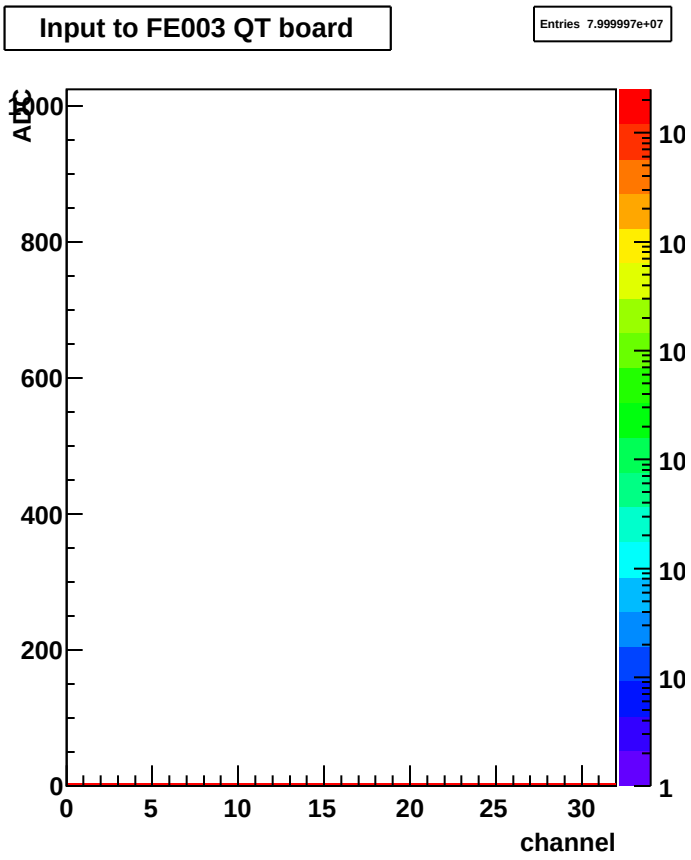

Input to FPD L2 DSM
Entries 7499997
HT bits
34965
129411
66450
70536
155306
68153
39887
2.23919e+06
2.30244e+06
2.39366e+06
SMALL
LARGE-S
LARGE-N

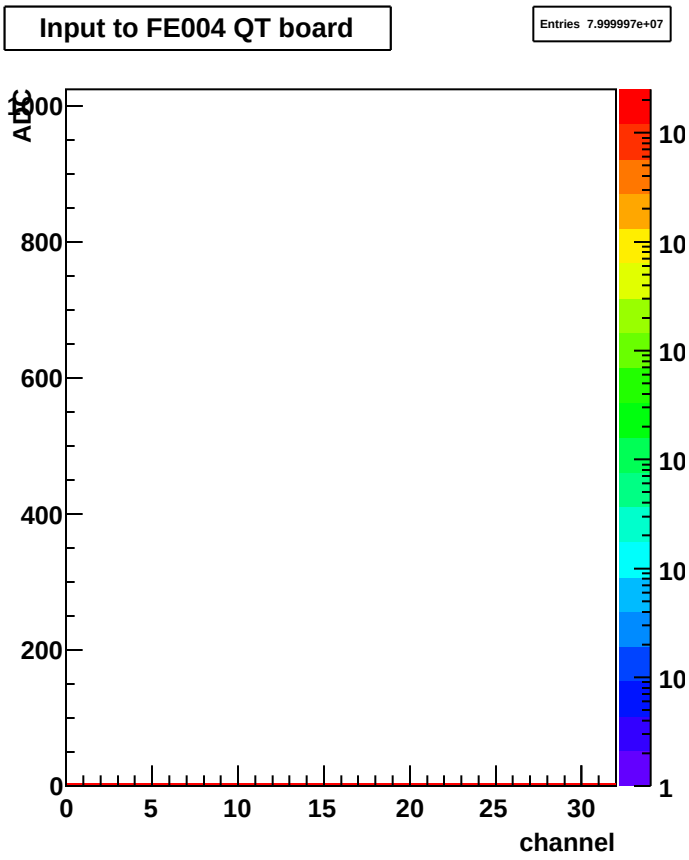

Input to FPD L2 DSM
Entries 1596
HT bits - simulated
557
23
16
672
302
12
6
4
3
1
SMALL
LARGE-S
LARGE-N

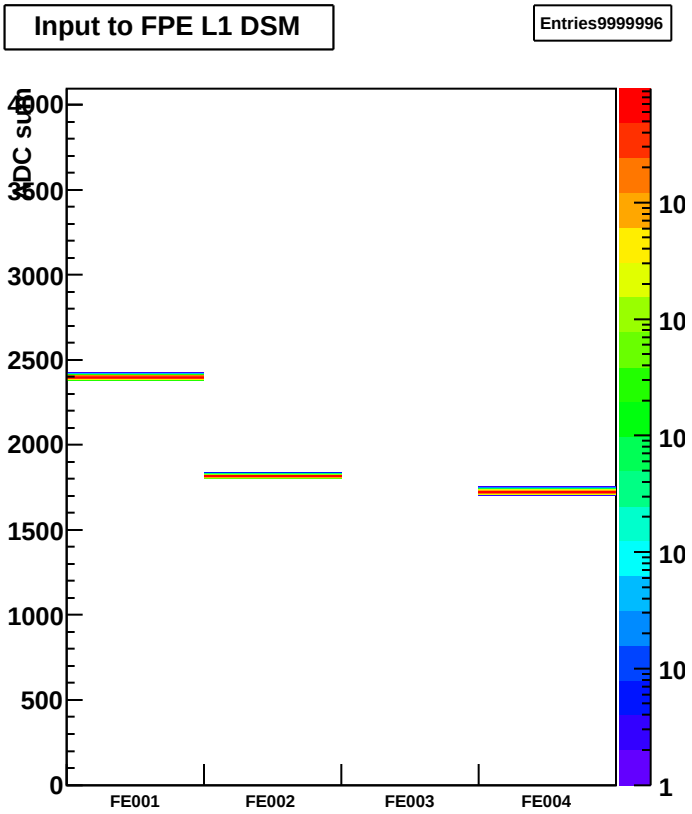

Input to FPD L2 DSM
Entries 1.499999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.999997e+07
ADC
channel

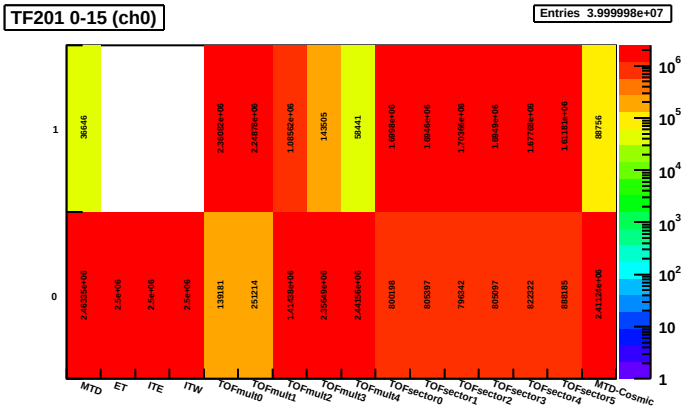

Input to FE002 QT board
Entries 7.999997e+07
ADC
channel


Input to FE003 QT board
Entries 7.999997e+07
ADC
channel


Input to FE004 QT board
Entries 7.999997e+07
ADC
channel


Input to FPE L1 DSM
Entries9999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

TF201 0-15 (ch0)



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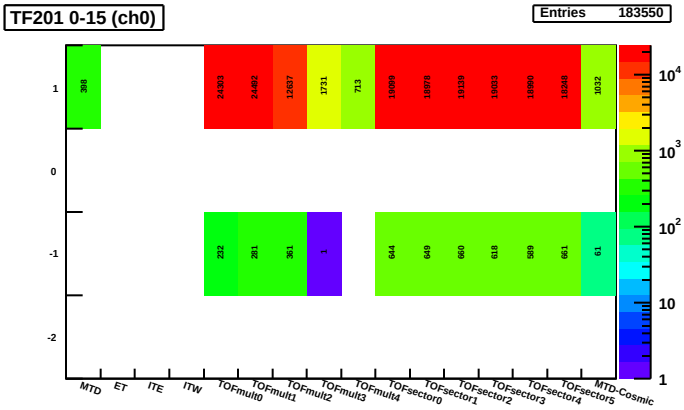

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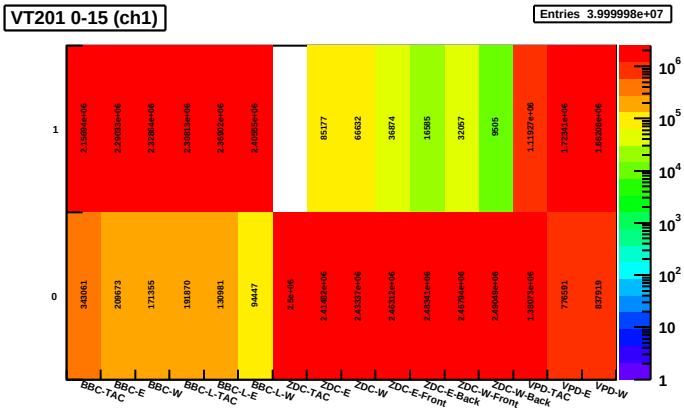

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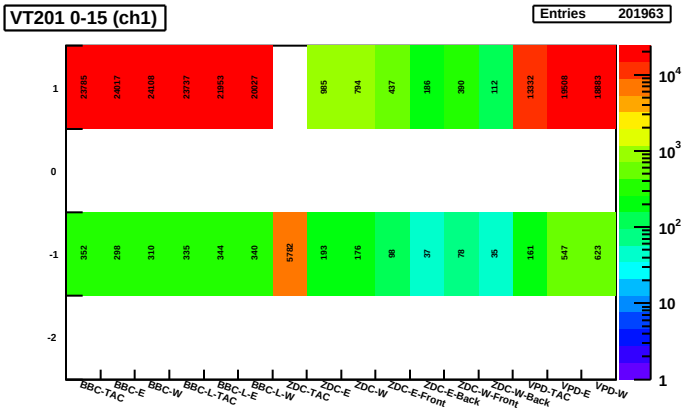

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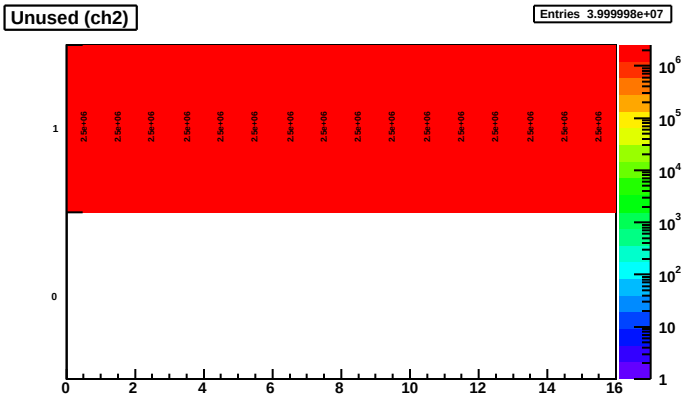

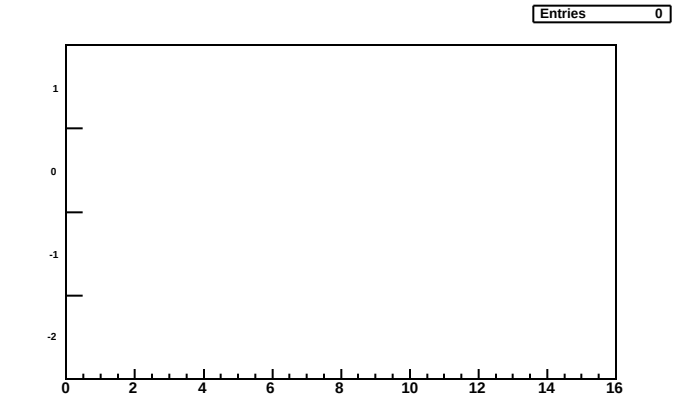

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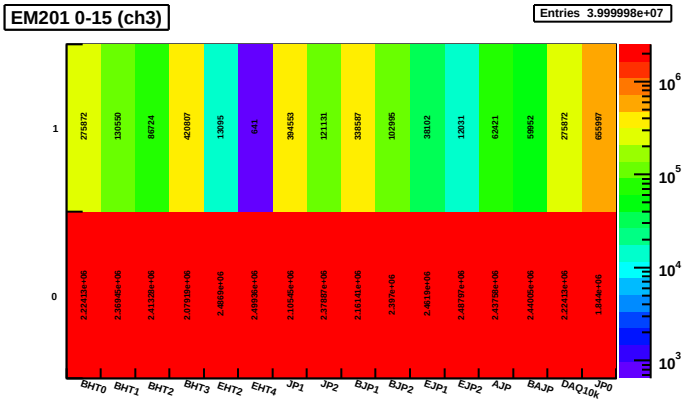

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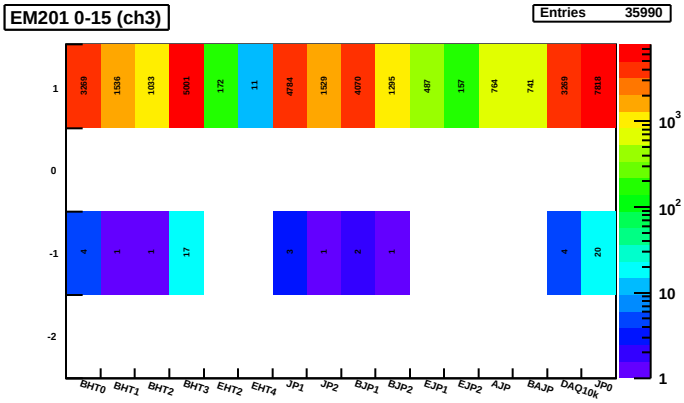
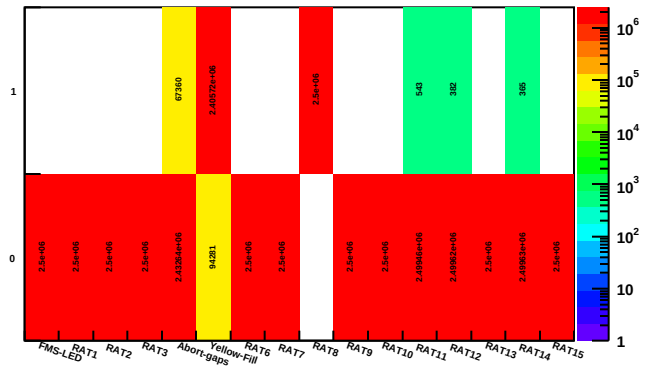
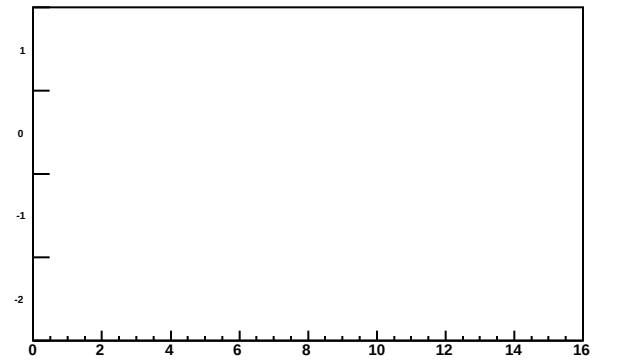


RAT board (ch4)





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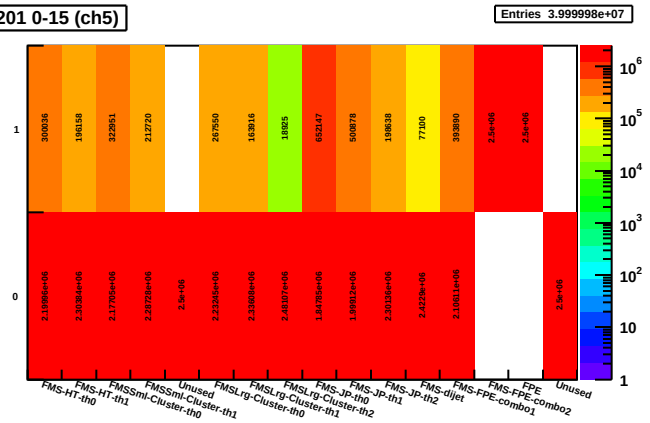

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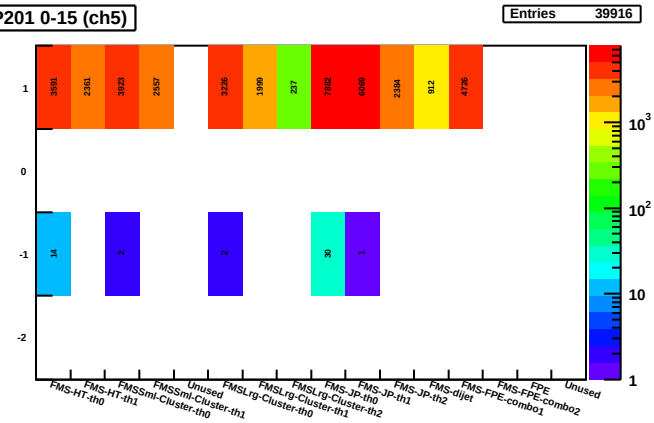

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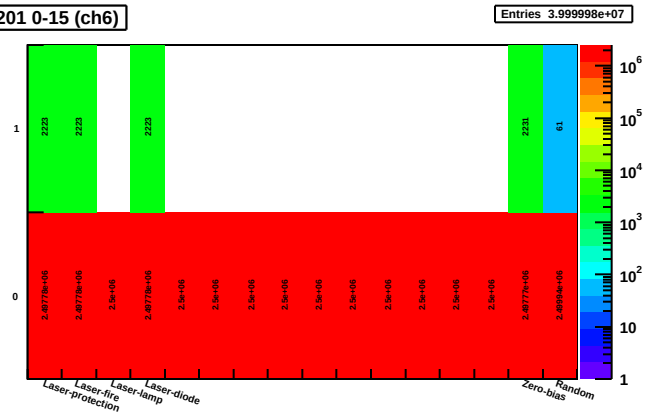

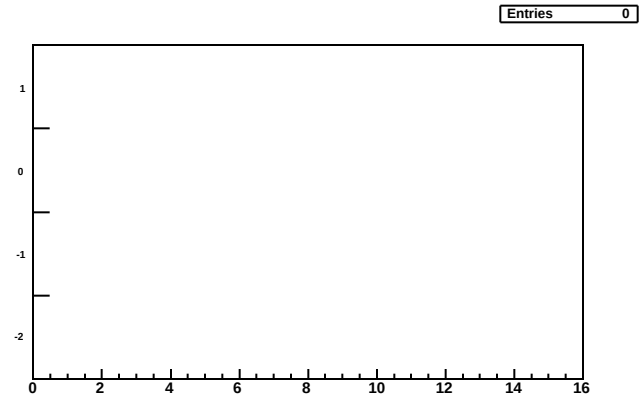
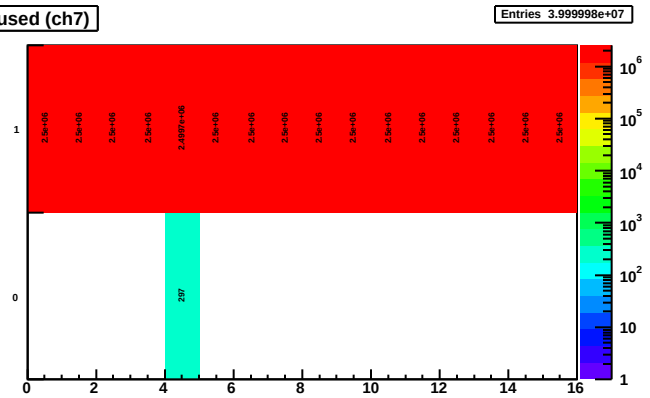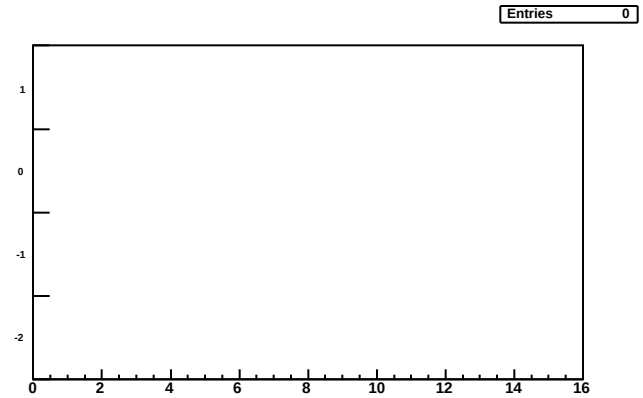
Unused (ch7)