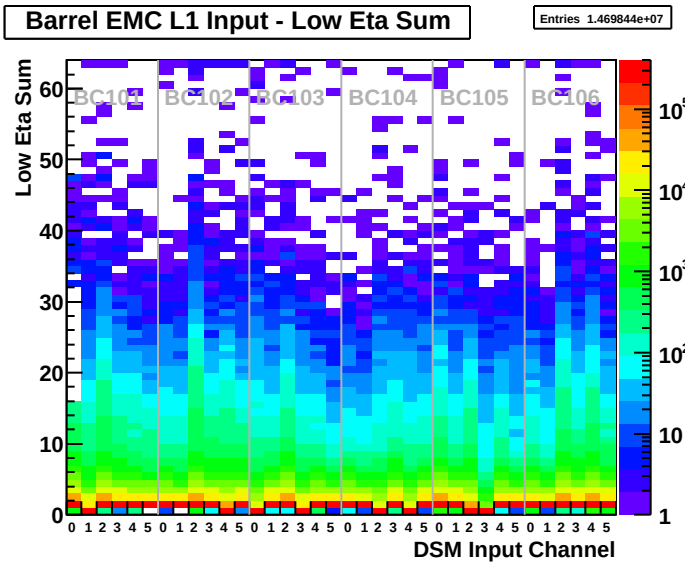
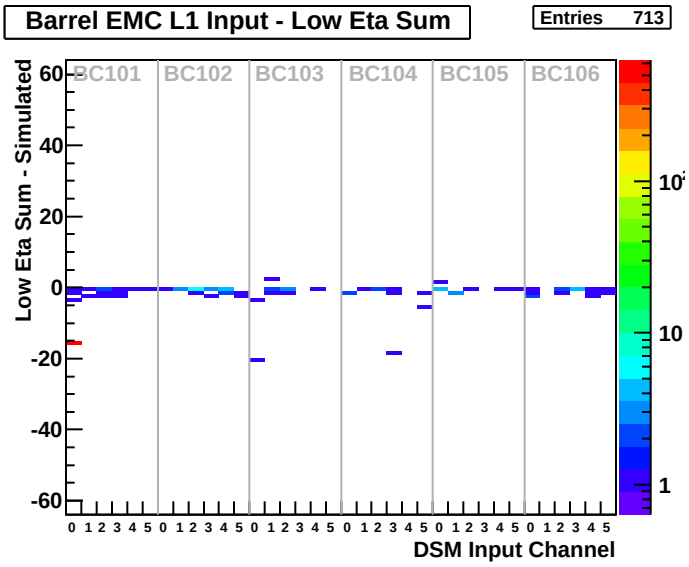
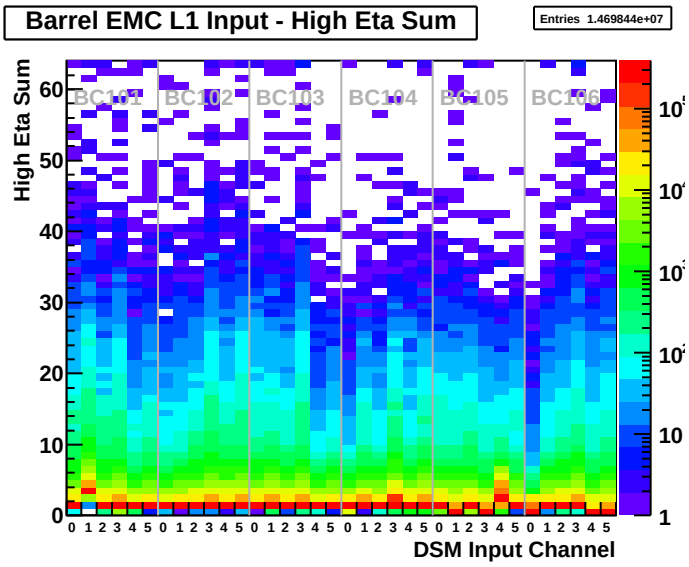
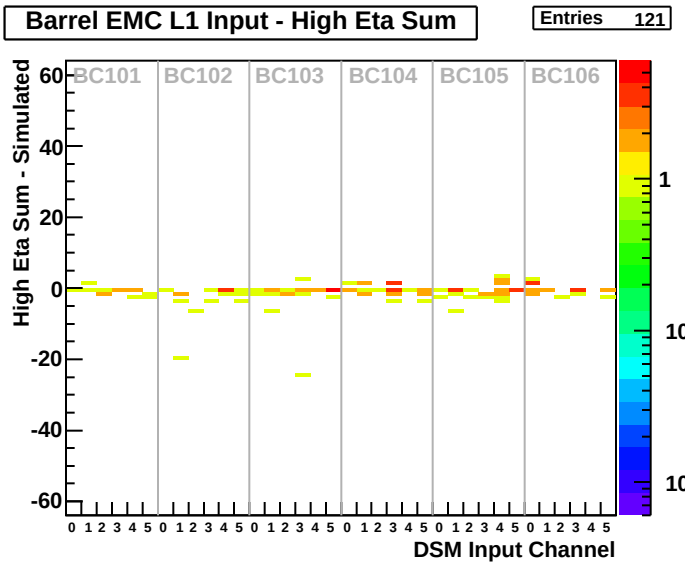
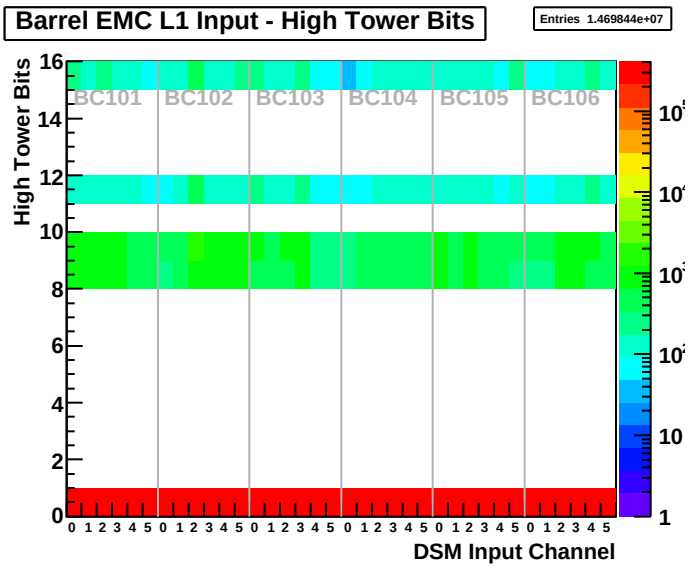
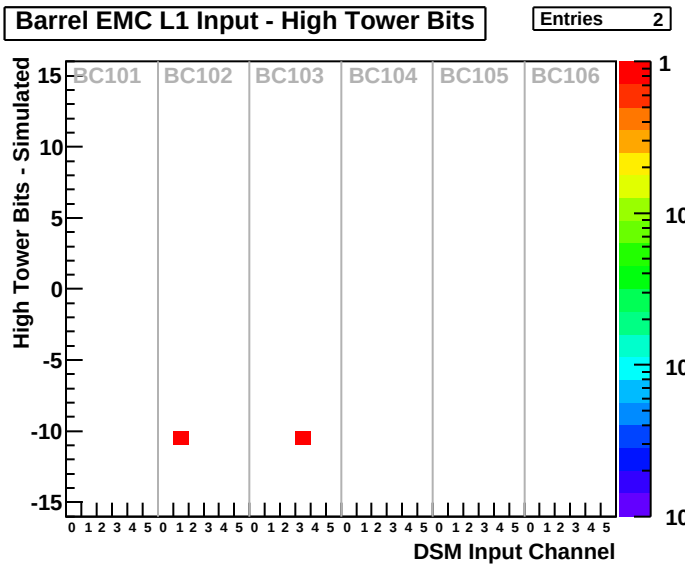

Barrel EMC L1 Input - Low Eta Sum
Entries 1.469844e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

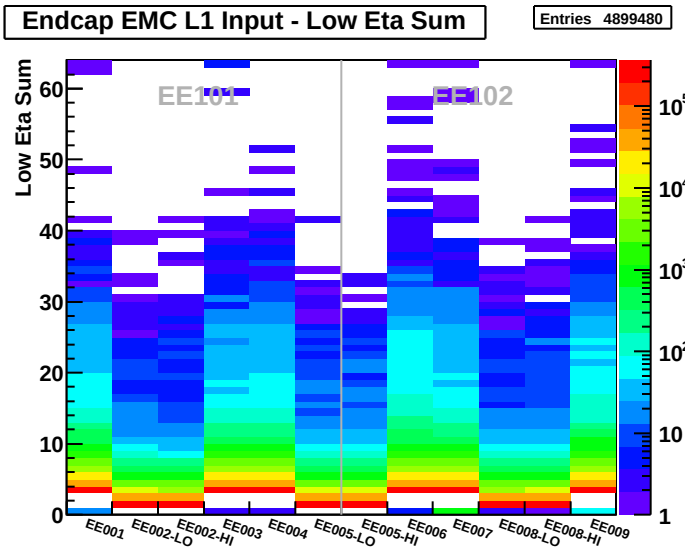

Barrel EMC L1 Input - Low Eta Sum
Entries 713
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

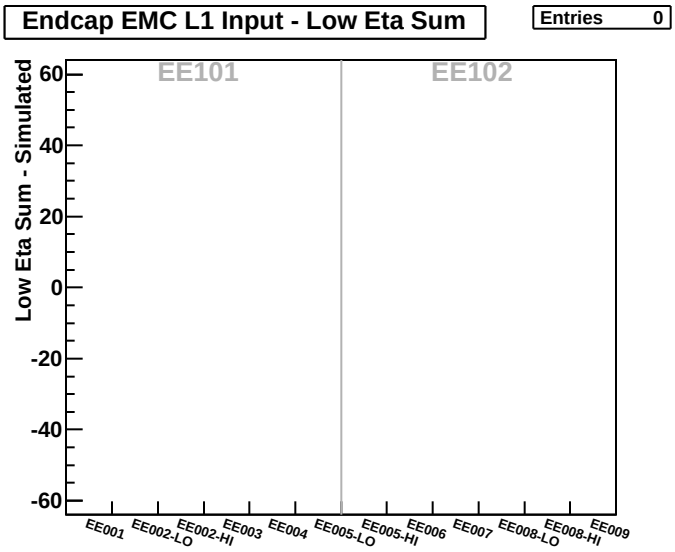

Barrel EMC L1 Input - High Eta Sum
Entries 1.469844e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

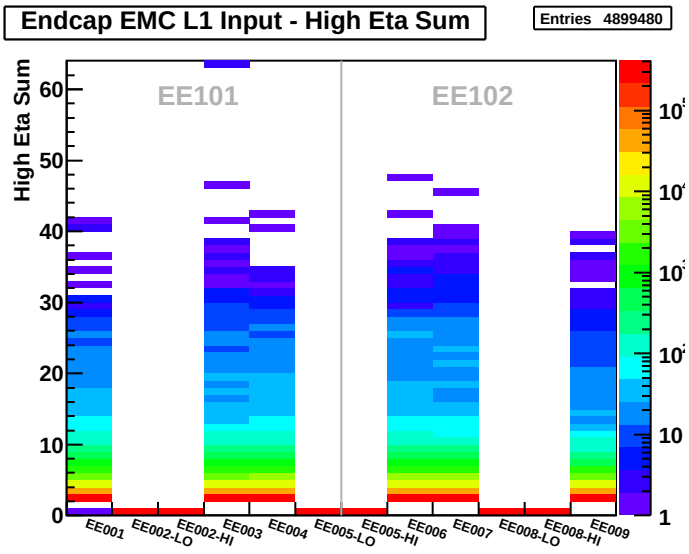

Barrel EMC L1 Input - High Eta Sum
Entries 121
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

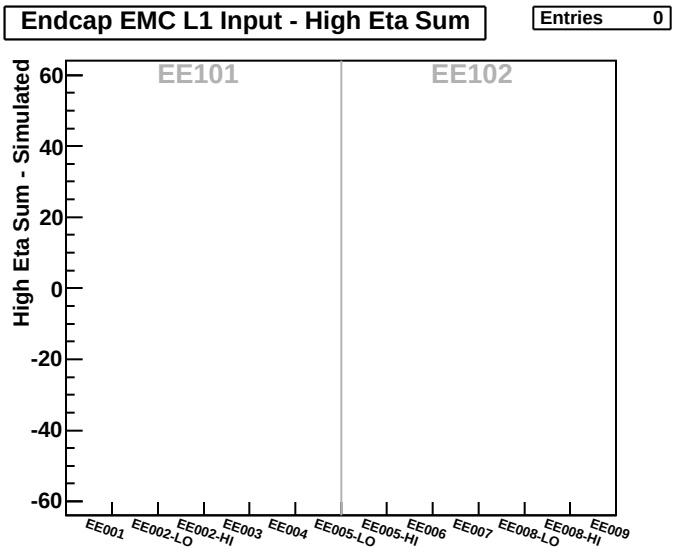

Barrel EMC L1 Input - High Tower Bits
Entries 1.469844e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

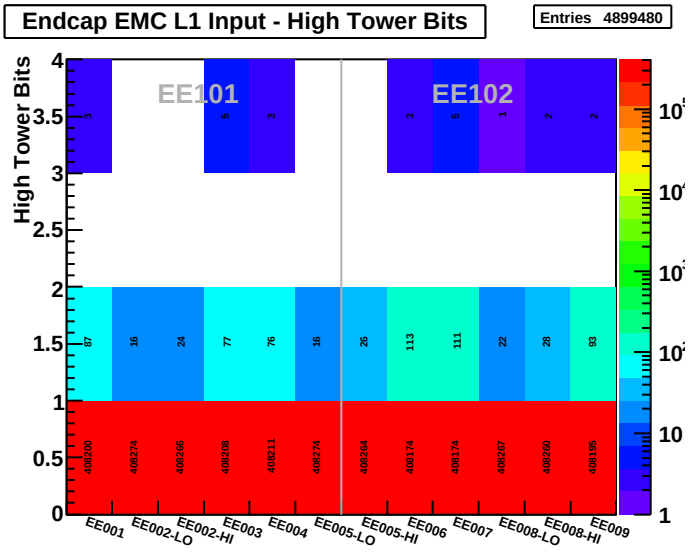

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

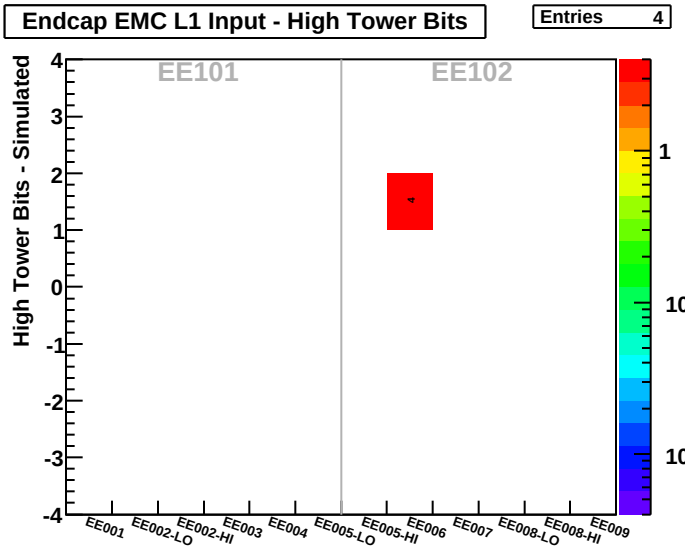

Endcap EMC L1 Input - Low Eta Sum
Entries 4899480
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

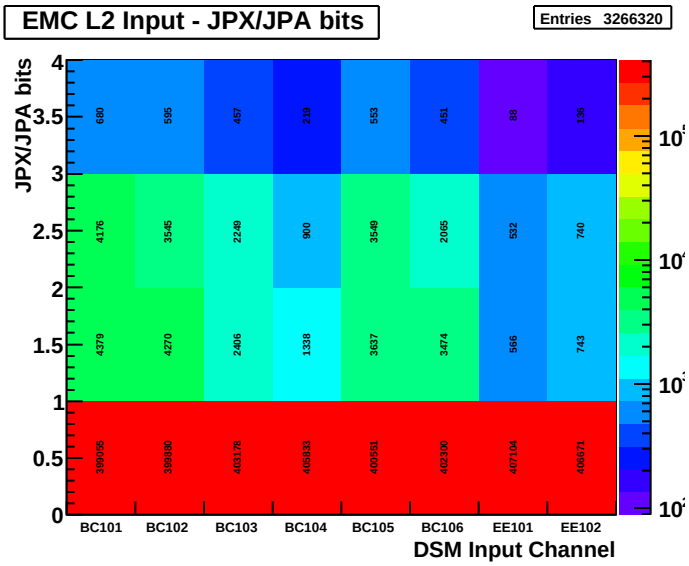

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

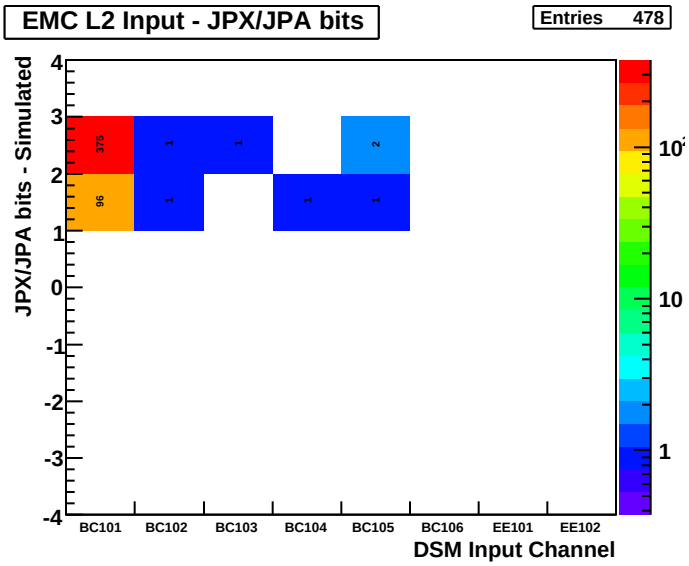

Endcap EMC L1 Input - High Eta Sum
Entries 4899480
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

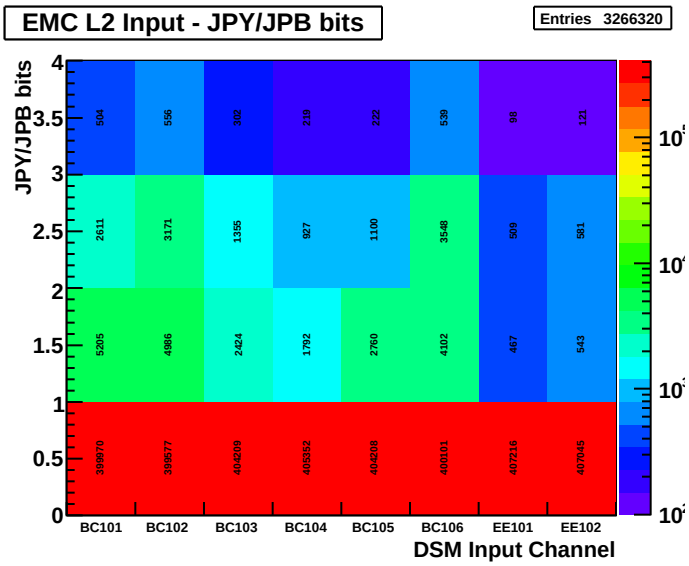

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

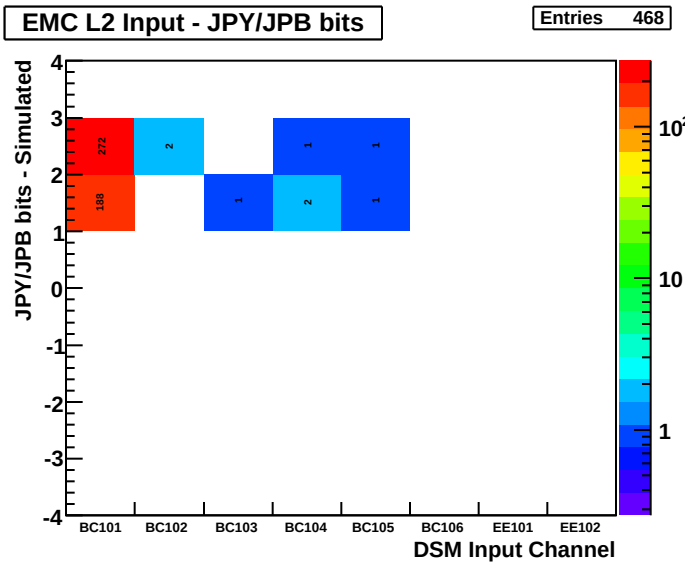

Endcap EMC L1 Input - High Tower Bits
Entries 4899480
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

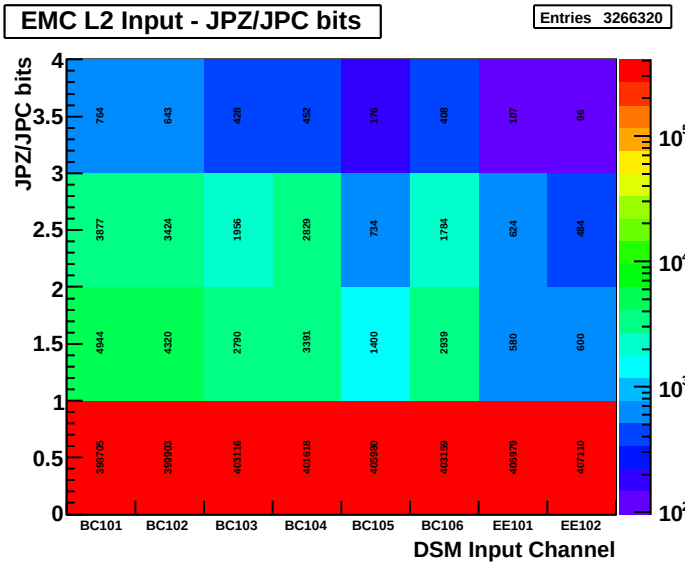

Endcap EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

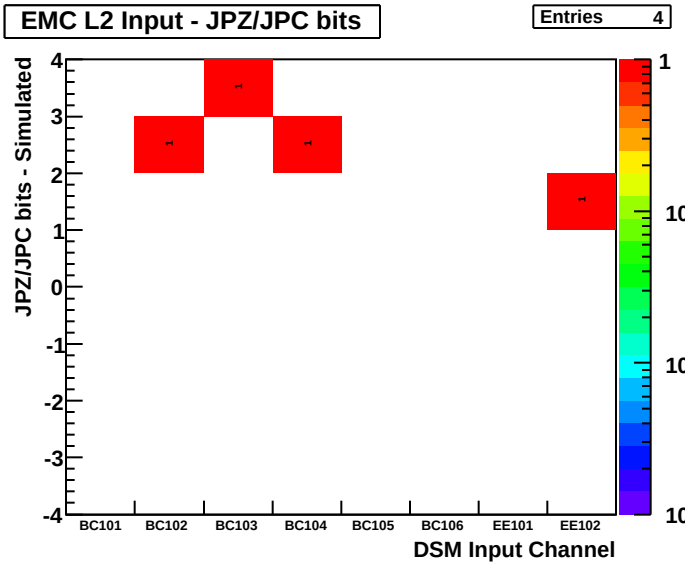

EMC L2 Input - JPX/JPA bits
Entries 3266320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
680 595 457 219 553 451 88 136
4176 3545 2249 900 3549 2065 532 740
4379 4270 2406 1338 3637 3474 566 743
399055 399800 403178 405833 400551 402300 407104 406671

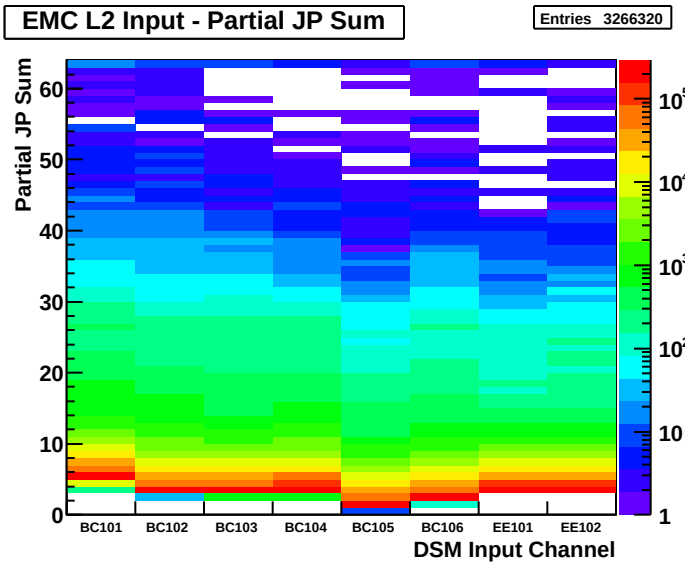

EMC L2 Input - JPX/JPA bits
Entries 478
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
375 1 1 2
96 1 1 1

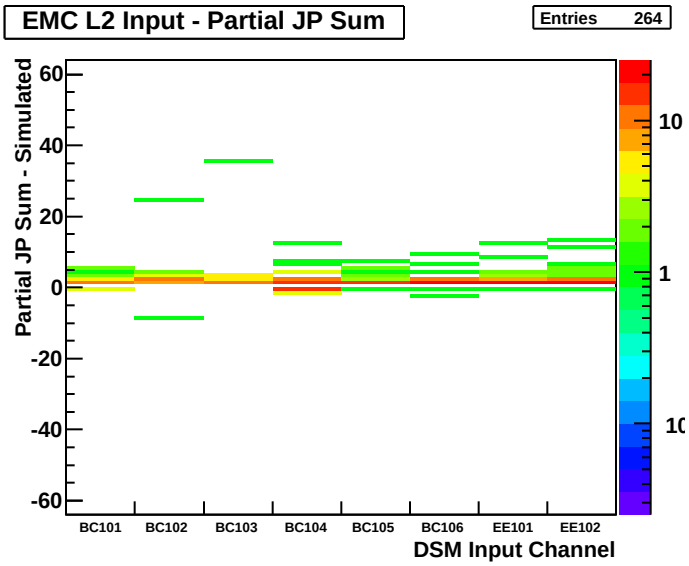

EMC L2 Input - JPY/JPB bits
Entries 3266320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
504 556 302 219 222 539 98 121
2611 3171 1355 927 1100 3548 509 581
5205 4986 2424 1792 2760 4102 467 543
399970 399577 404209 405352 404208 400101 407216 407045

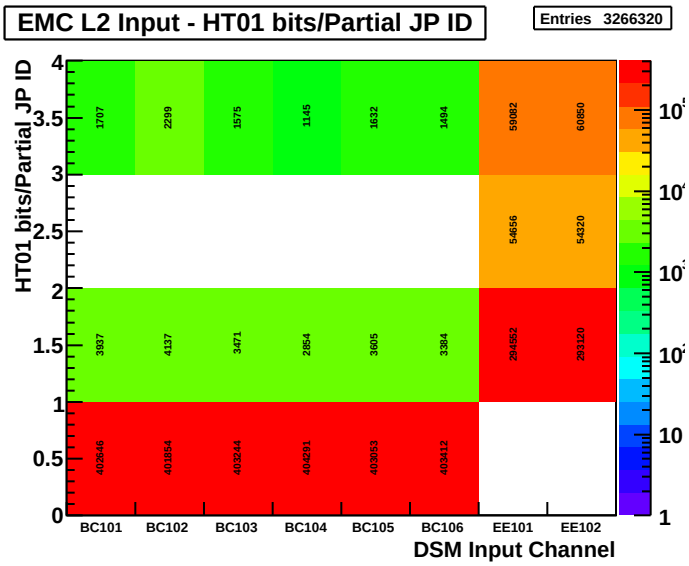

EMC L2 Input - JPY/JPB bits
Entries 468
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
272 2 1 1
188 1 2 1

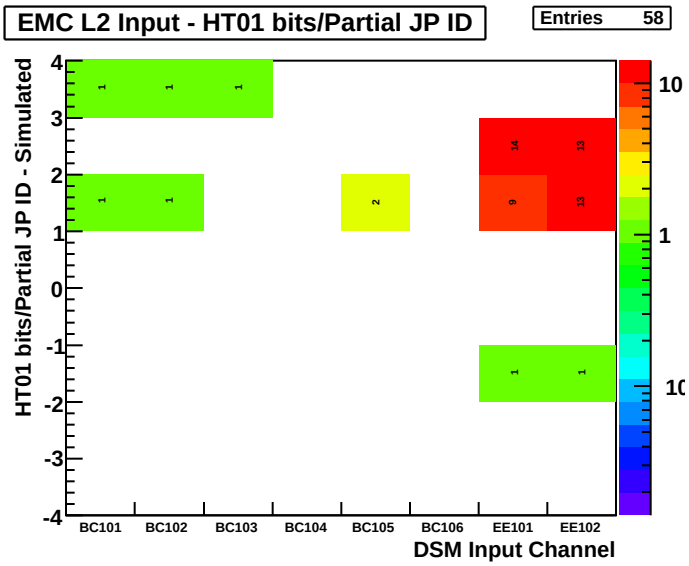

EMC L2 Input - JPZ/JPC bits
Entries 3266320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
764 643 428 452 176 408 107 96
3877 3424 1956 2829 734 1784 624 484
4944 4320 2790 3391 1400 2939 580 600
398705 399303 403116 401618 405980 403159 406979 407110

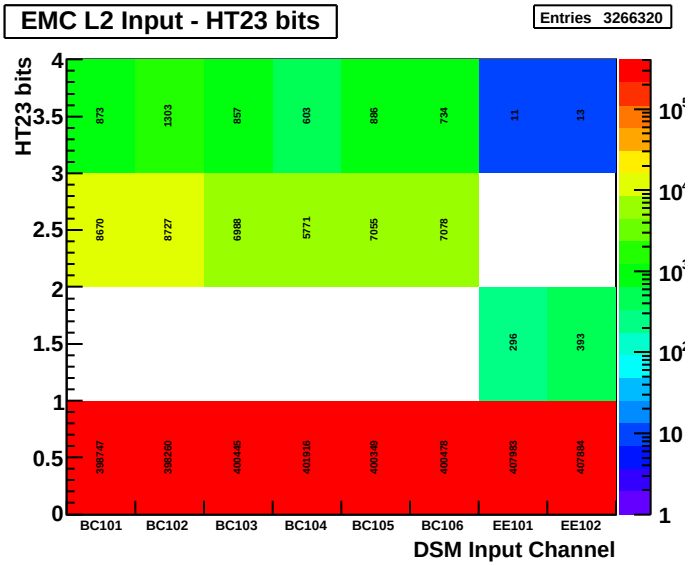

EMC L2 Input - JPZ/JPC bits
Entries 4
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1 1
1

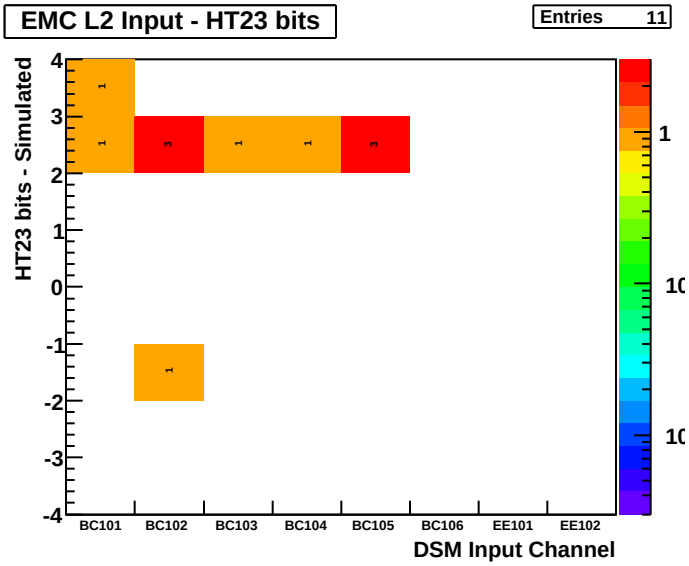

EMC L2 Input - Partial JP Sum
Entries 3266320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

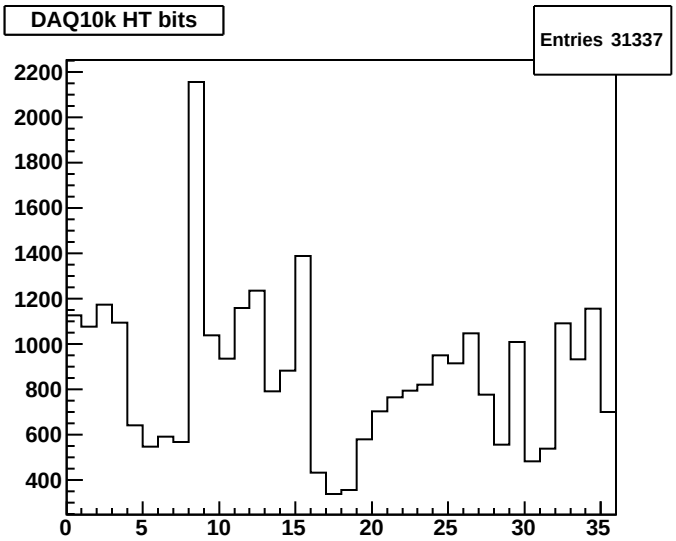

EMC L2 Input - Partial JP Sum
Entries 264
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

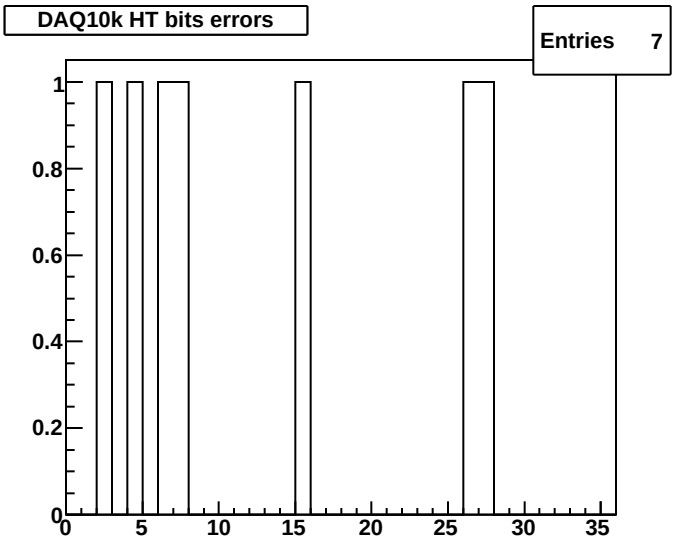

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3266320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

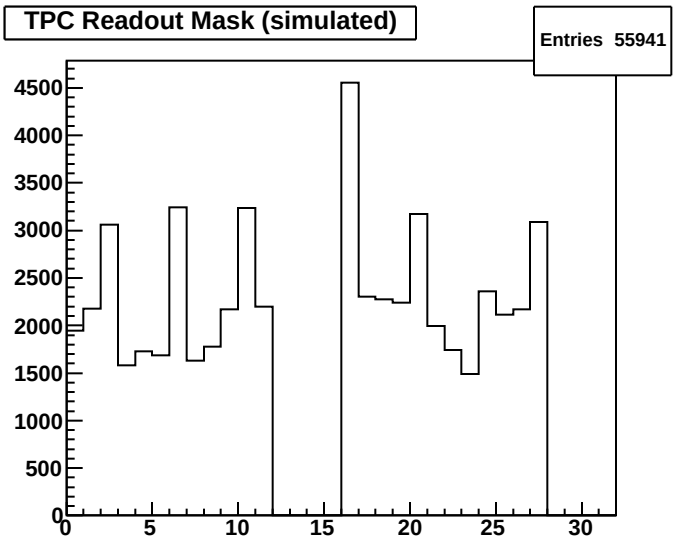

EMC L2 Input - HT01 bits/Partial JP ID
Entries 58
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

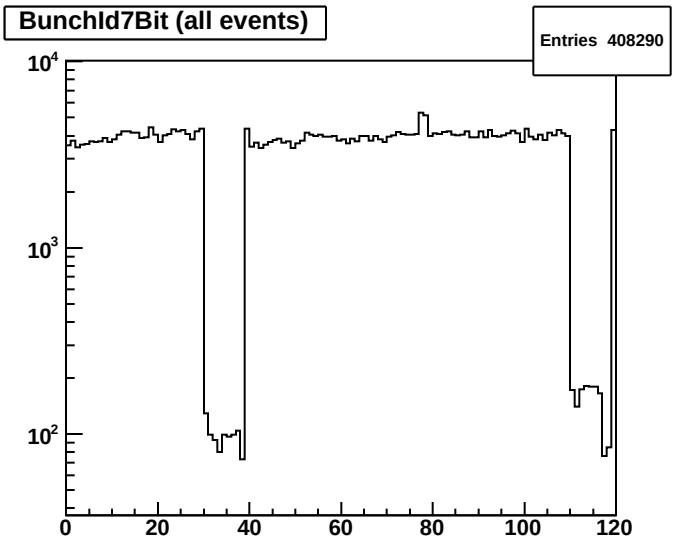

EMC L2 Input - HT23 bits
Entries 3266320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

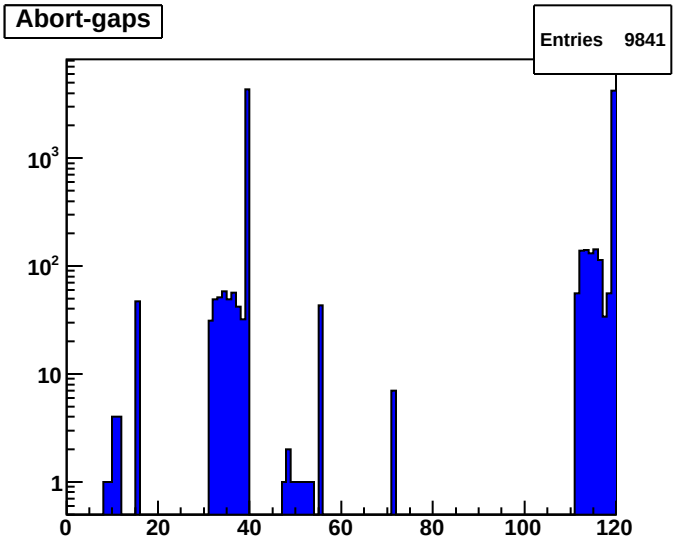

EMC L2 Input - HT23 bits
Entries 11
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

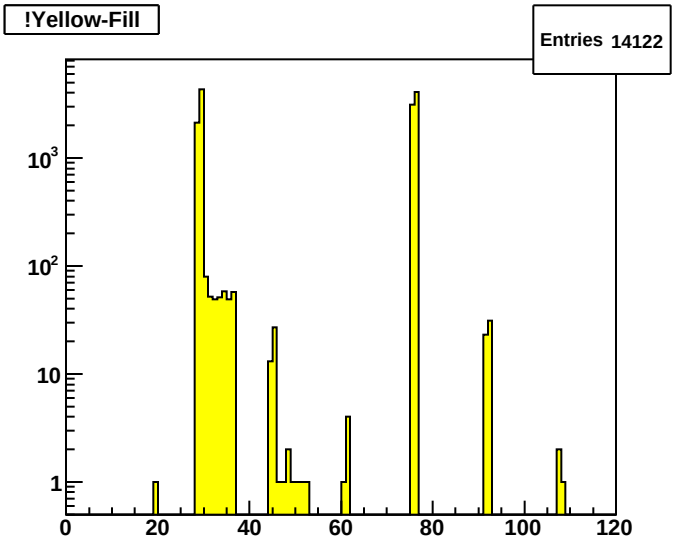

DAQ10k HT bits
Entries 31337

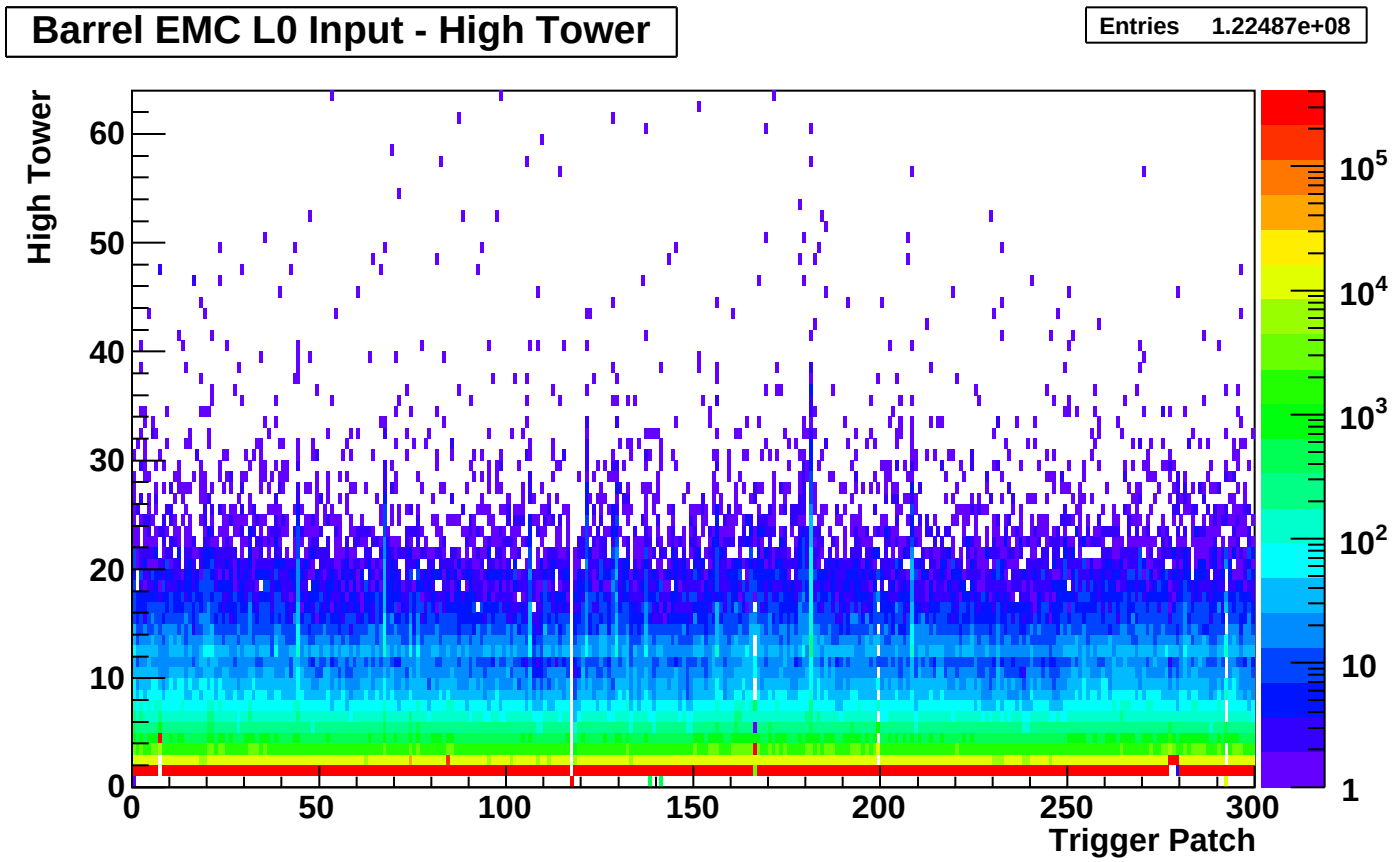

DAQ10k HT bits errors
Entries 7

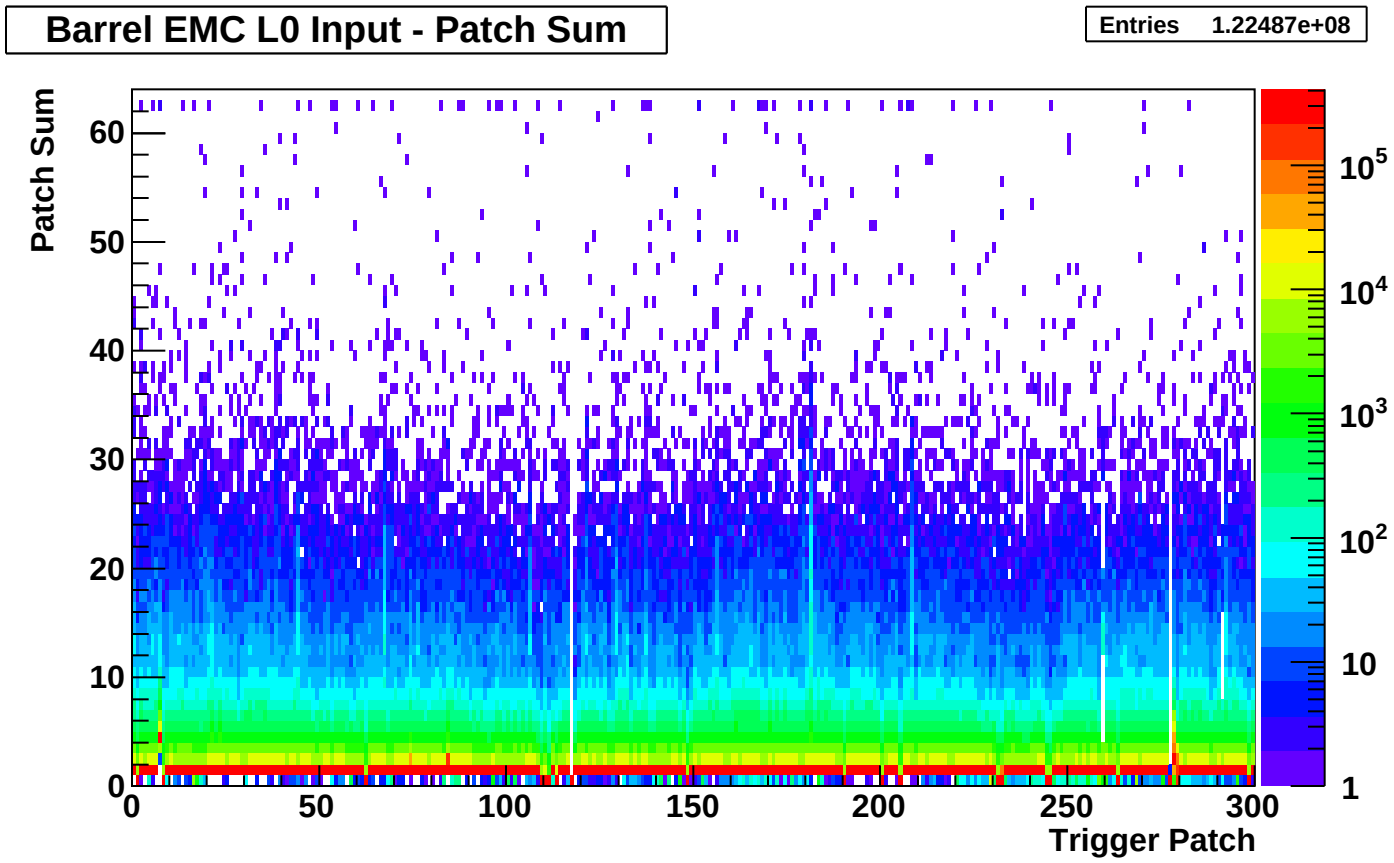

TPC Readout Mask (simulated)
Entries 55941

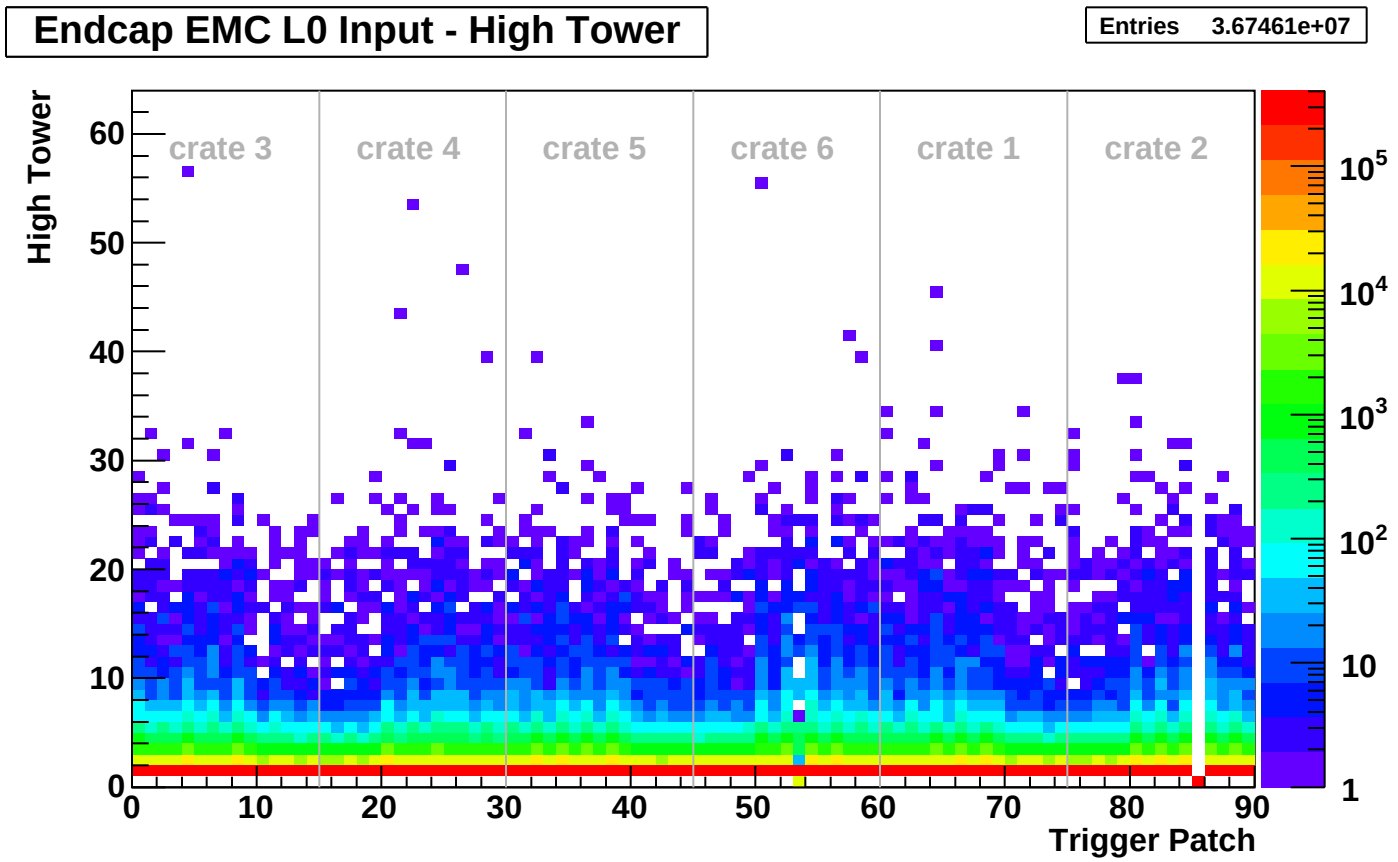

BunchId7Bit (all events)
Entries 408290

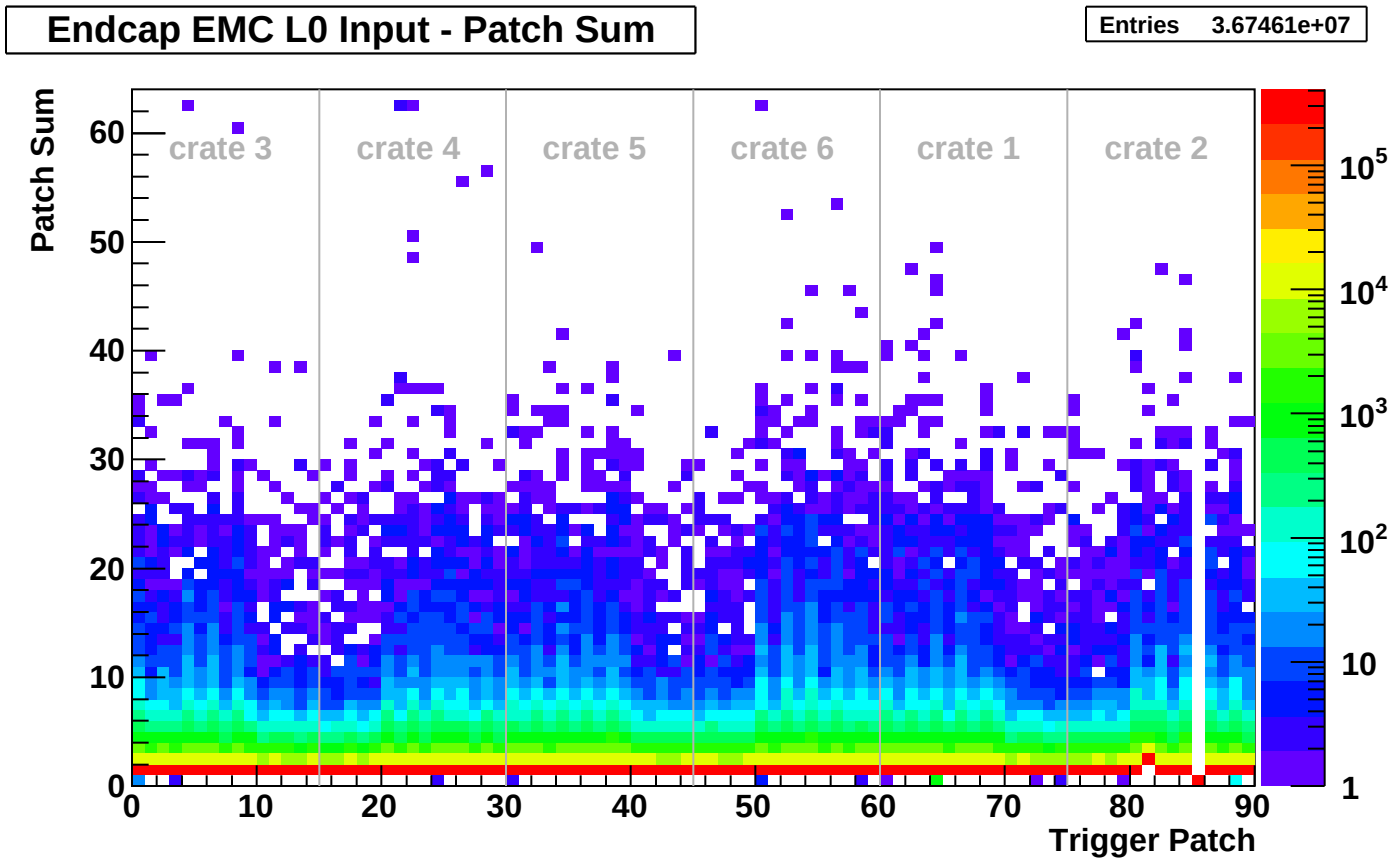

Abort-gaps
Entries 9841

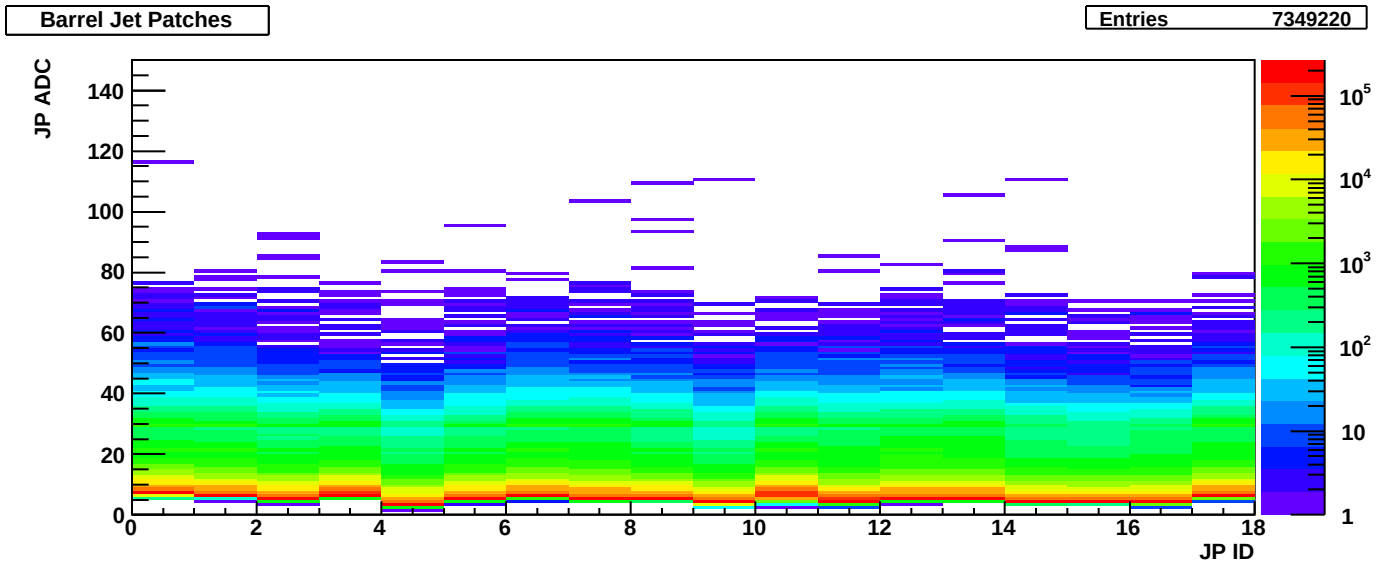

!Yellow-Fill
Entries 14122

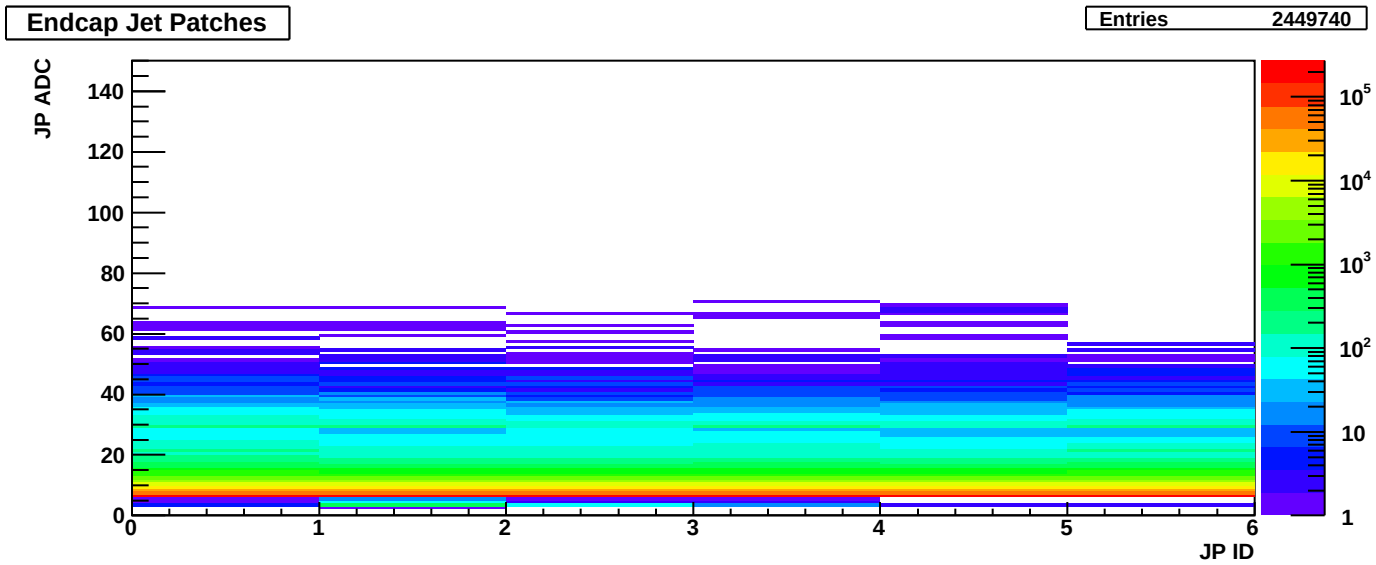

Barrel EMC L0 Input - High Tower
Entries 1.22487e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

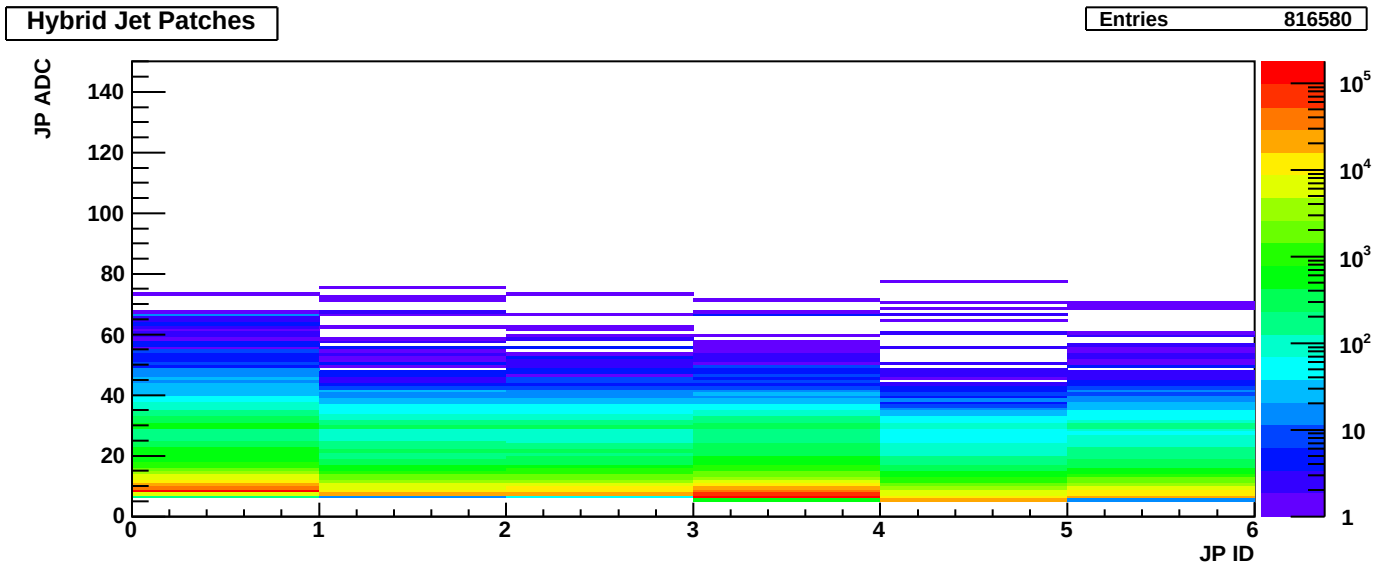

Barrel EMC L0 Input - Patch Sum
Entries 1.22487e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

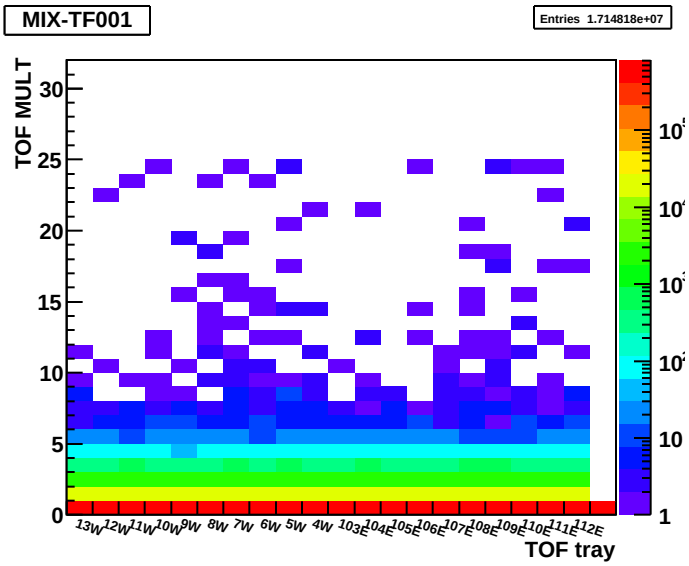

Endcap EMC L0 Input - High Tower
Entries 3.67461e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

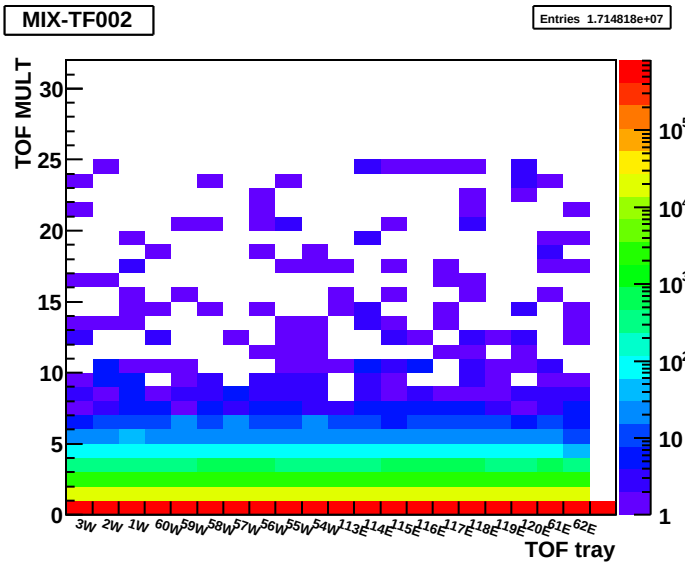

Endcap EMC L0 Input - Patch Sum
Entries 3.67461e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

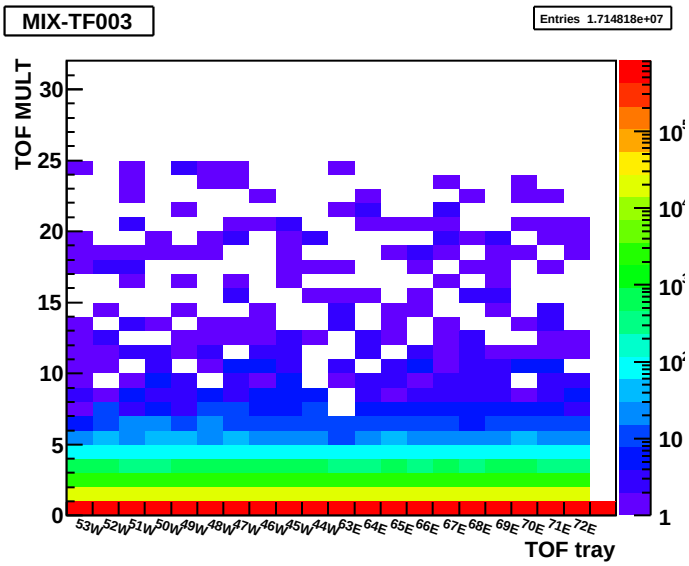

Barrel Jet Patches
Entries 7349220
JP ADC
JP ID

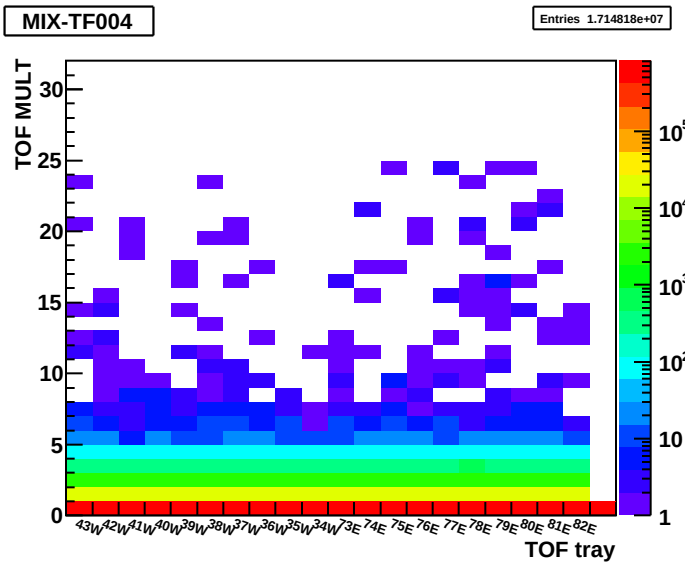

Endcap Jet Patches
Entries 2449740
JP ADC
JP ID

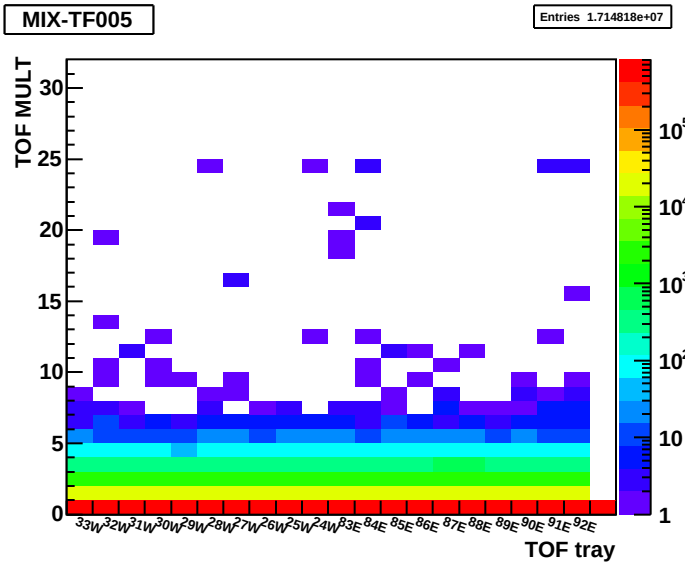

Hybrid Jet Patches
Entries 816580
JP ADC
JP ID

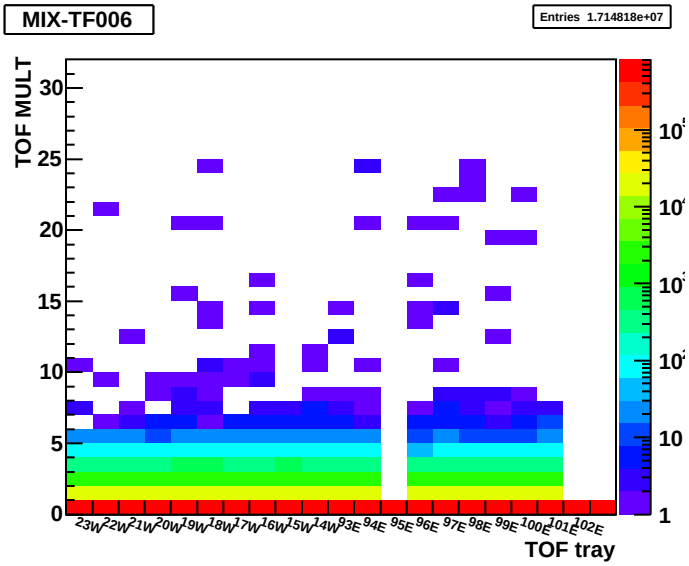

MIX-TF001
Entries 1.714818e+07
TOF MULT
TOF tray

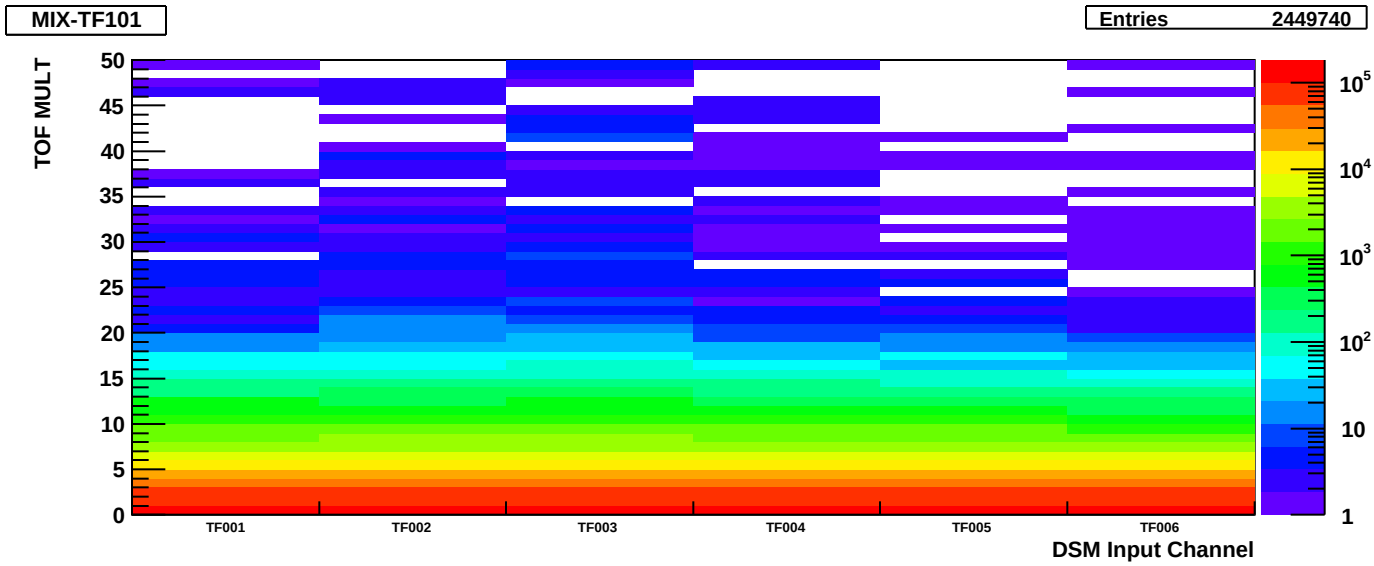

MIX-TF002
Entries 1.714818e+07
TOF MULT
TOF tray

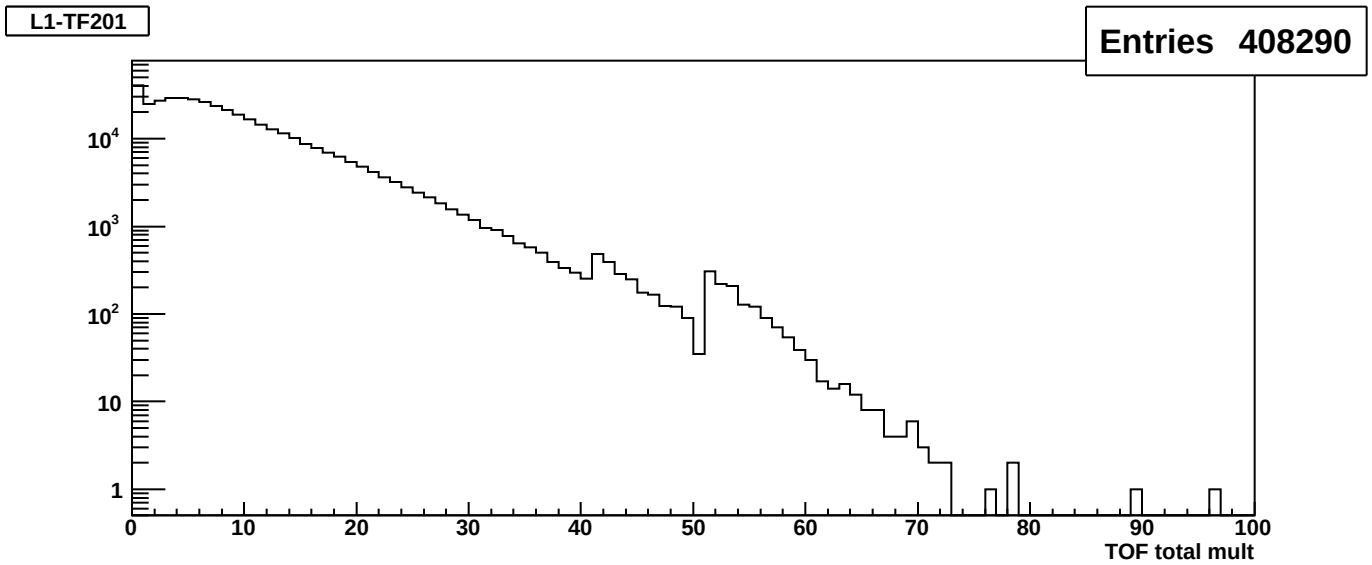

MIX-TF003
Entries 1.714818e+07
TOF MULT
TOF tray

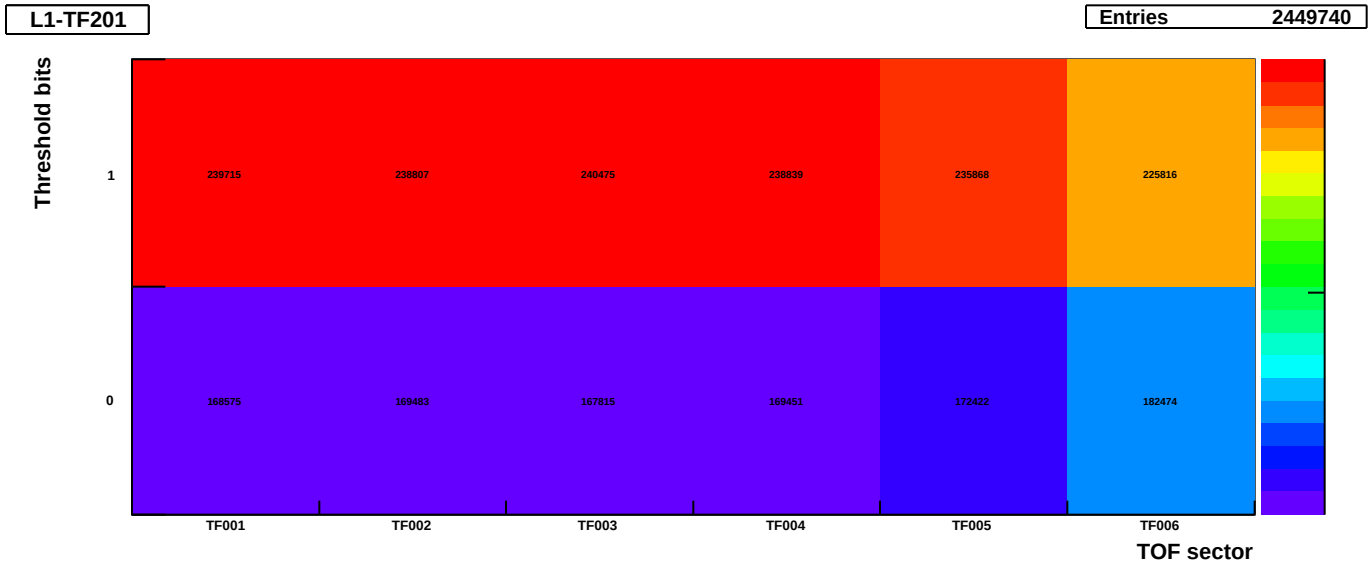

MIX-TF004
Entries 1.714818e+07
TOF MULT
TOF tray

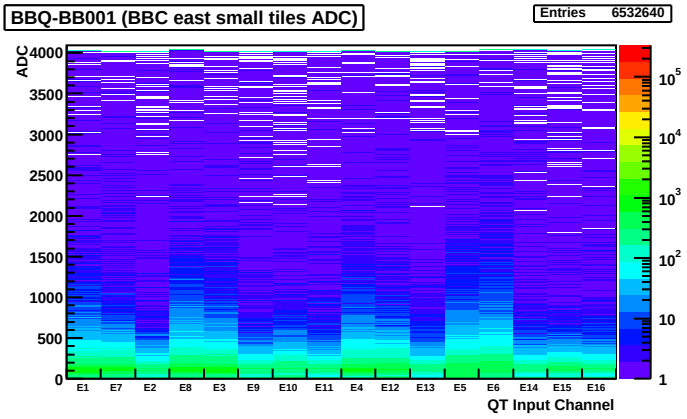

MIX-TF005
Entries 1.714818e+07
TOF MULT
TOF tray

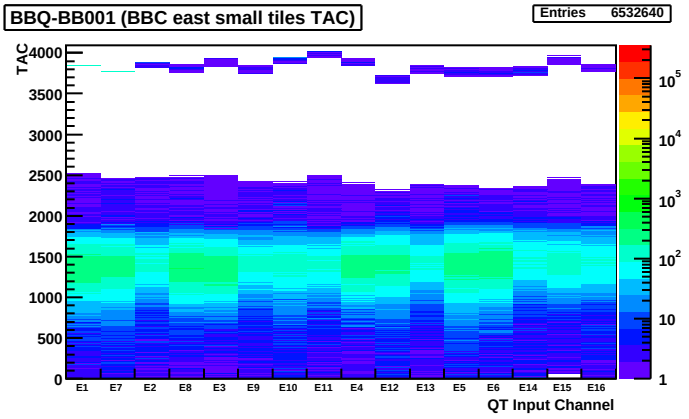

MIX-TF006
Entries 1.714818e+07
TOF MULT
TOF tray

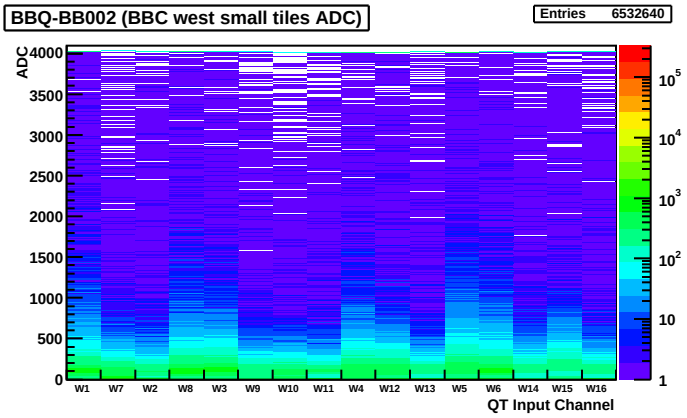

MIX-TF101
Entries 2449740
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

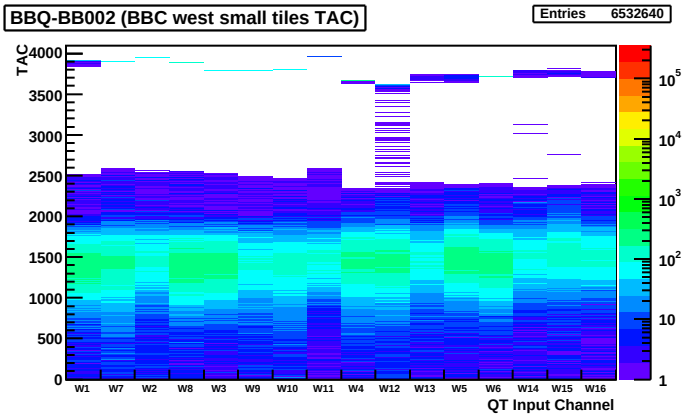

L1-TF201
Entries 408290
TOF total mult

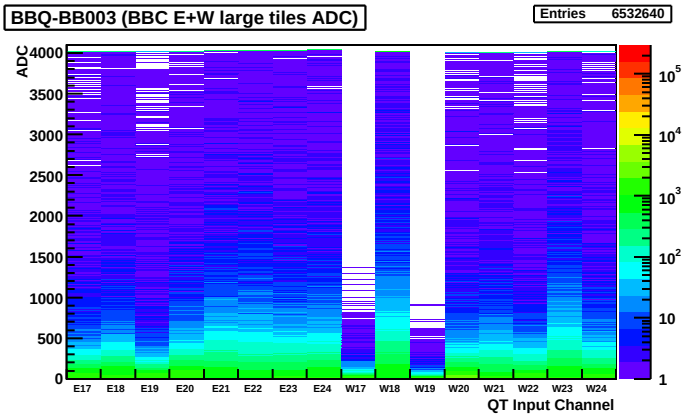

L1-TF201
Entries 2449740
Threshold bits
239715
238807
240475
238839
235868
225816
168575
169483
167815
169451
172422
182474
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

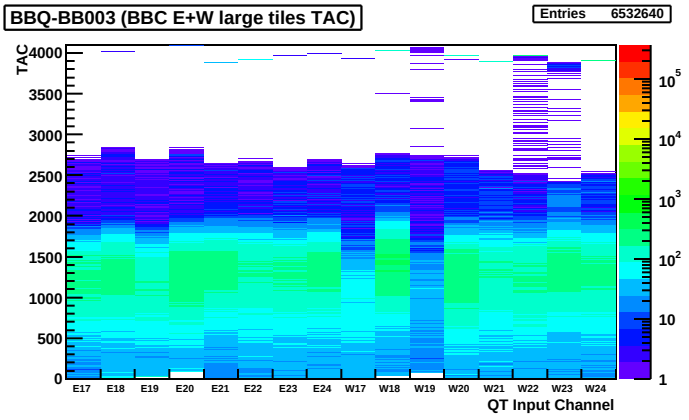

BBQ-BB001 (BBC east small tiles ADC)
Entries 6532640
ADC
QT Input Channel

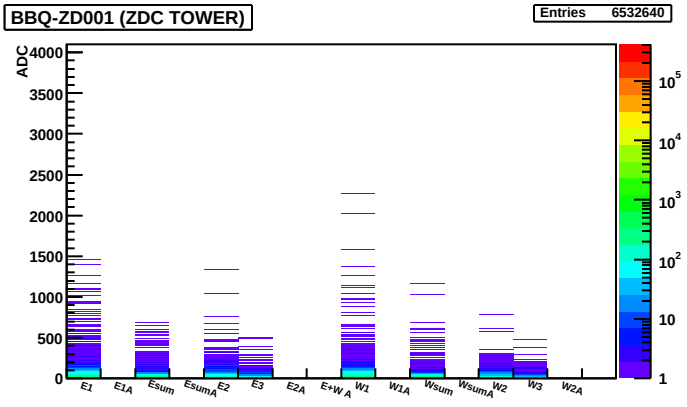

BBQ-BB001 (BBC east small tiles TAC)
Entries 6532640
TAC
QT Input Channel

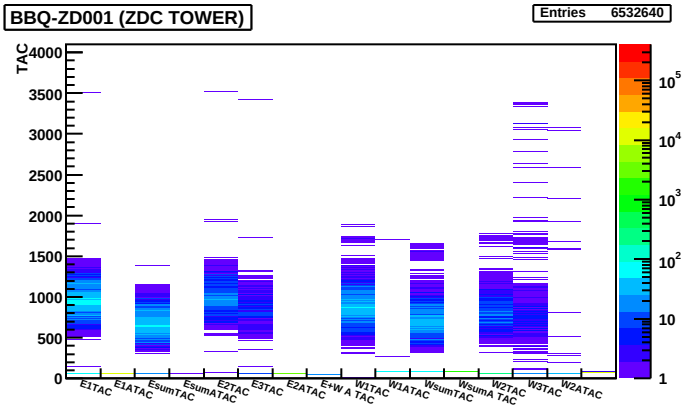

BBQ-BB002 (BBC west small tiles ADC)
Entries 6532640
ADC
QT Input Channel

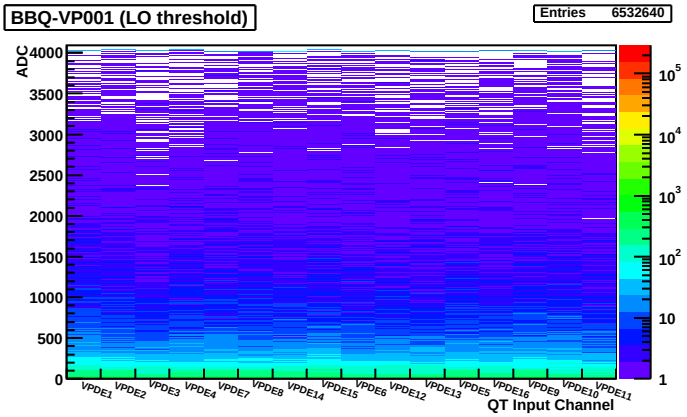

BBQ-BB002 (BBC west small tiles TAC)
Entries 6532640
TAC
QT Input Channel

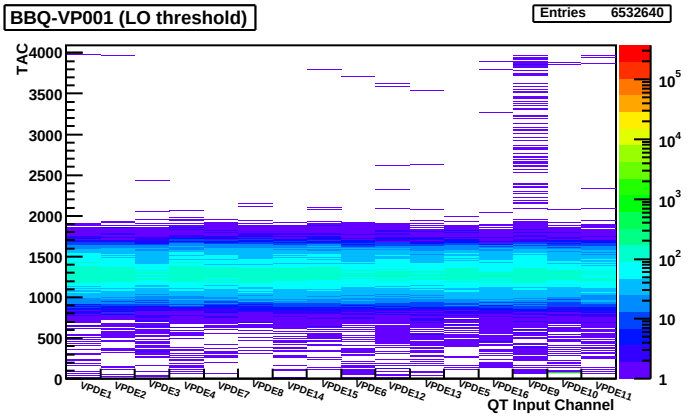

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6532640
ADC
QT Input Channel

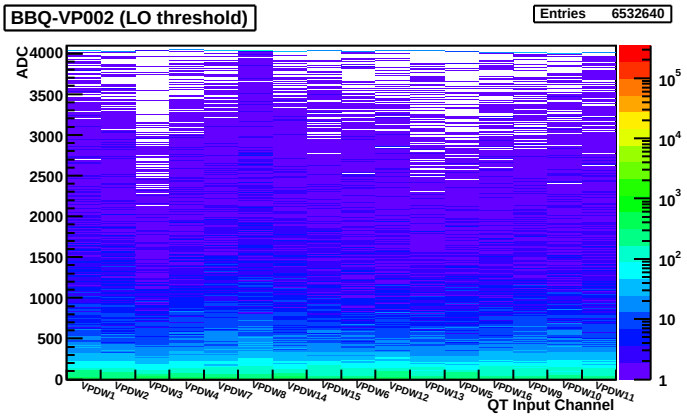

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6532640
TAC
QT Input Channel

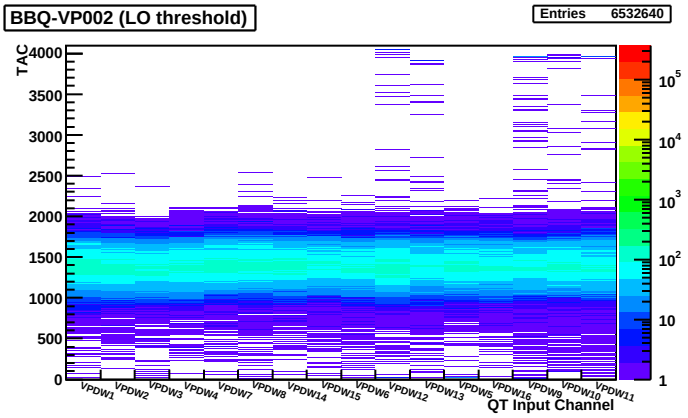

BBQ-ZD001 (ZDC TOWER)
Entries 6532640
ADC

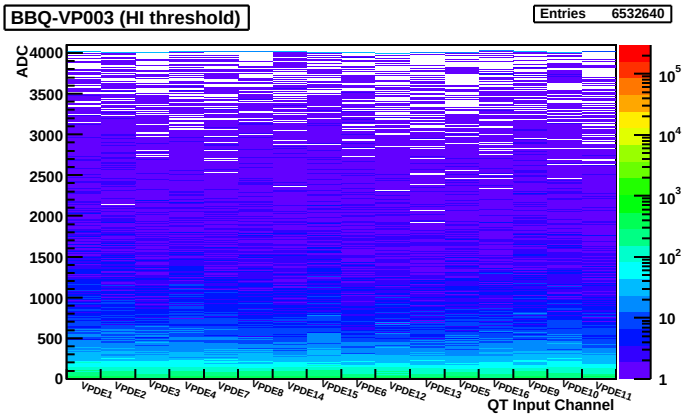

BBQ-ZD001 (ZDC TOWER)
Entries 6532640
TAC

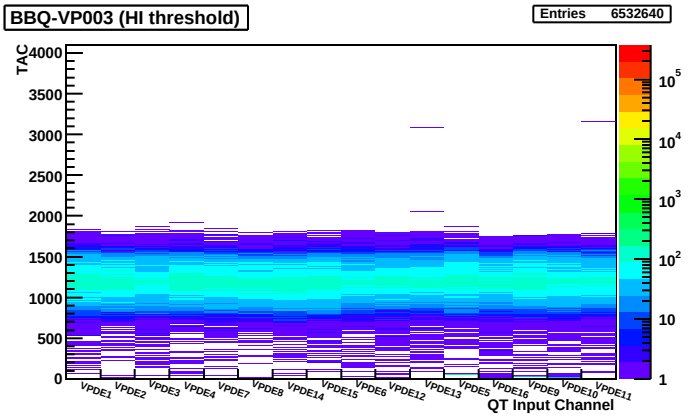

BBQ-VP001 (LO threshold)
Entries 6532640
ADC
QT Input Channel

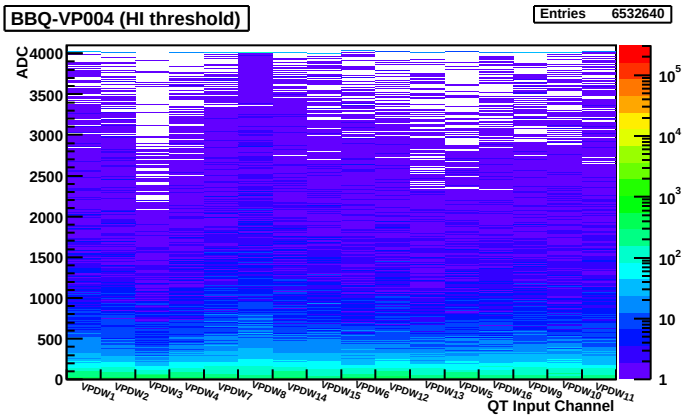

BBQ-VP001 (LO threshold)
Entries 6532640
TAC
QT Input Channel

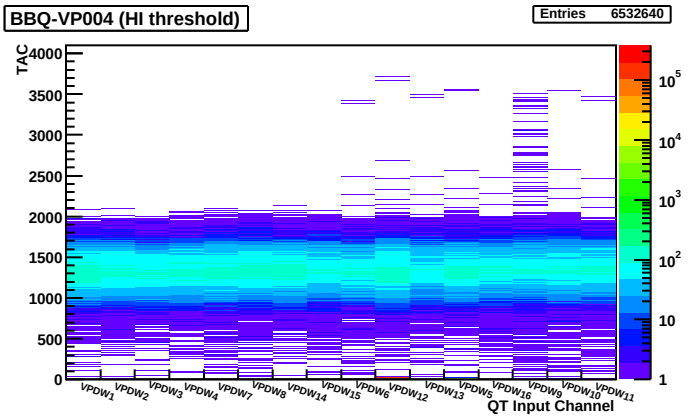

BBQ-VP002 (LO threshold)
Entries 6532640
ADC
QT Input Channel

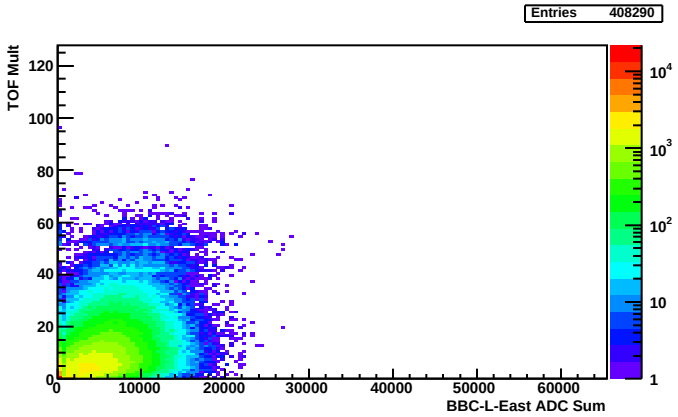

BBQ-VP002 (LO threshold)
Entries 6532640
TAC
QT Input Channel

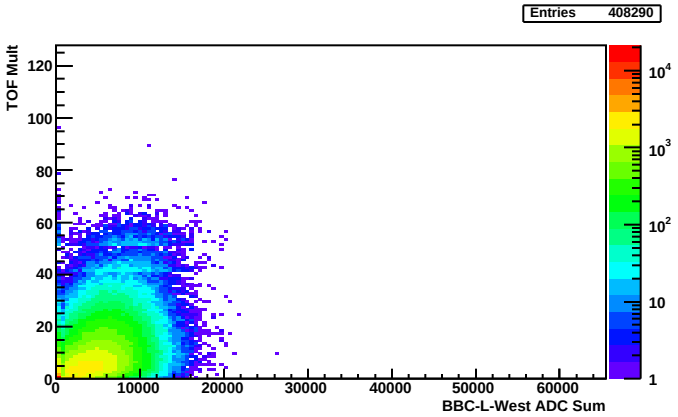

BBQ-VP003 (HI threshold)
Entries 6532640
ADC
QT Input Channel

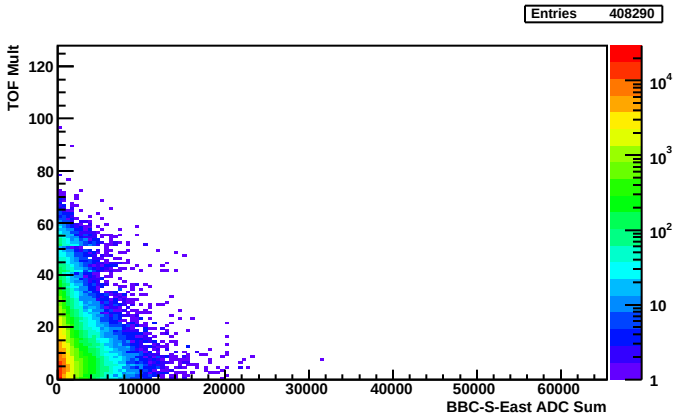

BBQ-VP003 (HI threshold)
Entries 6532640
TAC
QT Input Channel

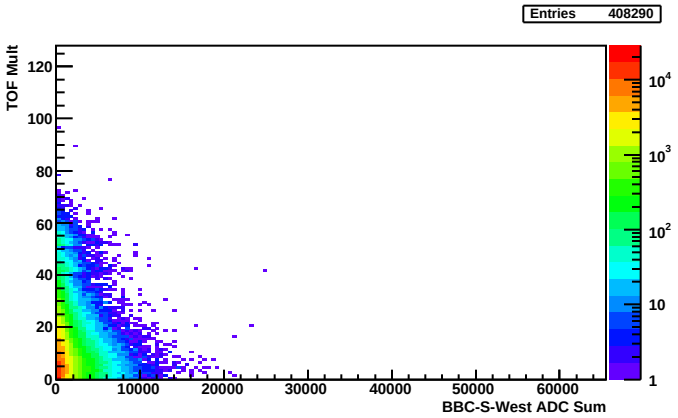

BBQ-VP004 (HI threshold)
Entries 6532640
ADC
QT Input Channel

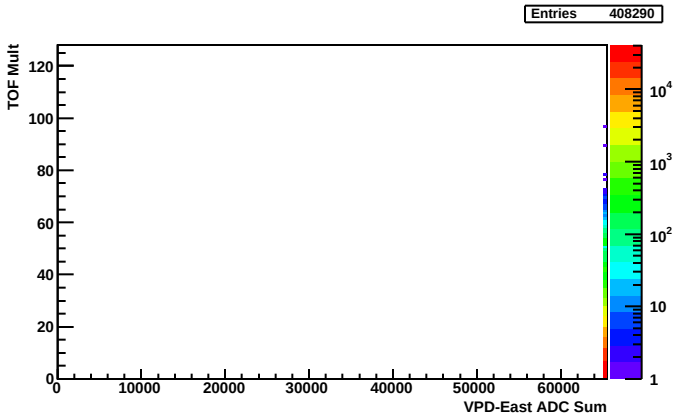

BBQ-VP004 (HI threshold)
Entries 6532640
TAC
QT Input Channel

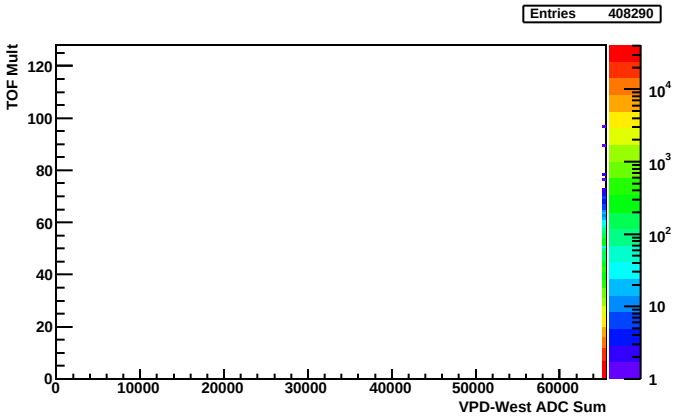

Entries 408290
TOF Mult
BBC-L-East ADC Sum

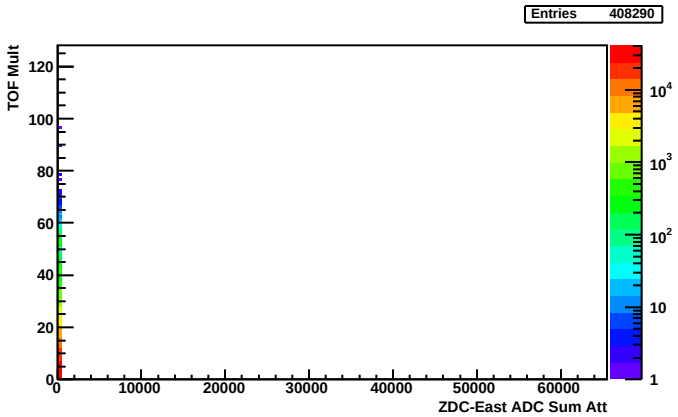

Entries 408290
TOF Mult
BBC-L-West ADC Sum

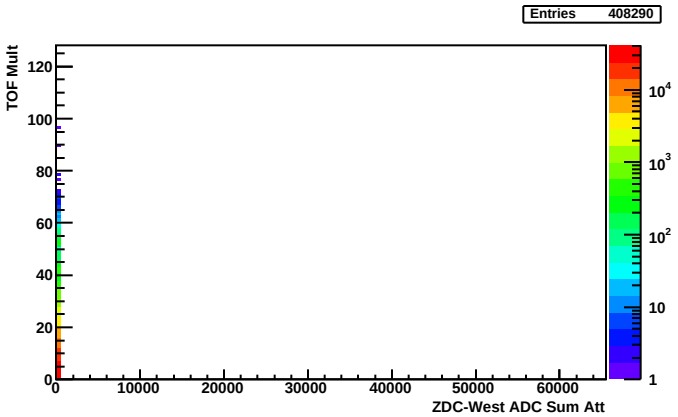

Entries 408290
TOF Mult
BBC-S-East ADC Sum

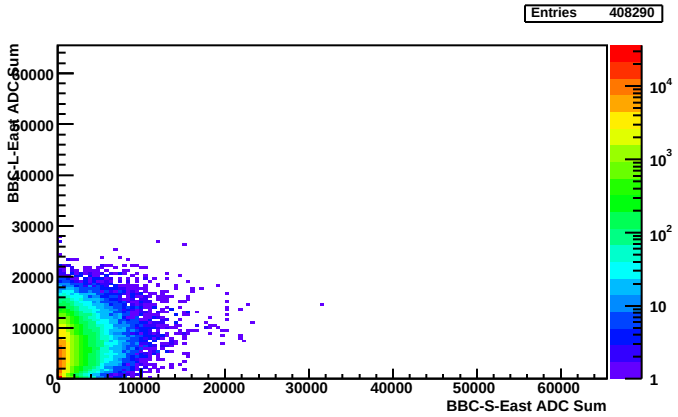

Entries 408290
TOF Mult
BBC-S-West ADC Sum

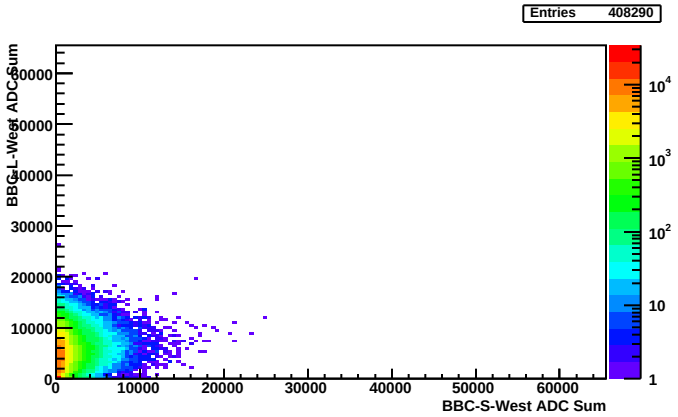

Entries 408290
TOF Mult
VPD-East ADC Sum

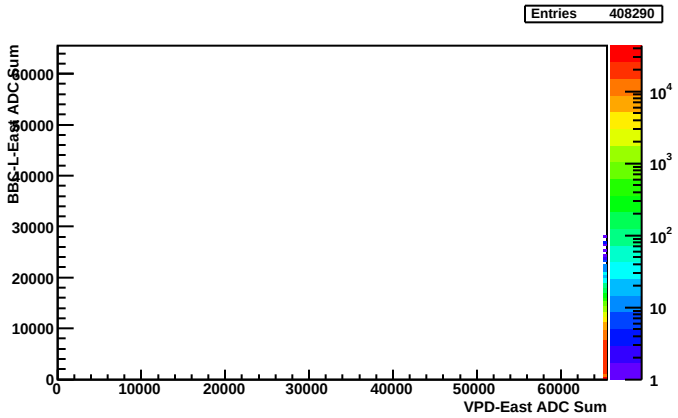

Entries 408290
TOF Mult
VPD-West ADC Sum

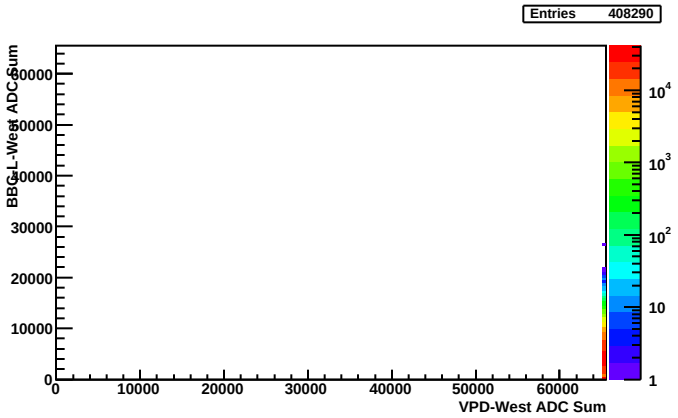

Entries 408290
TOF Mult
ZDC-East ADC Sum Att

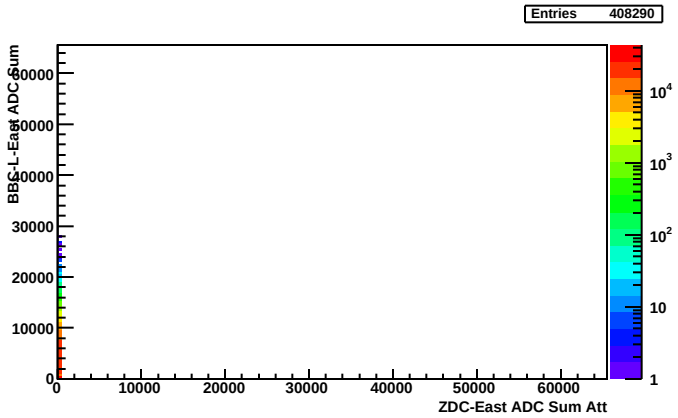

Entries 408290
TOF Mult
ZDC-West ADC Sum Att

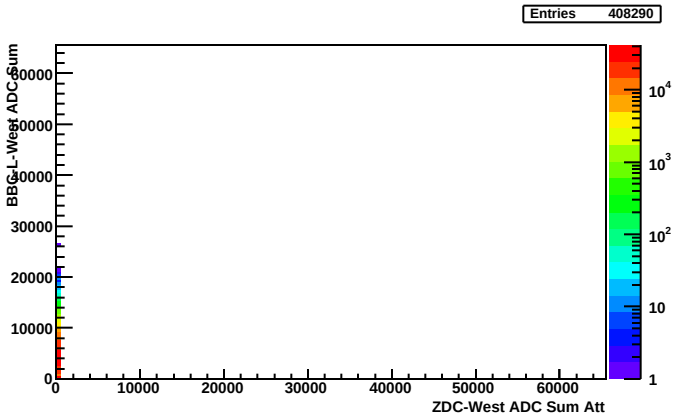

Entries 408290
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 408290
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 408290
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 408290
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 408290
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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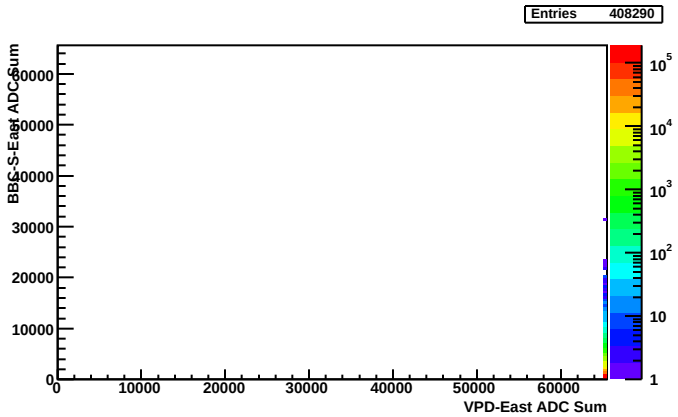
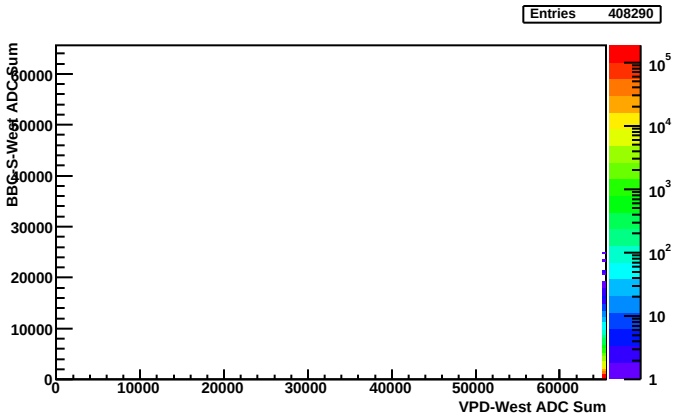
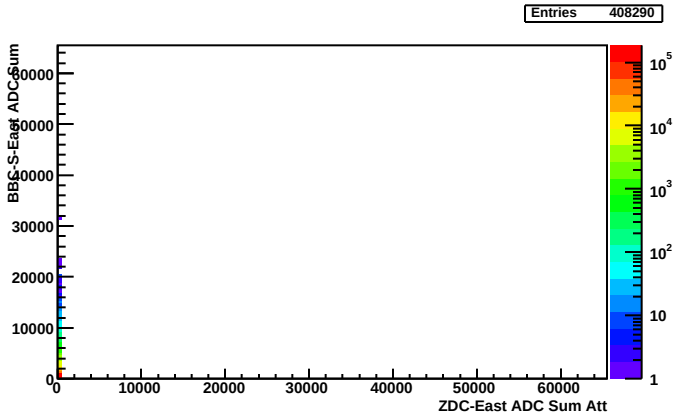
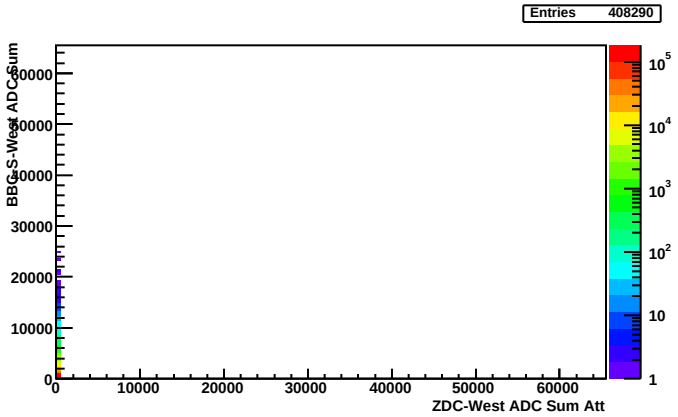
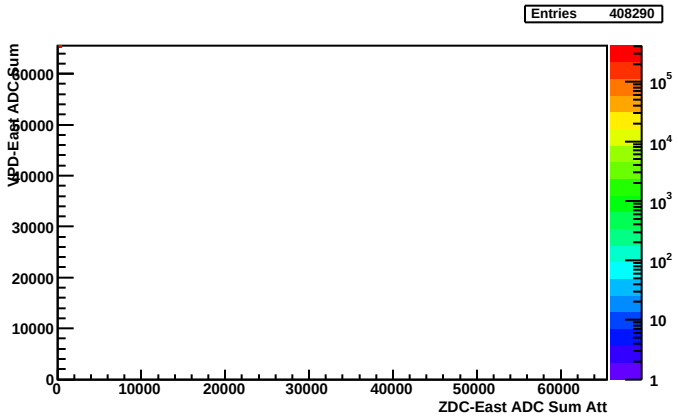
ZDC-West ADC Sum Att

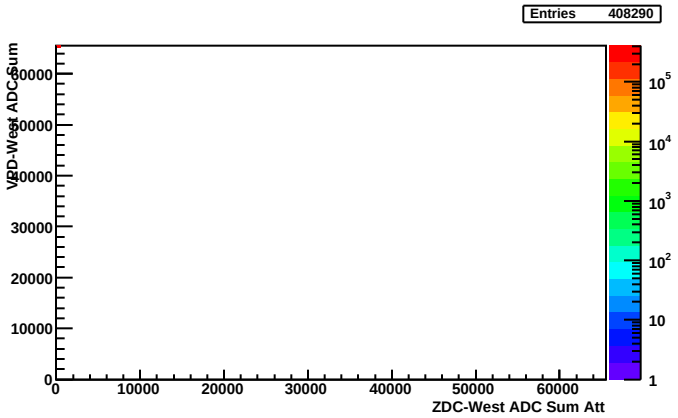

Entries 408290
BBC-S-East ADC Sum
VPD-East ADC Sum

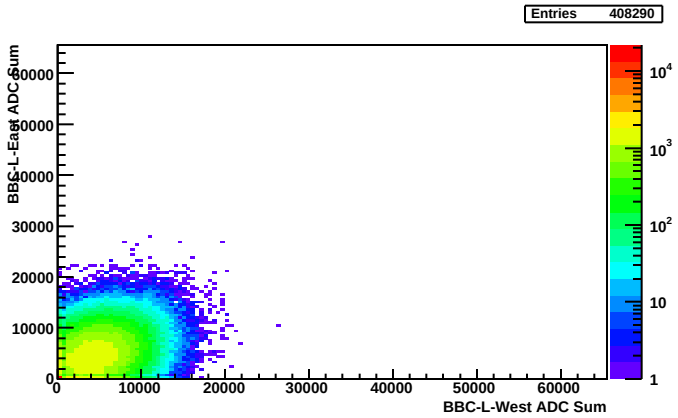

Entries 408290
BBC-S-West ADC Sum
VPD-West ADC Sum

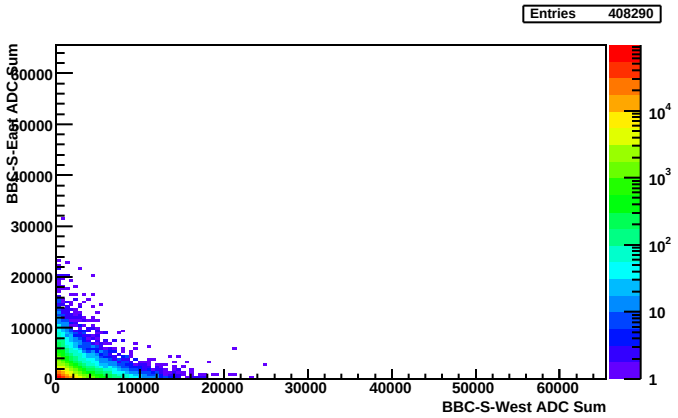

Entries 408290
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

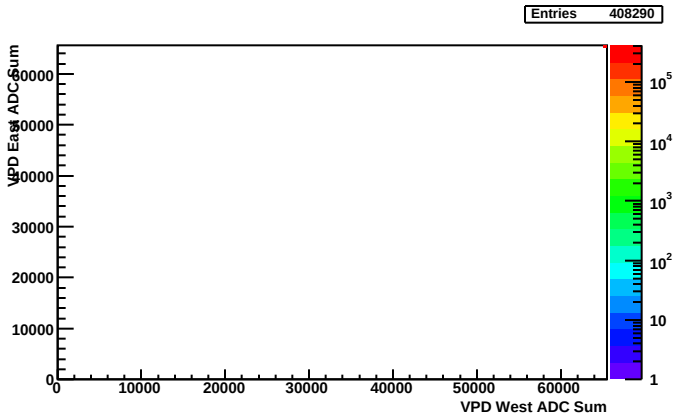

Entries 408290
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

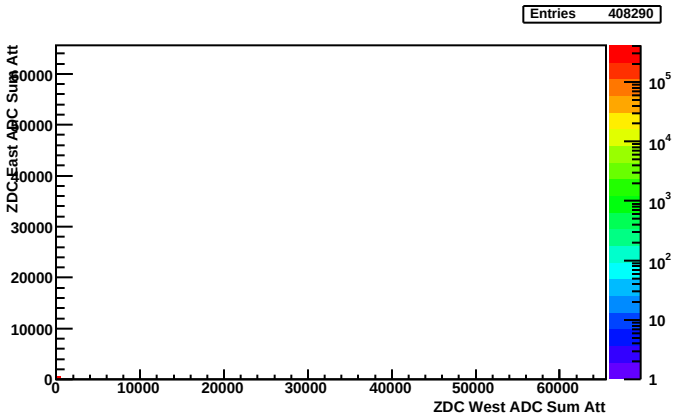

Entries 408290
VPD-East ADC Sum
ZDC-East ADC Sum Att

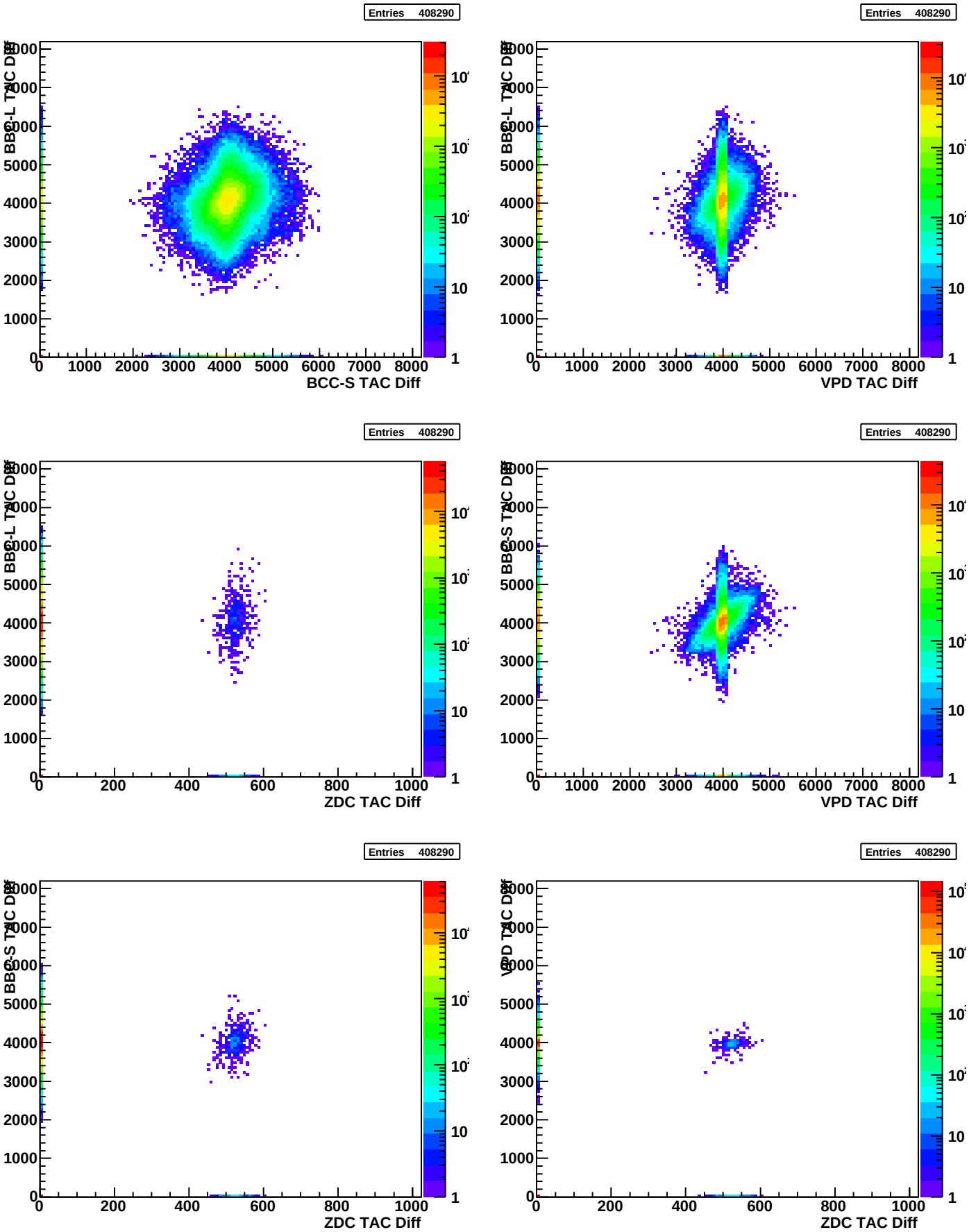

Entries 408290
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 408290
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 408290
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 408290
VPD East ADC Sum
VPD West ADC Sum


Entries 408290
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 408290
BBC-L TAC Diff
BCC-S TAC Diff
Entries 408290
BBC-L TAC Diff
VPD TAC Diff
Entries 408290
BBC-L TAC Diff
ZDC TAC Diff
Entries 408290
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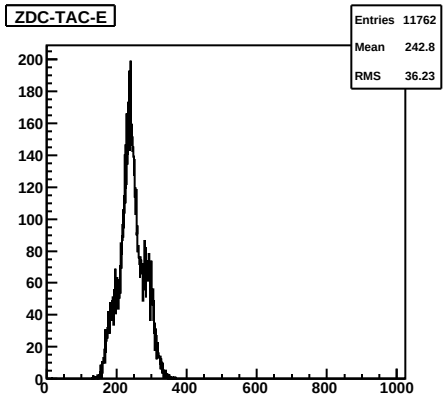
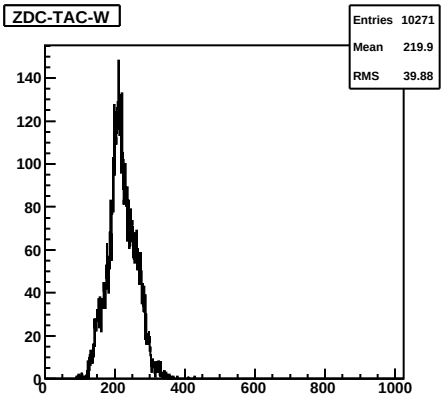
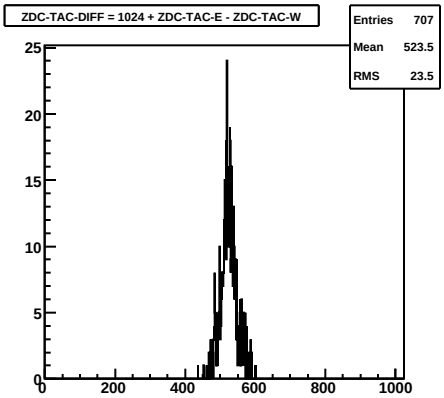
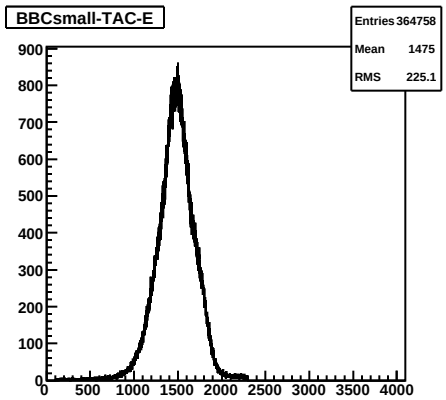
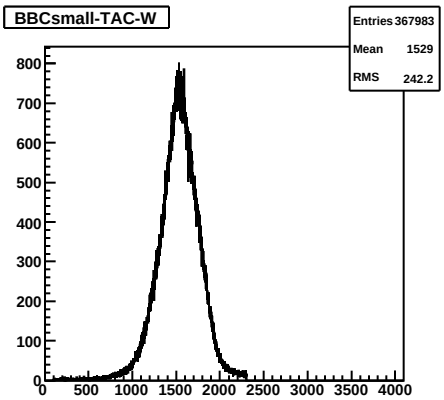
VPD TAC Diff
Entries 408290
BBC-S TAC Diff
ZDC TAC Diff
Entries 408290
VPD TAC Diff
ZDC TAC Diff

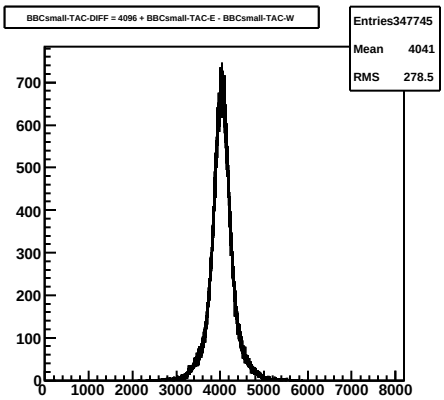

ZDC-TAC-E
Entries 11762
Mean 242.8
RMS 36.23

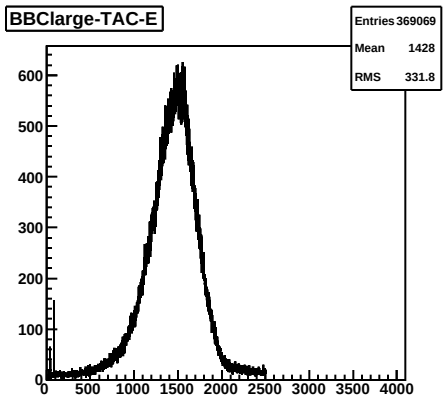

ZDC-TAC-W
Entries 10271
Mean 219.9
RMS 39.88

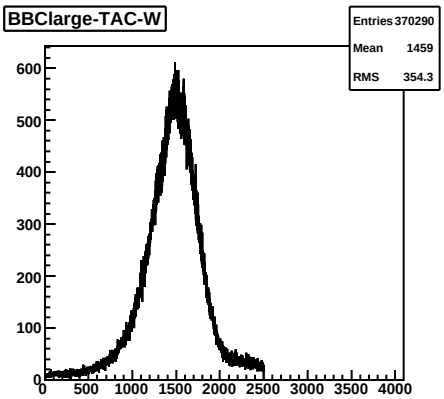

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 707
Mean 523.5
RMS 23.5

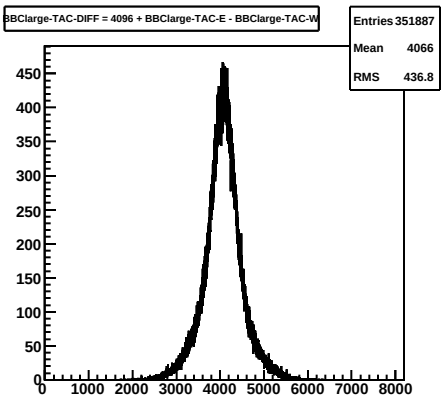

BBCsmall-TAC-E
Entries 364758
Mean 1475
RMS 225.1

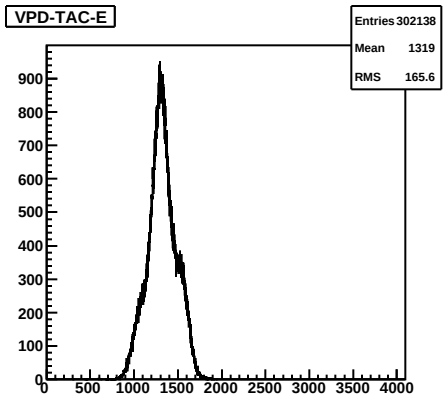

BBCsmall-TAC-W
Entries 367983
Mean 1529
RMS 242.2

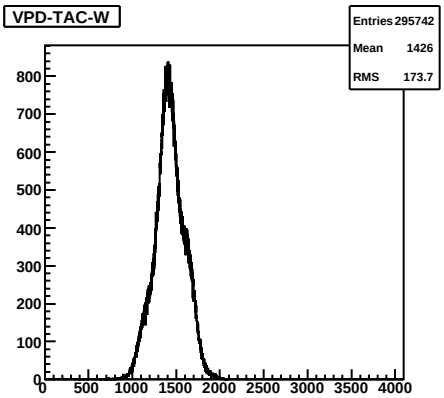

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 347745
Mean 4041
RMS 278.5

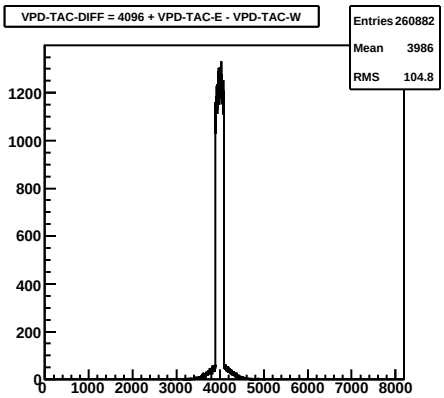

BBClarge-TAC-E
Entries 369069
Mean 1428
RMS 331.8

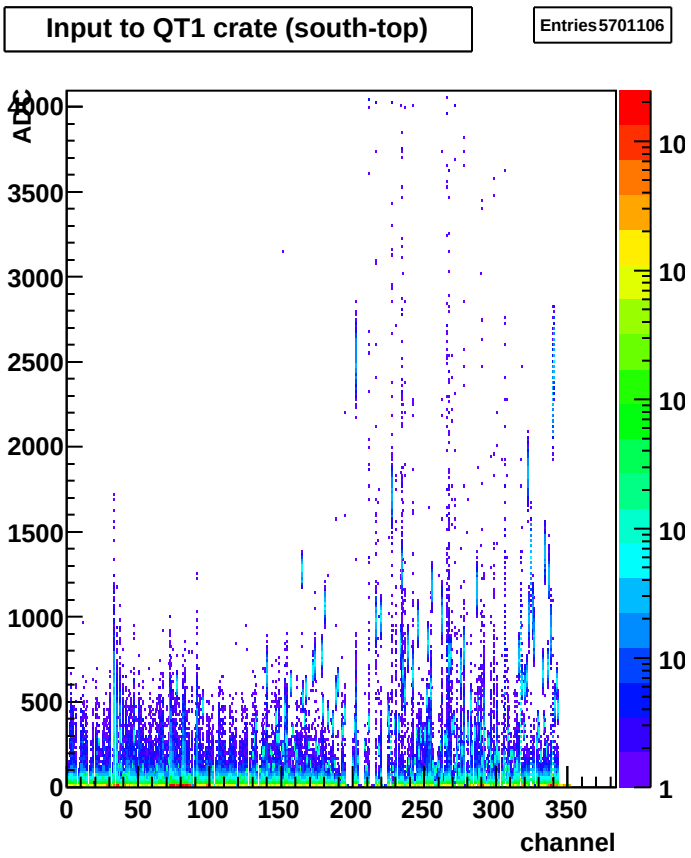

BBClarge-TAC-W
Entries 370290
Mean 1459
RMS 354.3

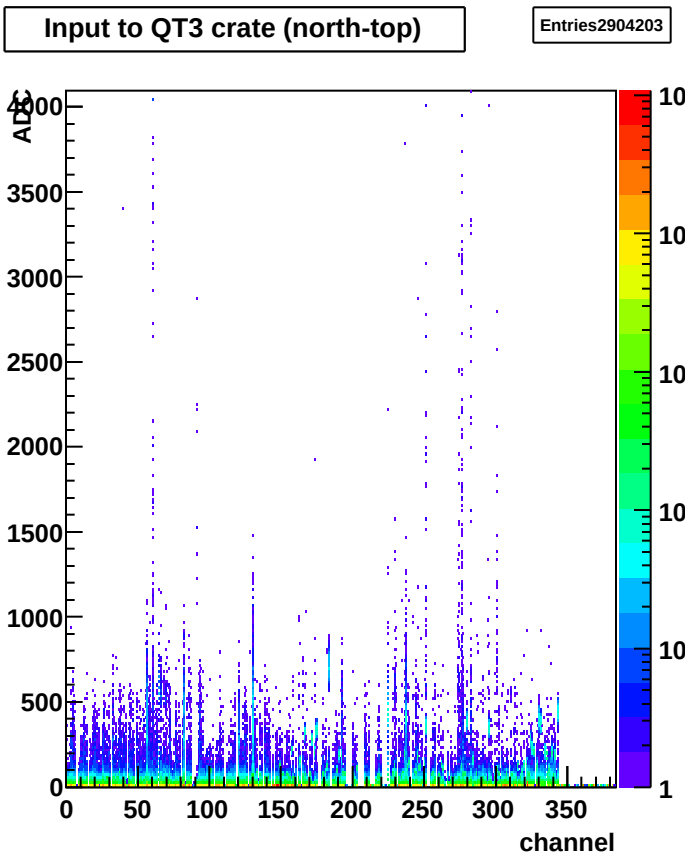

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 351887
Mean 4066
RMS 436.8

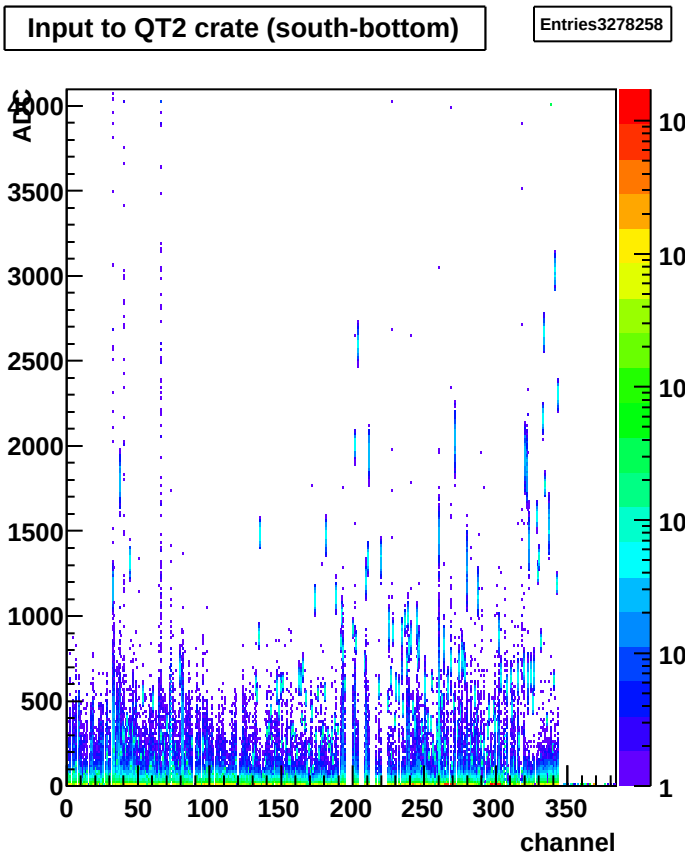

VPD-TAC-E
Entries 302138
Mean 1319
RMS 165.6

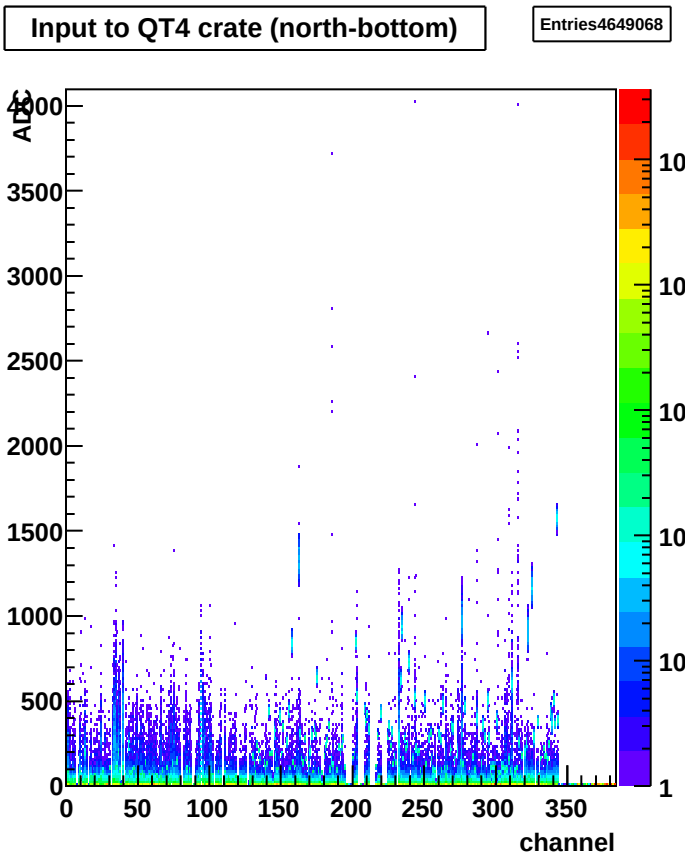

VPD-TAC-W
Entries 295742
Mean 1426
RMS 173.7

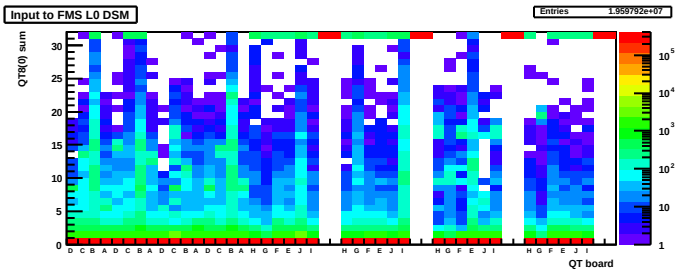

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 260882
Mean 3986
RMS 104.8

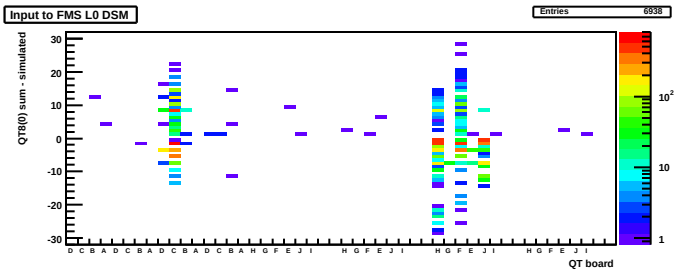

Input to QT1 crate (south-top)
Entries5701106
ADC
channel

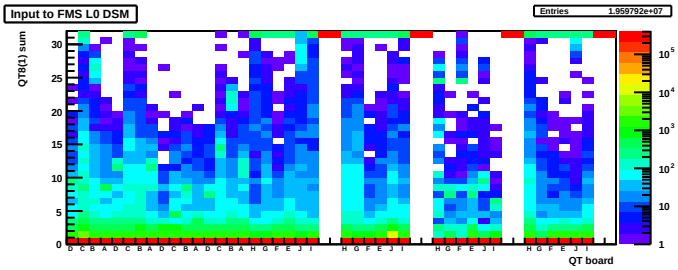

Input to QT3 crate (north-top)
Entries2904203
ADC
channel

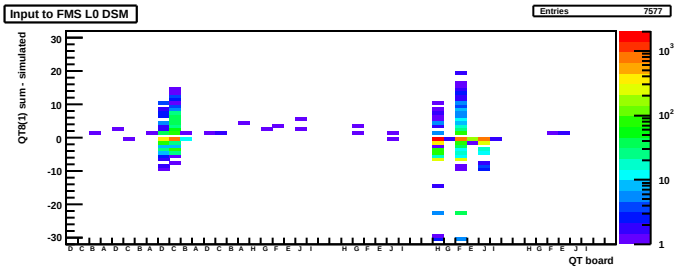

Input to QT2 crate (south-bottom)
Entries3278258
ADC
channel

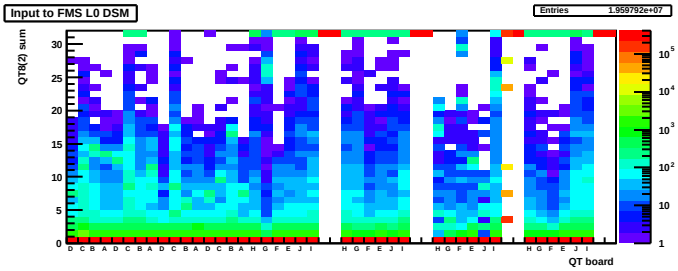

Input to QT4 crate (north-bottom)
Entries4649068
ADC
channel

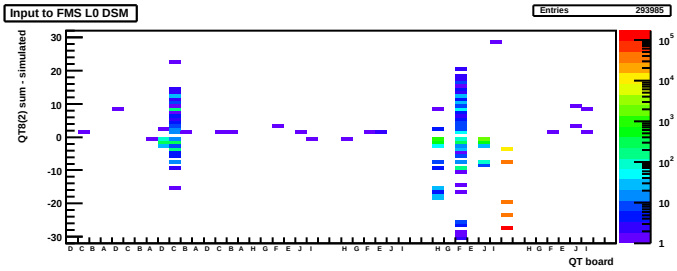

Input to FMS L0 DSM
Entries 1.959792e+07
QT8(0) sum
QT board

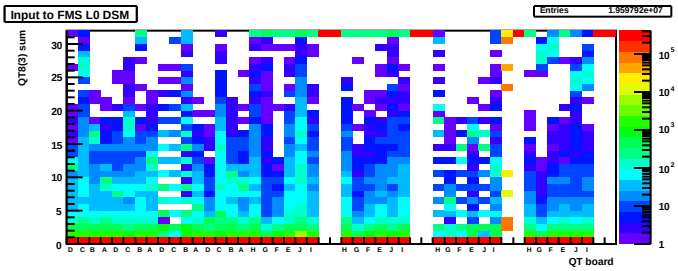

Input to FMS L0 DSM
Entries 6938
QT8(0) sum - simulated
QT board

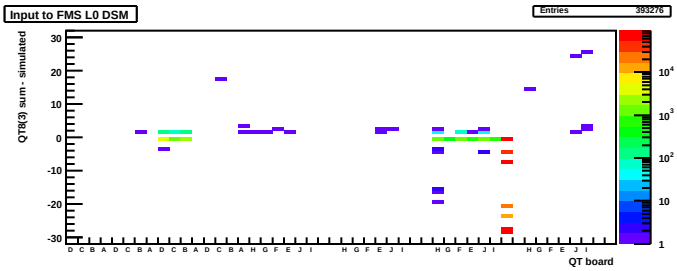

Input to FMS L0 DSM
Entries 1.959792e+07
QT8(1) sum
QT board

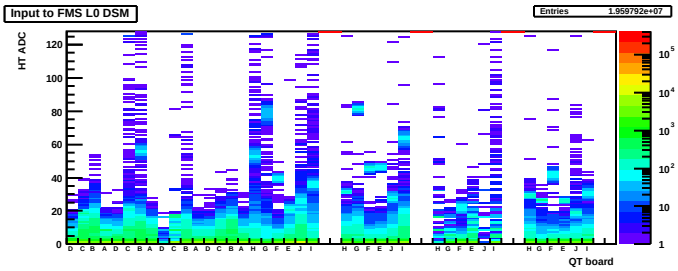

Input to FMS L0 DSM
Entries 7577
QT8(1) sum - simulated
QT board

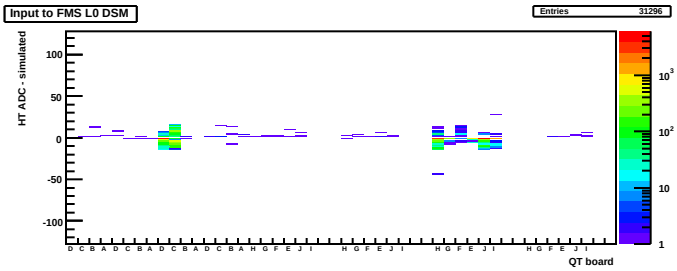

Input to FMS L0 DSM
Entries 1.959792e+07
QT8(2) sum
QT board

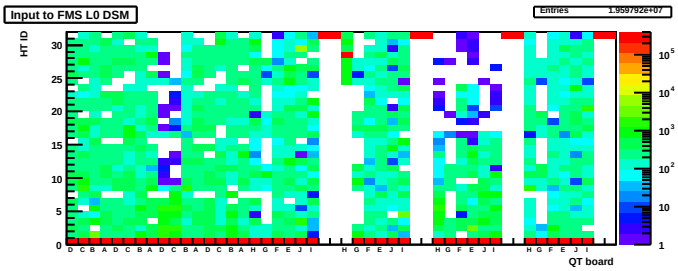

Input to FMS L0 DSM
Entries 283985
QT8(2) sum - simulated
QT board

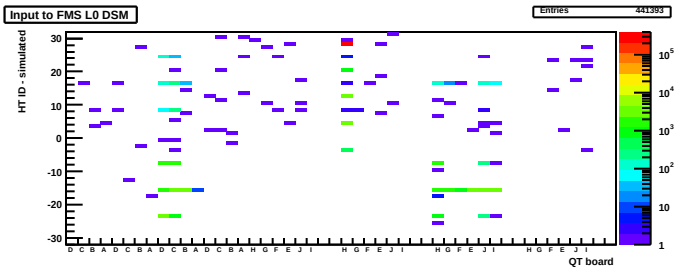

Input to FMS L0 DSM
Entries 1.959792e+07
QT8(3) sum
QT board

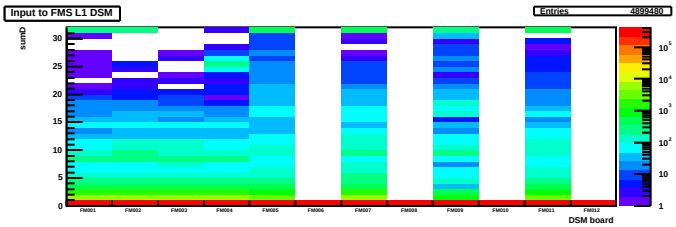

Input to FMS L0 DSM
Entries 393276
QT8(3) sum - simulated
QT board

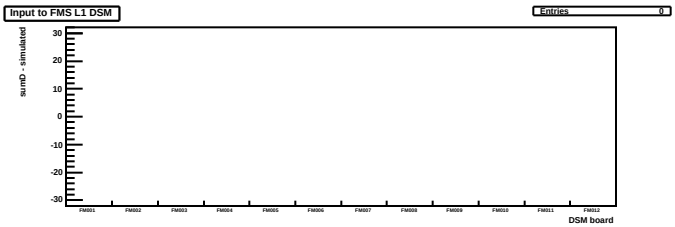

Input to FMS L0 DSM
Entries 1.959792e+07
HT ADC
QT board

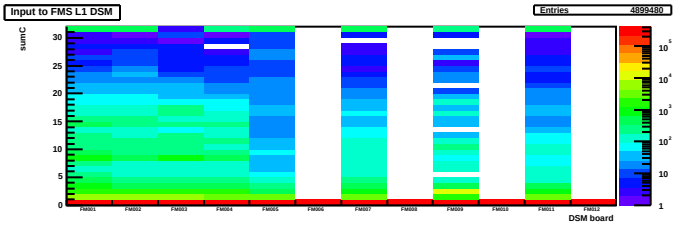

Input to FMS L0 DSM
Entries 31296
HT ADC - simulated
QT board

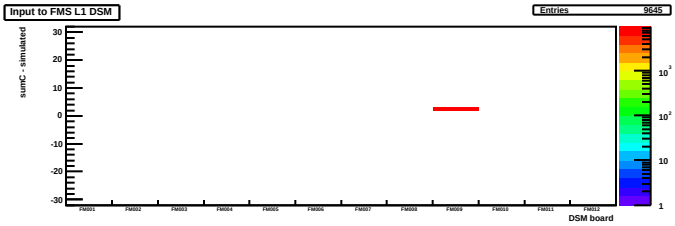

Input to FMS L0 DSM
Entries 1.959792e+07
HT ID
QT board

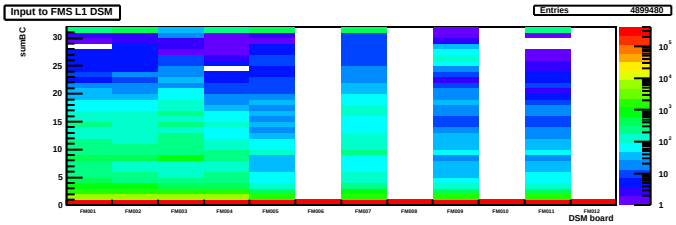

Input to FMS L0 DSM
Entries 441393
HT ID - simulated
QT board

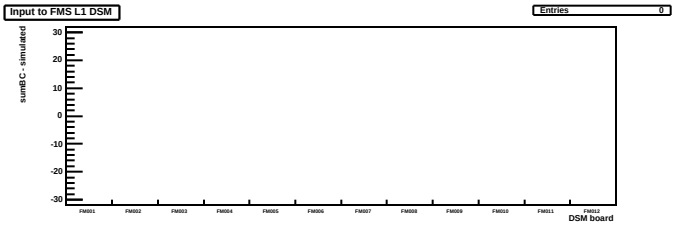

Input to FMS L1 DSM
Entries 4899480
sumD
DSM board

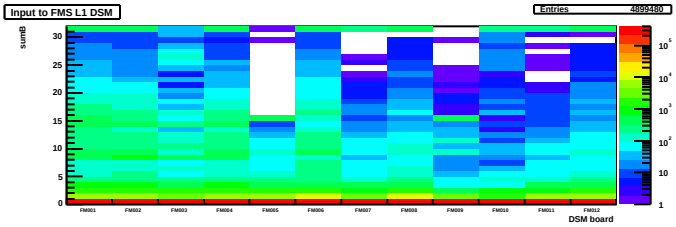

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

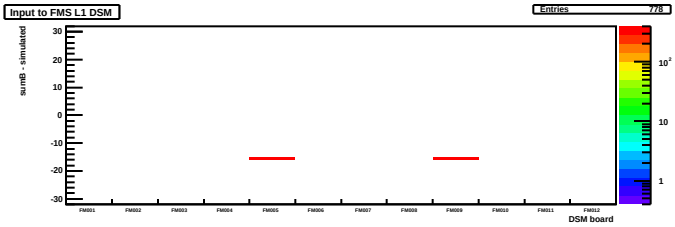

Input to FMS L1 DSM
Entries 4899480
sumC
DSM board

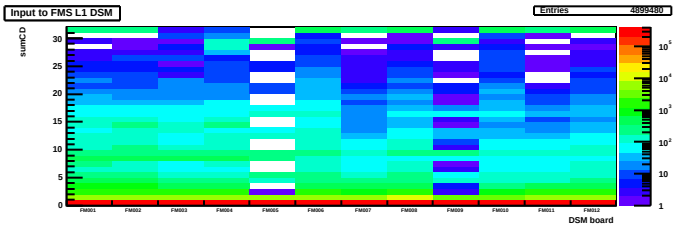

Input to FMS L1 DSM
Entries 9645
sumC - simulated
DSM board

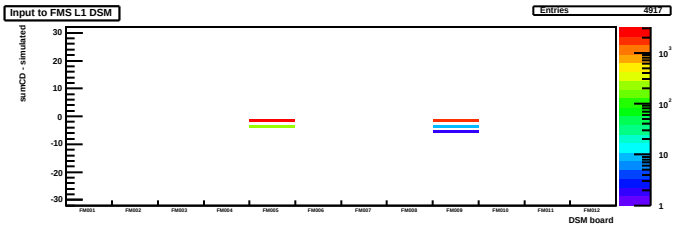

Input to FMS L1 DSM
Entries 4899480
sumBC
DSM board

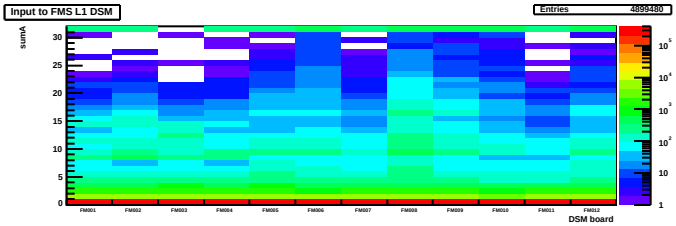

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

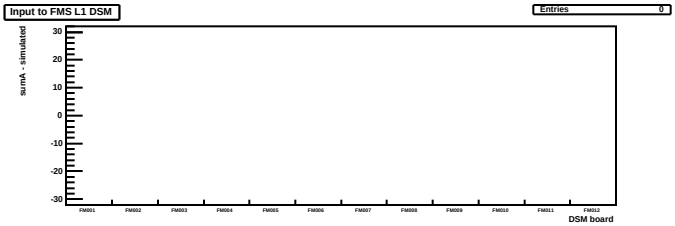

Input to FMS L1 DSM
Entries 4899480
sumB
DSM board

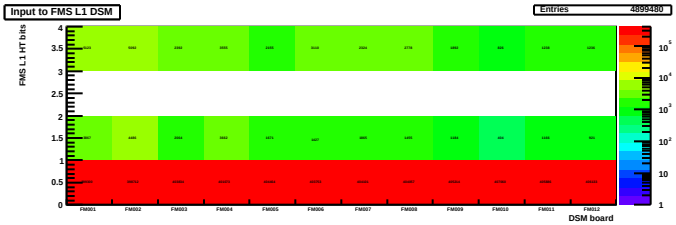

Input to FMS L1 DSM
Entries 778
sumB - simulated
DSM board

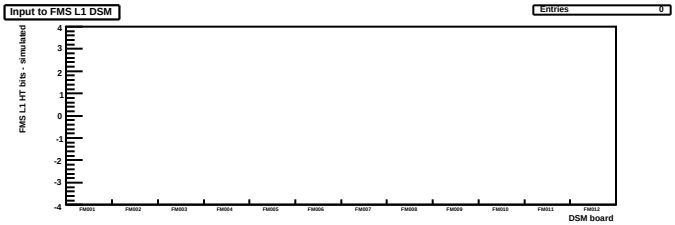

Input to FMS L1 DSM
Entries 4899480
sumCD
DSM board

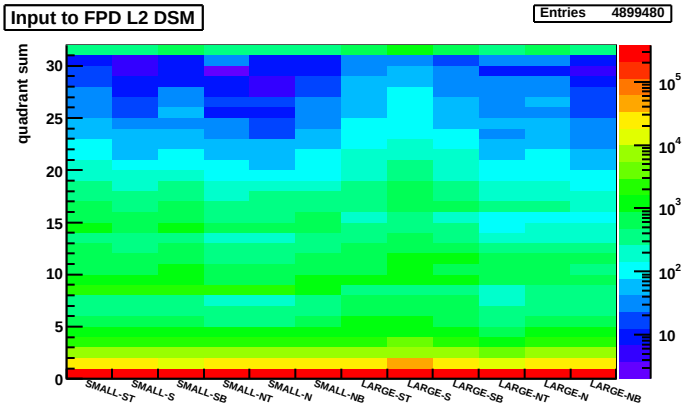

Input to FMS L1 DSM
Entries 4917
sumCD - simulated
DSM board

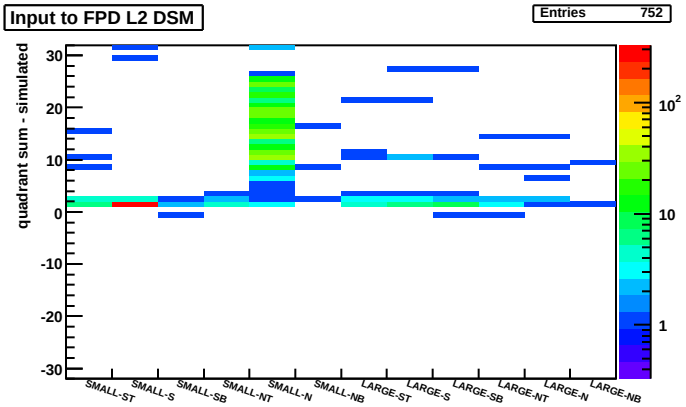

Input to FMS L1 DSM
Entries 4899480
sumA
DSM board

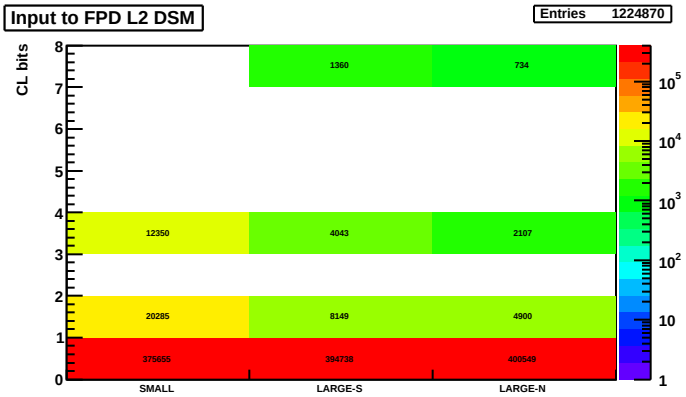

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

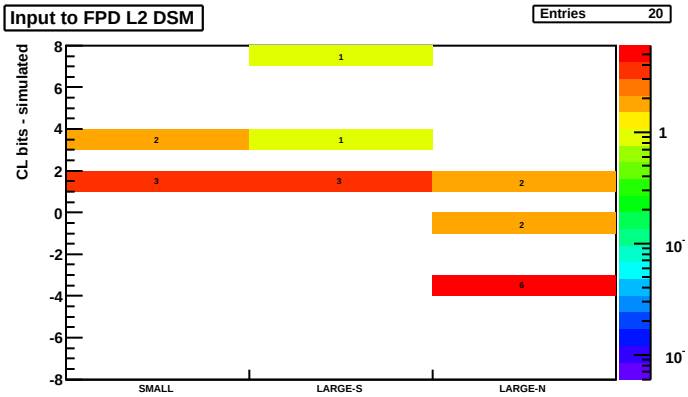

Input to FMS L1 DSM
Entries 4899480
FMS L1 HT bits
DSM board

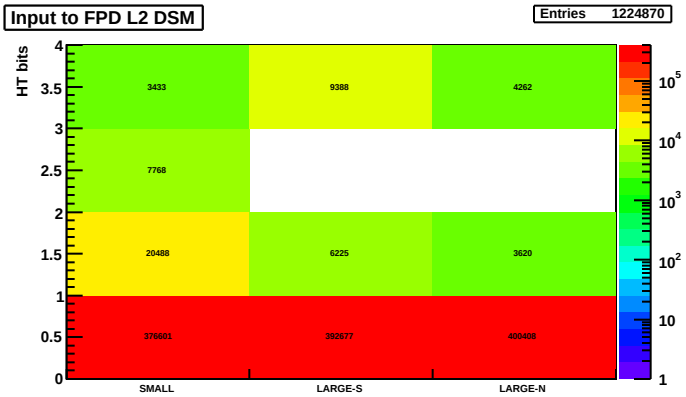

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

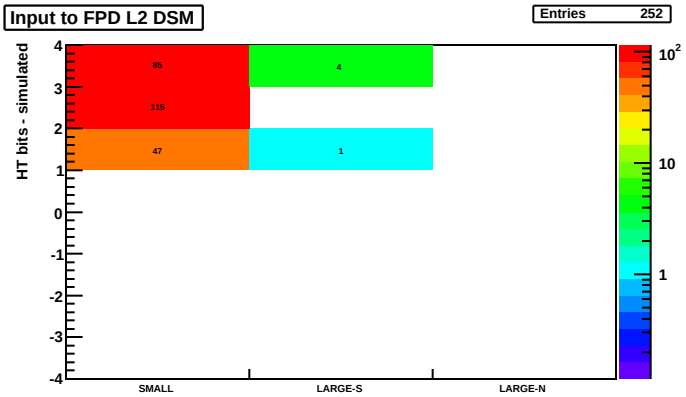

Input to FPD L2 DSM
Entries 4899480
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

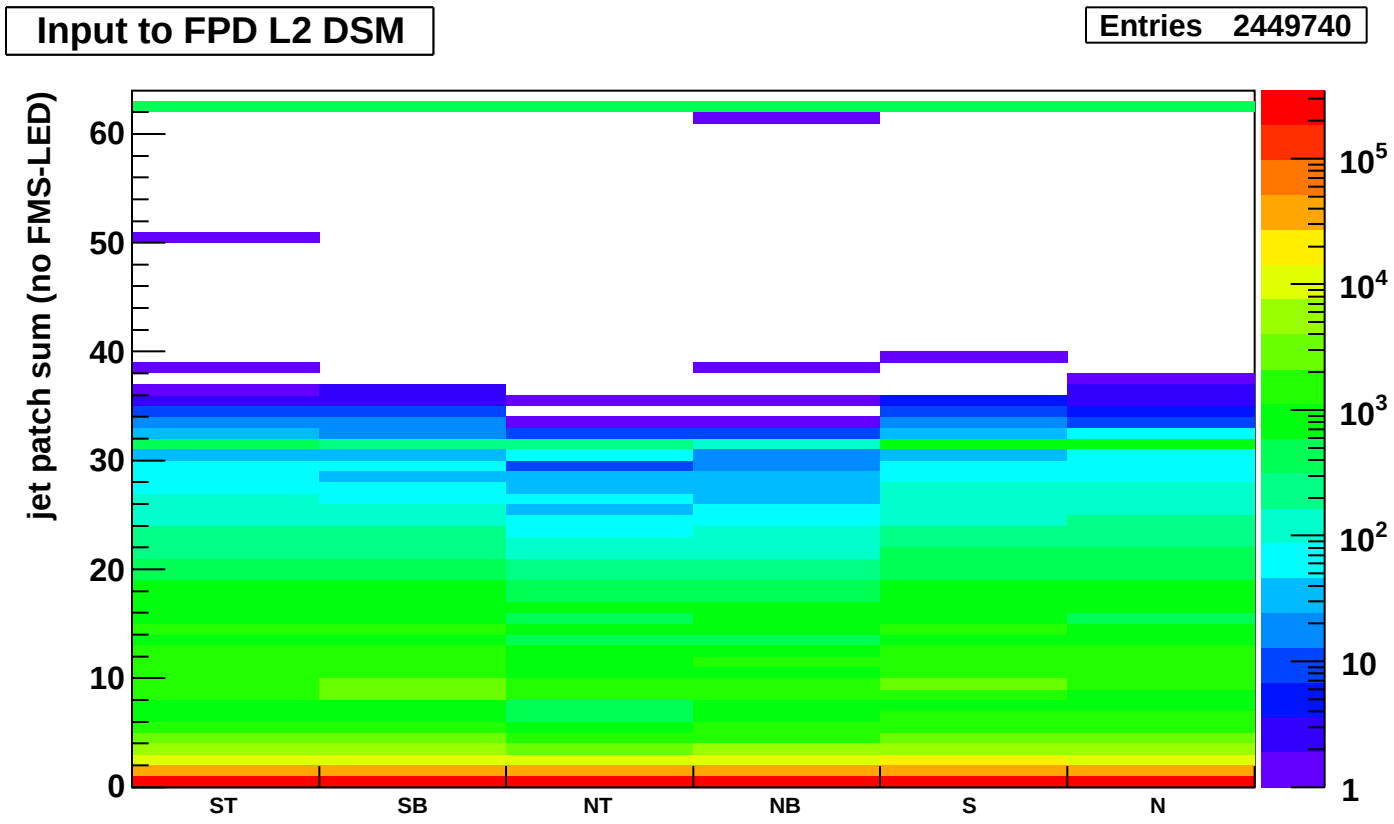

Input to FPD L2 DSM
Entries 752
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

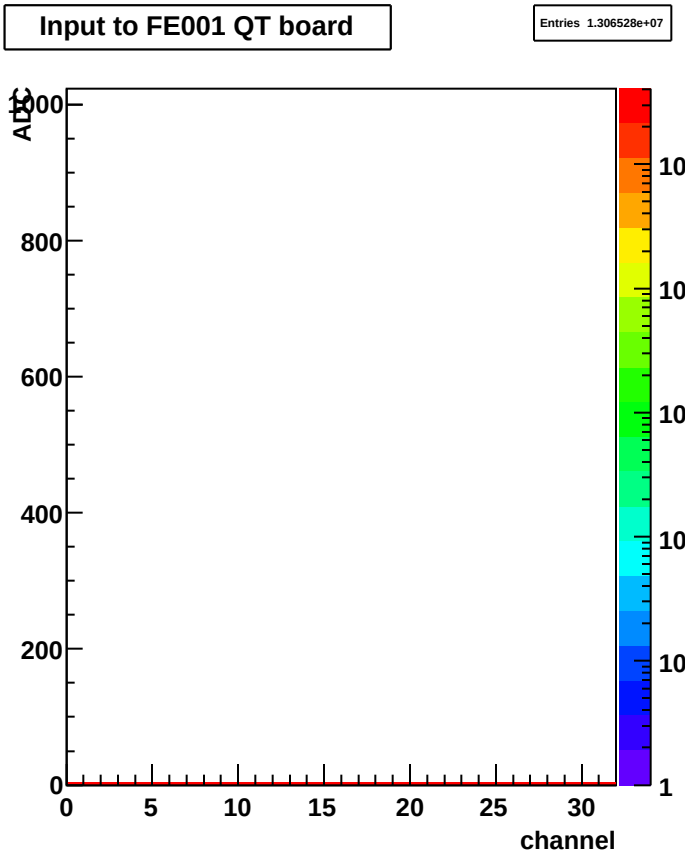

Input to FPD L2 DSM
Entries 1224870
CL bits
1360
734
12350
4043
2107
20285
8149
4900
375655
394738
400549
SMALL
LARGE-S
LARGE-N

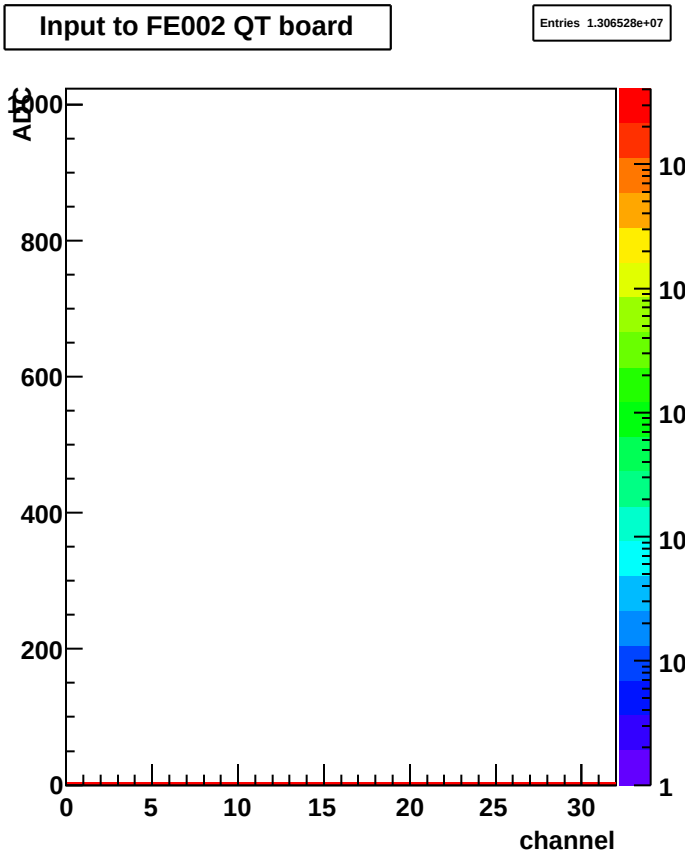

Input to FPD L2 DSM
Entries 20
CL bits - simulated
1
2
1
3
3
2
2
6
SMALL
LARGE-S
LARGE-N

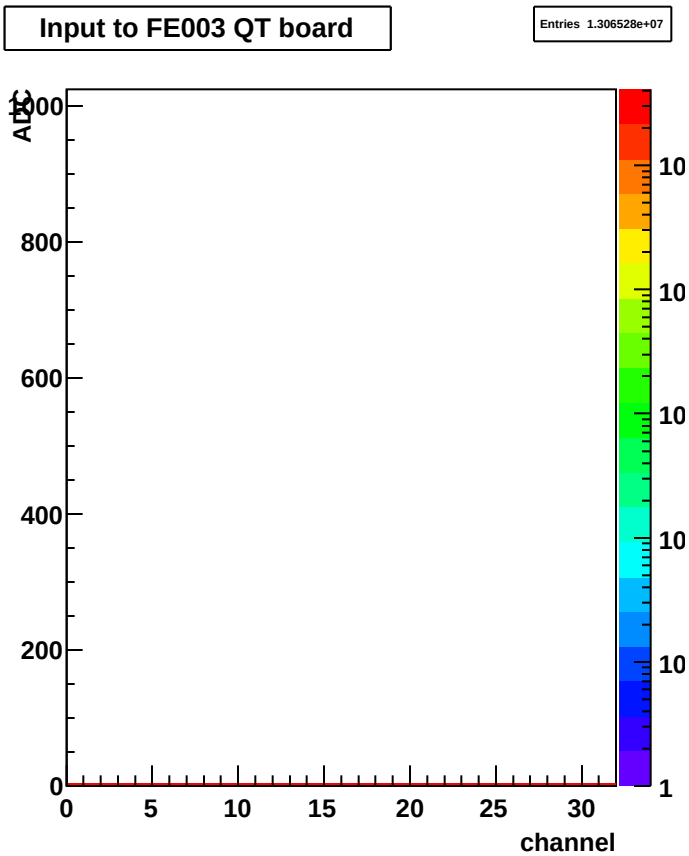

Input to FPD L2 DSM
Entries 1224870
HT bits
3433
9388
4262
7768
20488
6225
3620
376601
392677
400408
SMALL
LARGE-S
LARGE-N

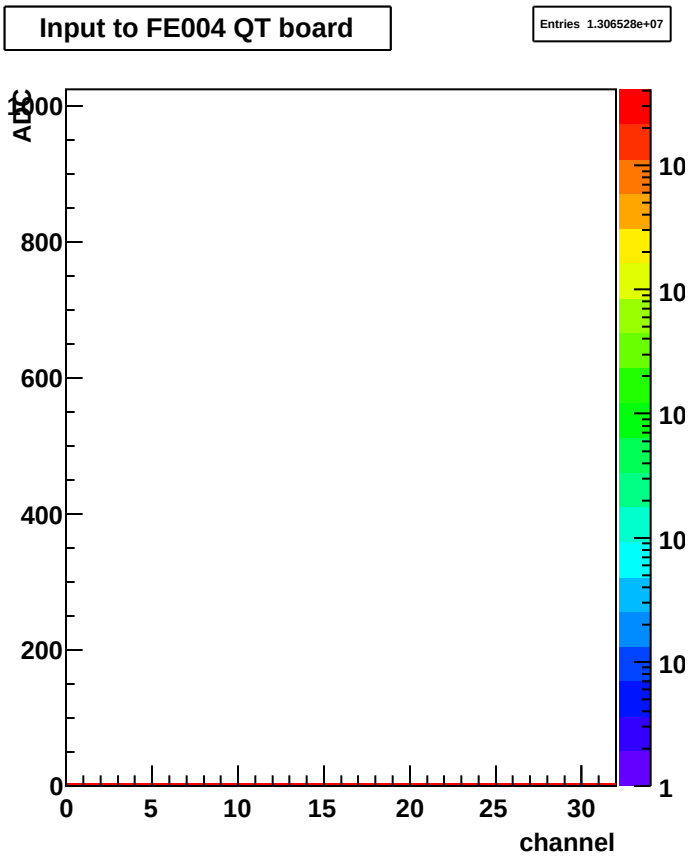

Input to FPD L2 DSM
Entries 252
HT bits - simulated
85
4
115
47
1
SMALL
LARGE-S
LARGE-N

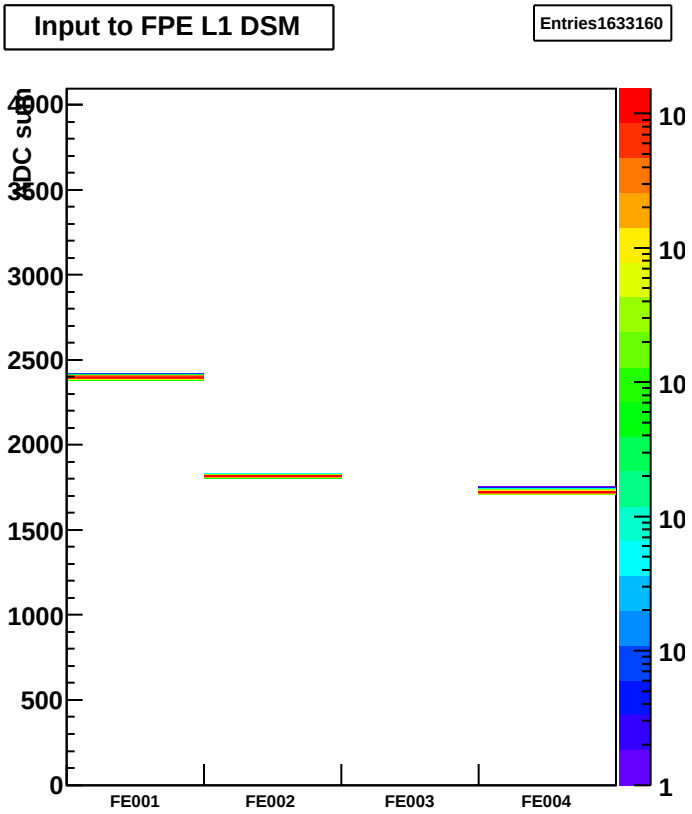

Input to FPD L2 DSM
Entries 2449740
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.306528e+07
ADC
channel

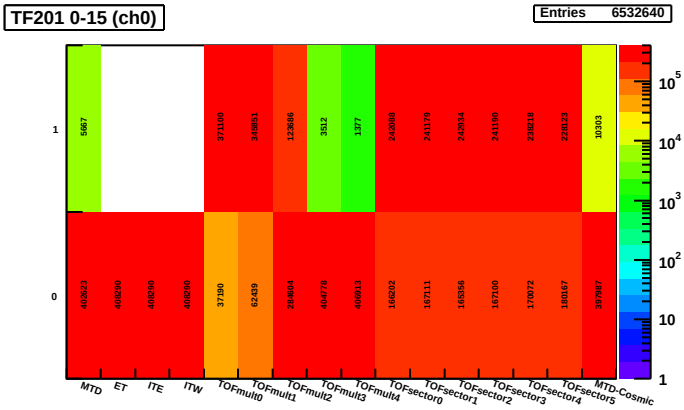

Input to FE002 QT board
Entries 1.306528e+07
ADC
channel


Input to FE003 QT board
Entries 1.306528e+07
ADC
channel

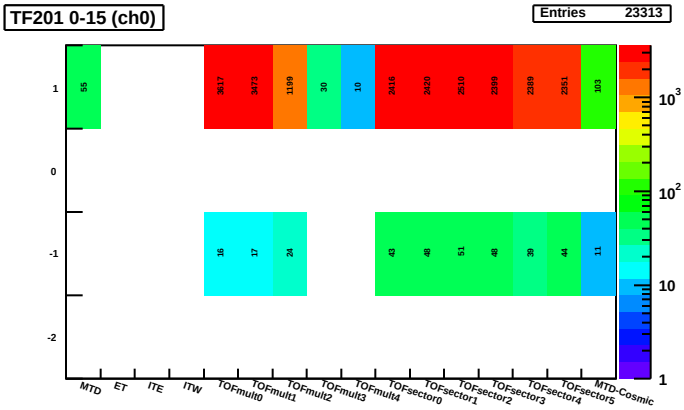

Input to FE004 QT board
Entries 1.306528e+07
ADC
channel


Input to FPE L1 DSM
Entries1633160
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

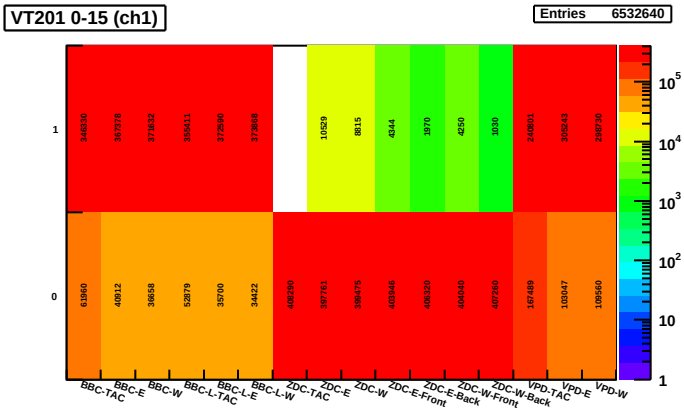
TF201 0-15 (ch0)



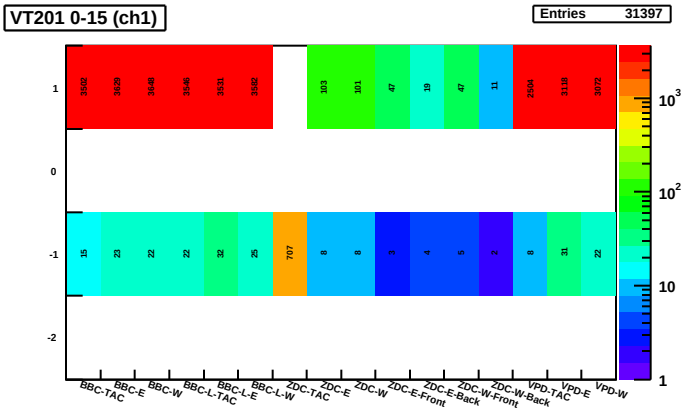
TF201 0-15 (ch0)



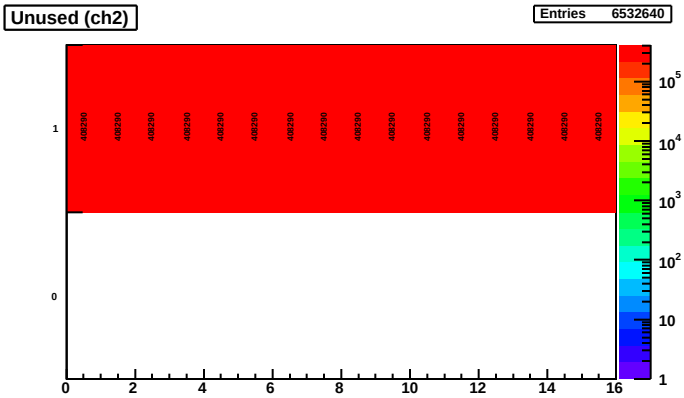
VT201 0-15 (ch1)

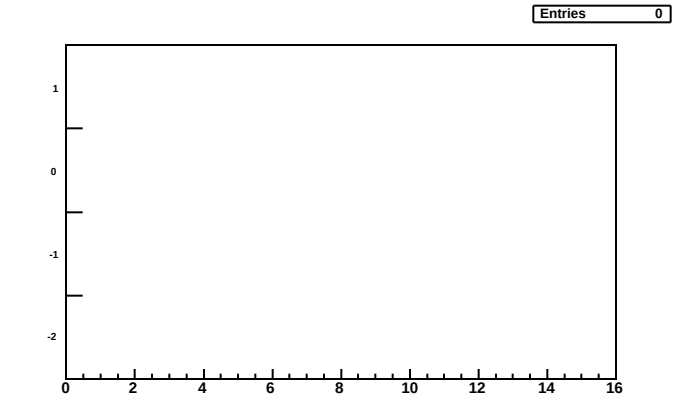


VT201 0-15 (ch1)

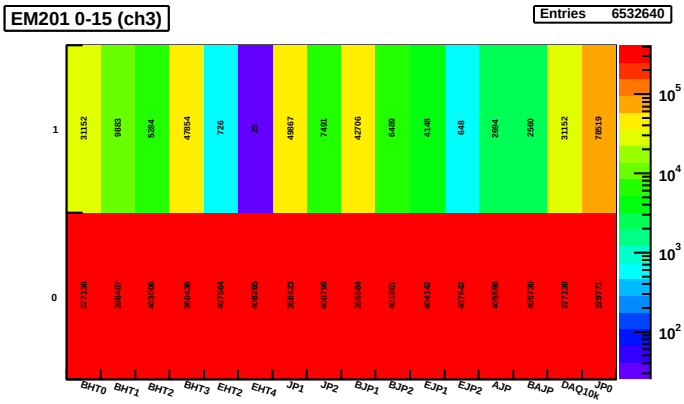


Unused (ch2)





EM201 0-15 (ch3)



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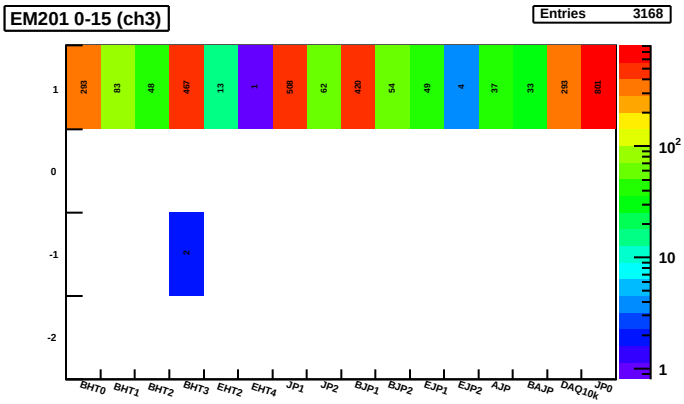
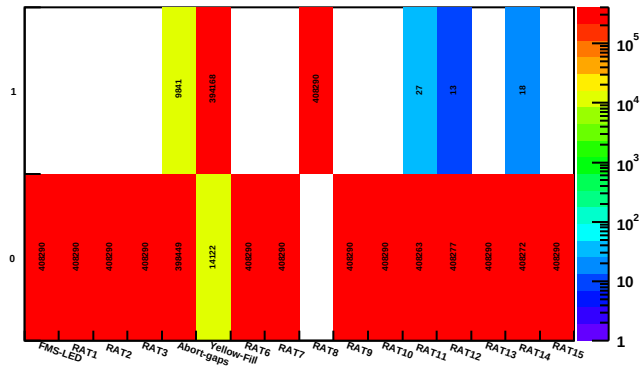
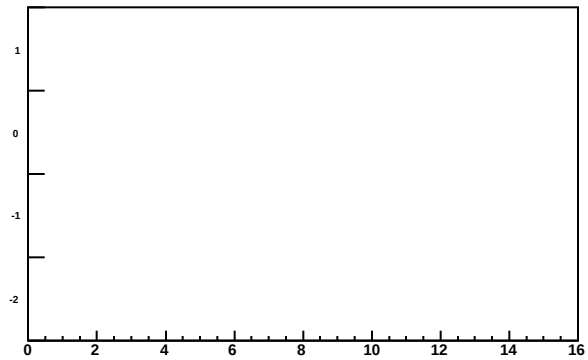


RAT board (ch4)





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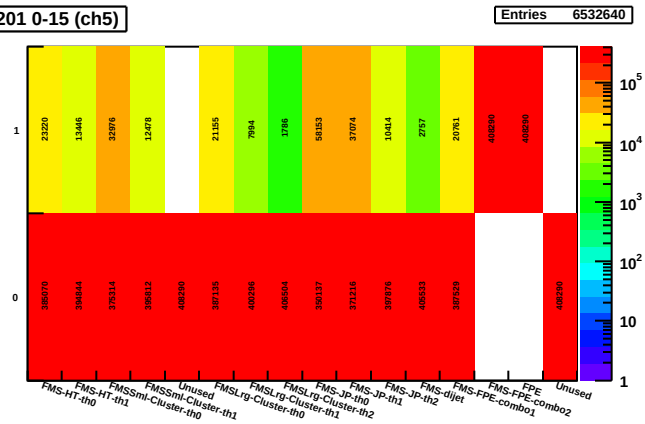

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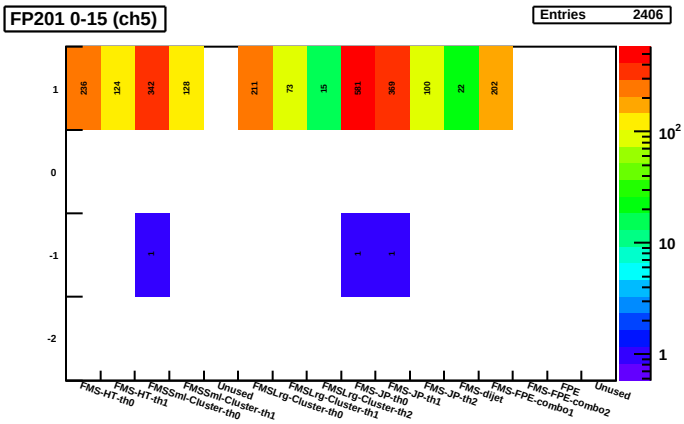

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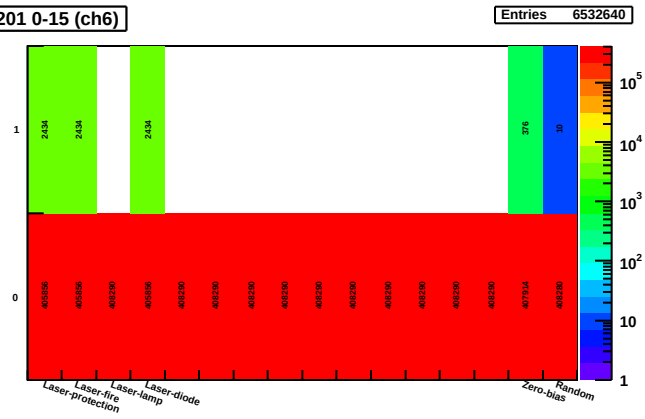

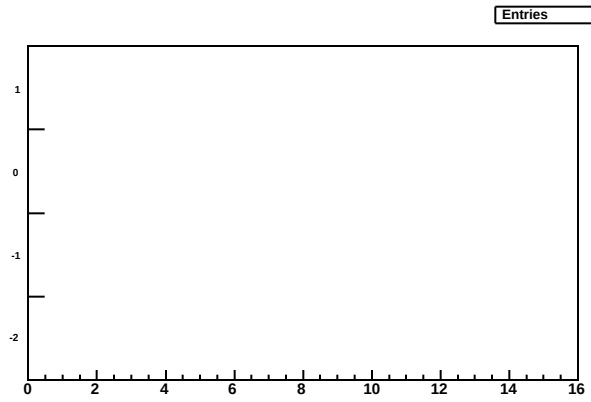
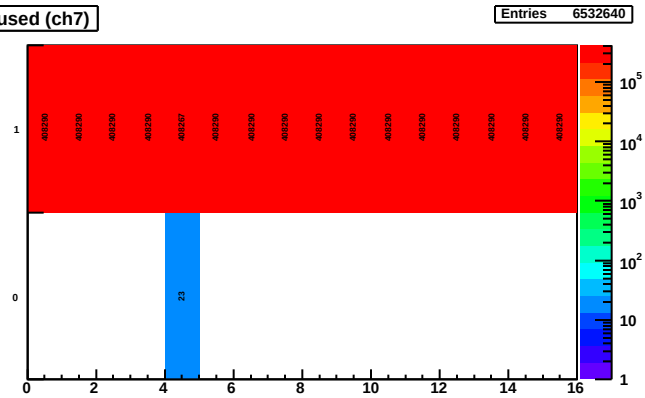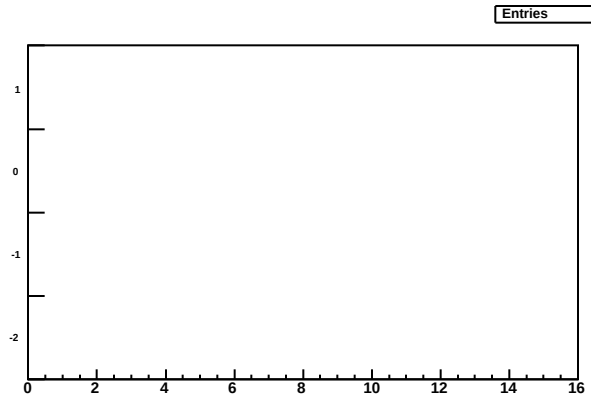
Unused (ch7)