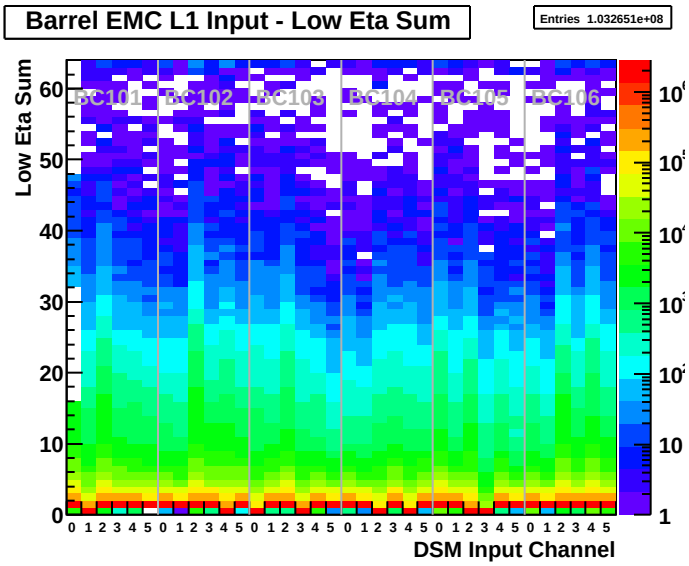
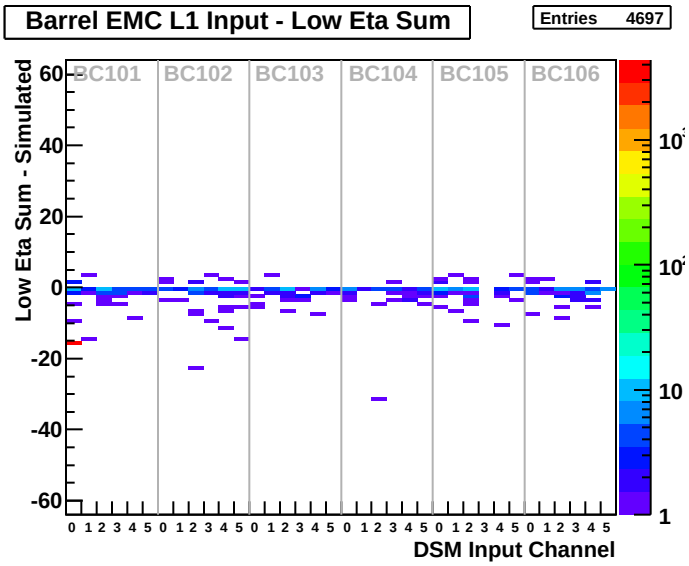
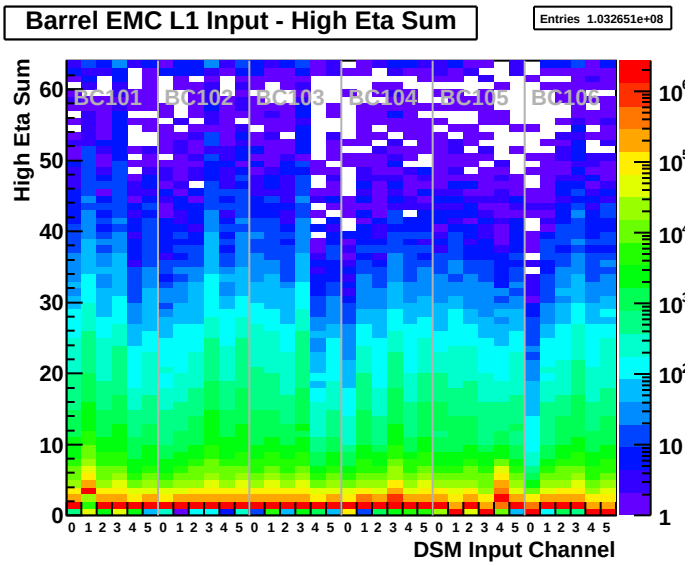
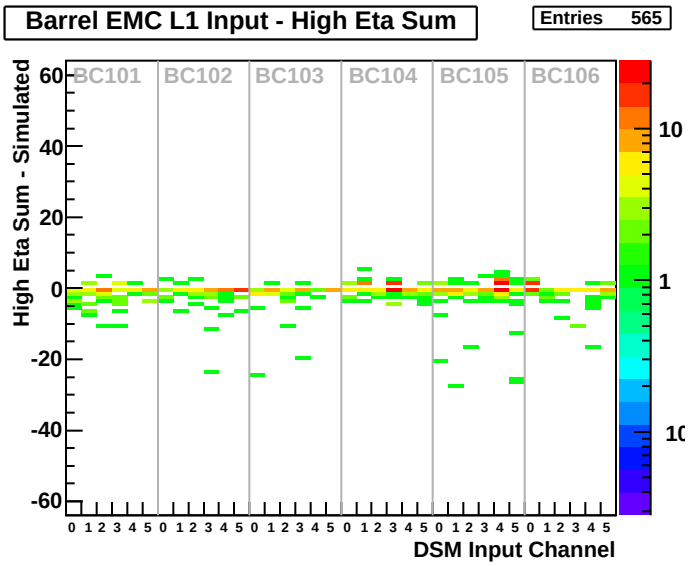
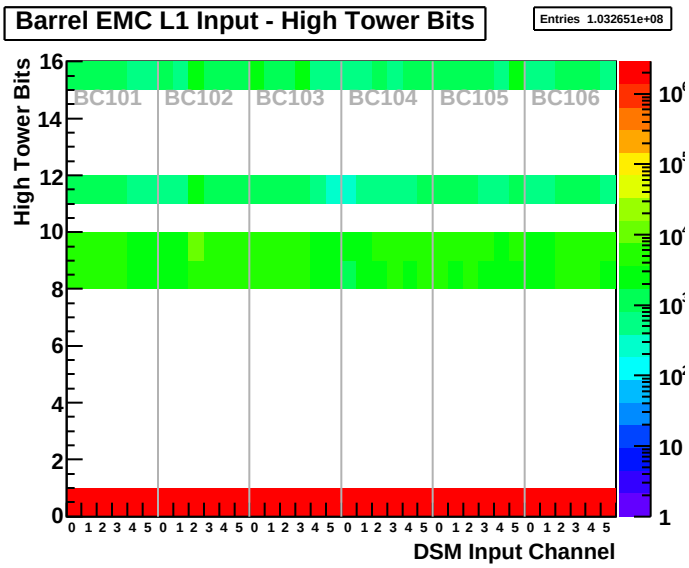
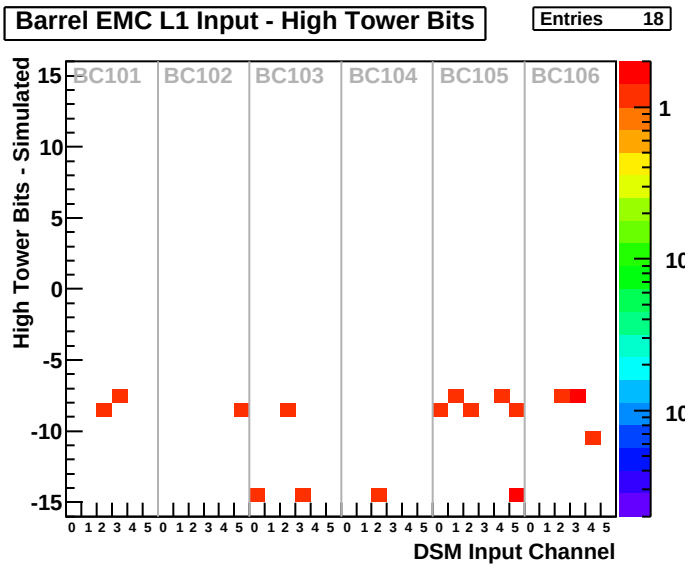

Barrel EMC L1 Input - Low Eta Sum
Entries 1.032651e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

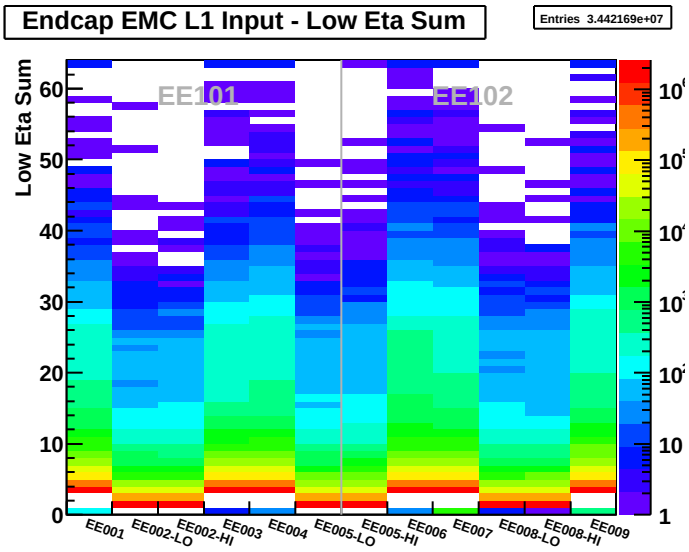

Barrel EMC L1 Input - Low Eta Sum
Entries 4697
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

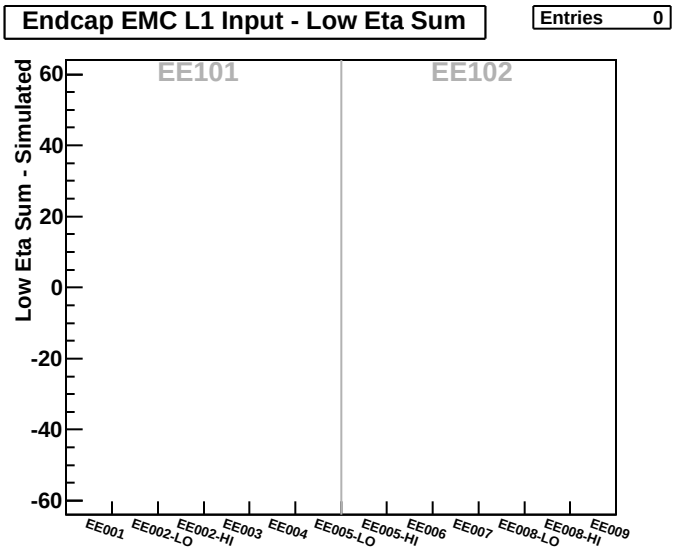

Barrel EMC L1 Input - High Eta Sum
Entries 1.032651e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

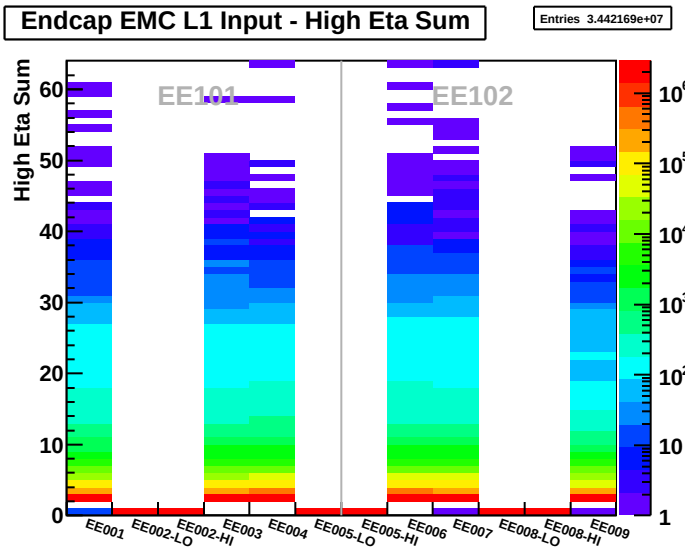

Barrel EMC L1 Input - High Eta Sum
Entries 565
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

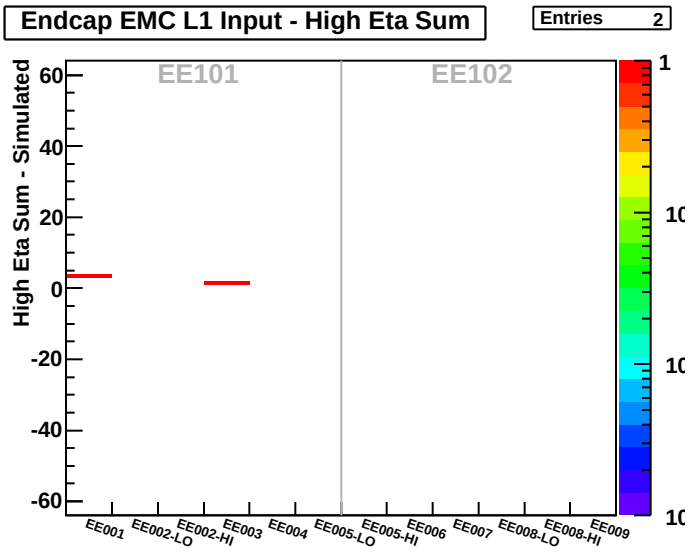

Barrel EMC L1 Input - High Tower Bits
Entries 1.032651e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

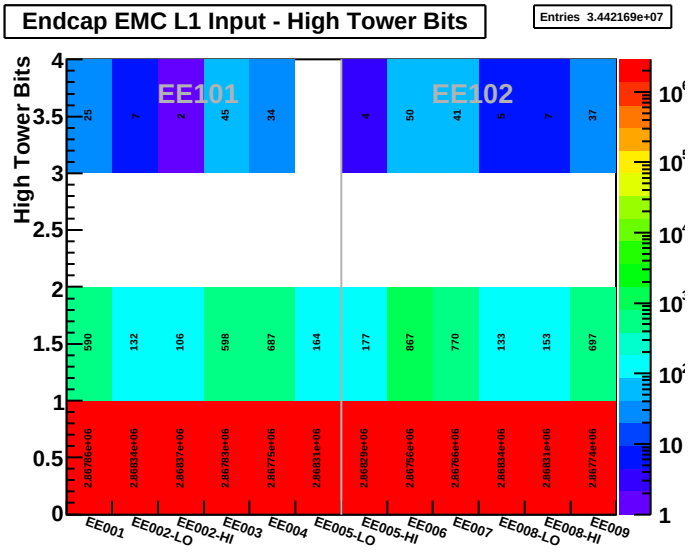

Barrel EMC L1 Input - High Tower Bits
Entries 18
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

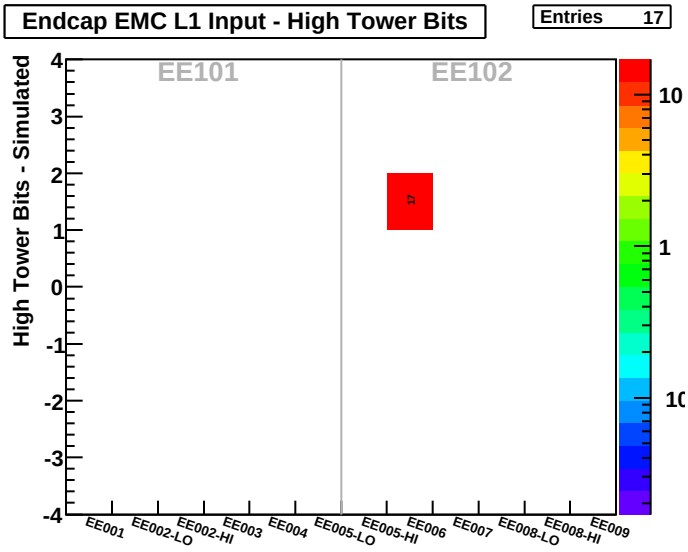

Endcap EMC L1 Input - Low Eta Sum
Entries 3.442169e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

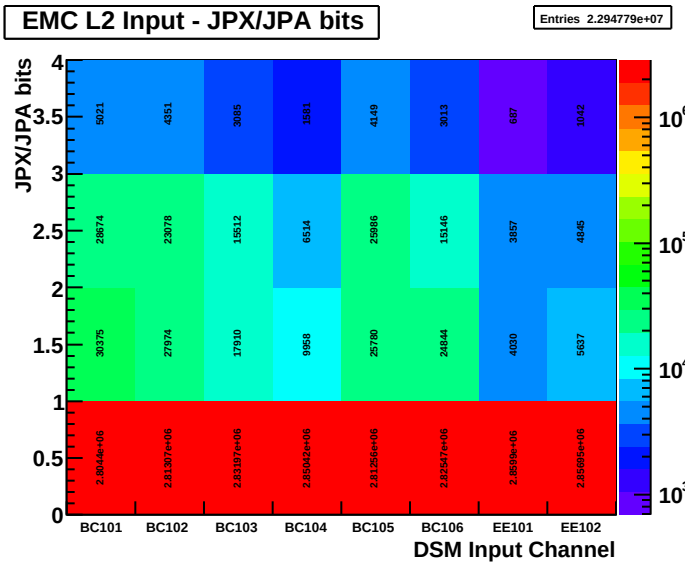

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

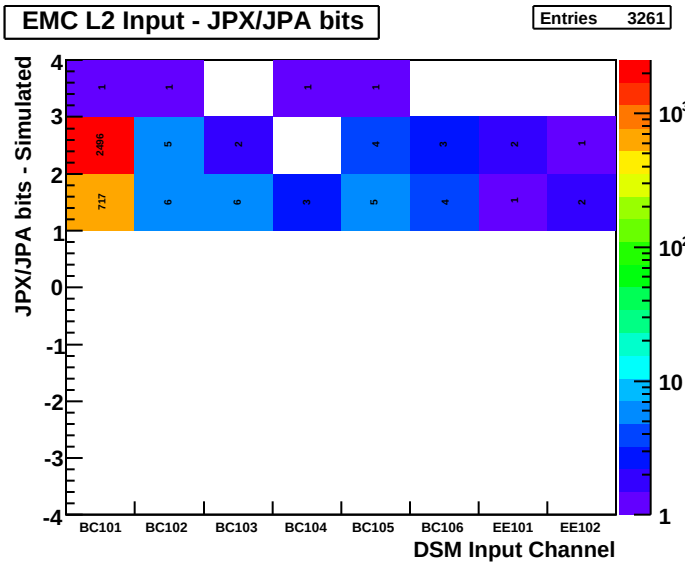

Endcap EMC L1 Input - High Eta Sum
Entries 3.442169e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

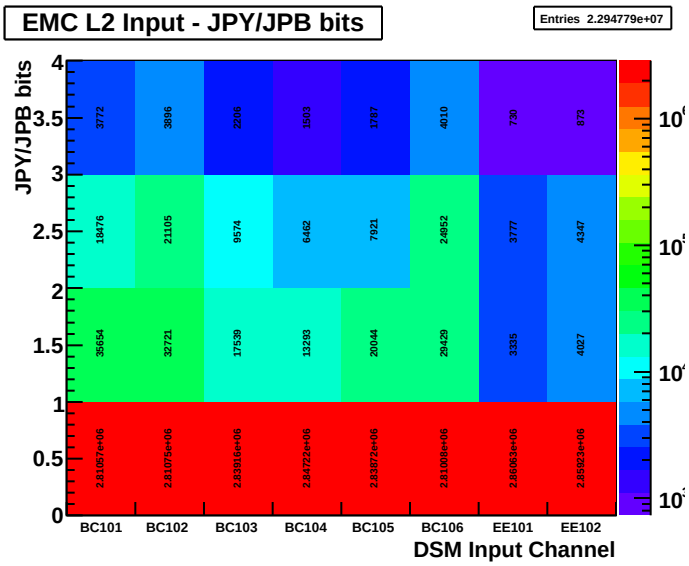

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

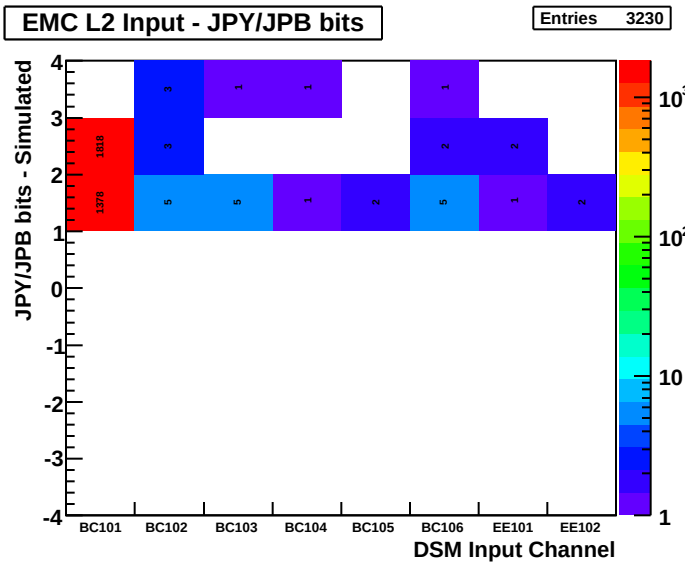

Endcap EMC L1 Input - High Tower Bits
Entries 3.442169e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

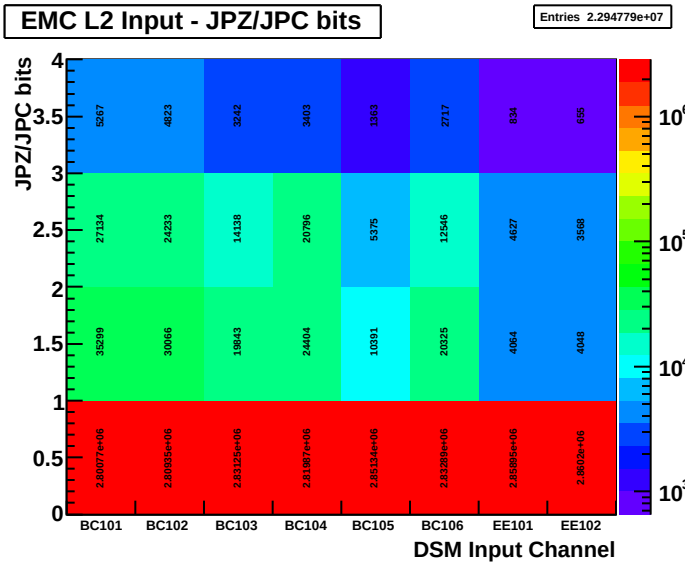

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

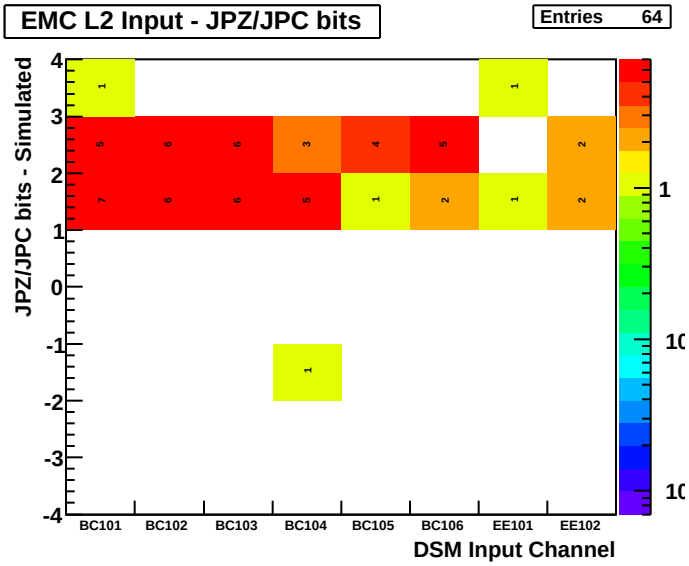

EMC L2 Input - JPX/JPA bits
Entries 2.294779e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5021 4351 3085 1581 4149 3013 687 1042
28674 23078 15512 6514 25986 15146 3857 4845
30375 27974 17910 9958 25780 24844 4030 5637
2.8044e+06 2.81307e+06 2.83197e+06 2.85942e+06 2.81256e+06 2.82547e+06 2.8599e+06 2.85605e+06

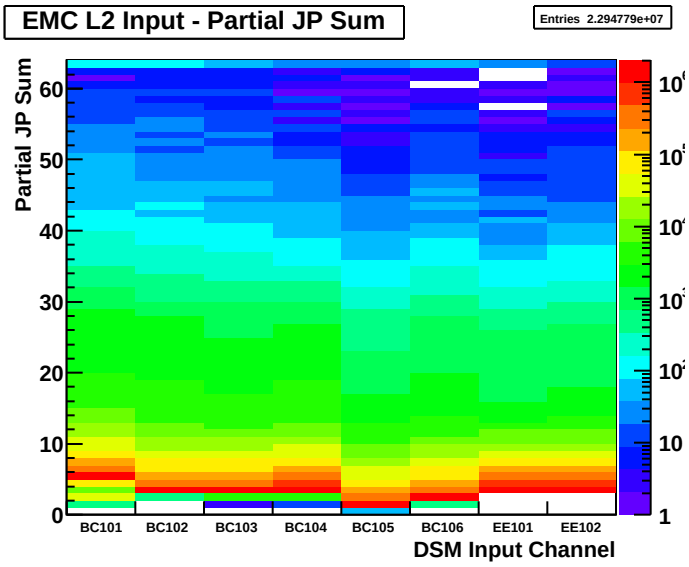

EMC L2 Input - JPX/JPA bits
Entries 3261
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 1
2496 5 2 4 3 2 1
717 6 6 3 5 4 1 2

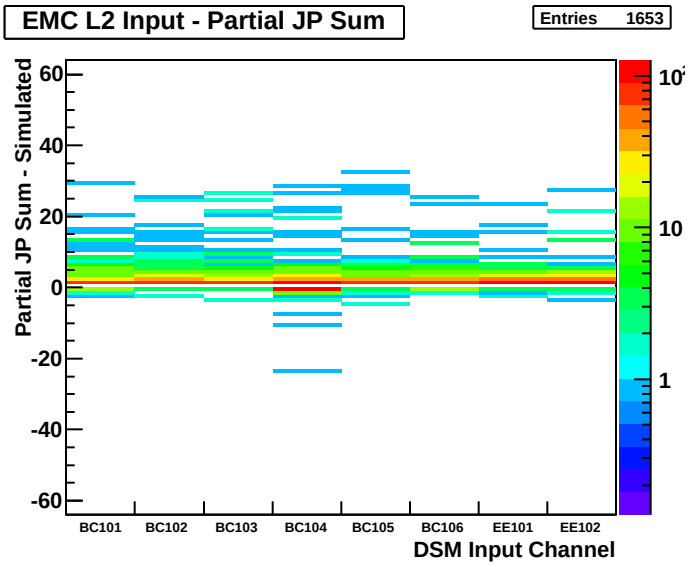

EMC L2 Input - JPY/JPB bits
Entries 2.294779e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3772 3896 2206 1503 1767 4010 730 873
18476 21105 9574 6462 7921 24952 3777 4347
35654 32721 17539 13293 20044 29429 3335 4027
2.81057e+06 2.81075e+06 2.83916e+06 2.84722e+06 2.83872e+06 2.81098e+06 2.86063e+06 2.85923e+06

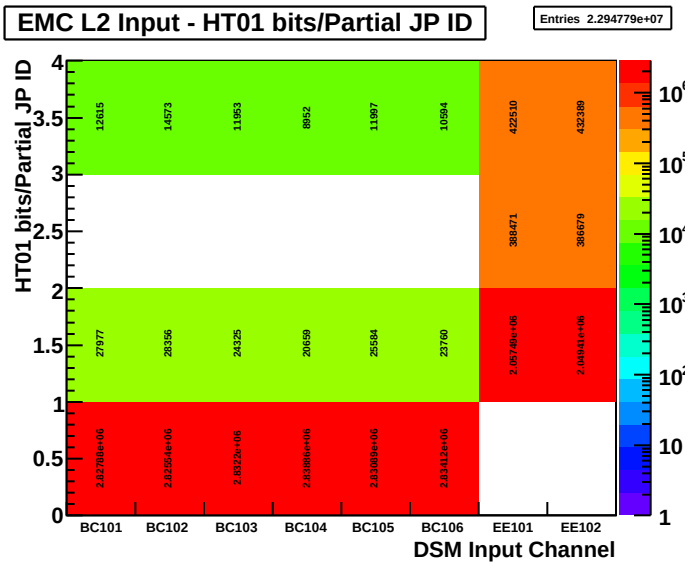

EMC L2 Input - JPY/JPB bits
Entries 3230
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 1 1 1
1818 3 2 2
1378 5 5 1 2 5 1 2

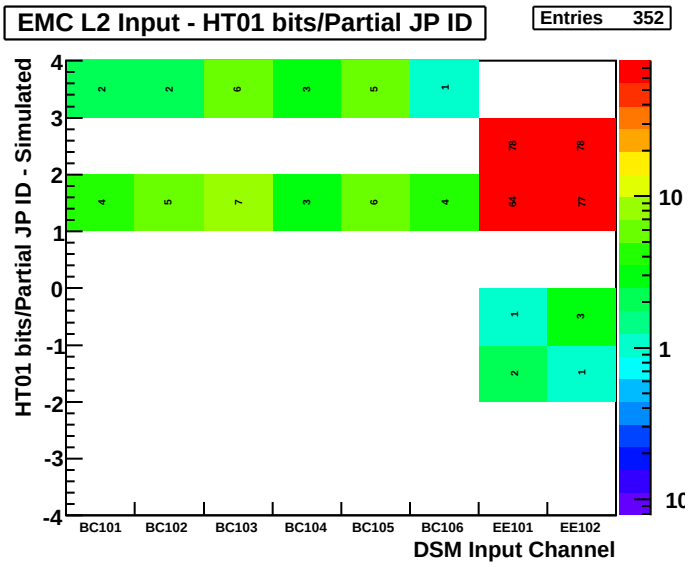

EMC L2 Input - JPZ/JPC bits
Entries 2.294779e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5267 4823 3242 3403 1363 2717 834 655
27134 24233 14138 20796 5375 12546 4627 3568
35299 30066 19843 24404 10391 20325 4064 4048
2.80077e+06 2.80935e+06 2.83125e+06 2.81987e+06 2.85134e+06 2.83289e+06 2.85895e+06 2.8602e+06

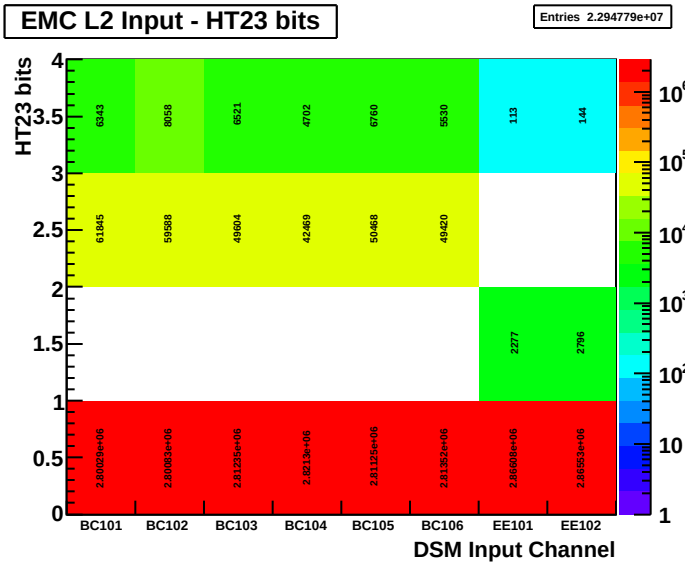

EMC L2 Input - JPZ/JPC bits
Entries 64
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1
5 6 6 3 4 5 2
7 6 6 5 1 2 1 2
1

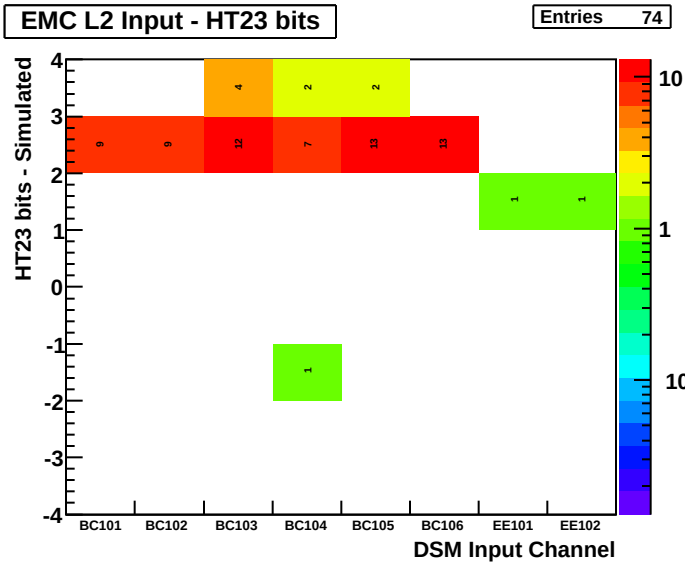

EMC L2 Input - Partial JP Sum
Entries 2.294779e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

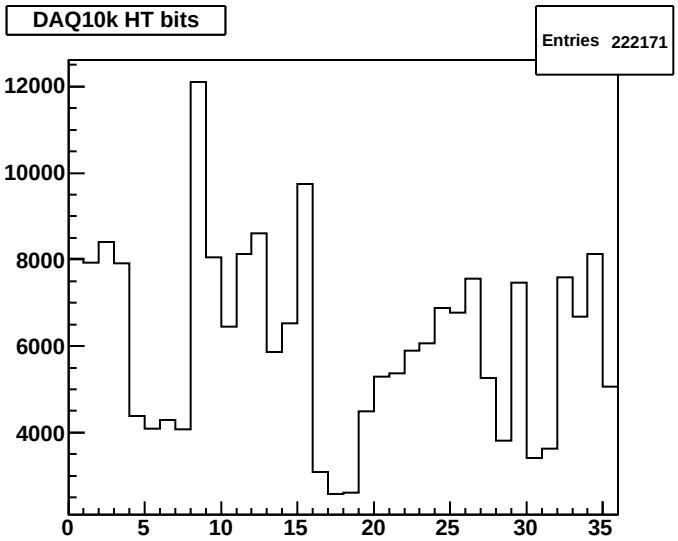

EMC L2 Input - Partial JP Sum
Entries 1653
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

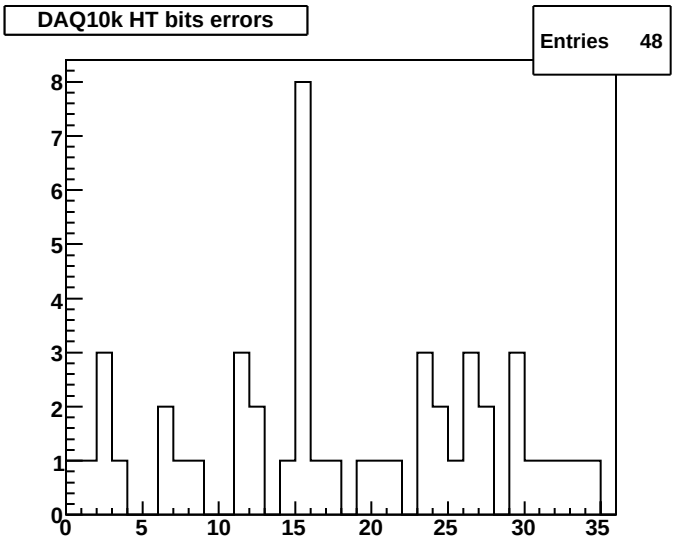

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.294779e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

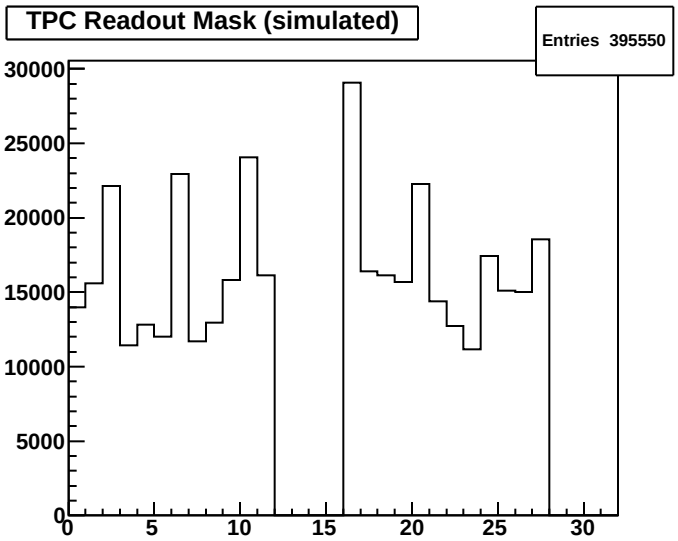

EMC L2 Input - HT01 bits/Partial JP ID
Entries 352
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

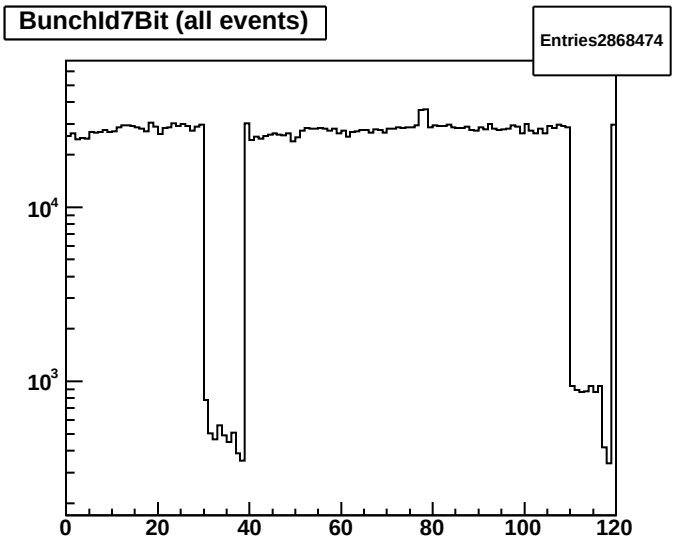

EMC L2 Input - HT23 bits
Entries 2.294779e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

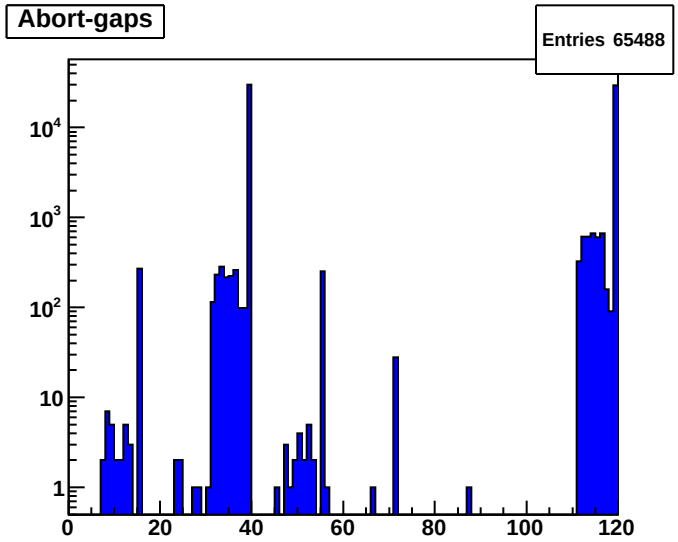

EMC L2 Input - HT23 bits
Entries 74
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

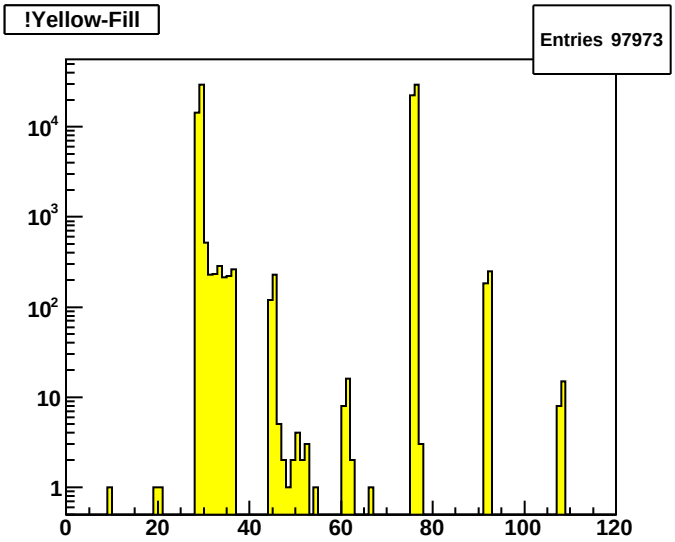

DAQ10k HT bits
Entries 222171

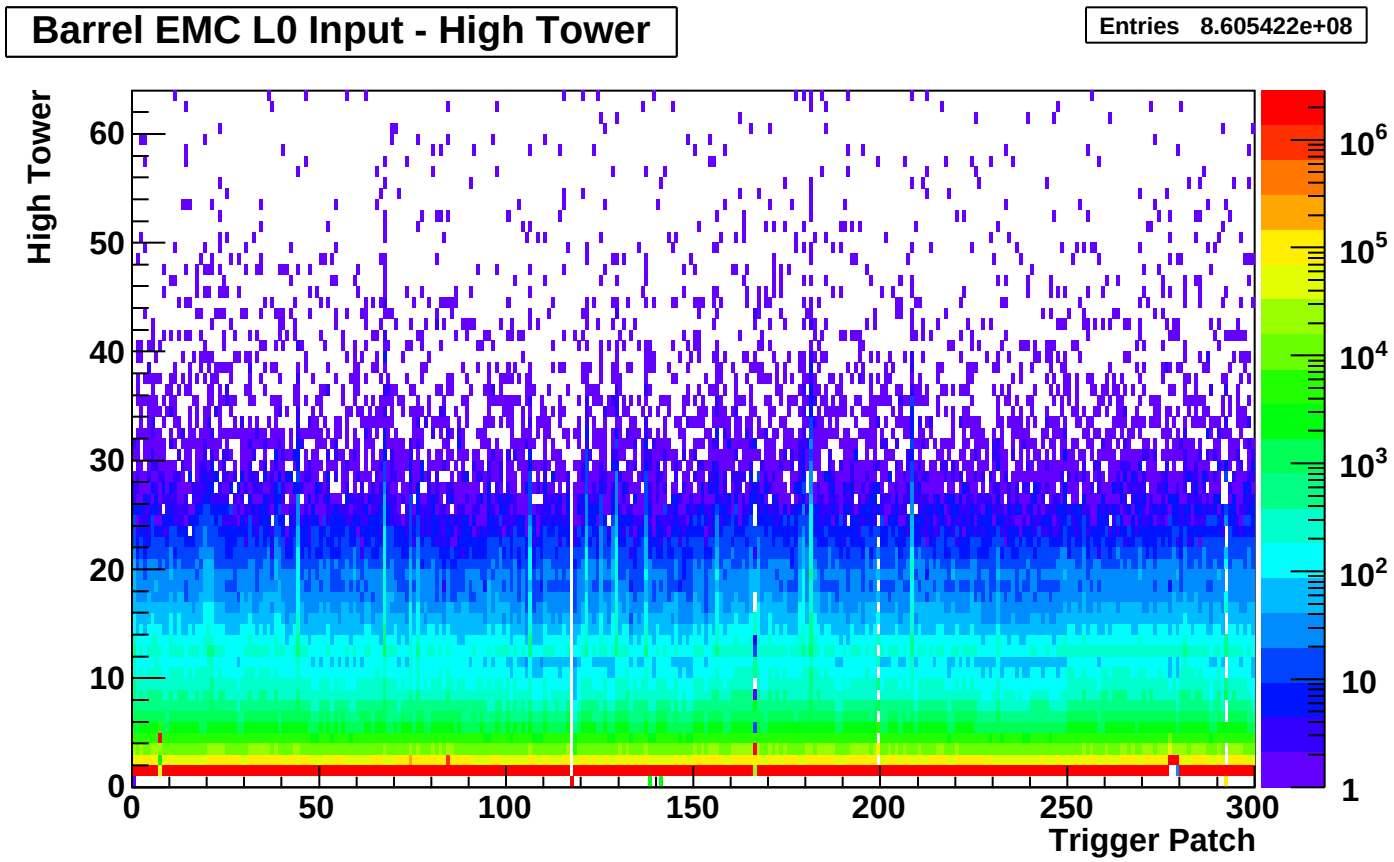

DAQ10k HT bits errors
Entries 48

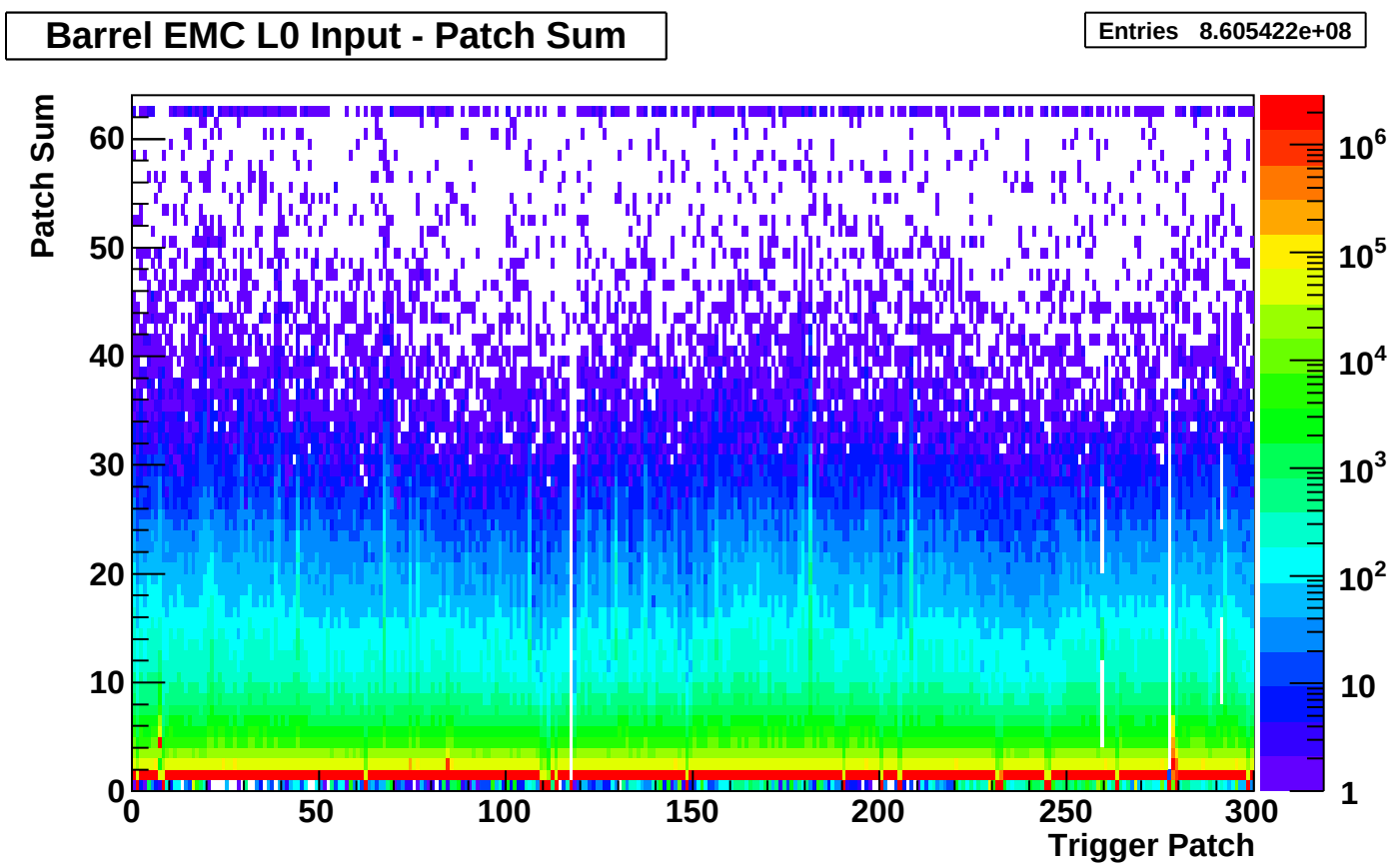

TPC Readout Mask (simulated)
Entries 395550

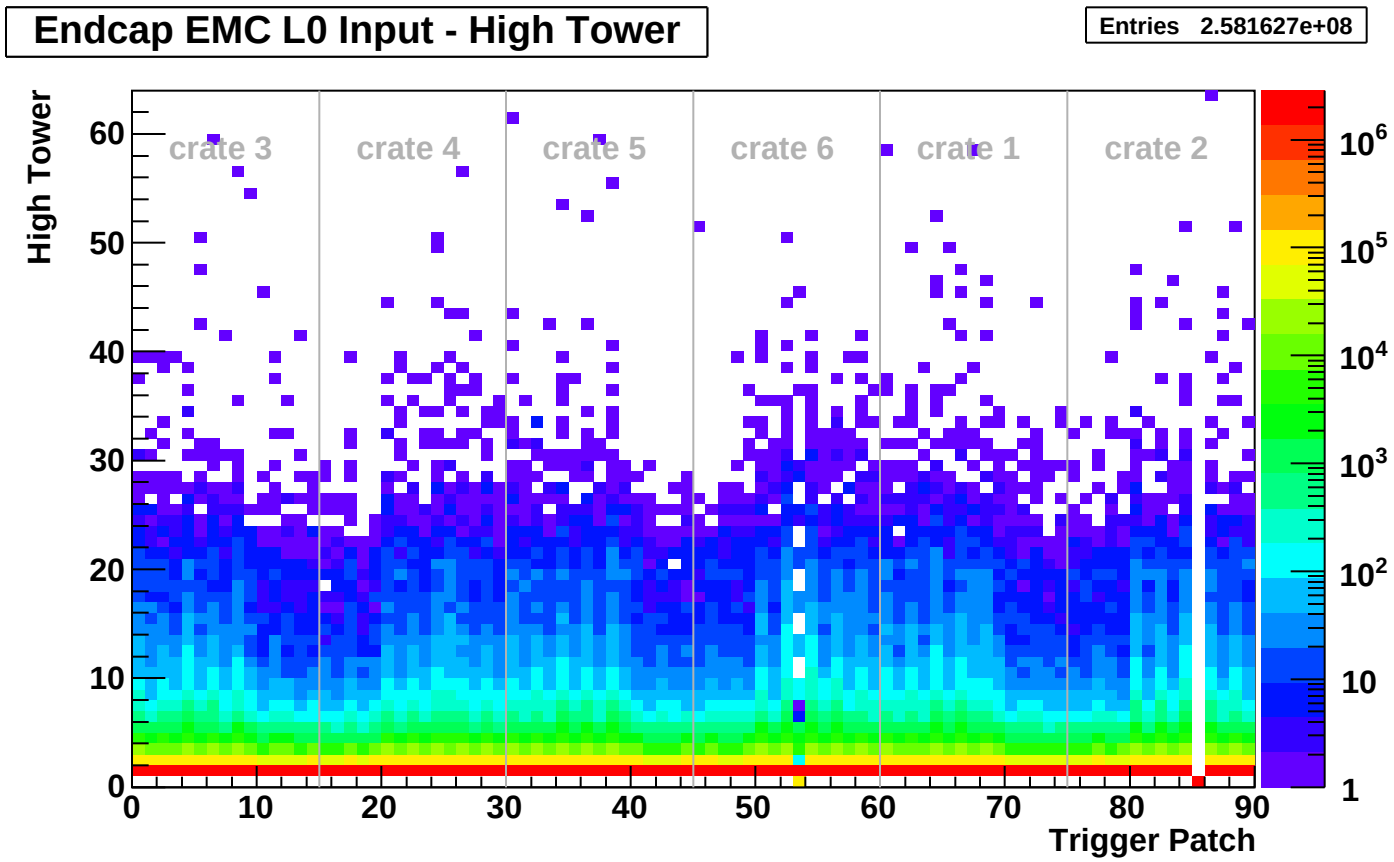

BunchId7Bit (all events)
Entries2868474

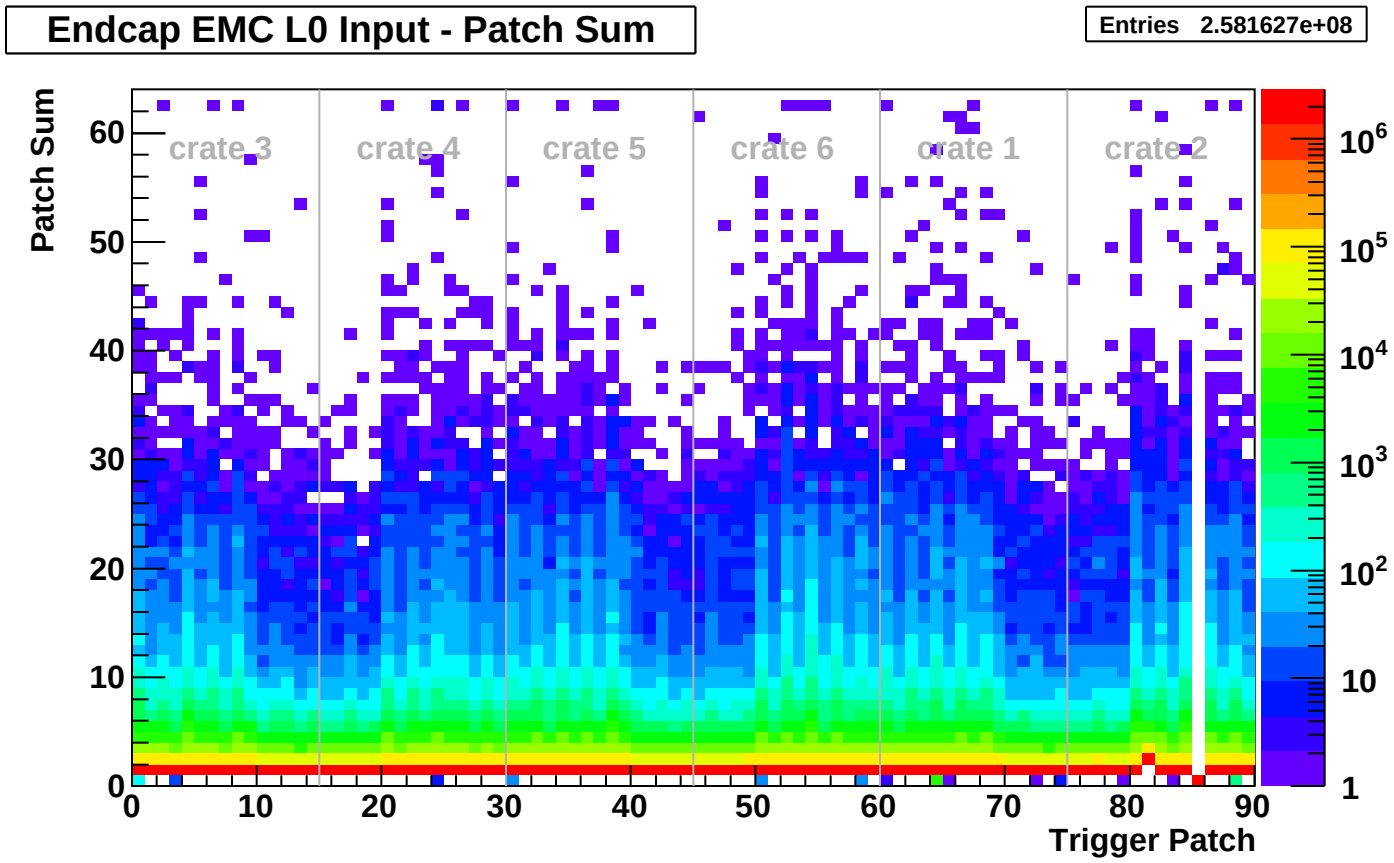

Abort-gaps
Entries 65488

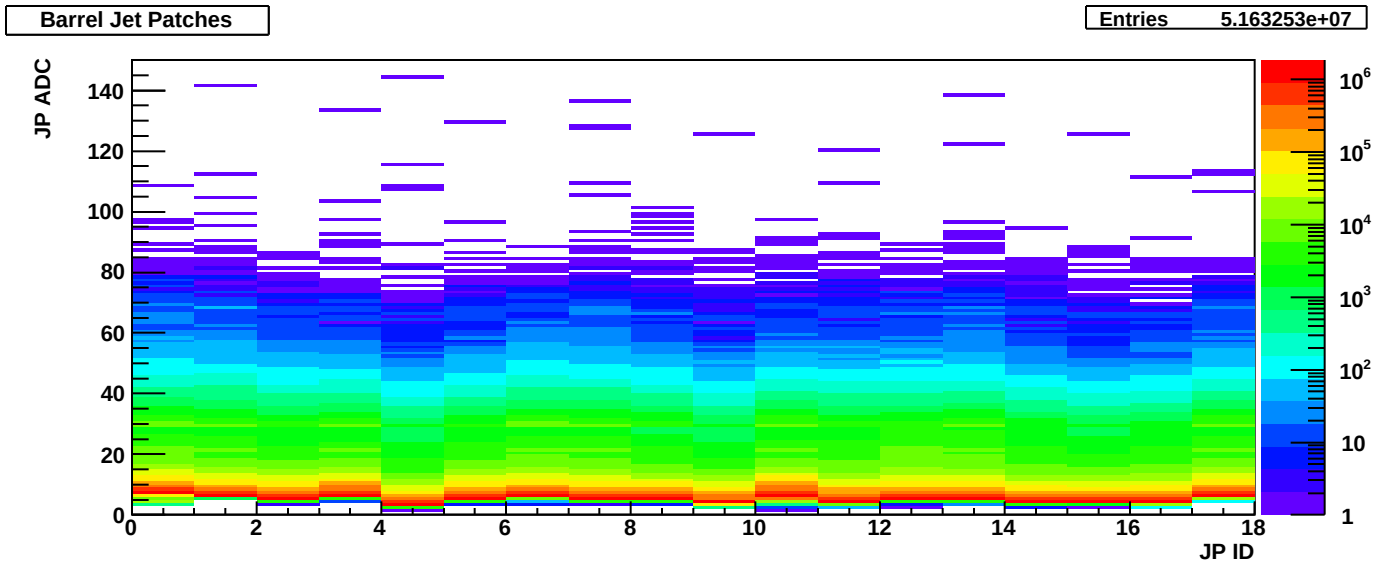

!Yellow-Fill
Entries 97973

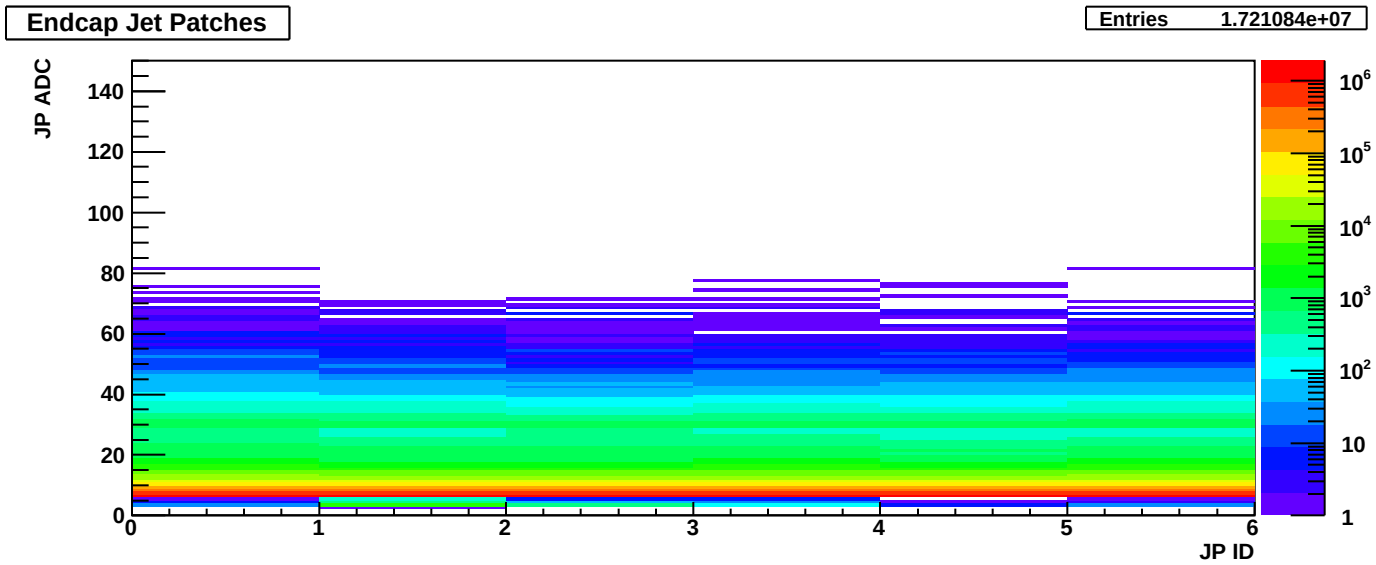

Barrel EMC L0 Input - High Tower
Entries 8.605422e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

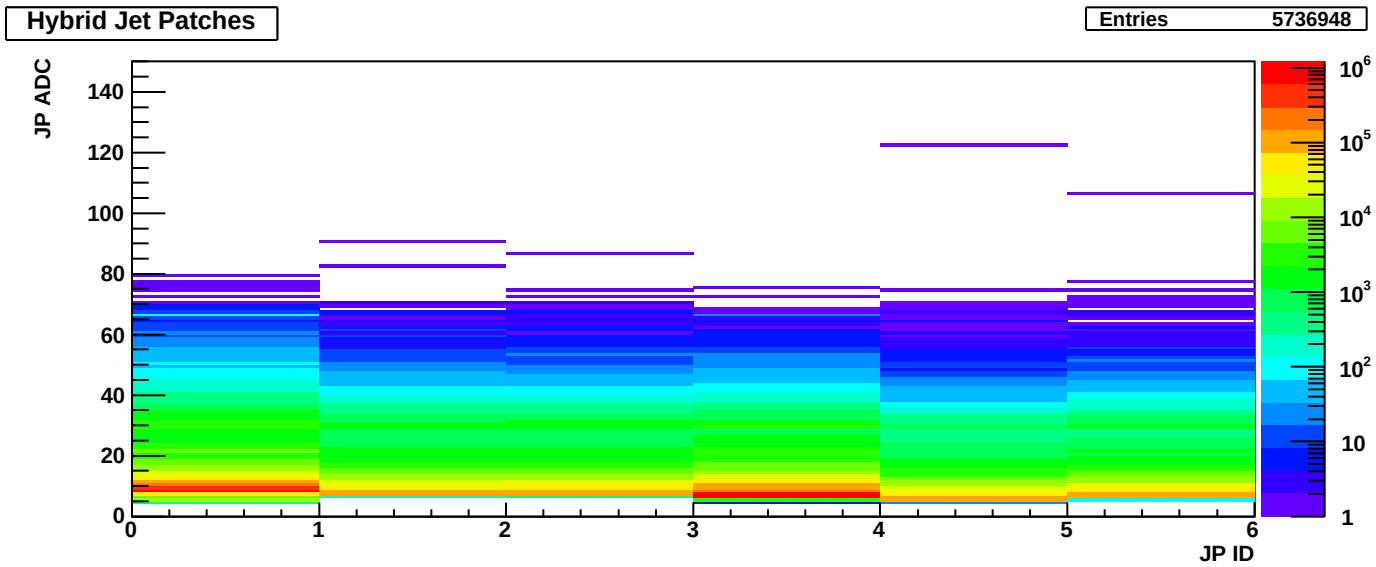

Barrel EMC L0 Input - Patch Sum
Entries 8.605422e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

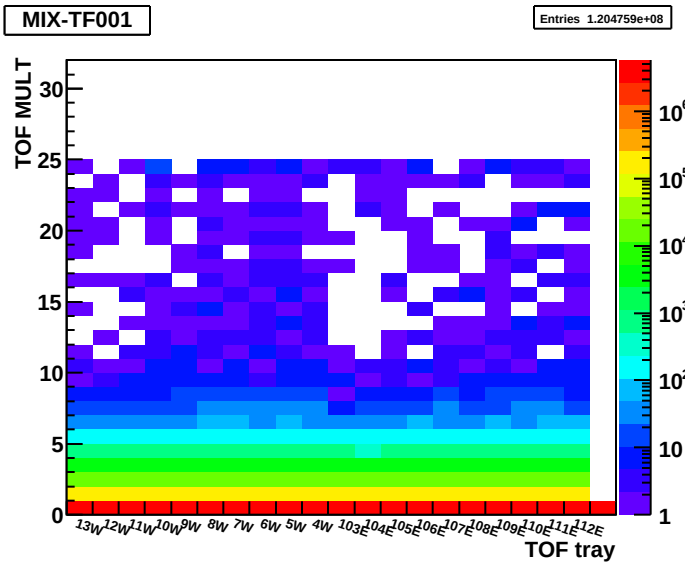

Endcap EMC L0 Input - High Tower
Entries 2.581627e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

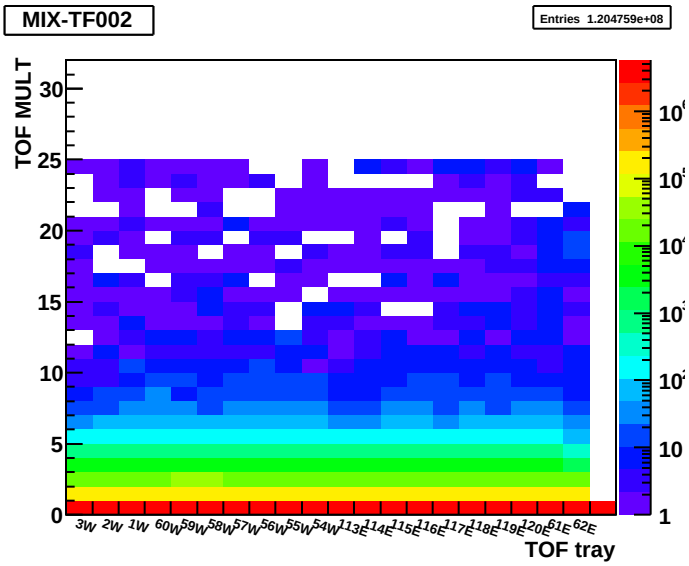

Endcap EMC L0 Input - Patch Sum
Entries 2.581627e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

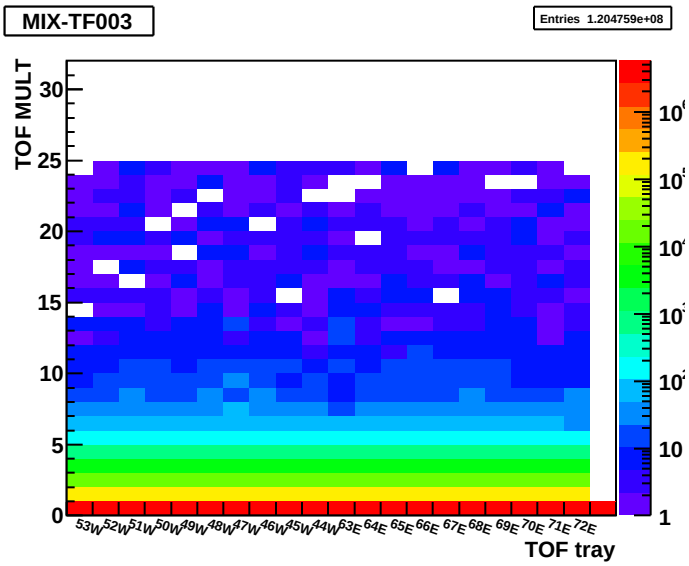

Barrel Jet Patches
Entries 5.163253e+07
JP ADC
JP ID

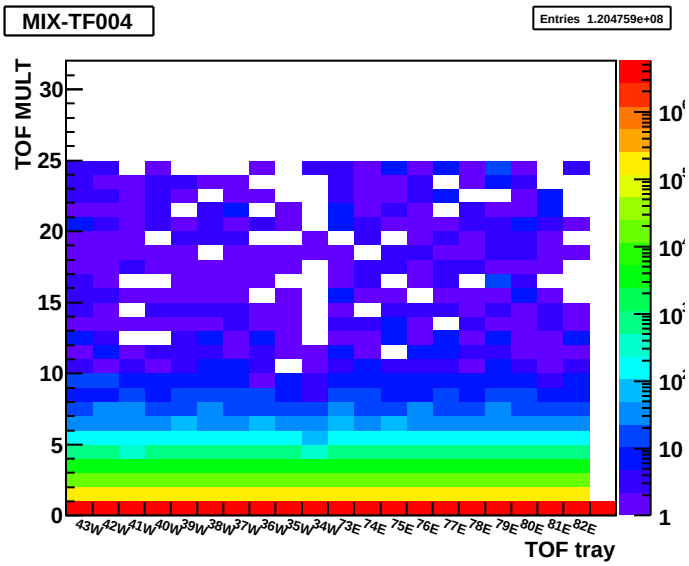

Endcap Jet Patches
Entries 1.721084e+07
JP ADC
JP ID

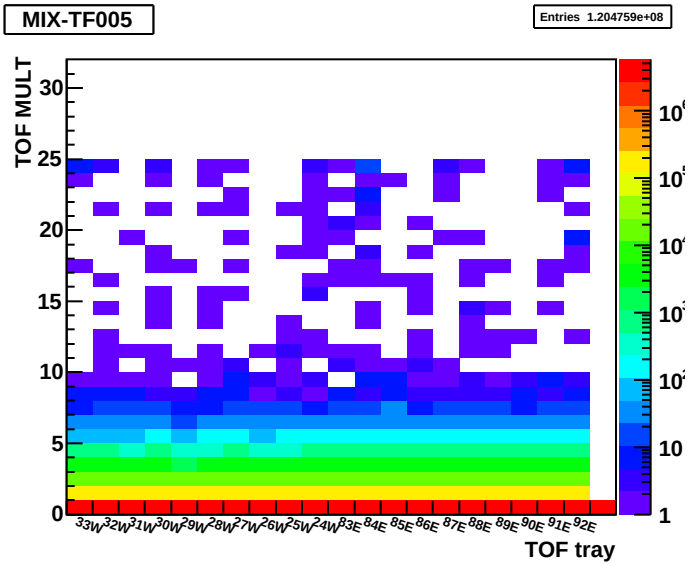

Hybrid Jet Patches
Entries 5736948
JP ADC
JP ID

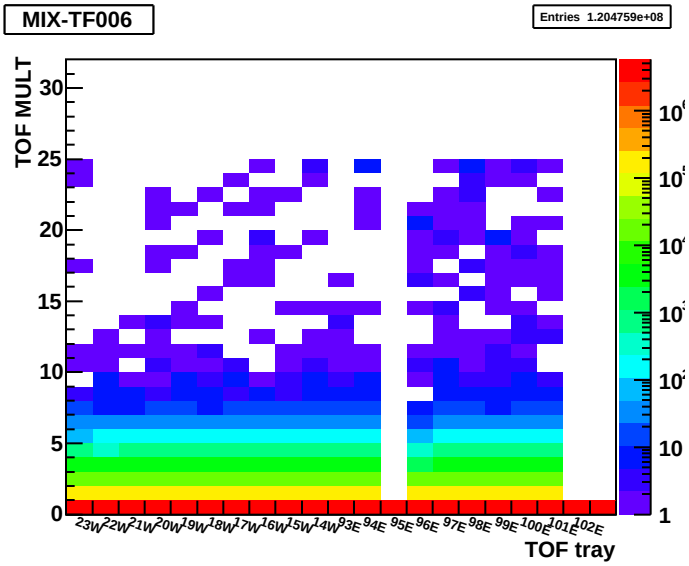

MIX-TF001
Entries 1.204759e+08
TOF MULT
TOF tray

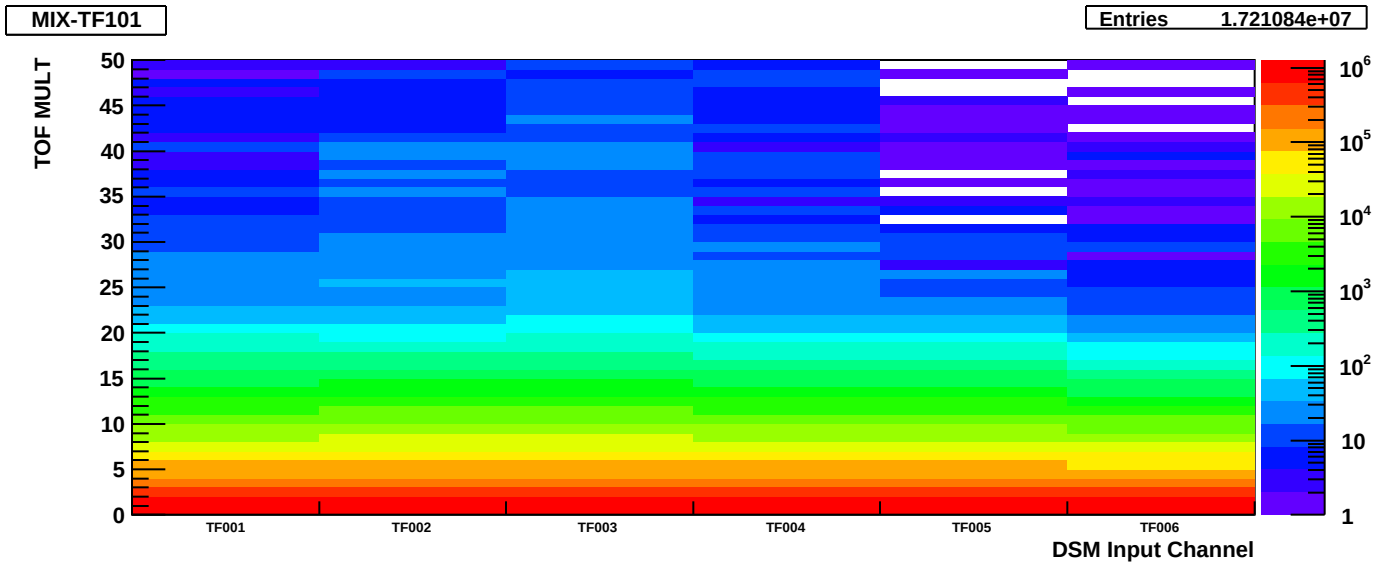

MIX-TF002
Entries 1.204759e+08
TOF MULT
TOF tray

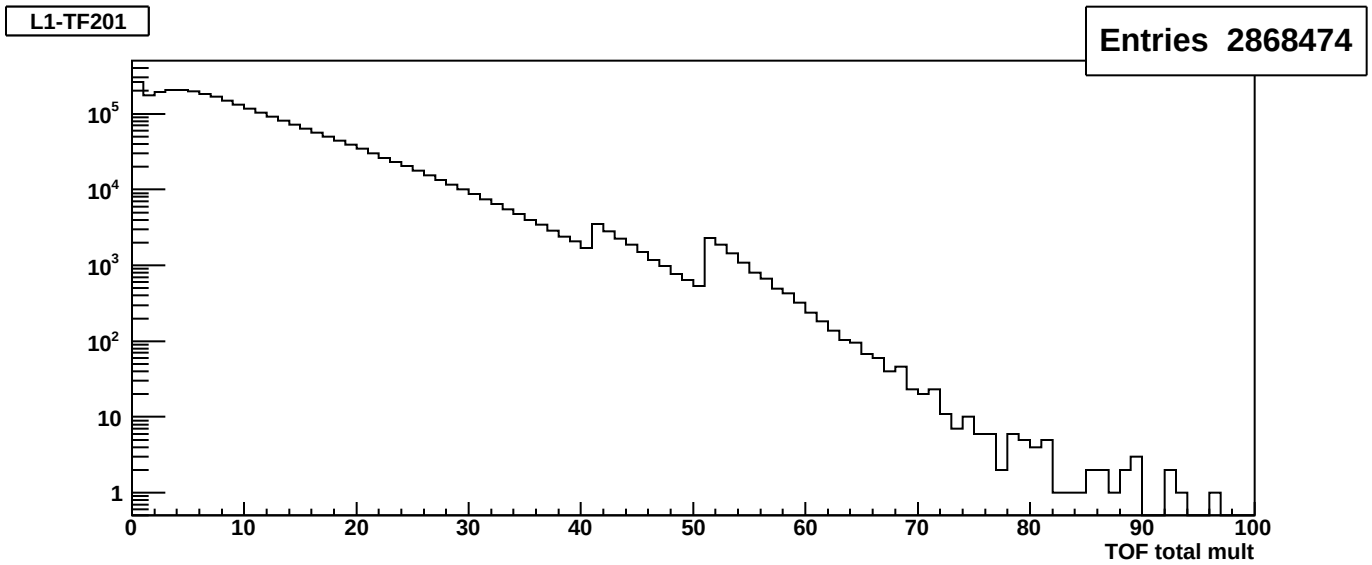

MIX-TF003
Entries 1.204759e+08
TOF MULT
TOF tray

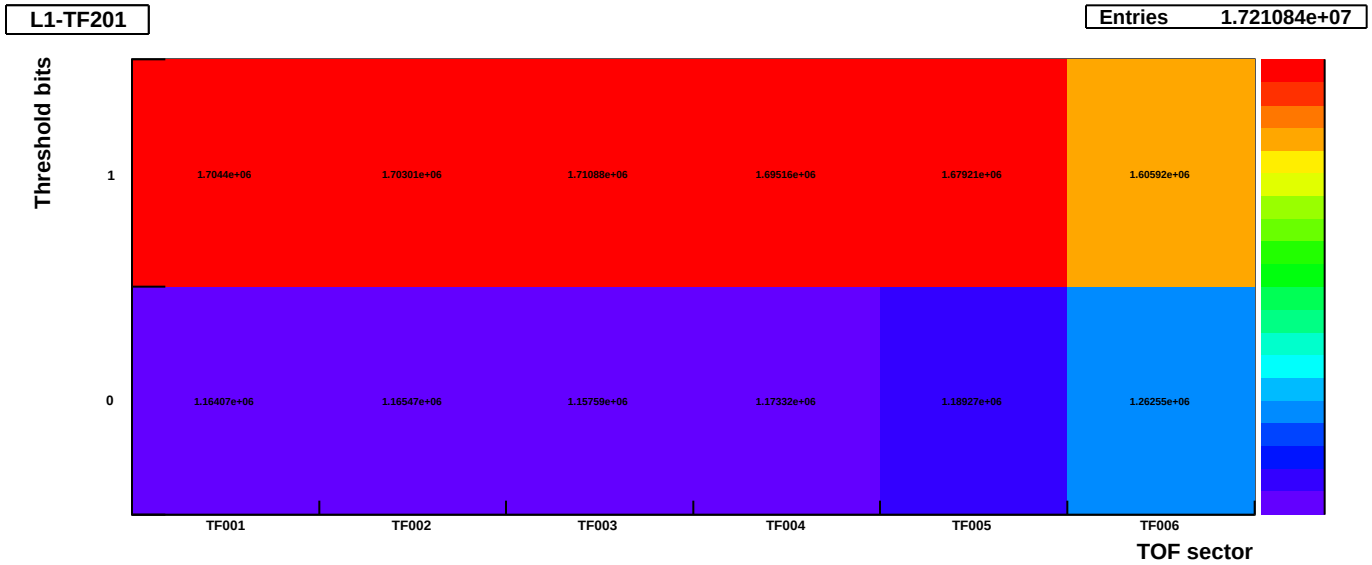

MIX-TF004
Entries 1.204759e+08
TOF MULT
TOF tray

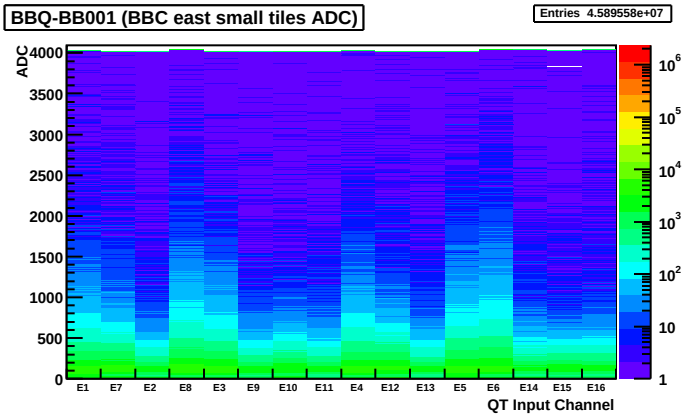

MIX-TF005
Entries 1.204759e+08
TOF MULT
TOF tray

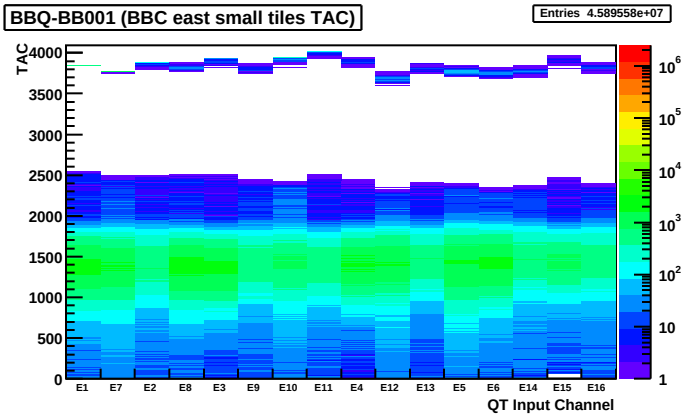

MIX-TF006
Entries 1.204759e+08
TOF MULT
TOF tray

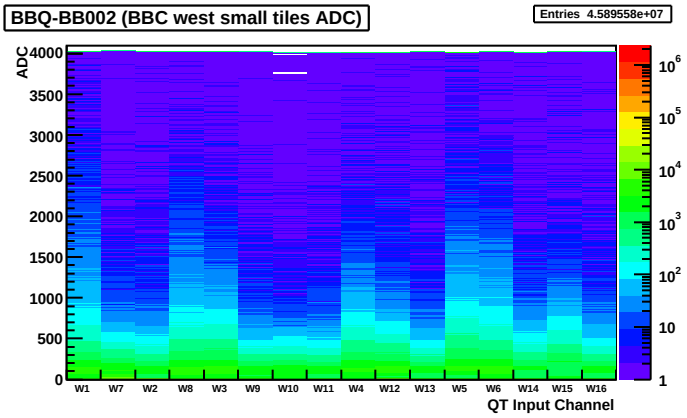

MIX-TF101
Entries 1.721084e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

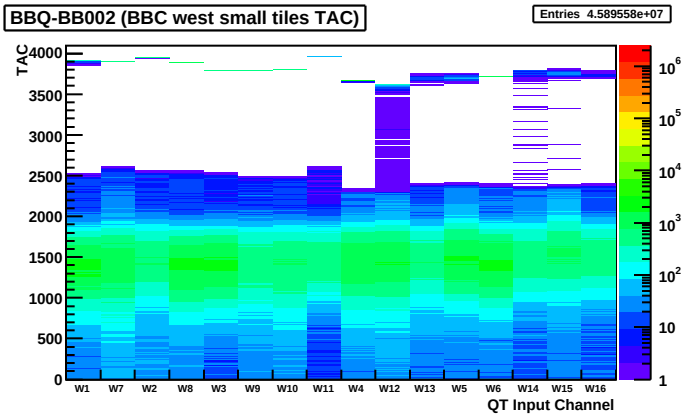

L1-TF201
Entries 2868474
TOF total mult

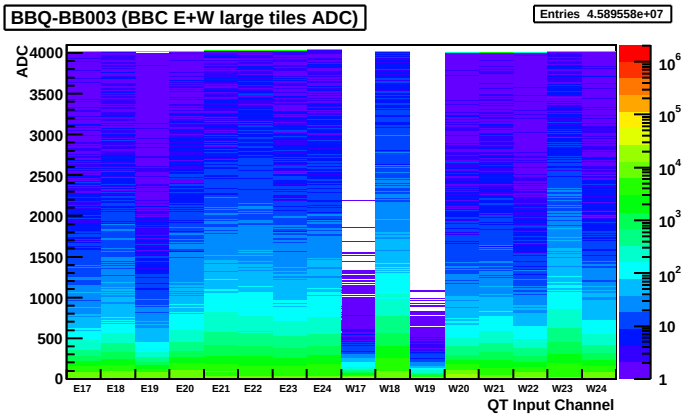

L1-TF201
Entries 1.721084e+07
Threshold bits
1.7044e+06
1.70301e+06
1.71088e+06
1.69516e+06
1.67921e+06
1.60592e+06
1.16407e+06
1.16547e+06
1.15759e+06
1.17332e+06
1.18927e+06
1.26255e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

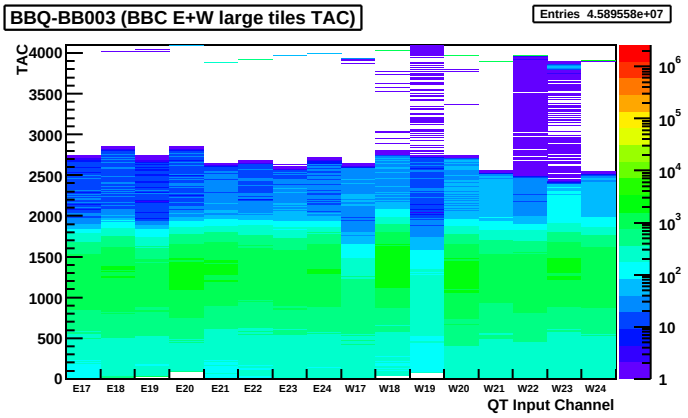

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.589558e+07
ADC
QT Input Channel

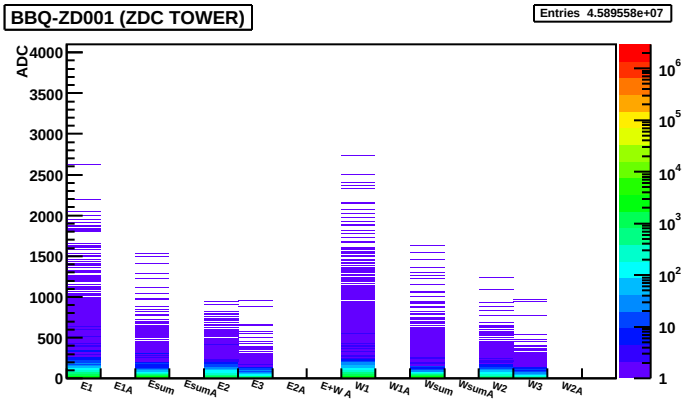

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.589558e+07
TAC
QT Input Channel

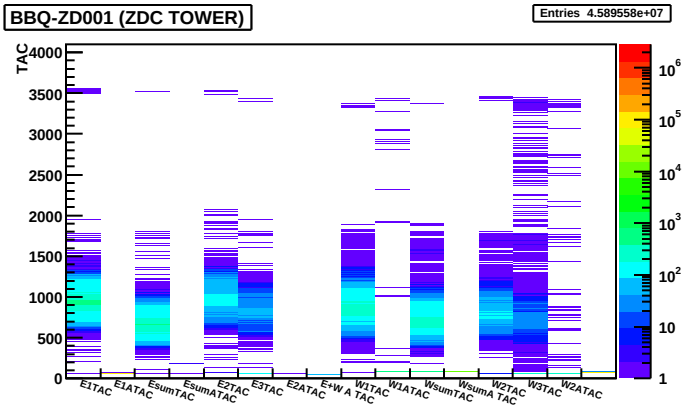

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.589558e+07
ADC
QT Input Channel

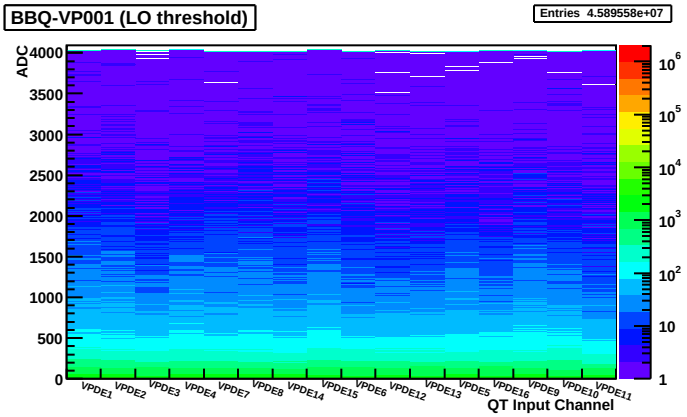

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.589558e+07
TAC
QT Input Channel

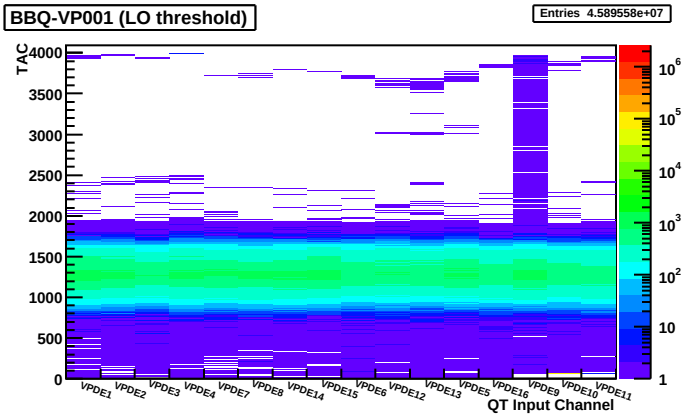

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.589558e+07
ADC
QT Input Channel

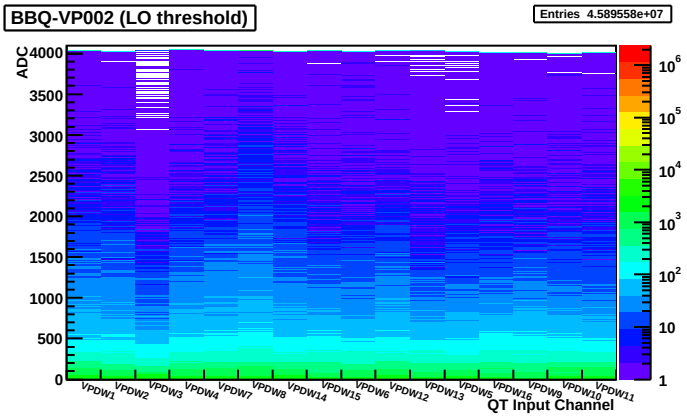

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.589558e+07
TAC
QT Input Channel

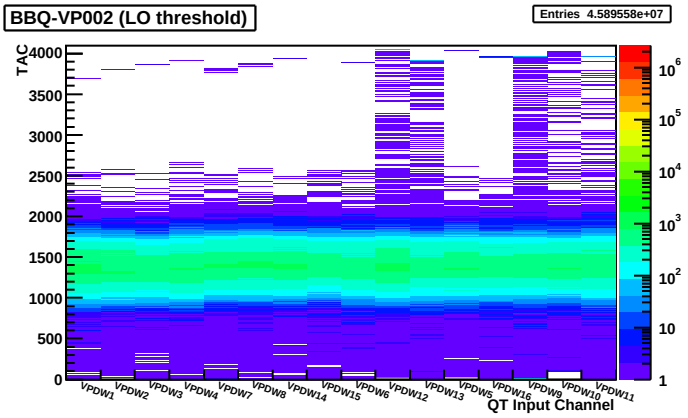

BBQ-ZD001 (ZDC TOWER)
Entries 4.589558e+07
ADC

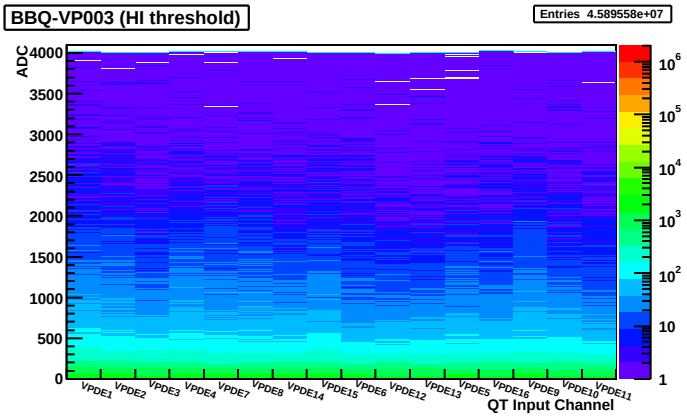

BBQ-ZD001 (ZDC TOWER)
Entries 4.589558e+07
TAC

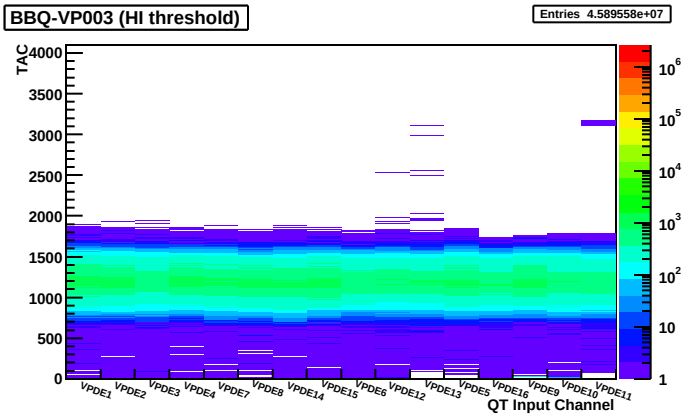

BBQ-VP001 (LO threshold)
Entries 4.589558e+07
ADC
QT Input Channel

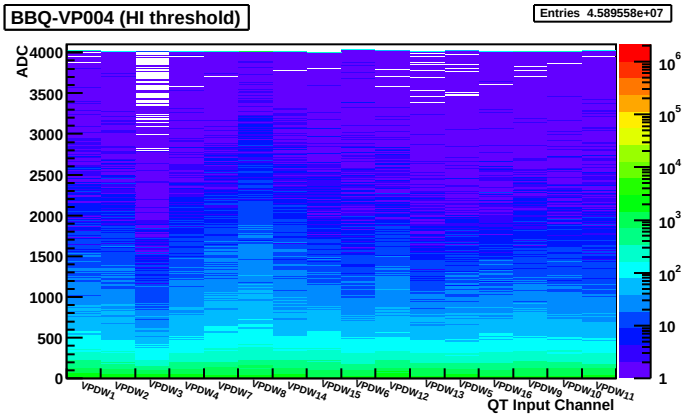

BBQ-VP001 (LO threshold)
Entries 4.589558e+07
TAC
QT Input Channel

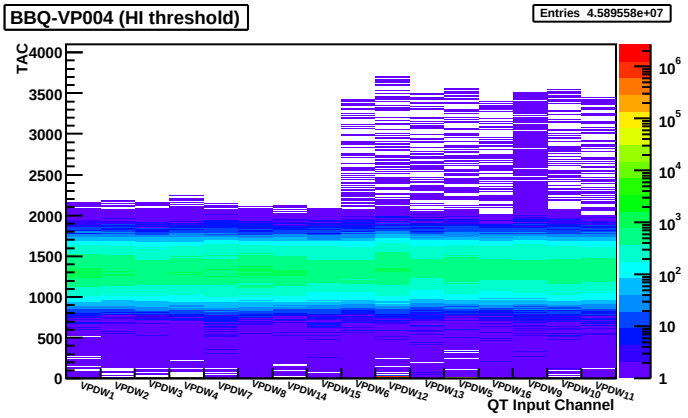

BBQ-VP002 (LO threshold)
Entries 4.589558e+07
ADC
QT Input Channel

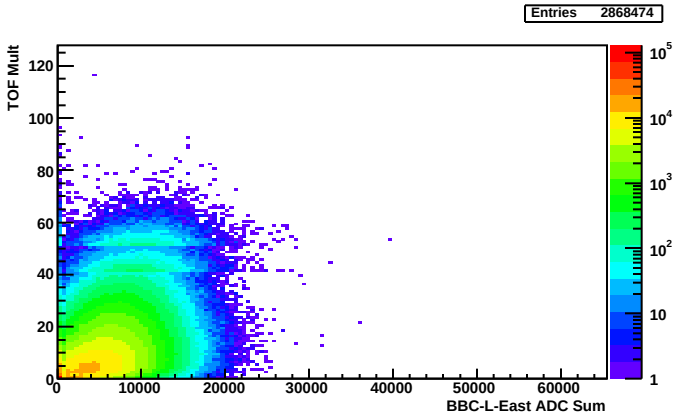

BBQ-VP002 (LO threshold)
Entries 4.589558e+07
TAC
QT Input Channel

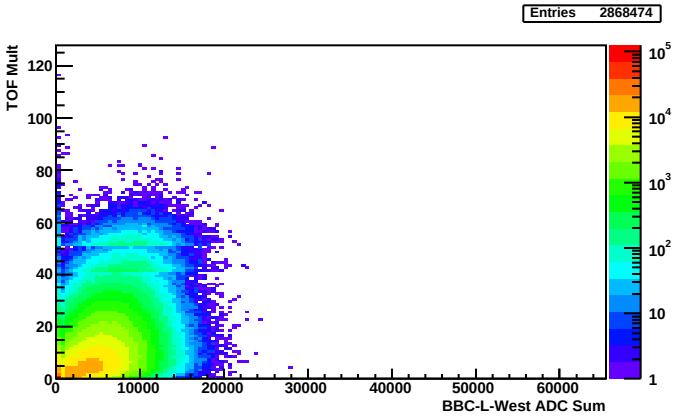

BBQ-VP003 (HI threshold)
Entries 4.589558e+07
ADC
QT Input Channel

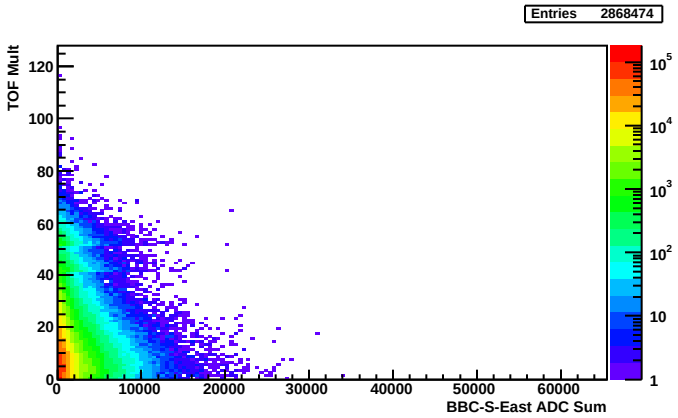

BBQ-VP003 (HI threshold)
Entries 4.589558e+07
TAC
QT Input Channel

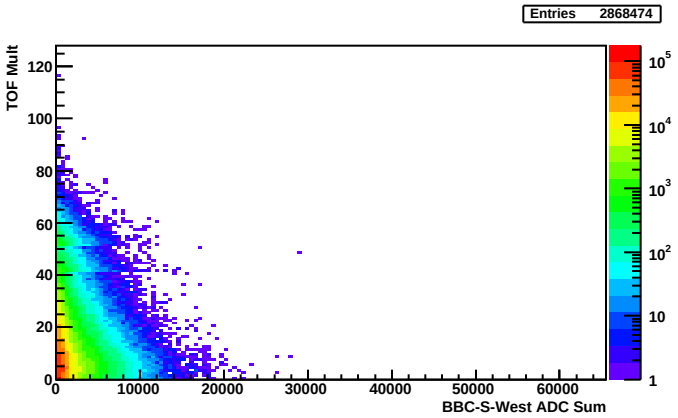

BBQ-VP004 (HI threshold)
Entries 4.589558e+07
ADC
QT Input Channel

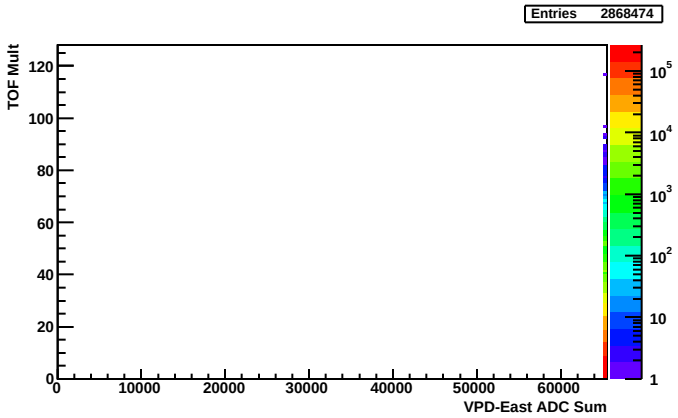

BBQ-VP004 (HI threshold)
Entries 4.589558e+07
TAC
QT Input Channel

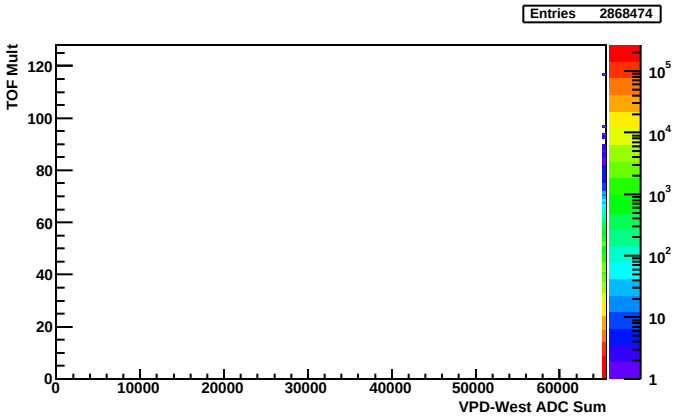

Entries 2868474
TOF Mult
BBC-L-East ADC Sum

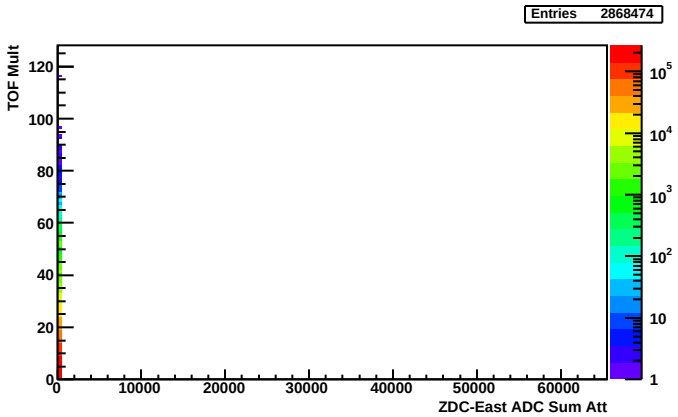

Entries 2868474
TOF Mult
BBC-L-West ADC Sum

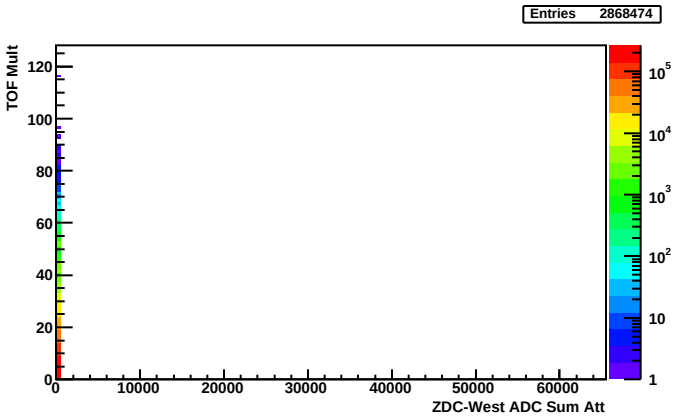

Entries 2868474
TOF Mult
BBC-S-East ADC Sum

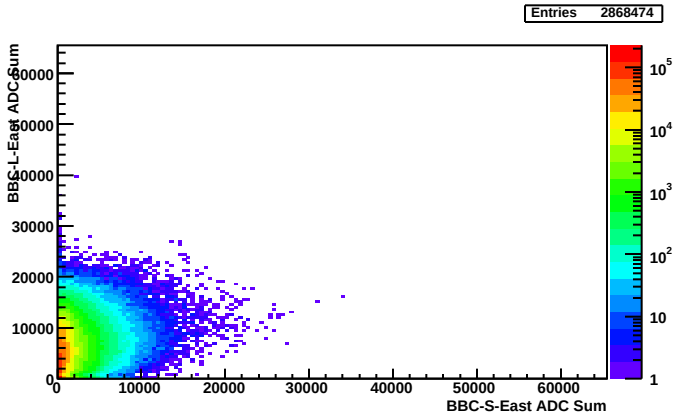

Entries 2868474
TOF Mult
BBC-S-West ADC Sum

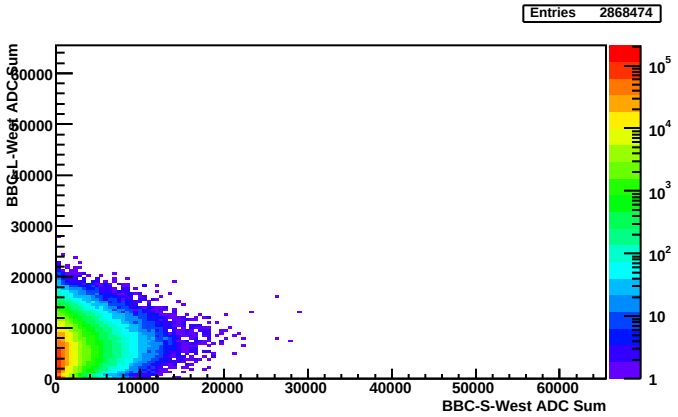

Entries 2868474
TOF Mult
VPD-East ADC Sum

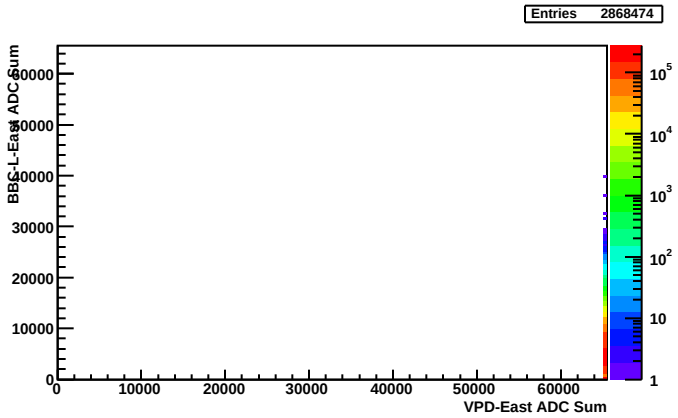

Entries 2868474
TOF Mult
VPD-West ADC Sum

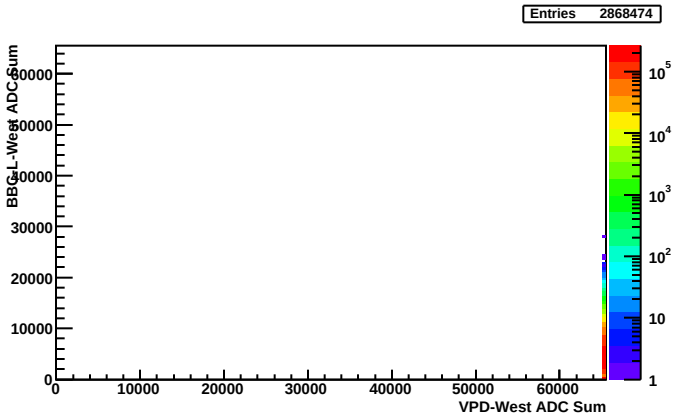

Entries 2868474
TOF Mult
ZDC-East ADC Sum Att

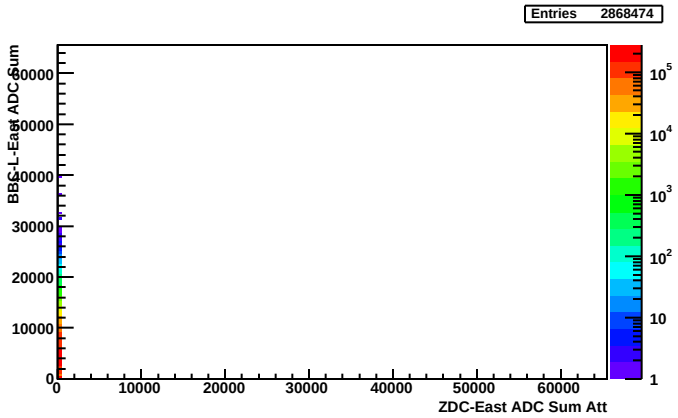

Entries 2868474
TOF Mult
ZDC-West ADC Sum Att

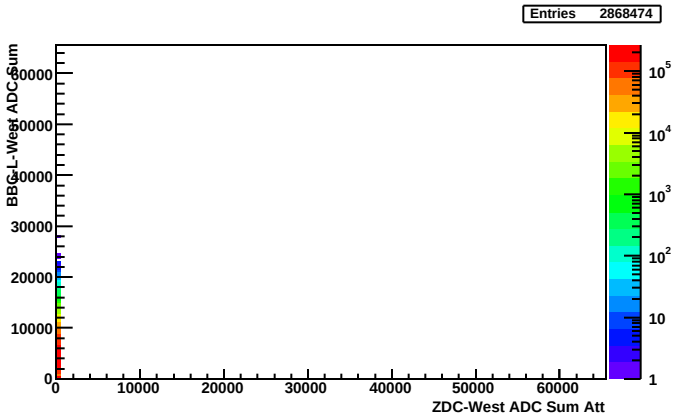

Entries 2868474
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2868474
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2868474
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2868474
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 2868474
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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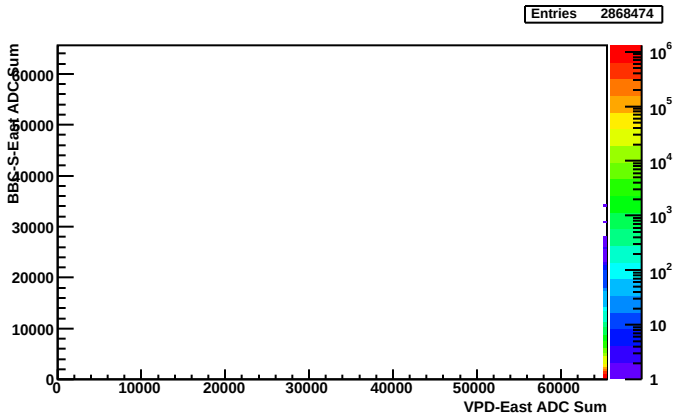
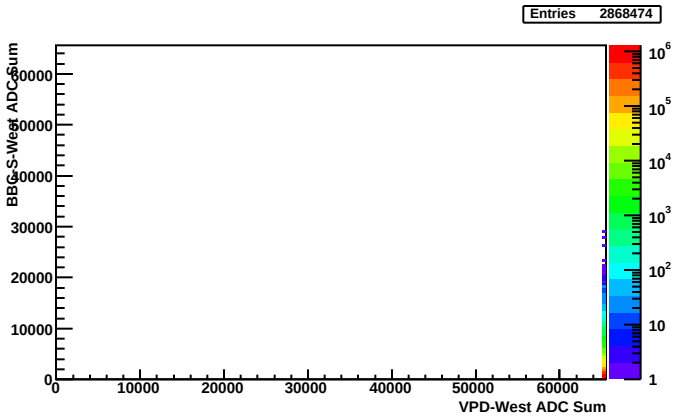
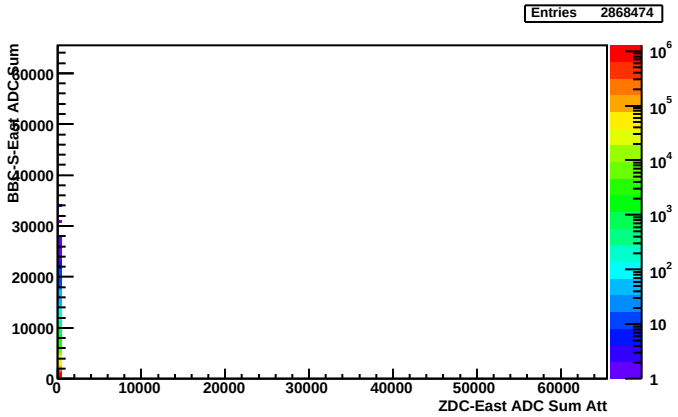
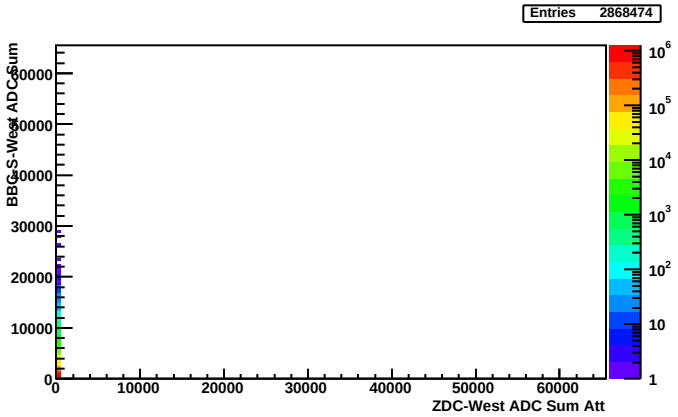
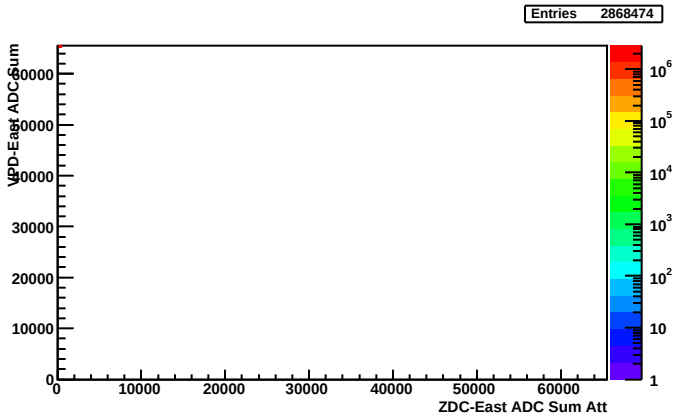
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

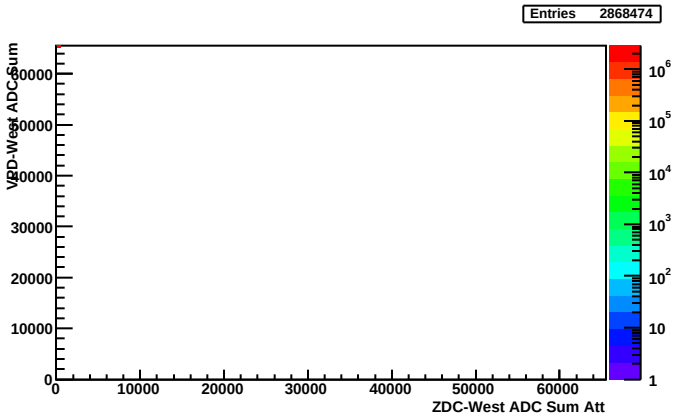

Entries 2868474
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2868474
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2868474
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2868474
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2868474
VPD-East ADC Sum
ZDC-East ADC Sum Att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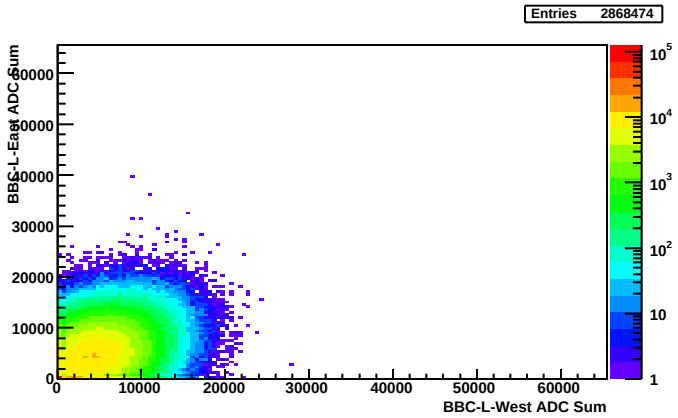
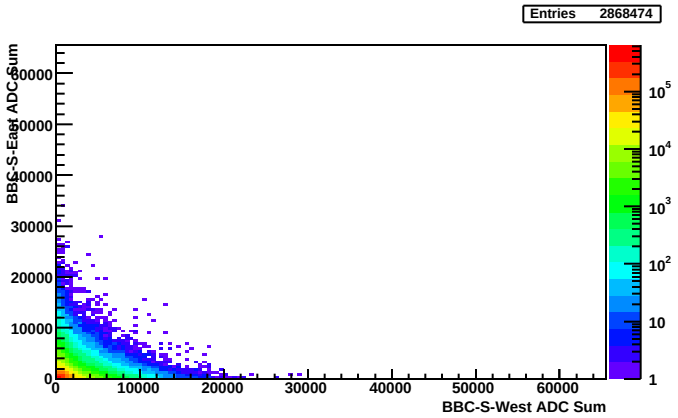
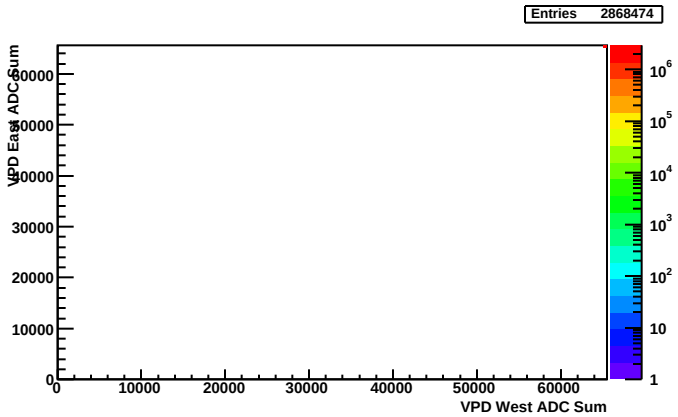
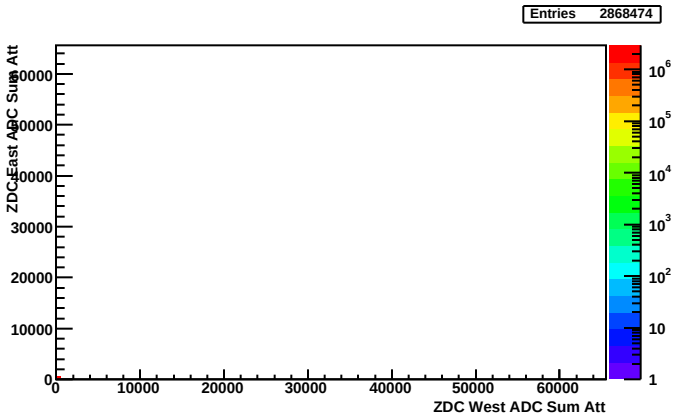
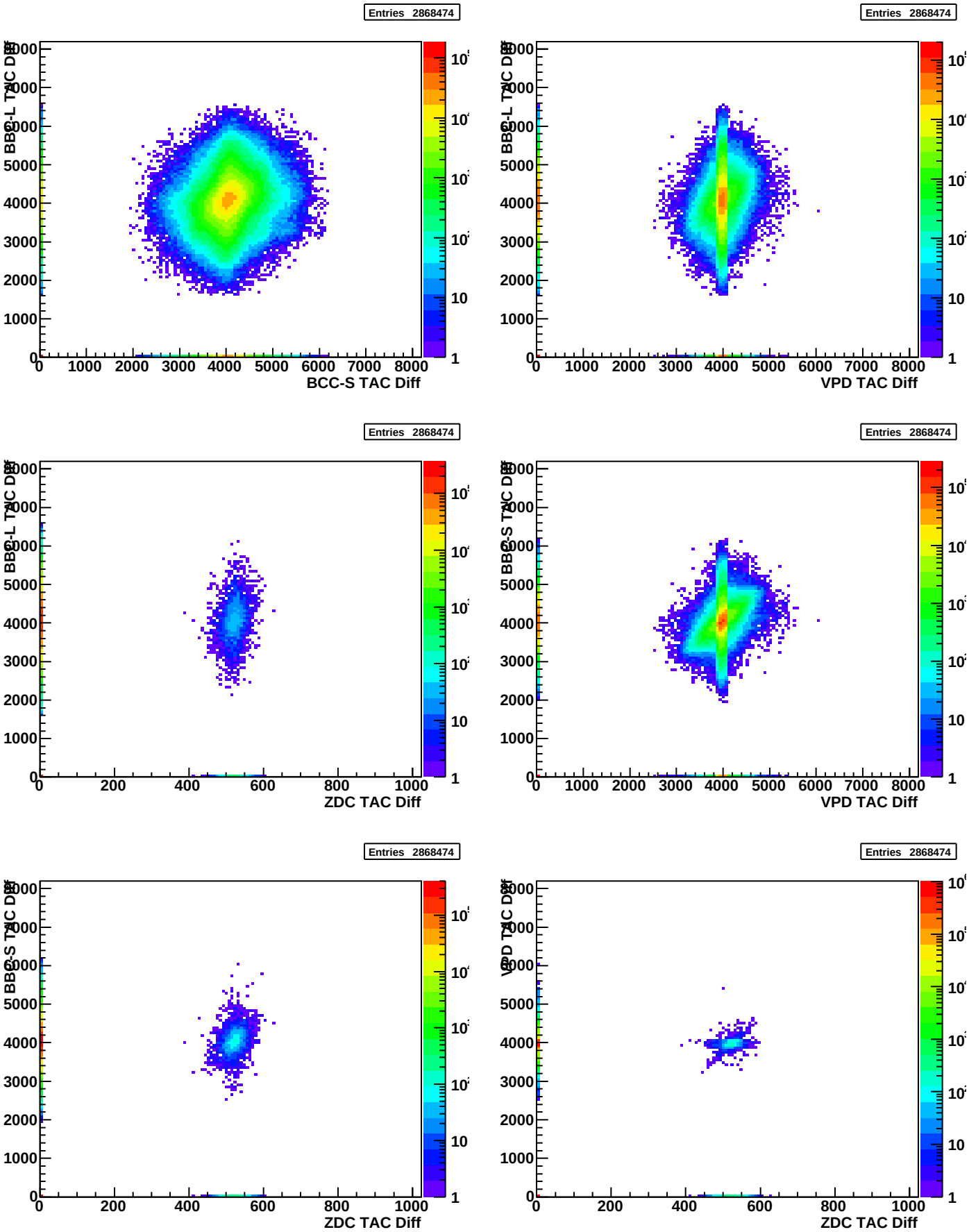
VPD-West ADC Sum
ZDC-West ADC Sum Att

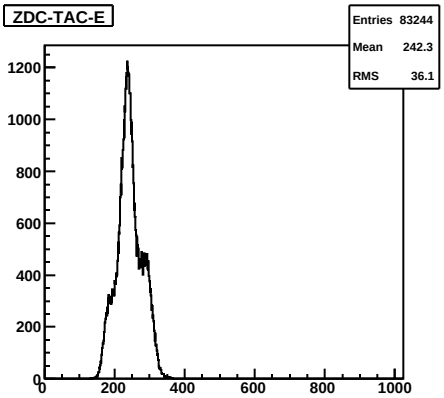

Entries 2868474
BBC-L-East ADC Sum
BBC-L-West ADC Sum

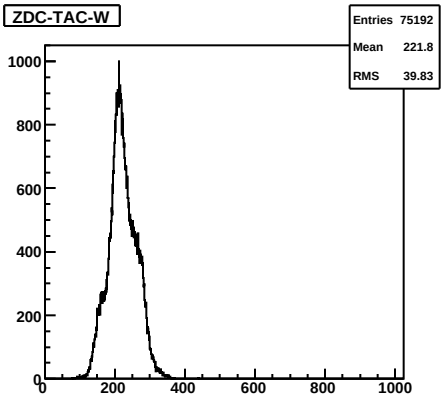

Entries 2868474
BBC-S-East ADC Sum
BBC-S-West ADC Sum

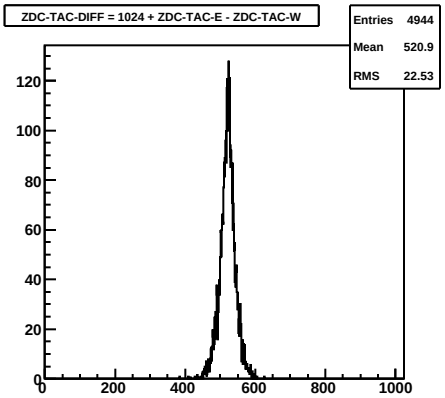

Entries 2868474
VPD East ADC Sum
VPD West ADC Sum

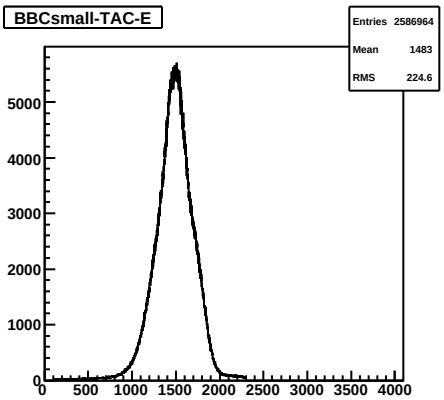

Entries 2868474
ZDC East ADC Sum Att
ZDC West ADC Sum Att

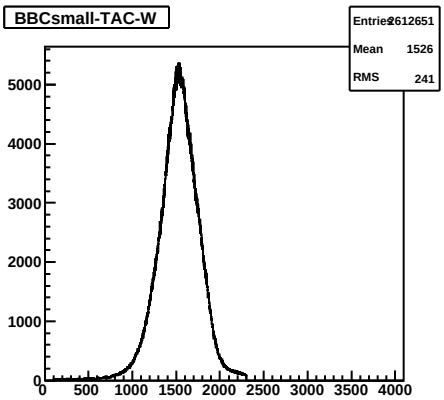

Entries 2868474
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2868474
BBC-L TAC Diff
VPD TAC Diff
Entries 2868474
BBC-L TAC Diff
ZDC TAC Diff
Entries 2868474
BBC-S TAC Diff
VPD TAC Diff
Entries 2868474
BBC-S TAC Diff
ZDC TAC Diff
Entries 2868474
VPD TAC Diff
ZDC TAC Diff

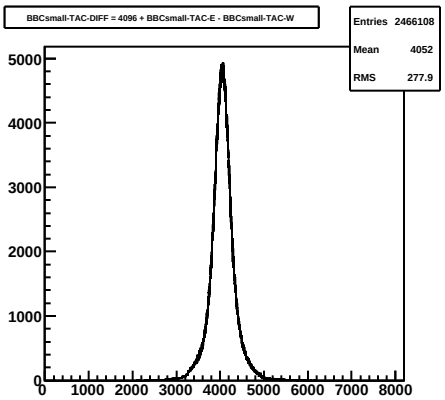

ZDC-TAC-E
Entries 83244
Mean 242.3
RMS 36.1

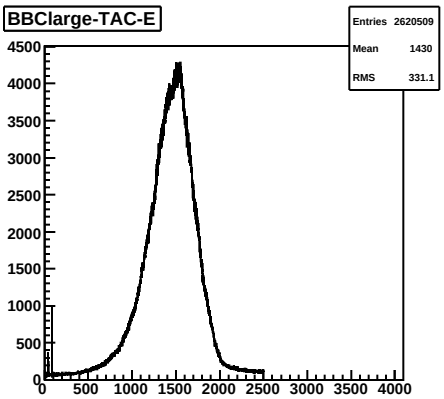

ZDC-TAC-W
Entries 75192
Mean 221.8
RMS 39.83

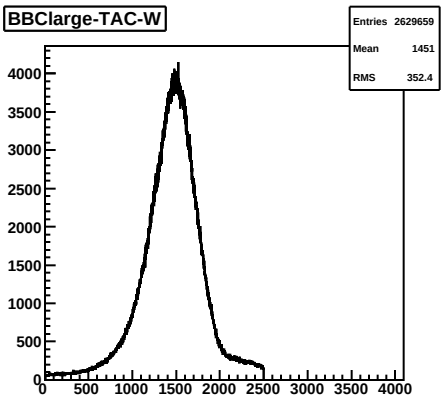

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4944
Mean 520.9
RMS 22.53

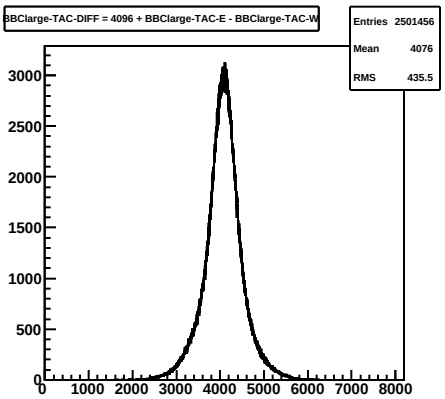

BBCsmall-TAC-E
Entries 2586964
Mean 1483
RMS 224.6

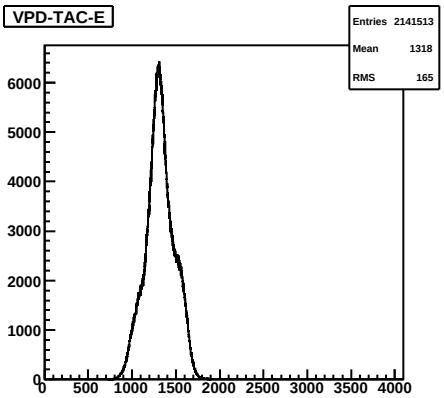

BBCsmall-TAC-W
Entries 2612651
Mean 1526
RMS 241

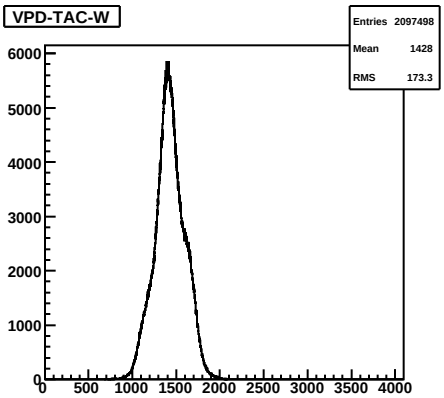

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2466108
Mean 4052
RMS 277.9

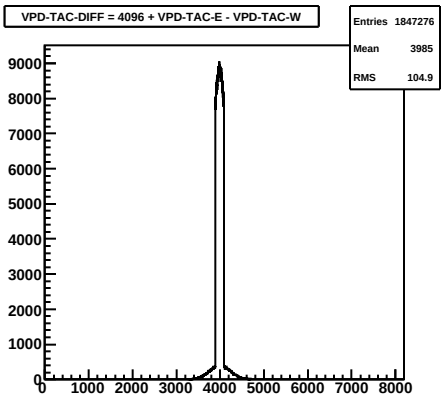

BBClarge-TAC-E
Entries 2620509
Mean 1430
RMS 331.1

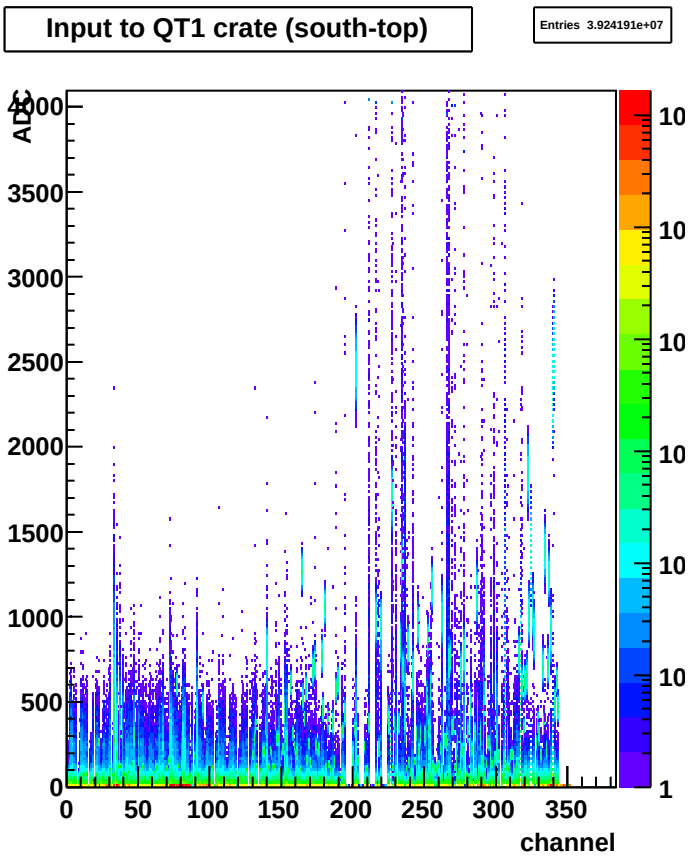

BBClarge-TAC-W
Entries 2629659
Mean 1451
RMS 352.4

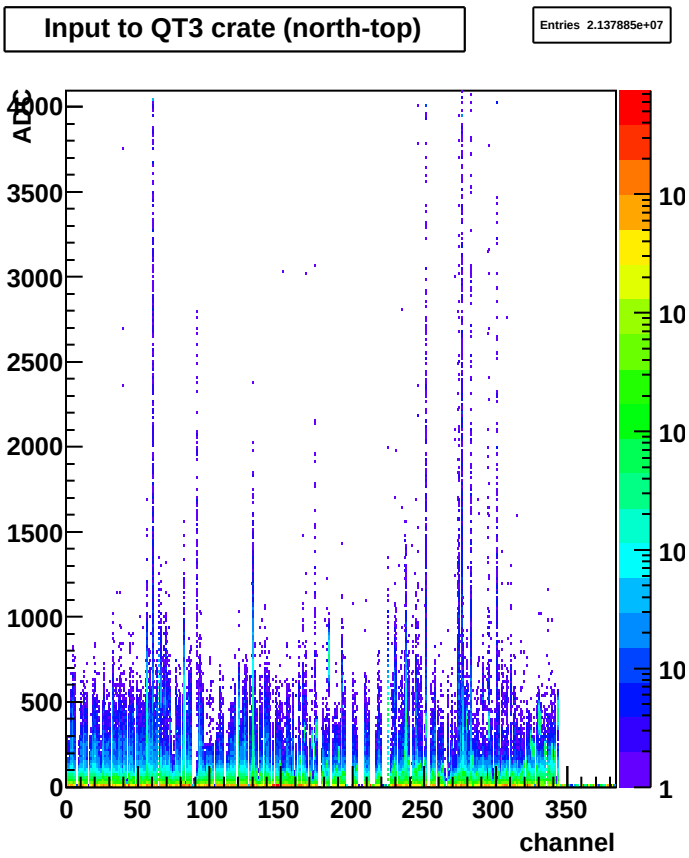

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2501456
Mean 4076
RMS 435.5

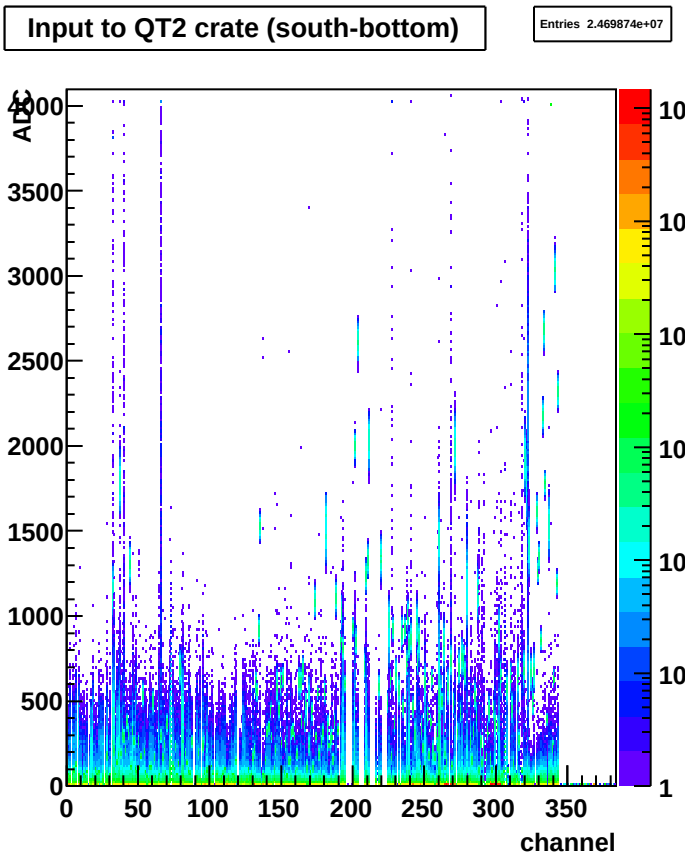

VPD-TAC-E
Entries 2141513
Mean 1318
RMS 165

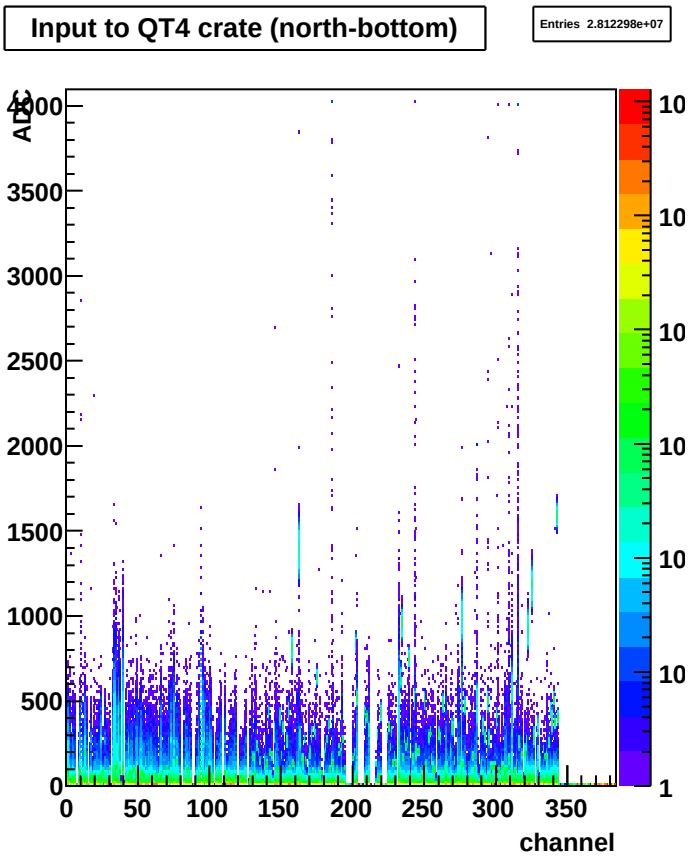

VPD-TAC-W
Entries 2097498
Mean 1428
RMS 173.3

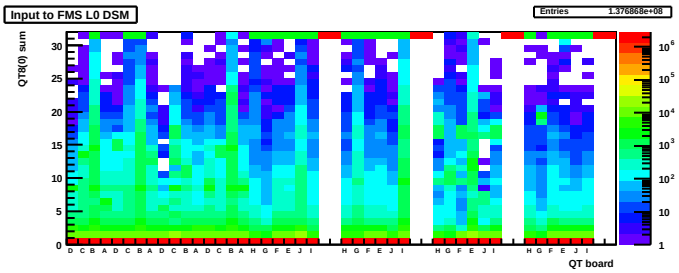

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1847276
Mean 3985
RMS 104.9

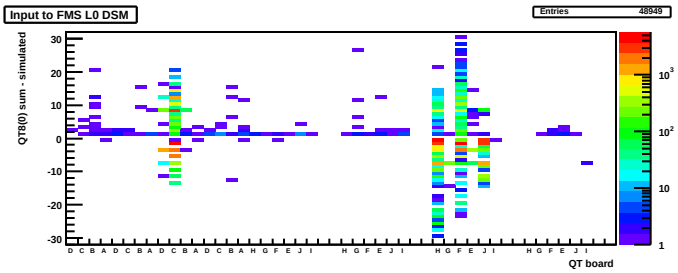

Input to QT1 crate (south-top)
Entries 3.924191e+07
ADC
channel

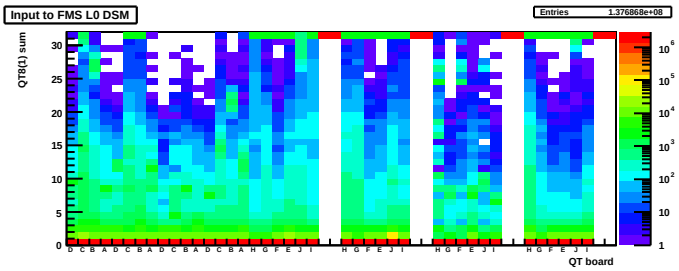

Input to QT3 crate (north-top)
Entries 2.137885e+07
ADC
channel

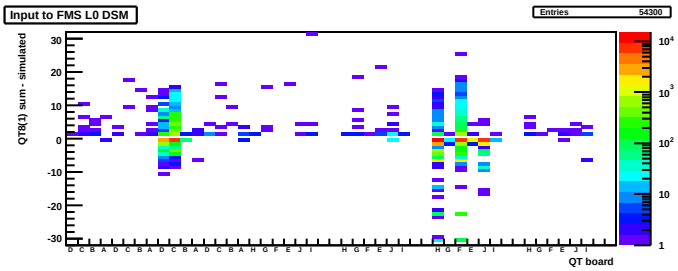

Input to QT2 crate (south-bottom)
Entries 2.469874e+07
ADC
channel

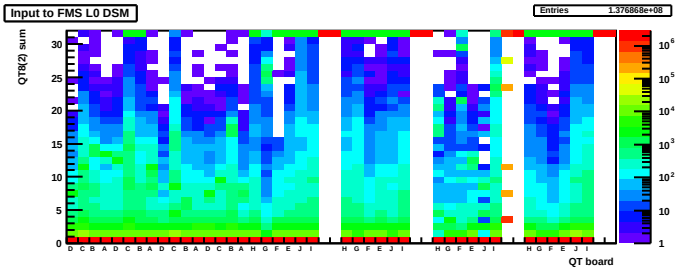

Input to QT4 crate (north-bottom)
Entries 2.812298e+07
ADC
channel

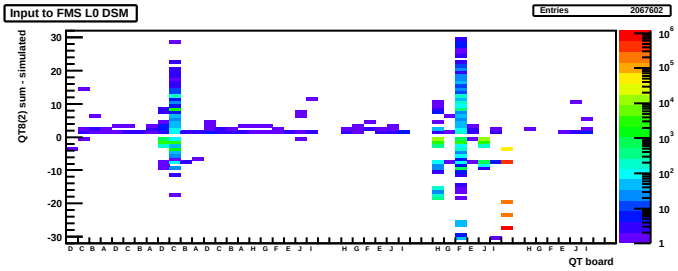

Input to FMS L0 DSM
Entries 1.376868e+08
QT8(0) sum
QT board

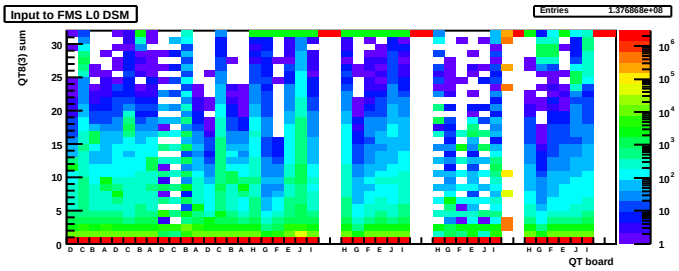

Input to FMS L0 DSM
Entries 48949
QT8(0) sum - simulated
QT board

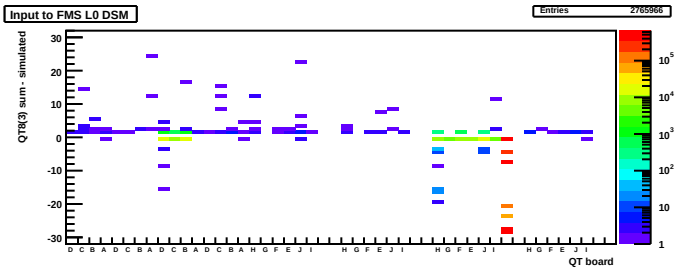

Input to FMS L0 DSM
Entries 1.376868e+08
QT8(1) sum
QT board

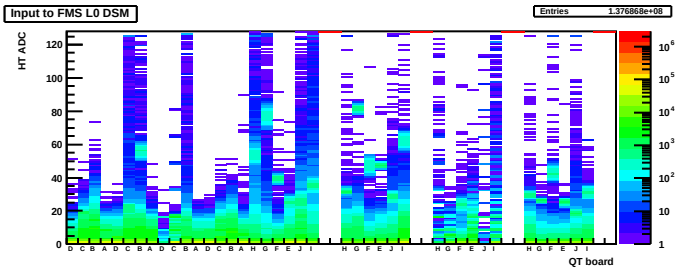

Input to FMS L0 DSM
Entries 54300
QT8(1) sum - simulated
QT board

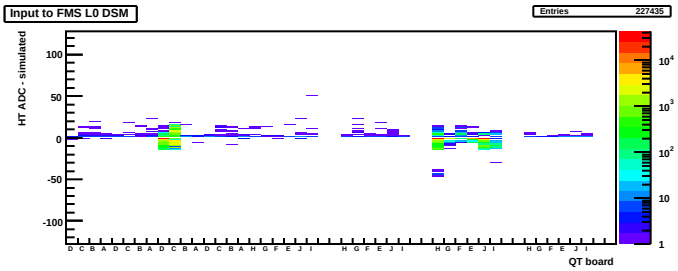

Input to FMS L0 DSM
Entries 1.376868e+08
QT8(2) sum
QT board

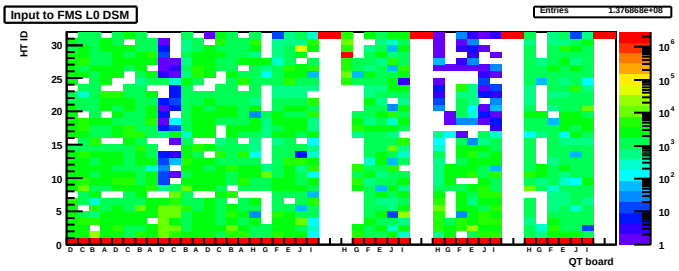

Input to FMS L0 DSM
Entries 2067602
QT8(2) sum - simulated
QT board

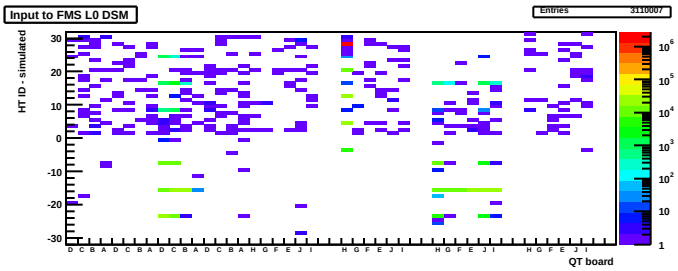

Input to FMS L0 DSM
Entries 1.376868e+08
QT8(3) sum
QT board

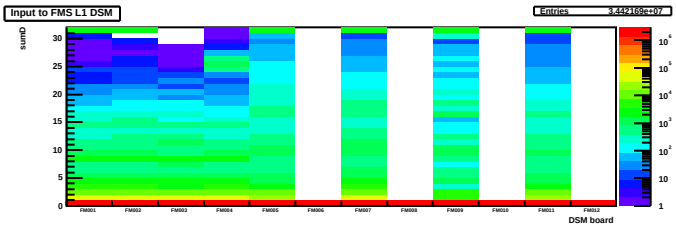

Input to FMS L0 DSM
Entries 2765966
QT8(3) sum - simulated
QT board

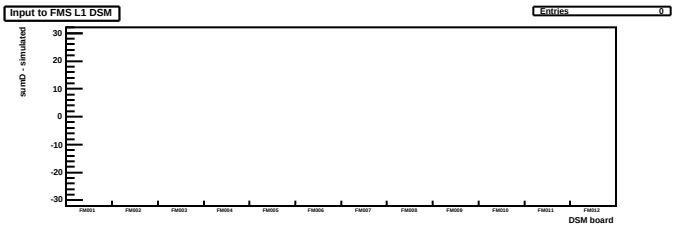

Input to FMS L0 DSM
Entries 1.376868e+08
HT ADC
QT board

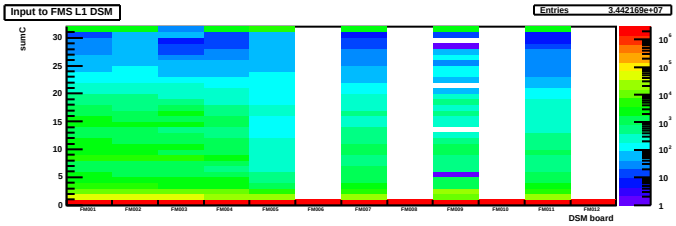

Input to FMS L0 DSM
Entries 227435
HT ADC - simulated
QT board

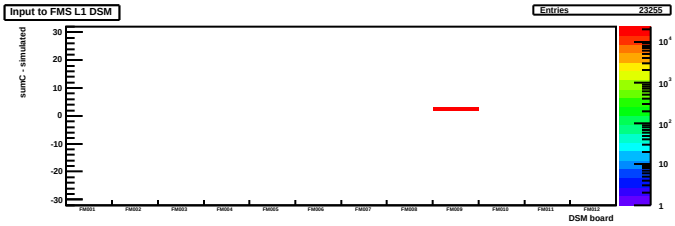

Input to FMS L0 DSM
Entries 1.376868e+08
HT ID
QT board

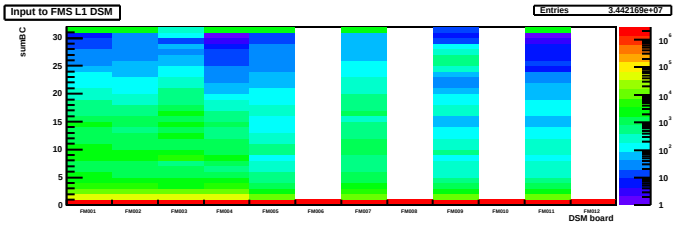

Input to FMS L0 DSM
Entries 3110007
HT ID - simulated
QT board

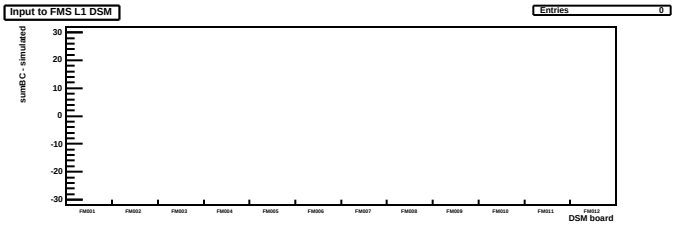

Input to FMS L1 DSM
Entries 3.442169e+07
sumD
DSM board

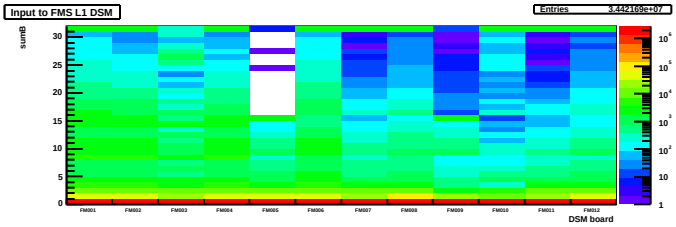

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

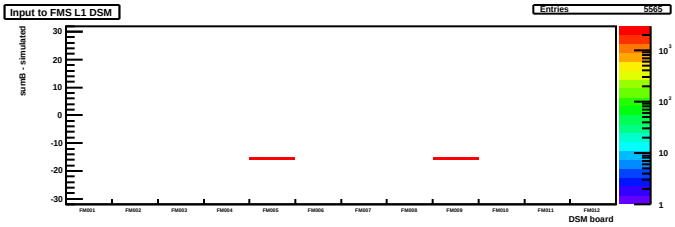

Input to FMS L1 DSM
Entries 3.442169e+07
sumC
DSM board

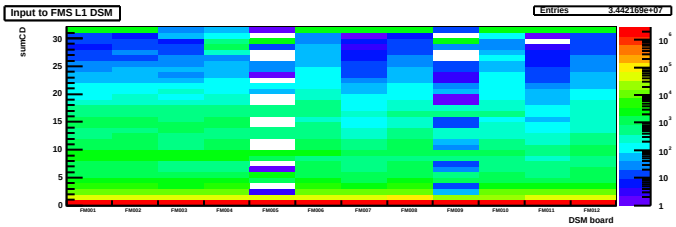

Input to FMS L1 DSM
Entries 23255
sumC - simulated
DSM board

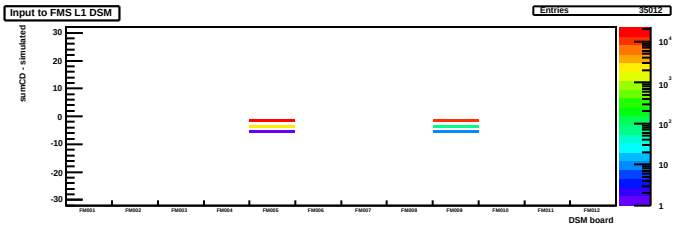

Input to FMS L1 DSM
Entries 3.442169e+07
sumBC
DSM board

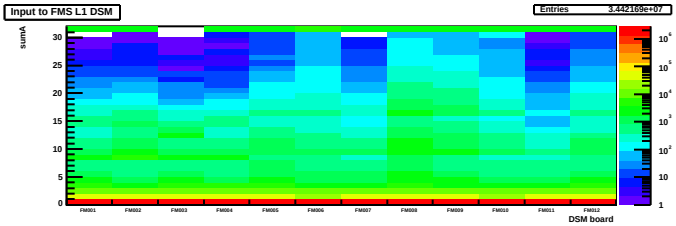

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

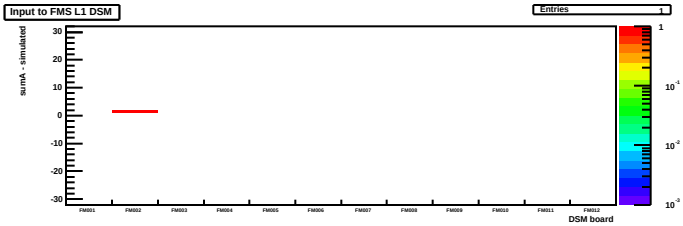

Input to FMS L1 DSM
Entries 3.442169e+07
sumB
DSM board

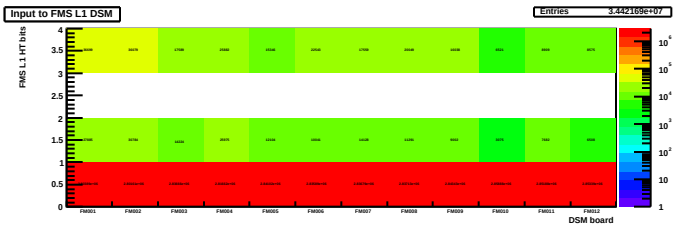

Input to FMS L1 DSM
Entries 5565
sumB - simulated
DSM board

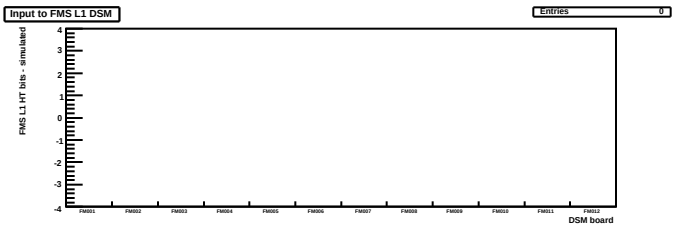

Input to FMS L1 DSM
Entries 3.442169e+07
sumCD
DSM board

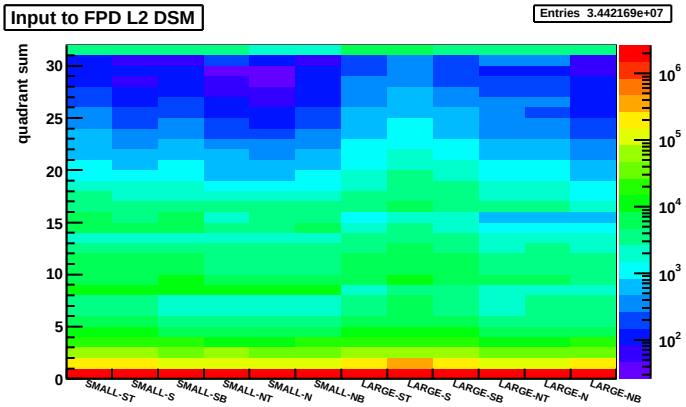

Input to FMS L1 DSM
Entries 35012
sumCD - simulated
DSM board

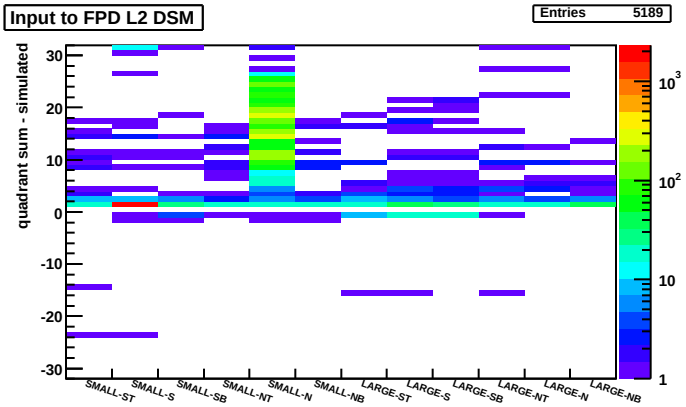

Input to FMS L1 DSM
Entries 3.442169e+07
sumA
DSM board

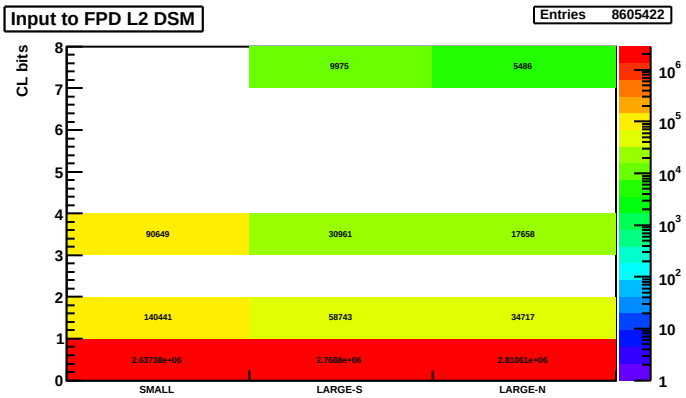

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

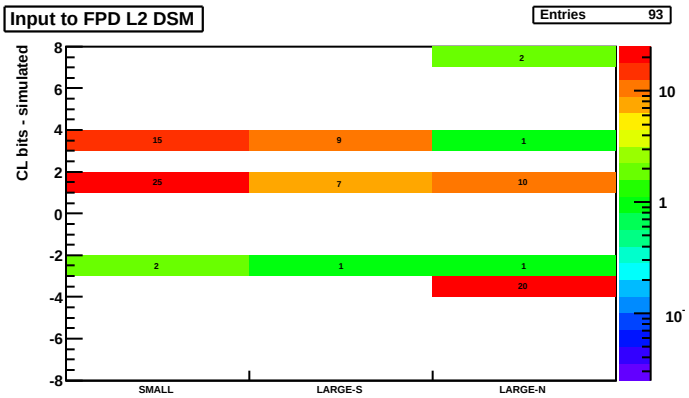

Input to FMS L1 DSM
Entries 3.442169e+07
FMS L1 HT bits
DSM board

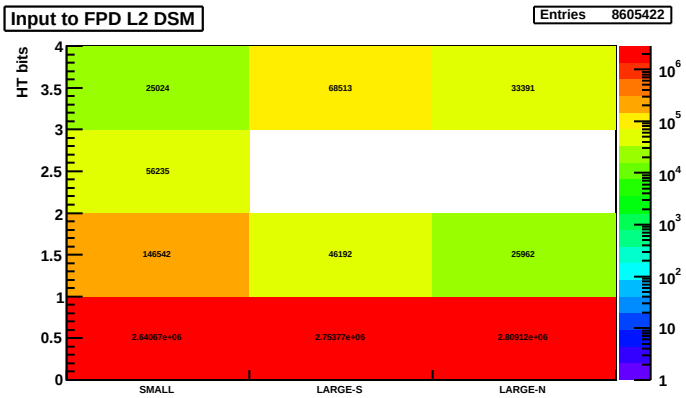

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

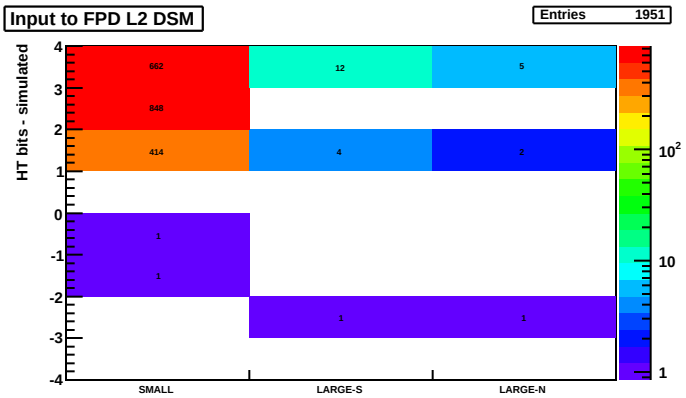

Input to FPD L2 DSM
Entries 3.442169e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

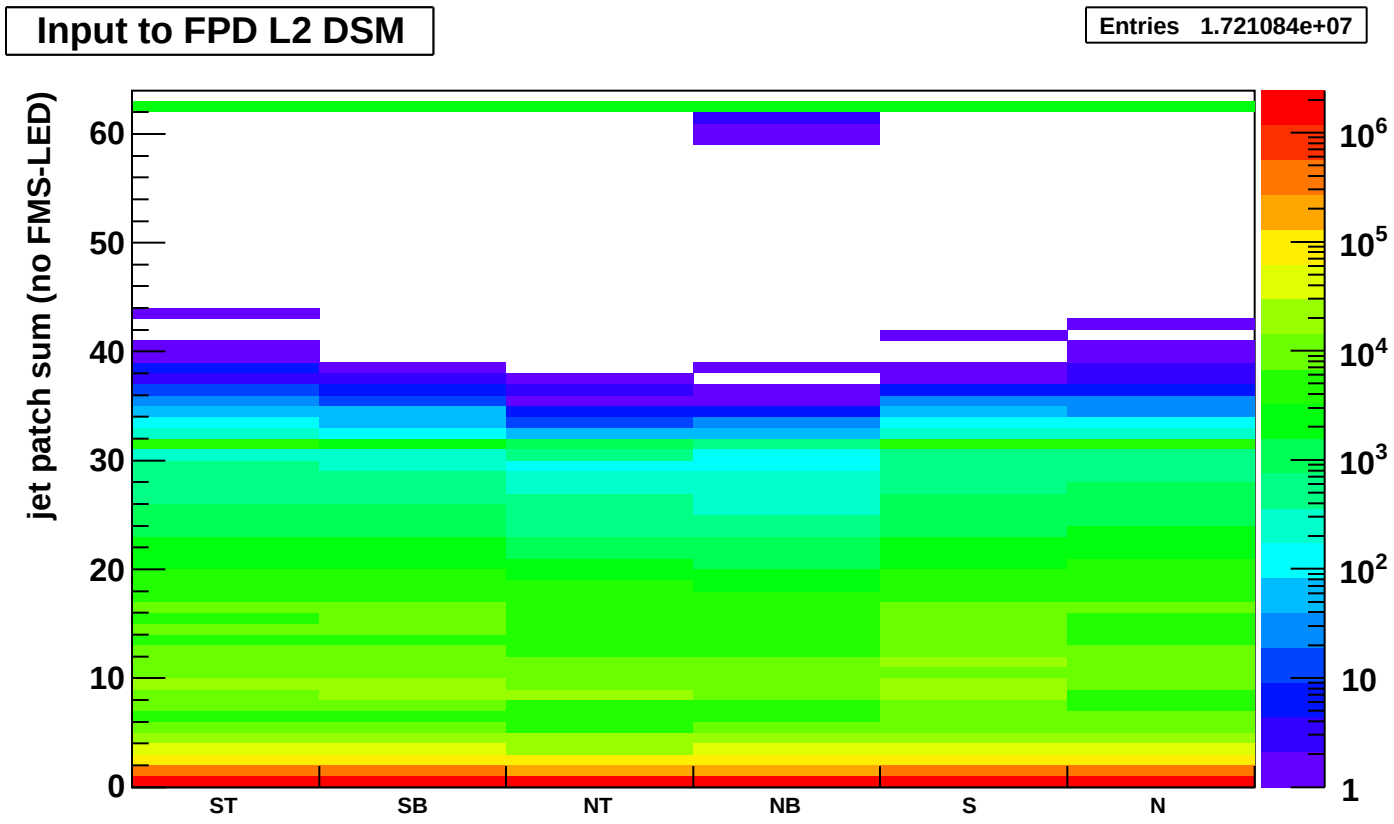

Input to FPD L2 DSM
Entries 5189
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

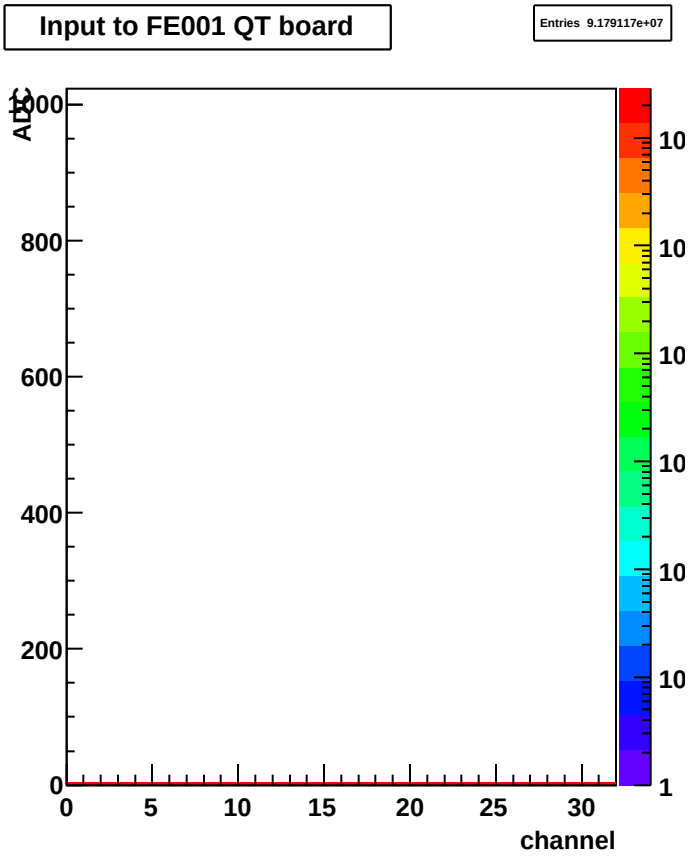

Input to FPD L2 DSM
Entries 8605422
CL bits
9975
5486
90649
30961
17658
140441
58743
34717
2.63738e+06
2.7688e+06
2.81061e+06
SMALL
LARGE-S
LARGE-N

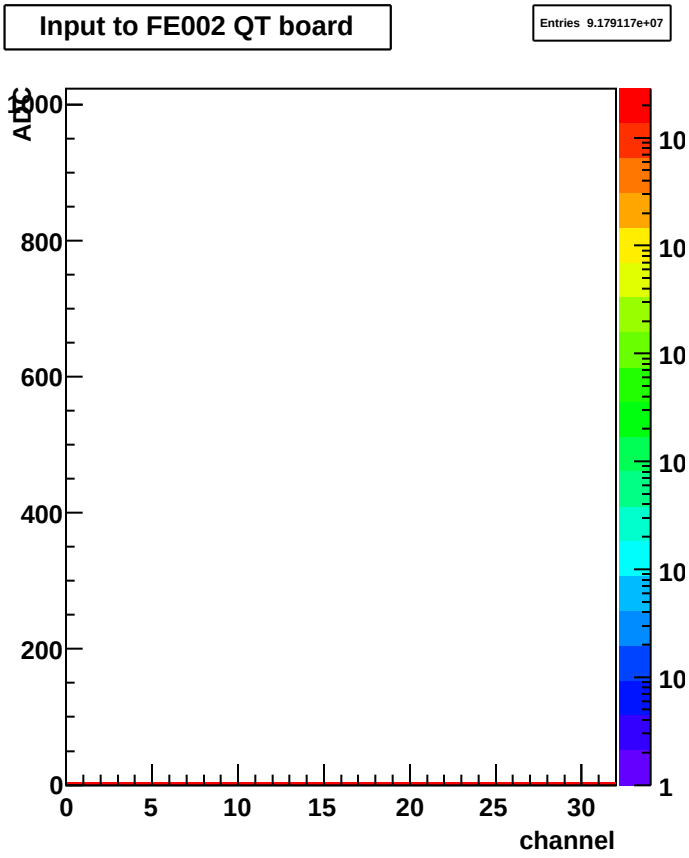

Input to FPD L2 DSM
Entries 93
CL bits - simulated
2
15
9
1
25
7
10
2
1
1
20
SMALL
LARGE-S
LARGE-N

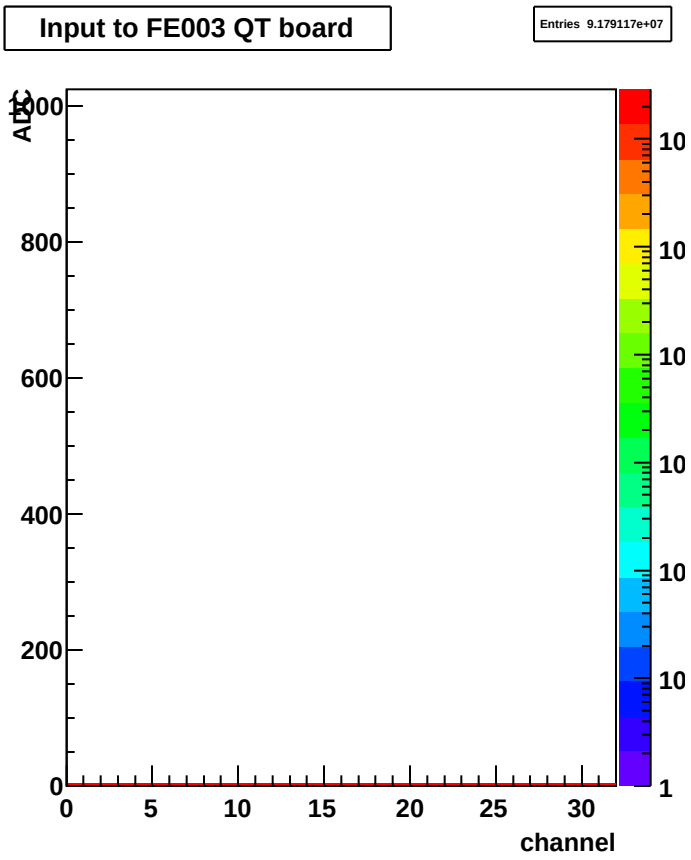

Input to FPD L2 DSM
Entries 8605422
HT bits
25024
68513
33391
56235
146542
46192
25962
2.64067e+06
2.75377e+06
2.80912e+06
SMALL
LARGE-S
LARGE-N

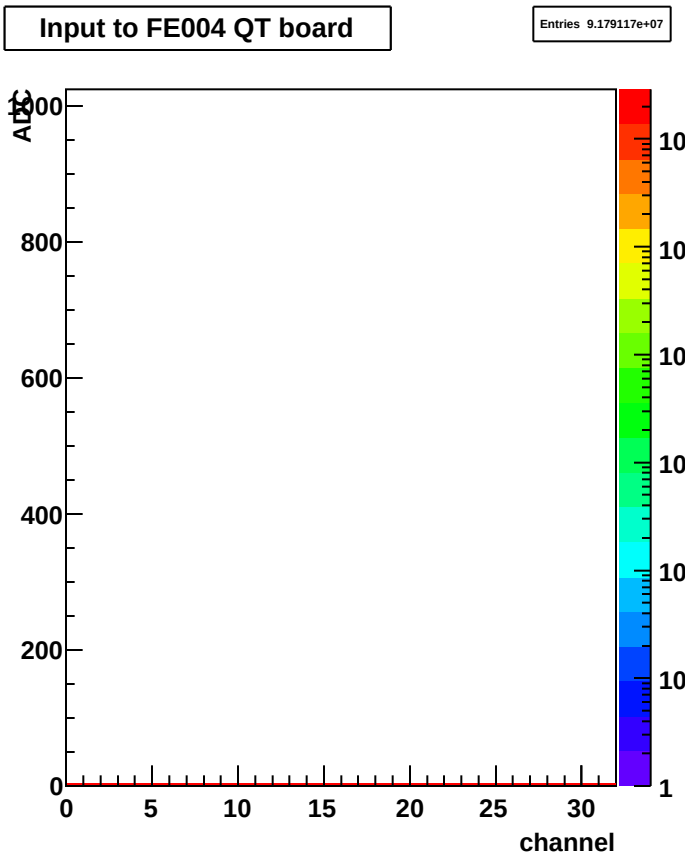

Input to FPD L2 DSM
Entries 1951
HT bits - simulated
662
12
5
848
414
4
2
1
1
1
1
SMALL
LARGE-S
LARGE-N

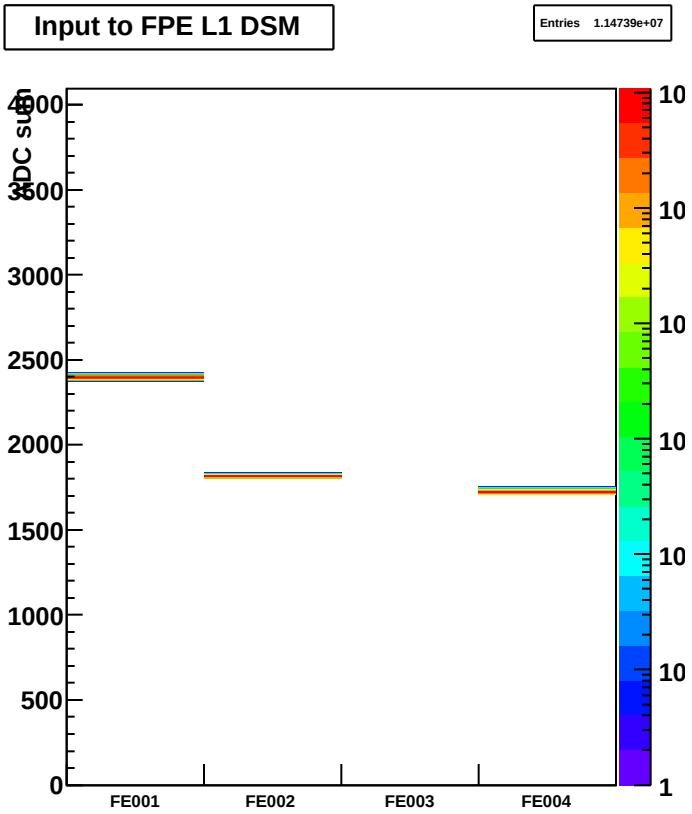

Input to FPD L2 DSM
Entries 1.721084e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 9.179117e+07
ADC
channel

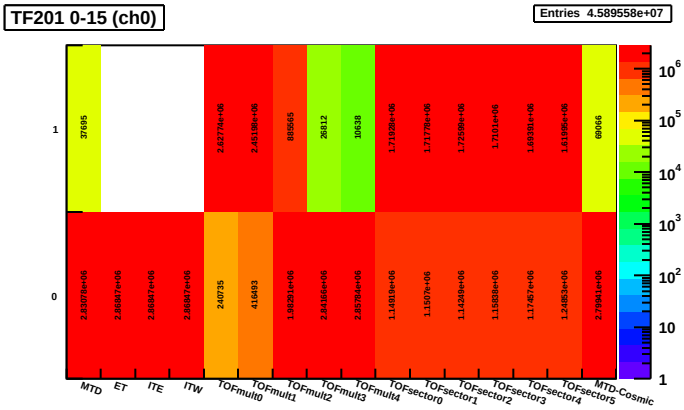

Input to FE002 QT board
Entries 9.179117e+07
ADC
channel


Input to FE003 QT board
Entries 9.179117e+07
ADC
channel

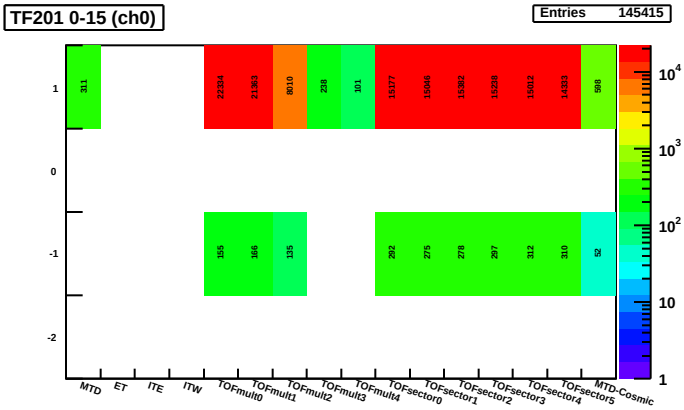

Input to FE004 QT board
Entries 9.179117e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.14739e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

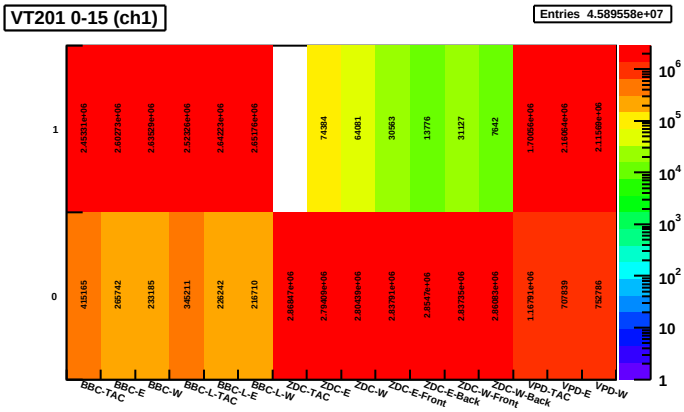
TF201 0-15 (ch0)



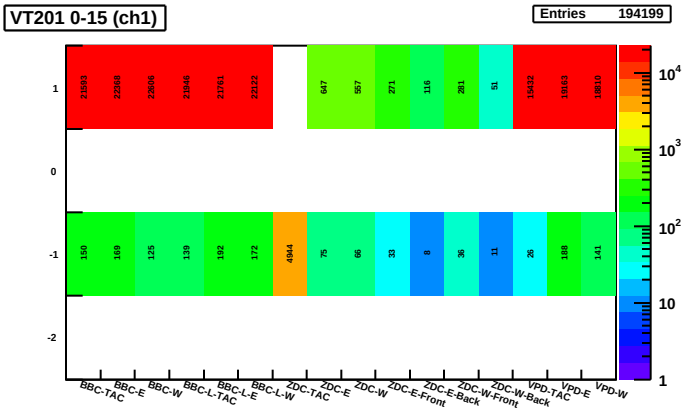
TF201 0-15 (ch0)



VT201 0-15 (ch1)



VT201 0-15 (ch1)



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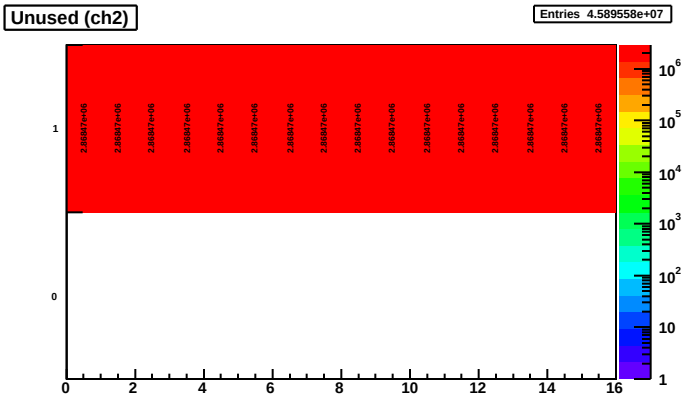

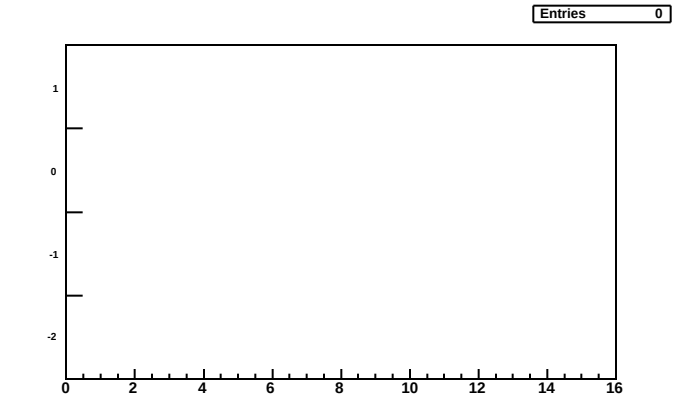

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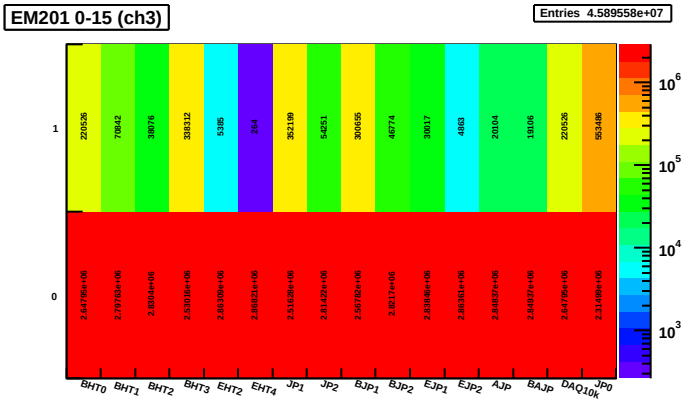

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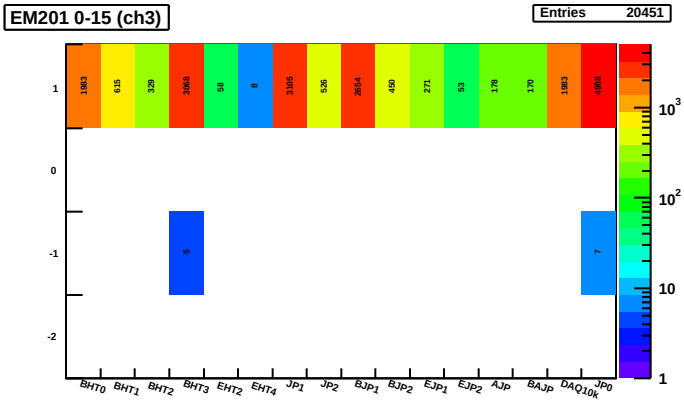
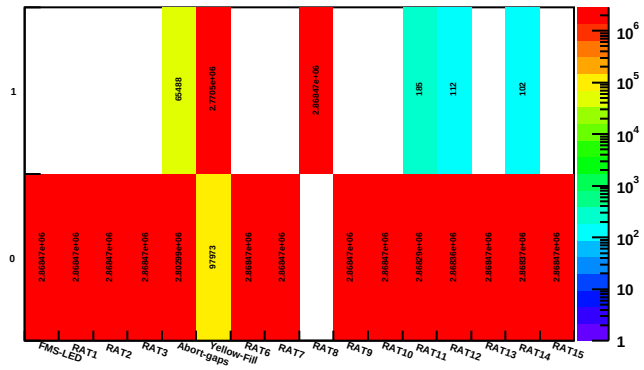
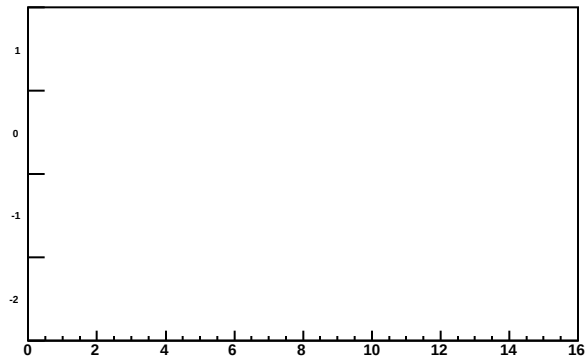
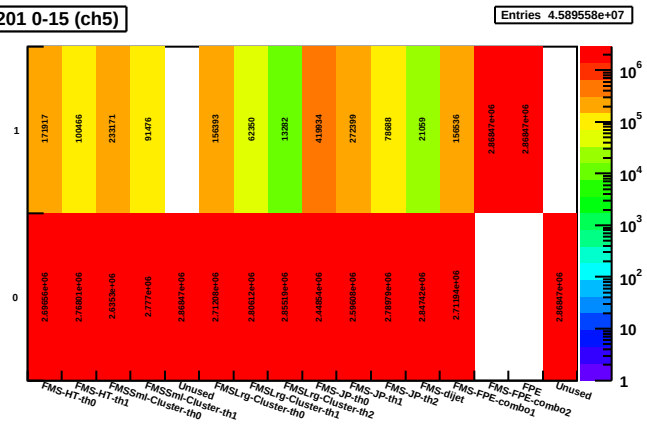


RAT board (ch4)

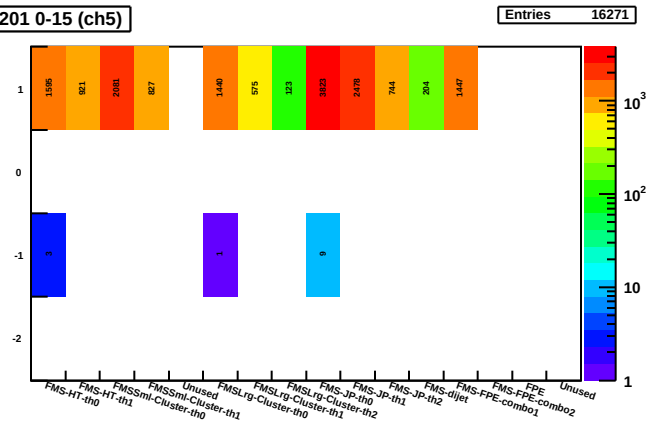




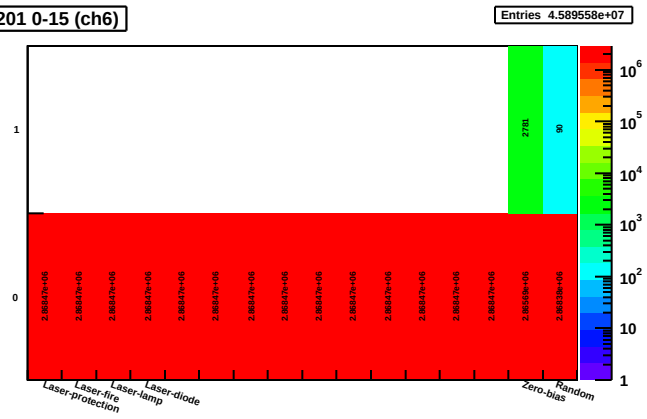
FP201 0-15 (ch5)

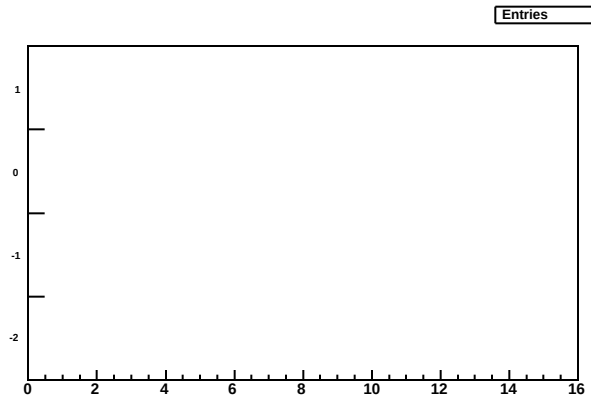
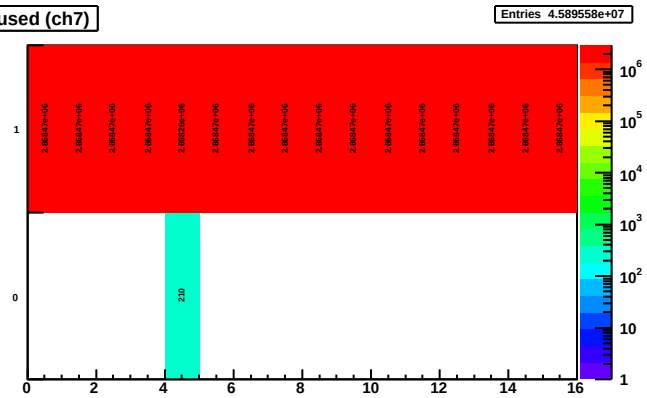
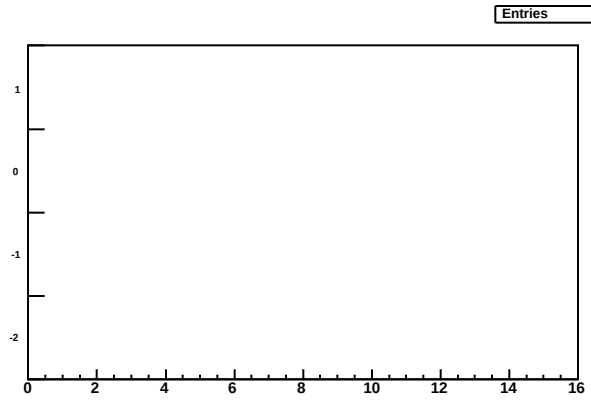


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)