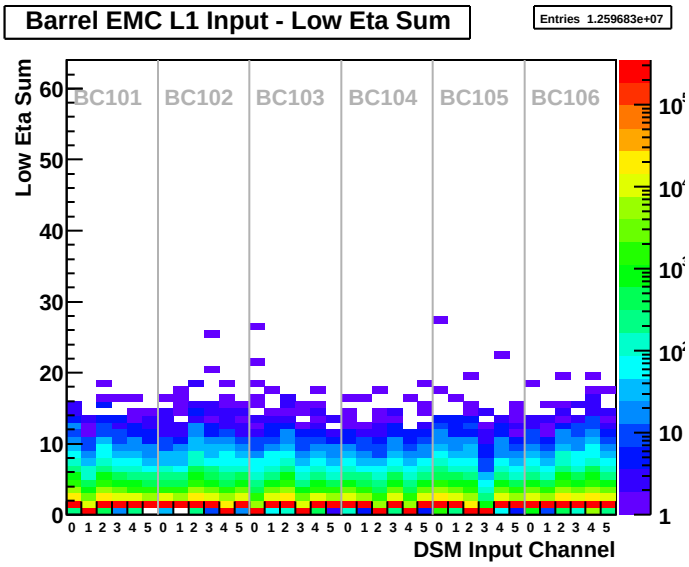
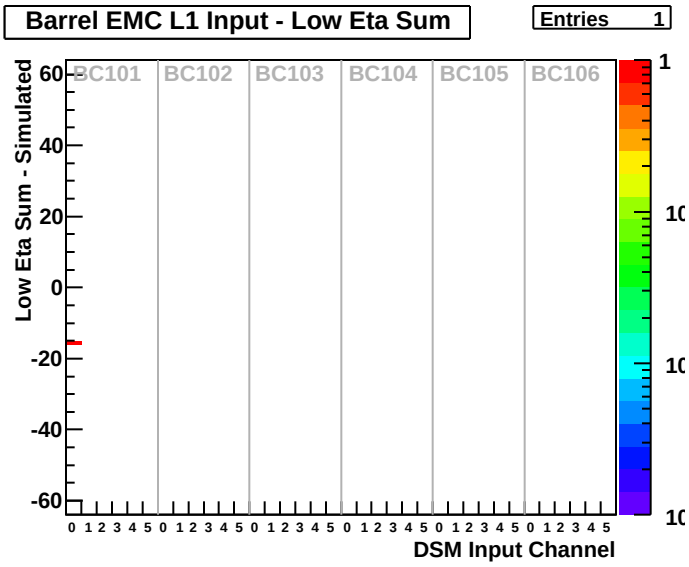
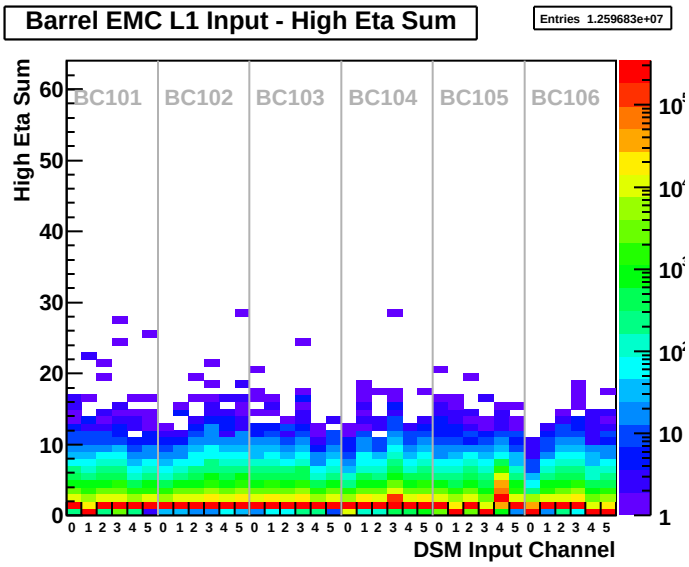
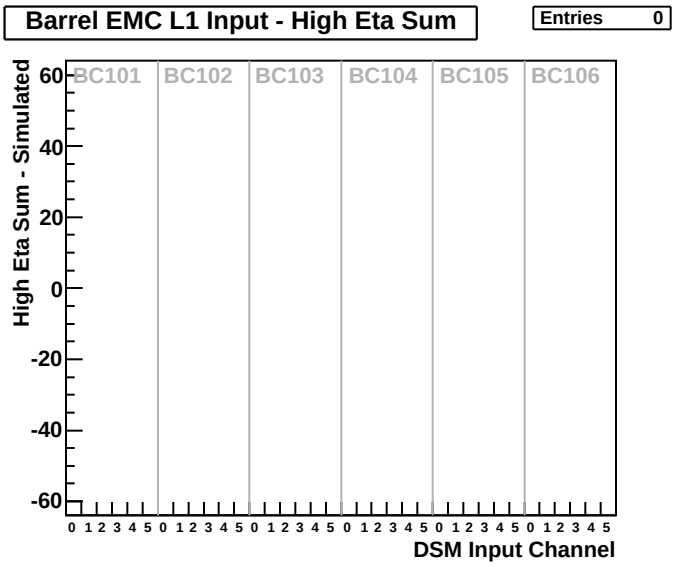
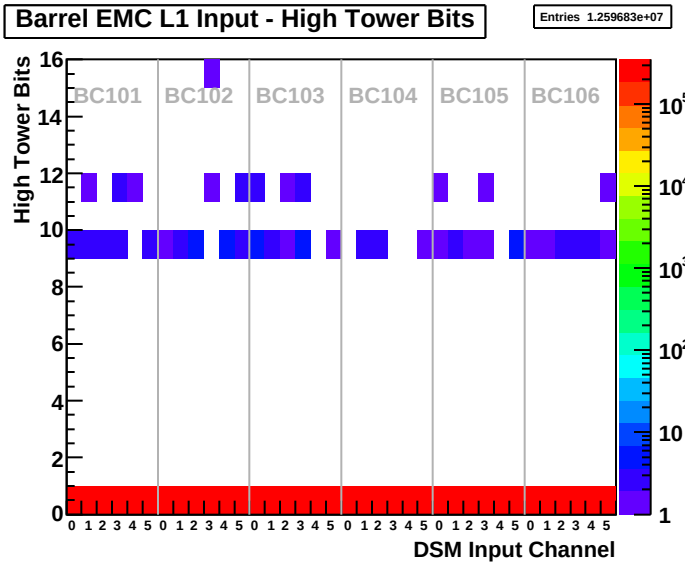
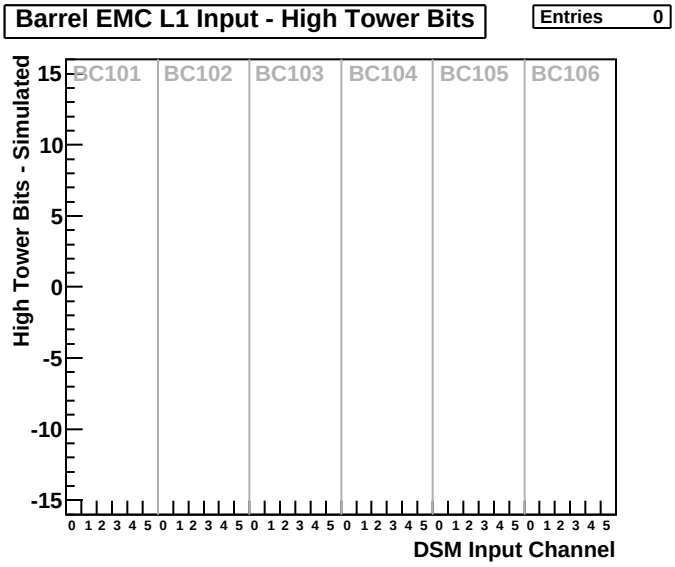

Barrel EMC L1 Input - Low Eta Sum
Entries 1.259683e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

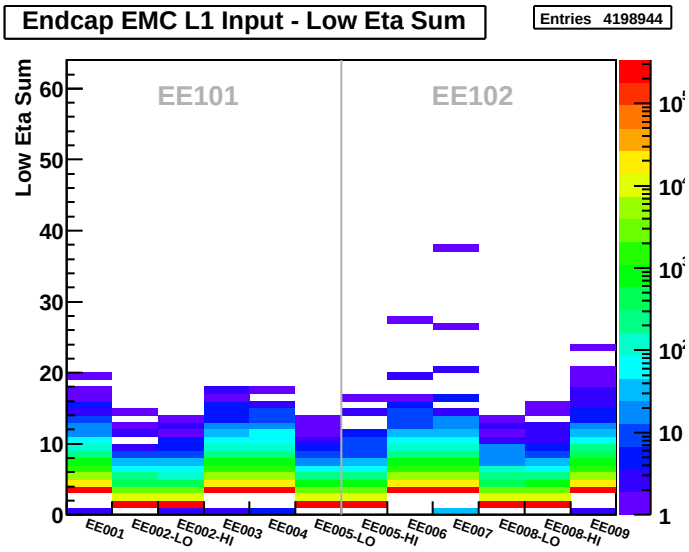

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

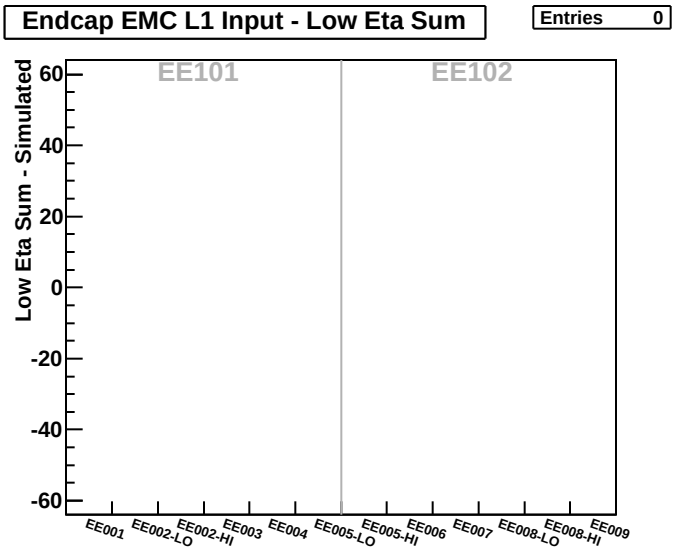

Barrel EMC L1 Input - High Eta Sum
Entries 1.259683e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

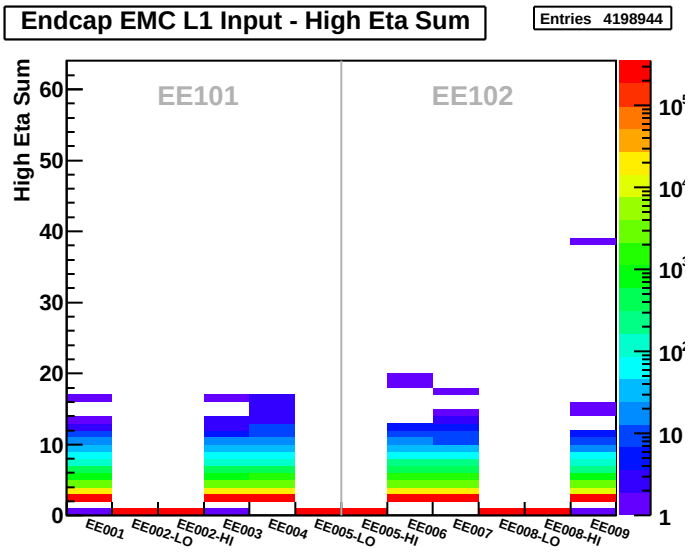

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

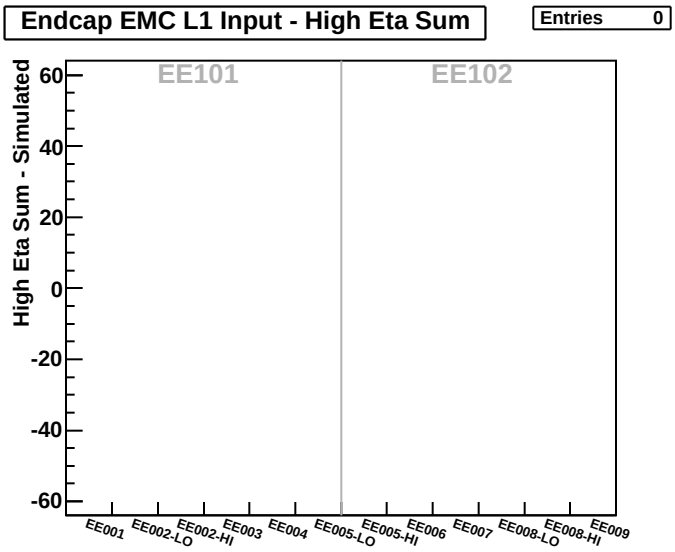

Barrel EMC L1 Input - High Tower Bits
Entries 1.259683e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

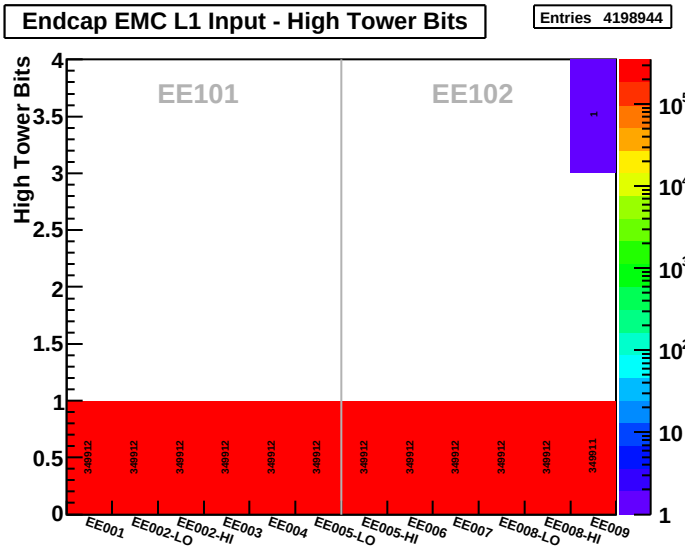

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

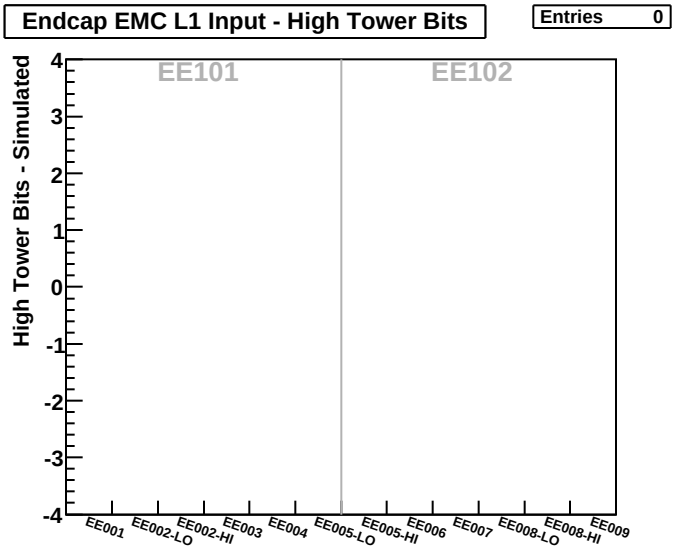

Endcap EMC L1 Input - Low Eta Sum
Entries 4198944
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

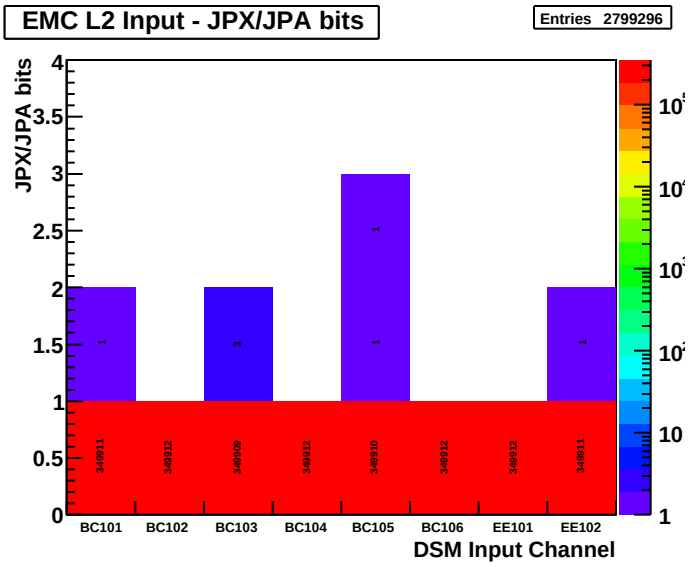

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

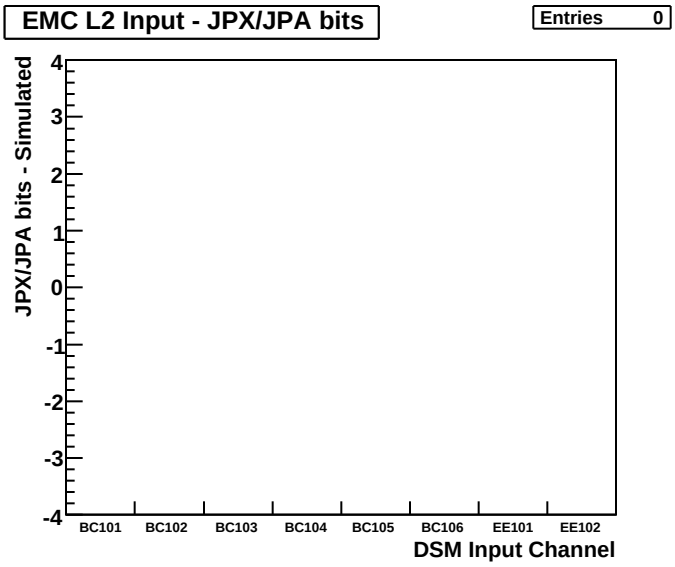

Endcap EMC L1 Input - High Eta Sum
Entries 4198944
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

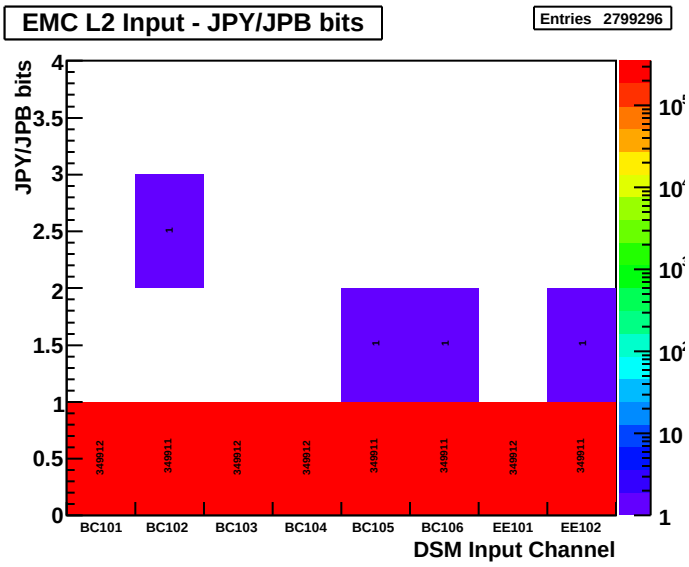

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

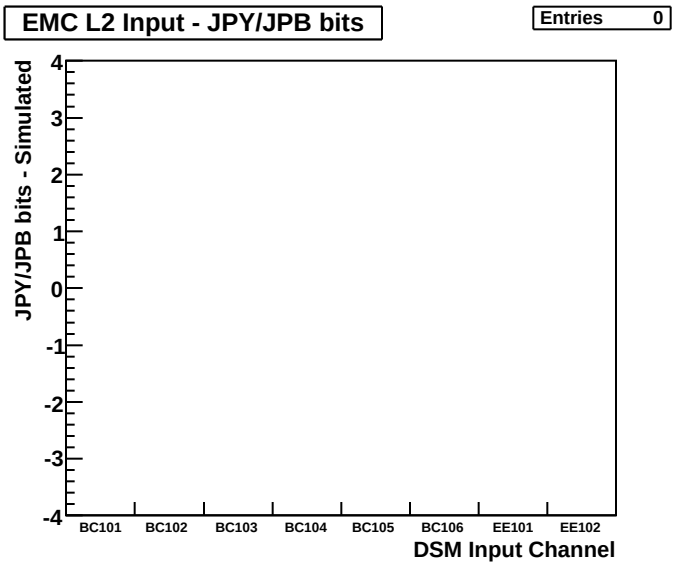

Endcap EMC L1 Input - High Tower Bits
Entries 4198944
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

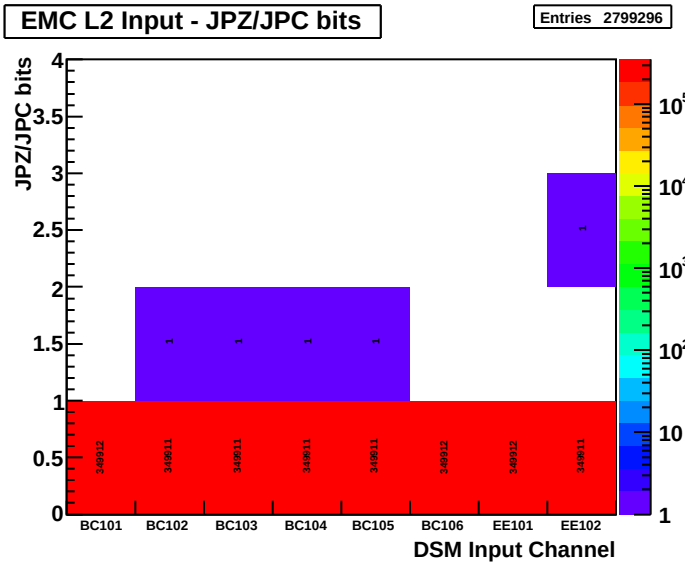

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

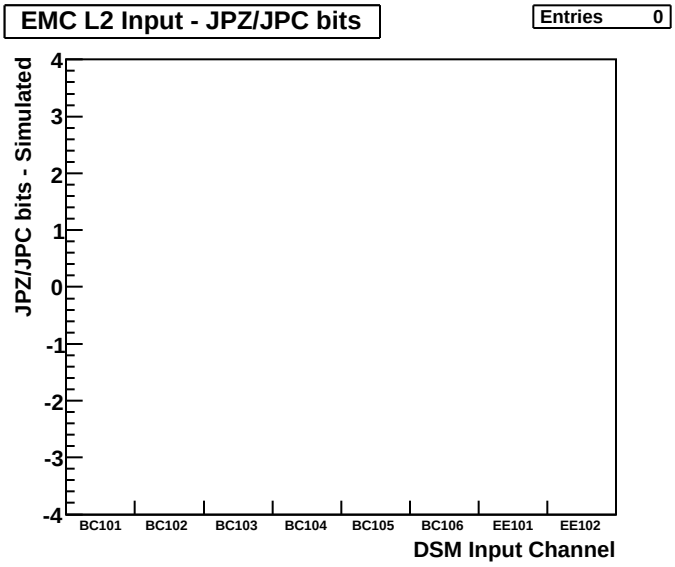

EMC L2 Input - JPX/JPA bits
Entries 2799296
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

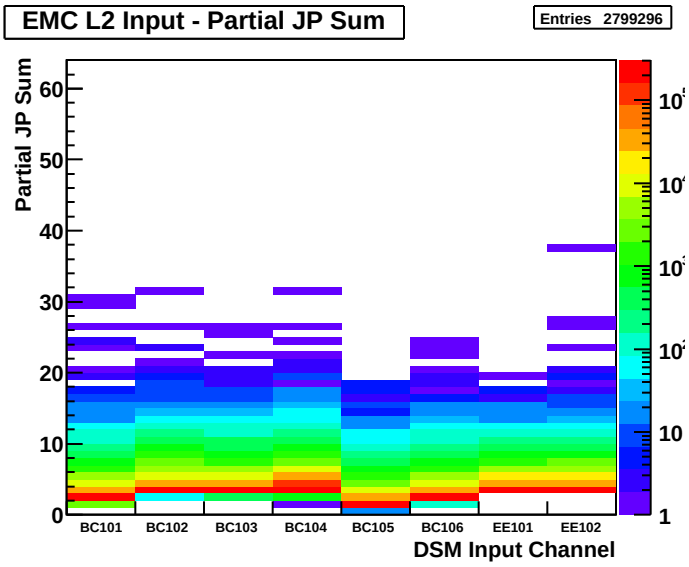

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

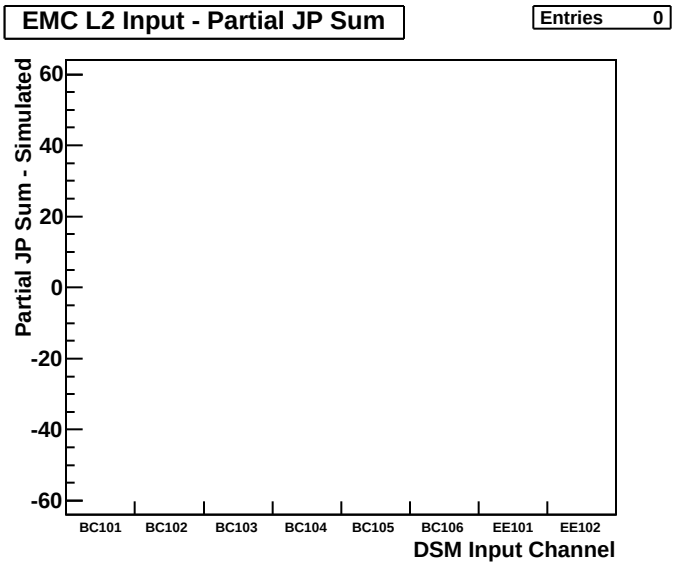

EMC L2 Input - JPY/JPB bits
Entries 2799296
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

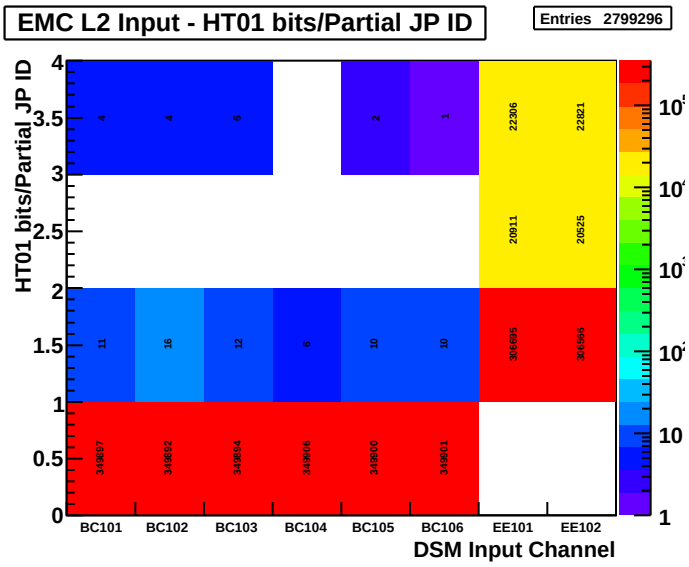

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

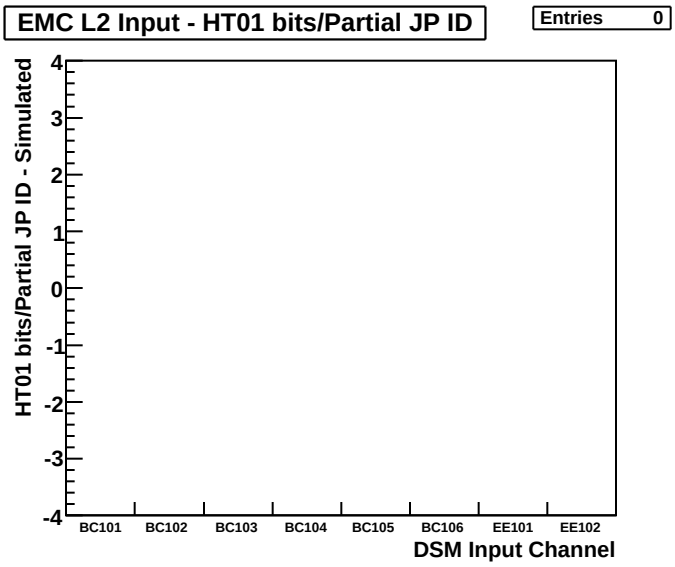

EMC L2 Input - JPZ/JPC bits
Entries 2799296
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

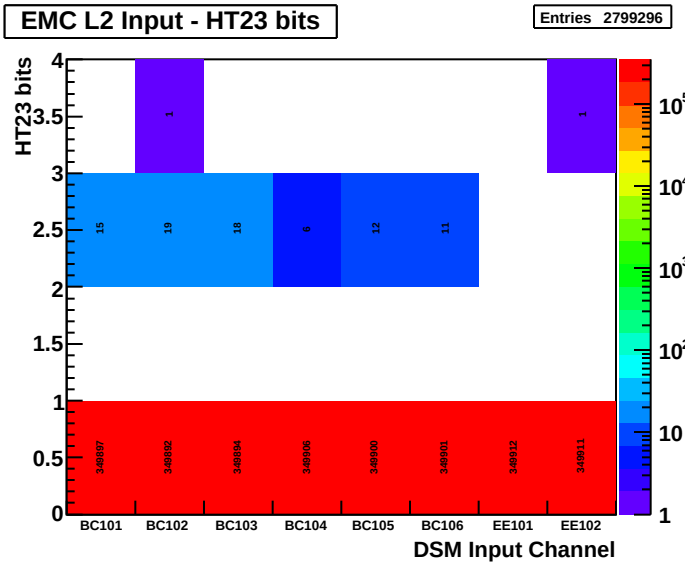

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

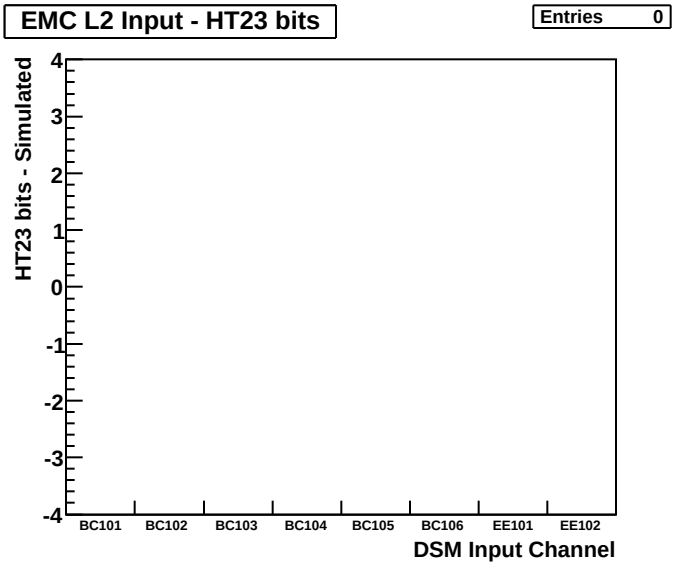

EMC L2 Input - Partial JP Sum
Entries 2799296
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

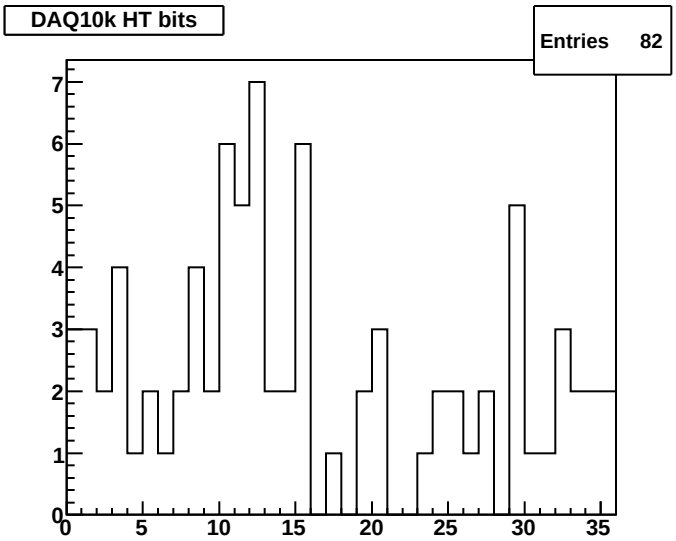

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

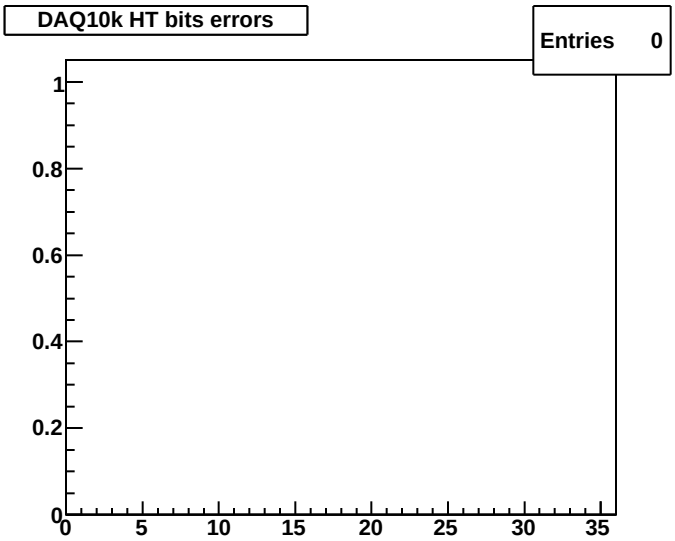

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2799296
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

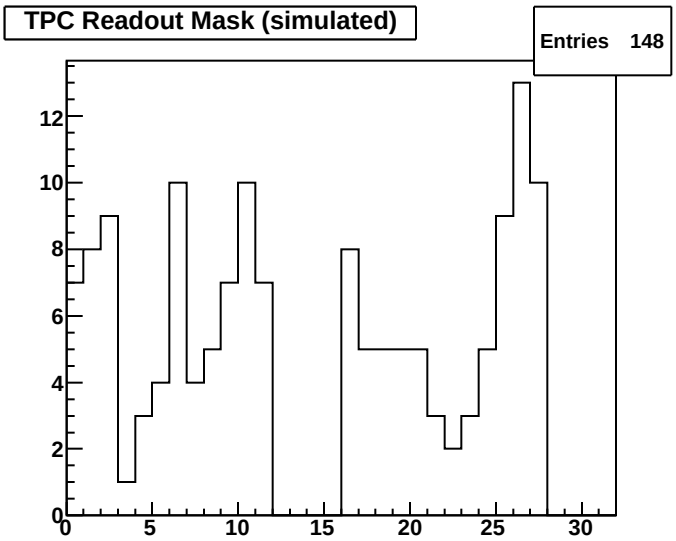

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

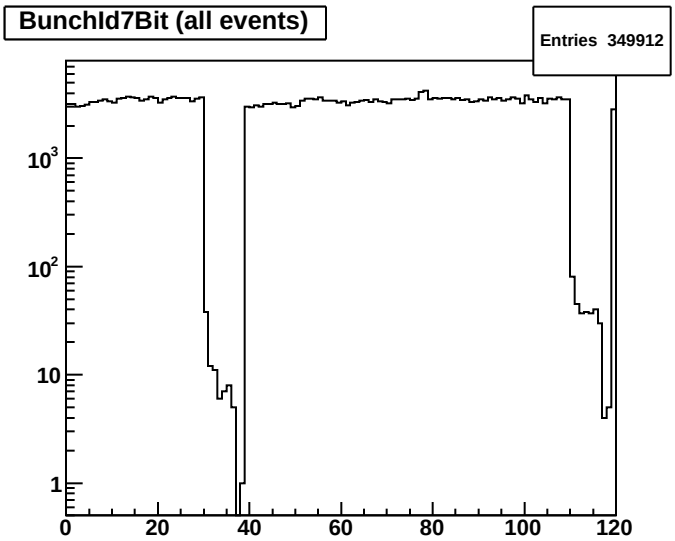

EMC L2 Input - HT23 bits
Entries 2799296
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

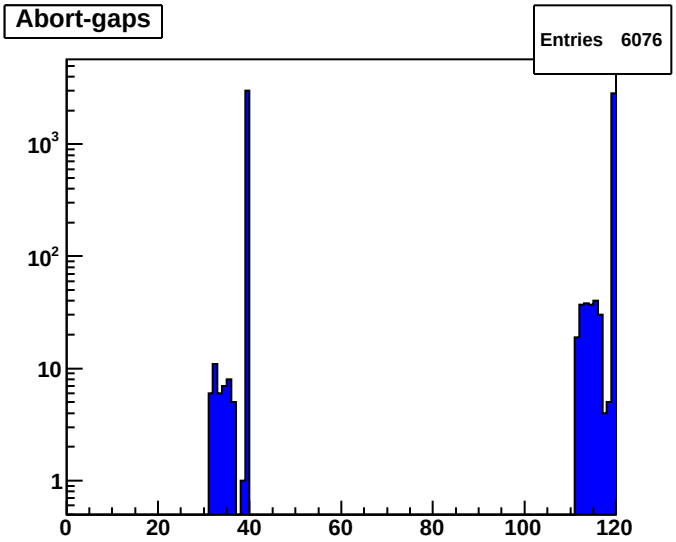

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

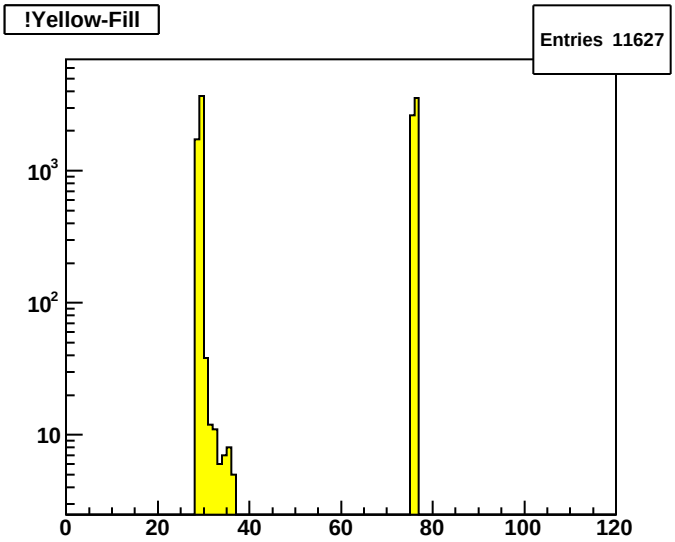

DAQ10k HT bits
Entries 82

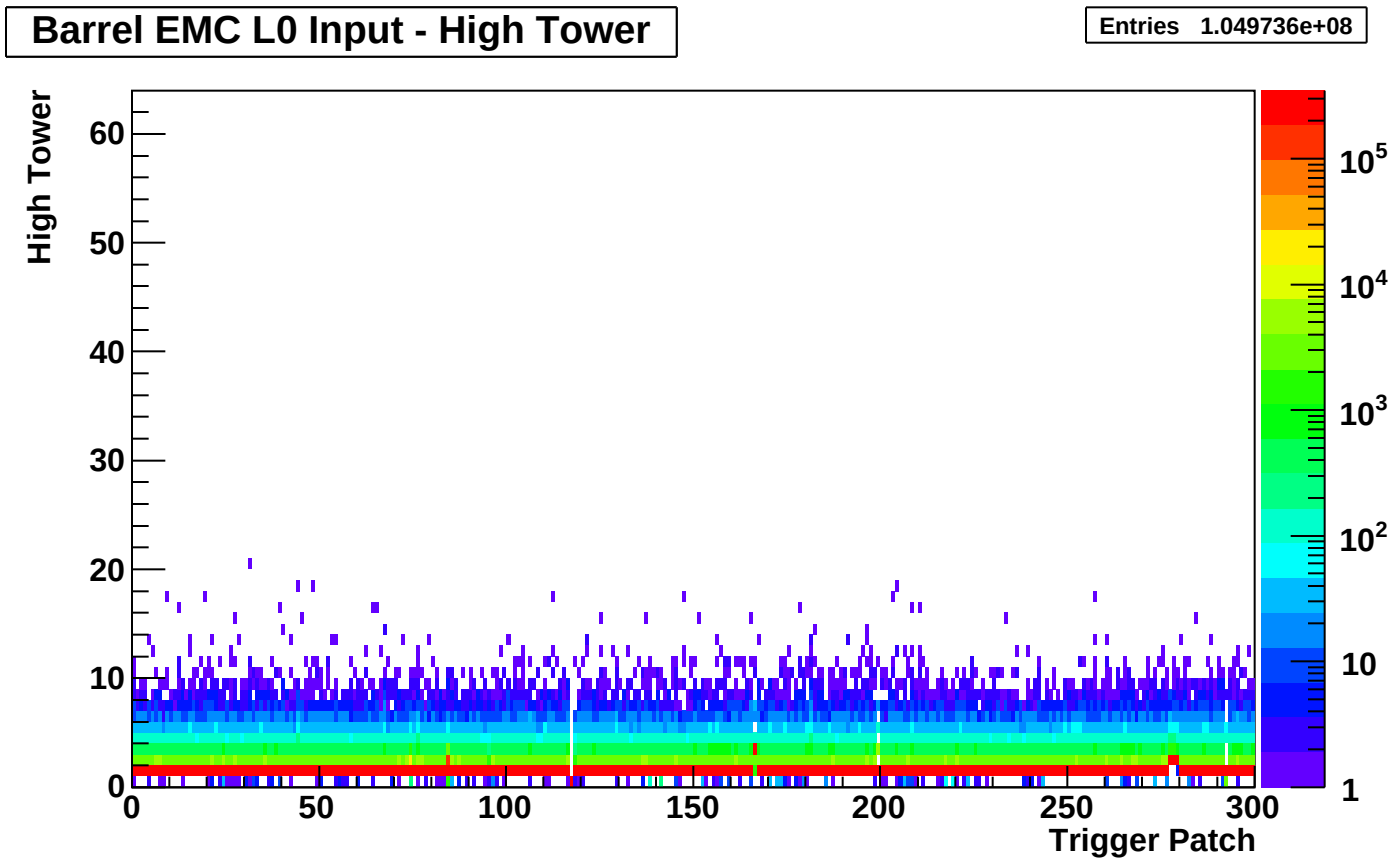

DAQ10k HT bits errors
Entries 0

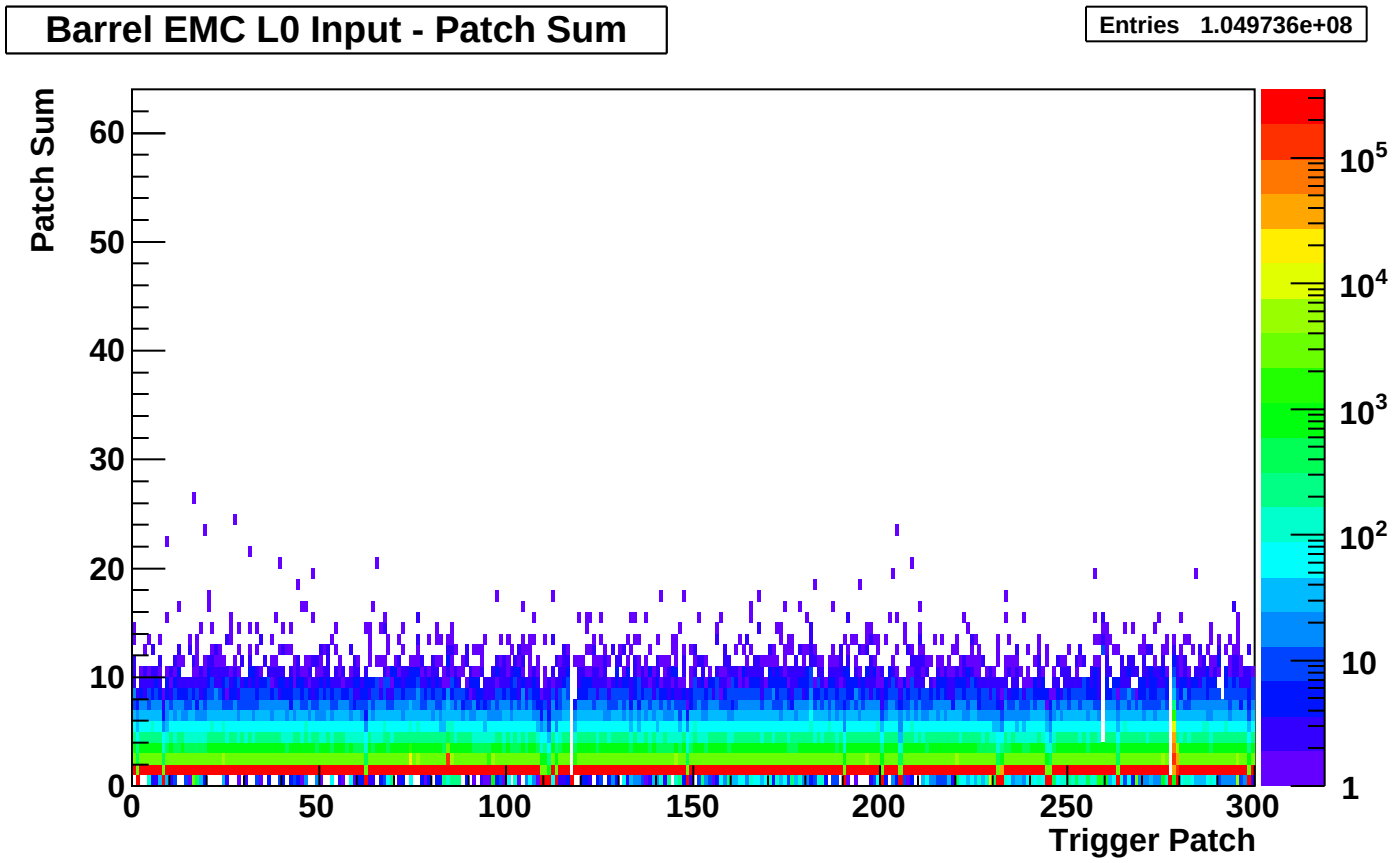

TPC Readout Mask (simulated)
Entries 148

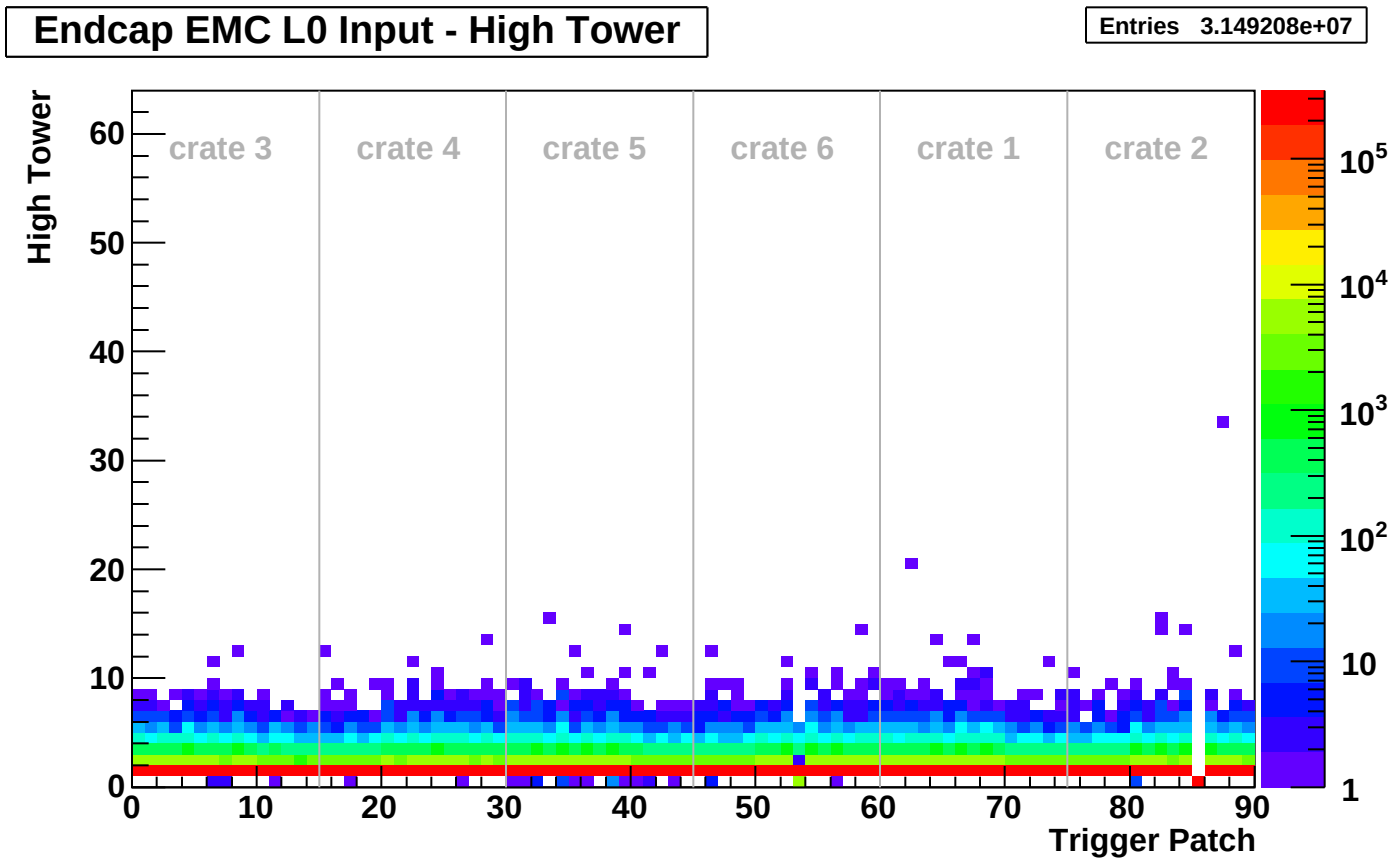

BunchId7Bit (all events)
Entries 349912

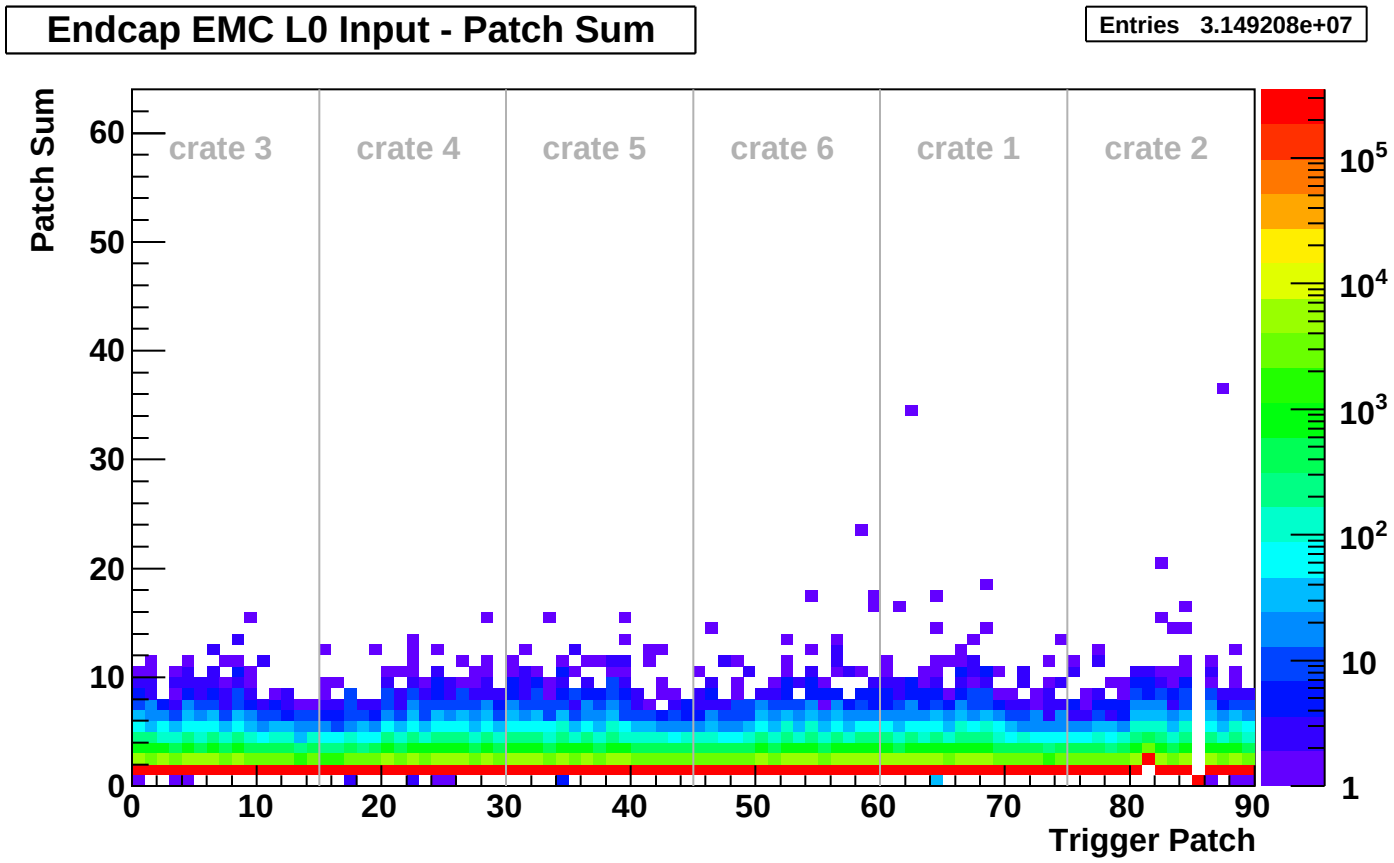

Abort-gaps
Entries 6076

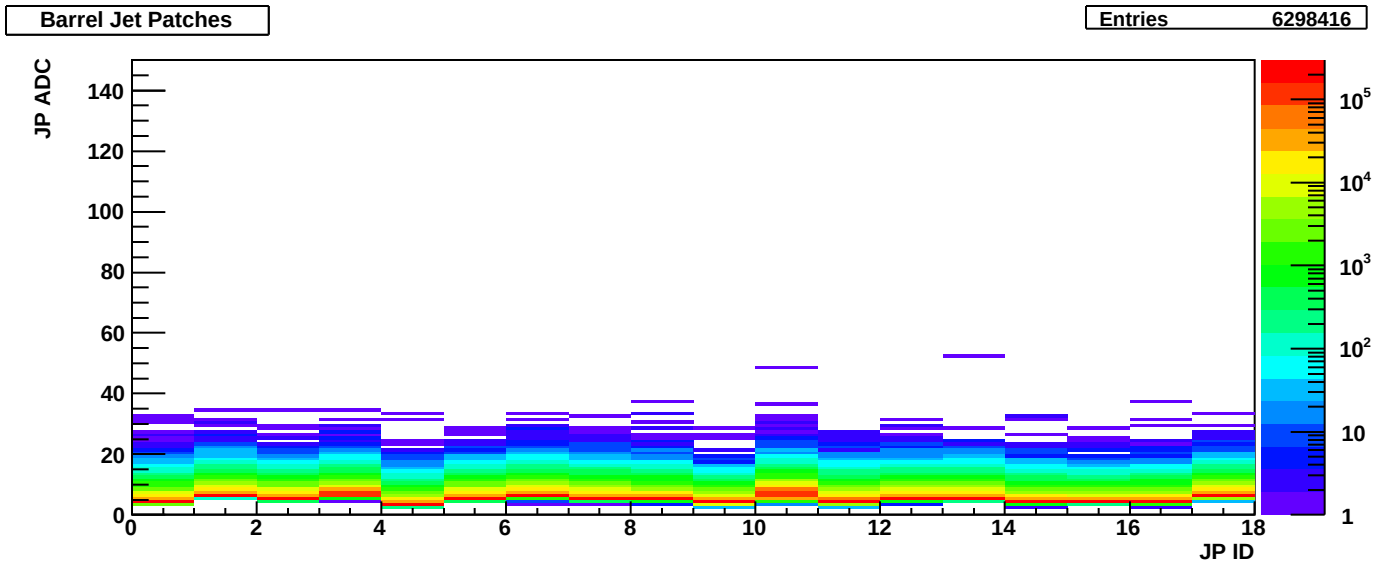

!Yellow-Fill
Entries 11627

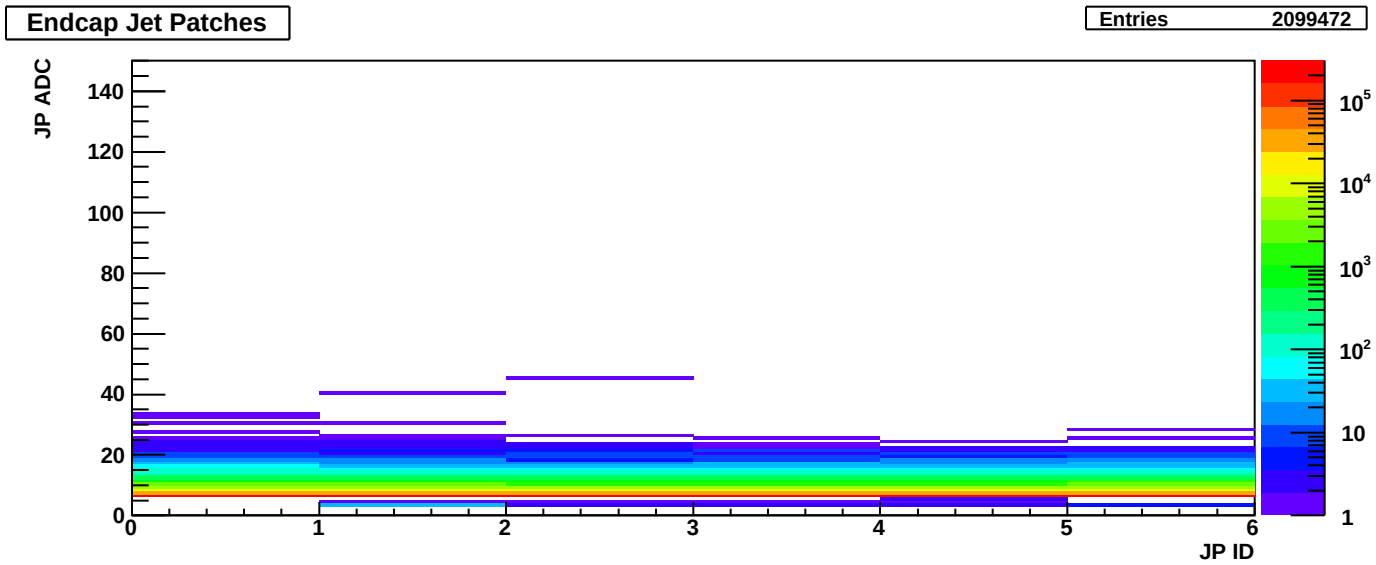

Barrel EMC L0 Input - High Tower
Entries 1.049736e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

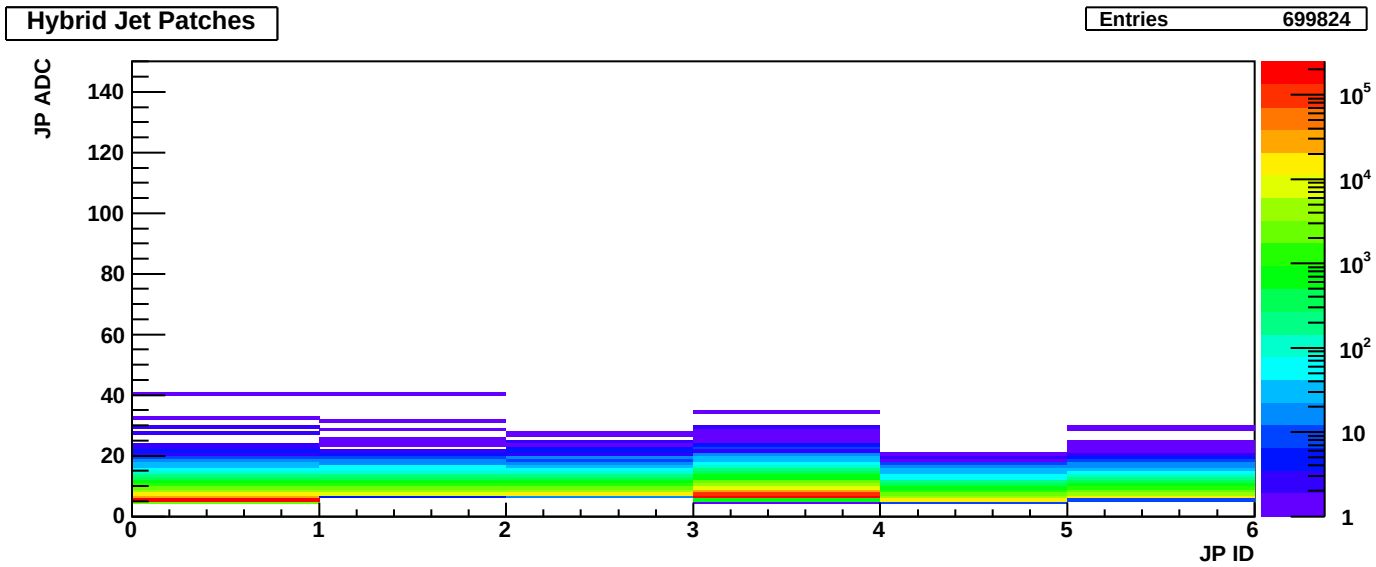

Barrel EMC L0 Input - Patch Sum
Entries 1.049736e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

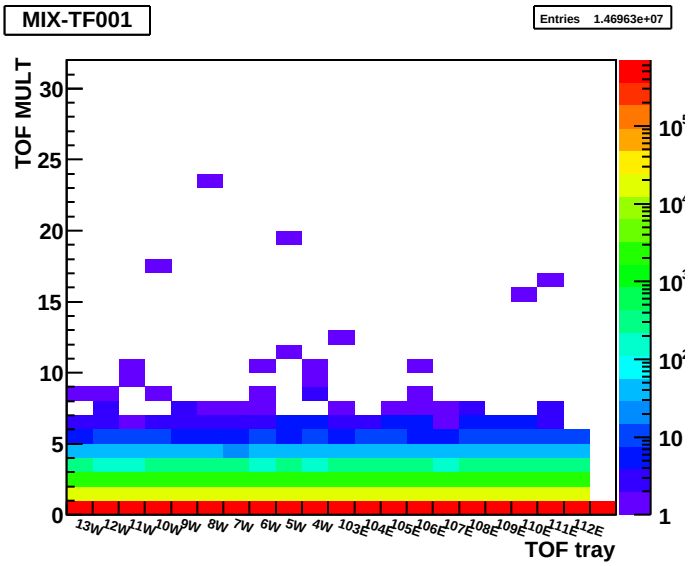

Endcap EMC L0 Input - High Tower
Entries 3.149208e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

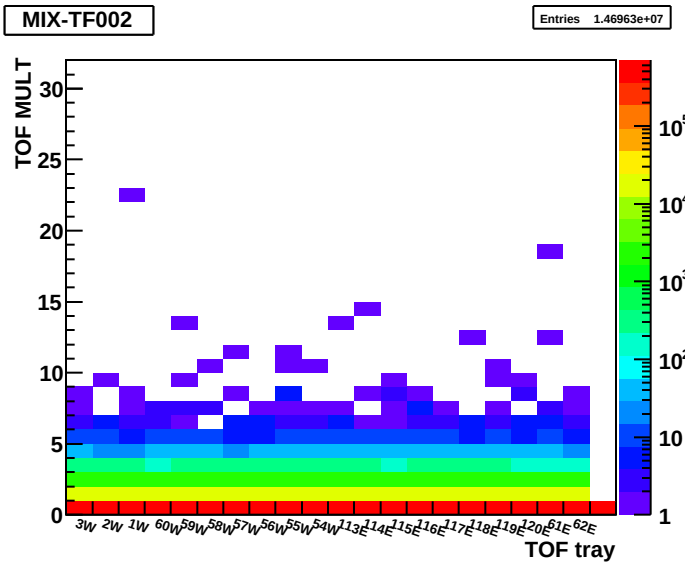

Endcap EMC L0 Input - Patch Sum
Entries 3.149208e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

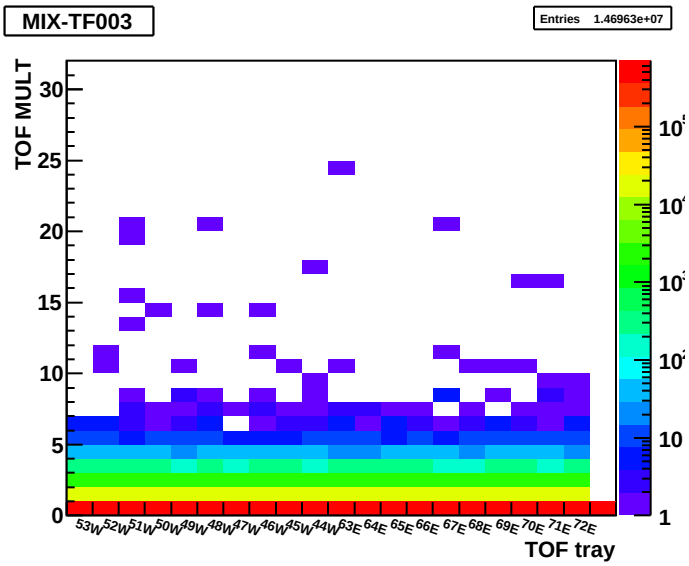

Barrel Jet Patches
Entries 6298416
JP ADC
JP ID

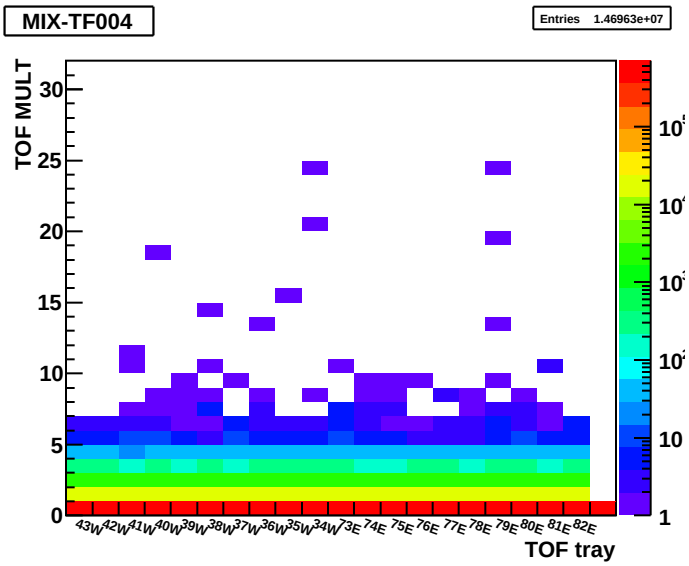

Endcap Jet Patches
Entries 2099472
JP ADC
JP ID

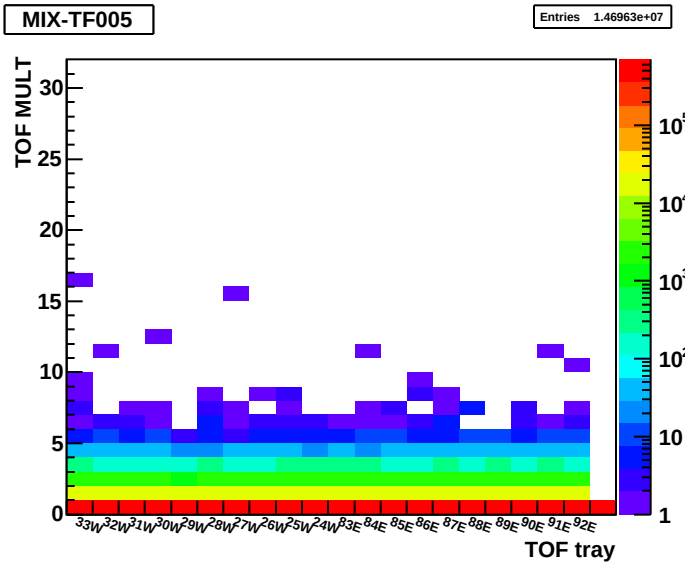

Hybrid Jet Patches
Entries 699824
JP ADC
JP ID

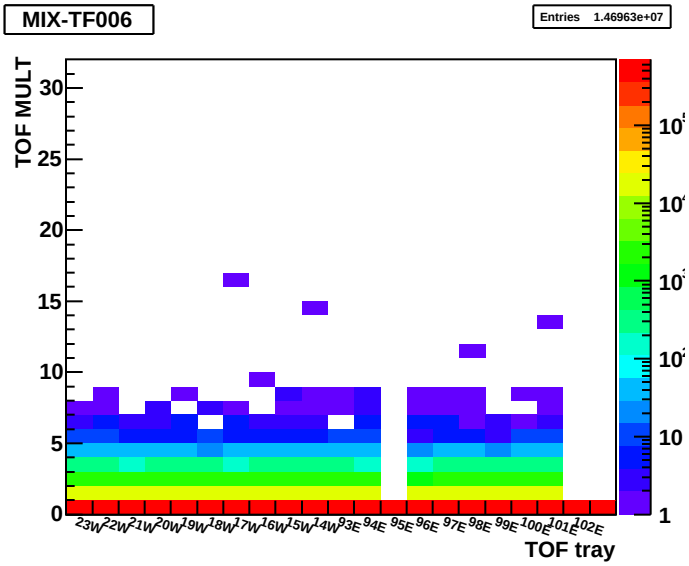

MIX-TF001
Entries 1.46963e+07
TOF MULT
TOF tray

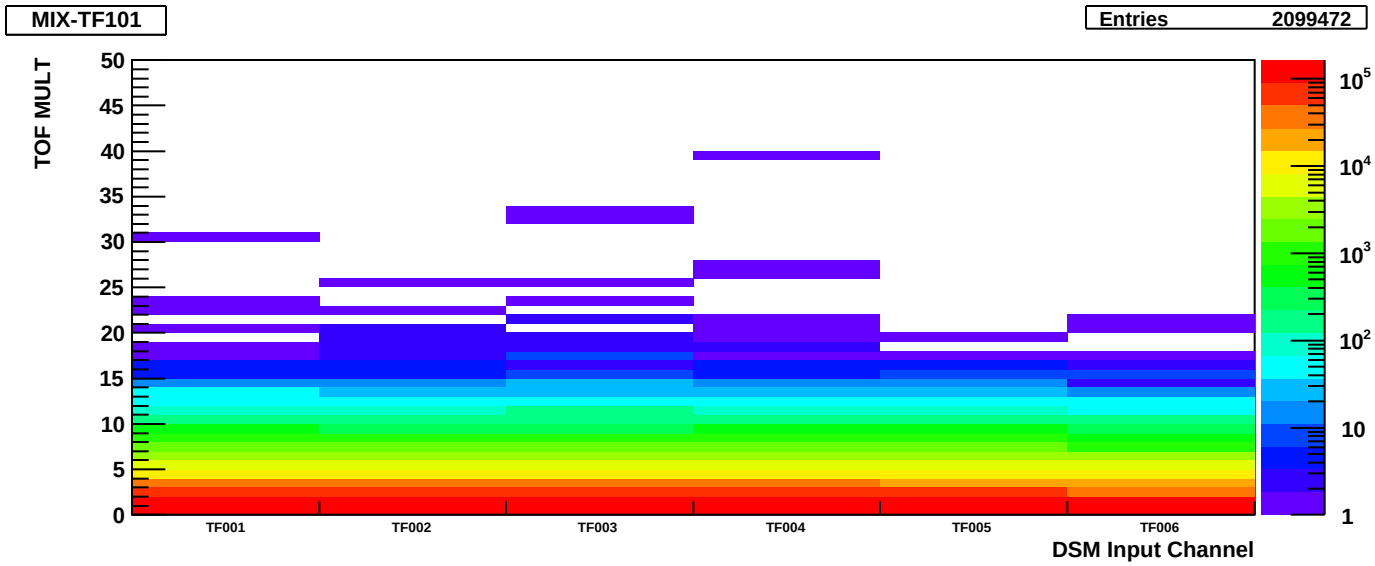

MIX-TF002
Entries 1.46963e+07
TOF MULT
TOF tray

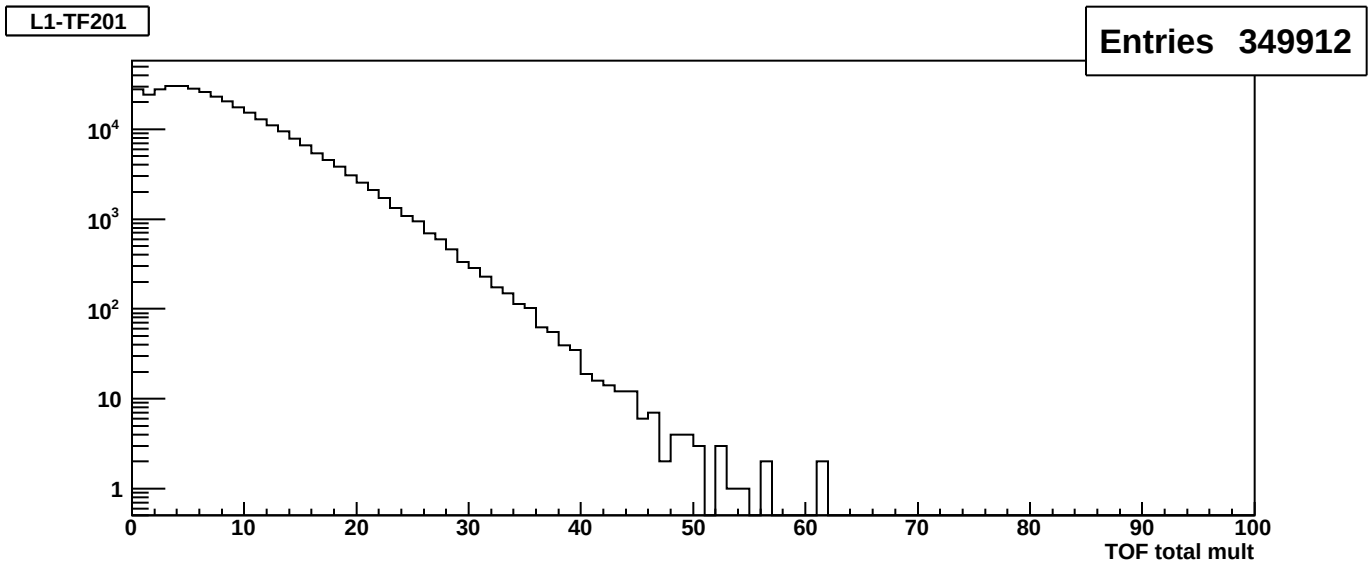

MIX-TF003
Entries 1.46963e+07
TOF MULT
TOF tray

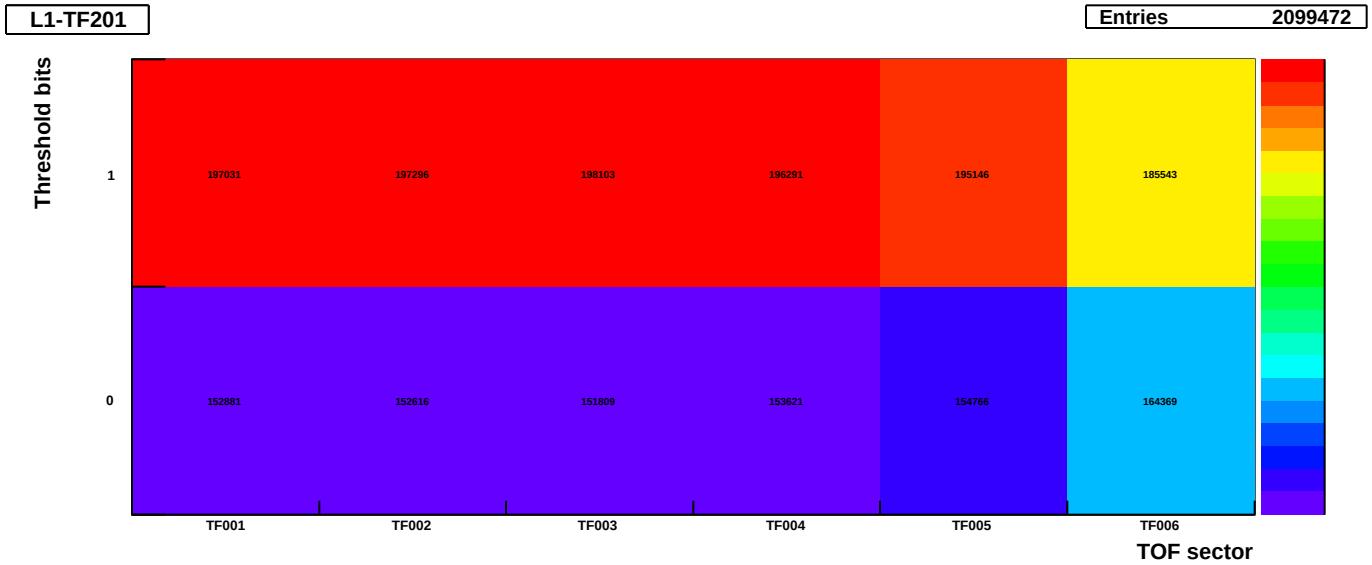

MIX-TF004
Entries 1.46963e+07
TOF MULT
TOF tray

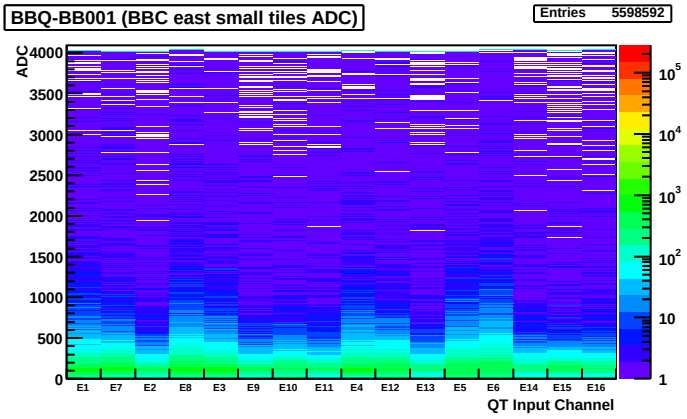

MIX-TF005
Entries 1.46963e+07
TOF MULT
TOF tray

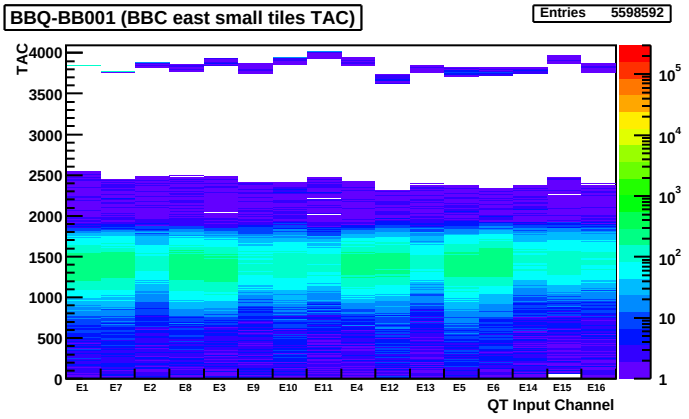

MIX-TF006
Entries 1.46963e+07
TOF MULT
TOF tray

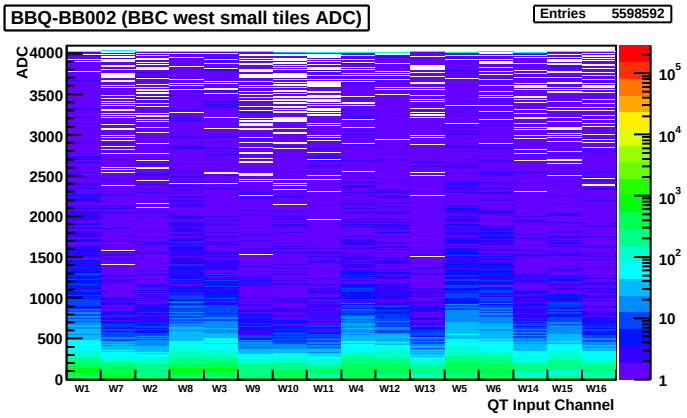

MIX-TF101
Entries 2099472
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

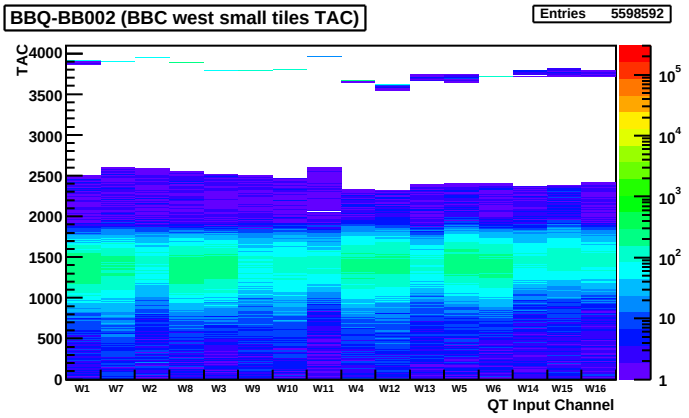

L1-TF201
Entries 349912
TOF total mult

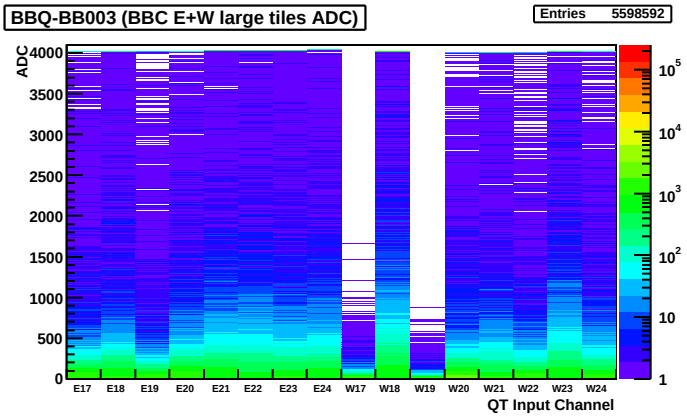

L1-TF201
Entries 2099472
Threshold bits
197031
197296
198103
196291
195146
185543
152881
152616
151809
153621
154766
164369
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

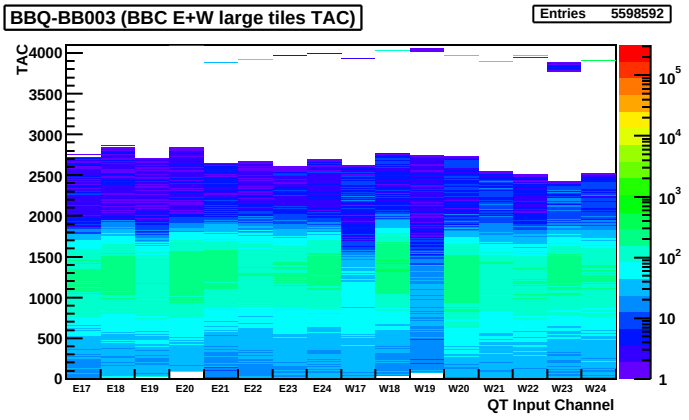

BBQ-BB001 (BBC east small tiles ADC)
Entries 5598592
ADC
QT Input Channel

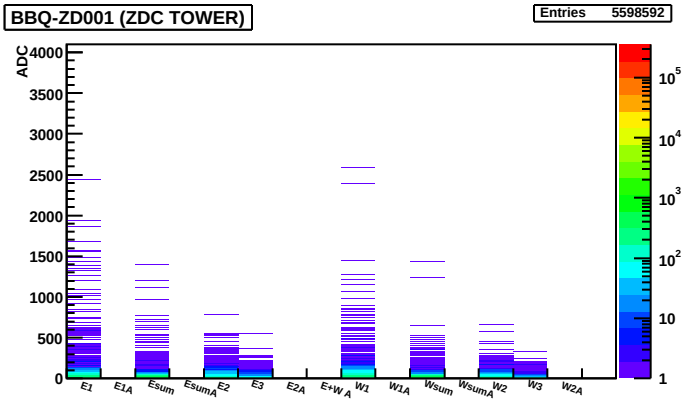

BBQ-BB001 (BBC east small tiles TAC)
Entries 5598592
TAC
QT Input Channel

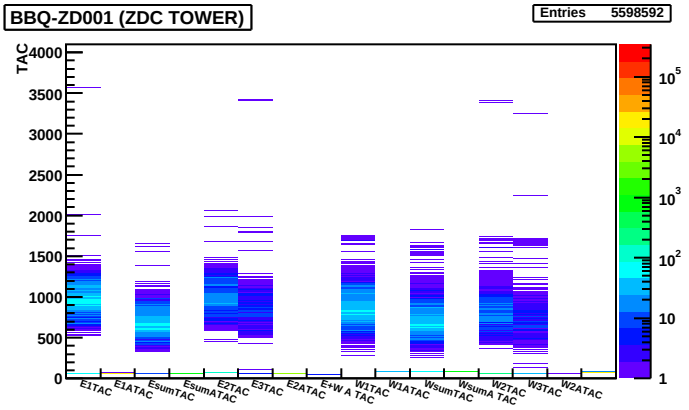

BBQ-BB002 (BBC west small tiles ADC)
Entries 5598592
ADC
QT Input Channel

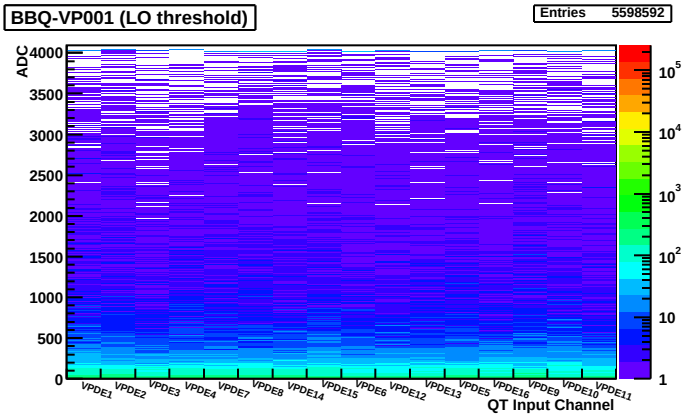

BBQ-BB002 (BBC west small tiles TAC)
Entries 5598592
TAC
QT Input Channel

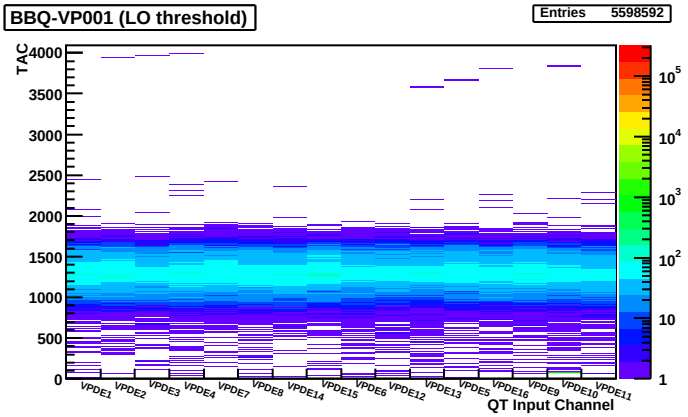

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5598592
ADC
QT Input Channel

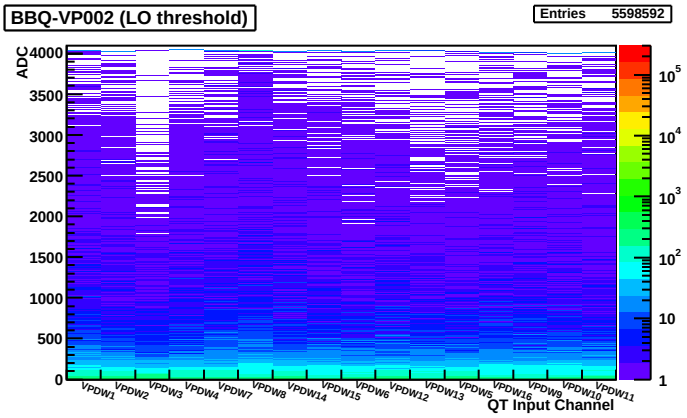

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5598592
TAC
QT Input Channel

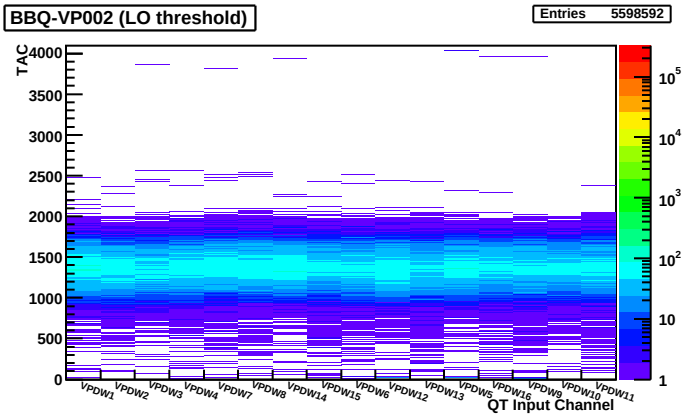

BBQ-ZD001 (ZDC TOWER)
Entries 5598592
ADC

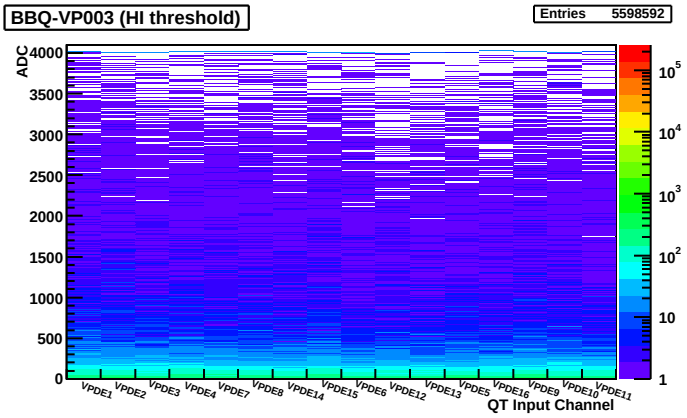

BBQ-ZD001 (ZDC TOWER)
Entries 5598592
TAC

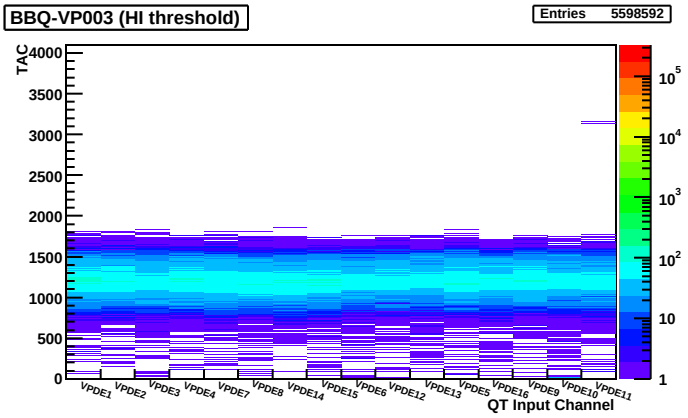

BBQ-VP001 (LO threshold)
Entries 5598592
ADC
QT Input Channel

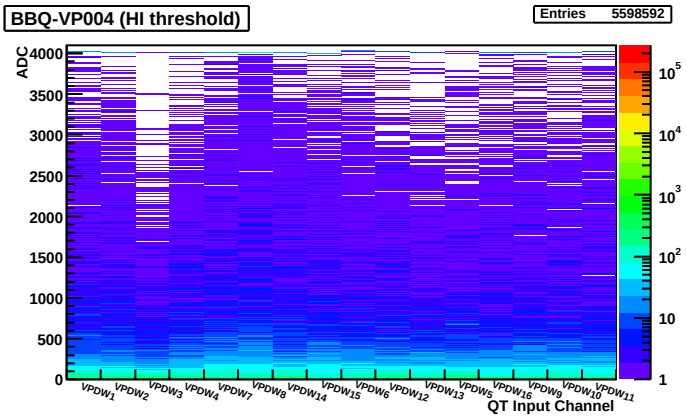

BBQ-VP001 (LO threshold)
Entries 5598592
TAC
QT Input Channel

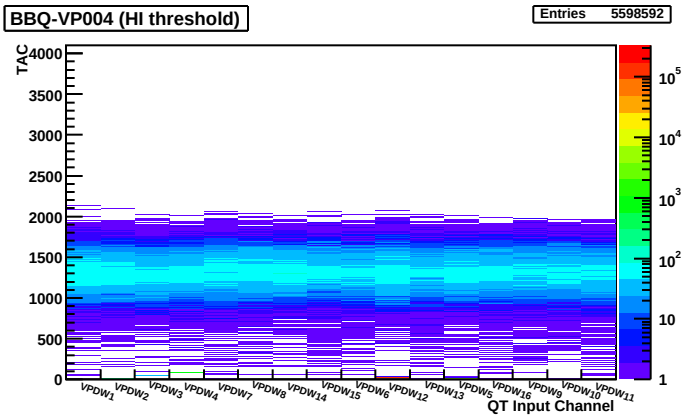

BBQ-VP002 (LO threshold)
Entries 5598592
ADC
QT Input Channel

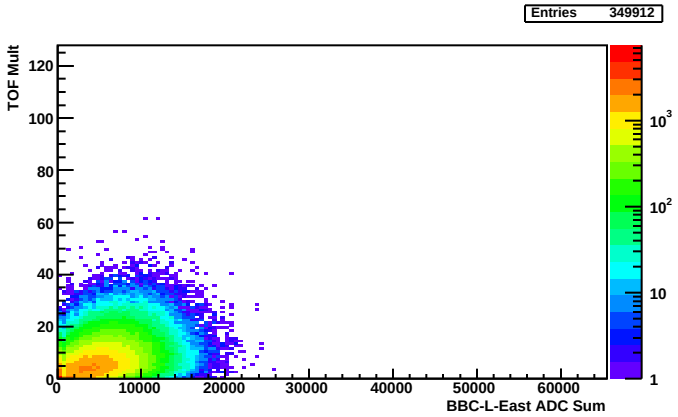

BBQ-VP002 (LO threshold)
Entries 5598592
TAC
QT Input Channel

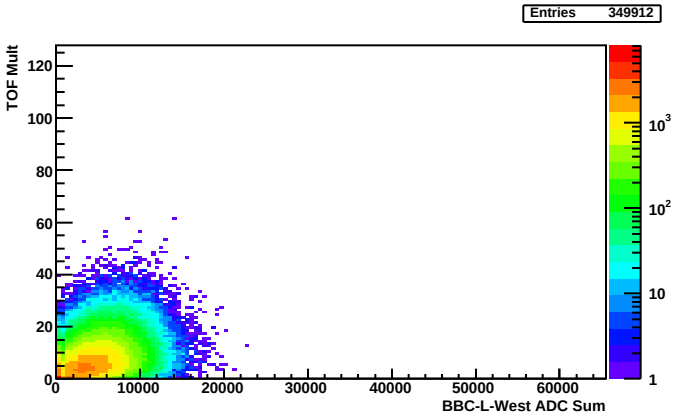

BBQ-VP003 (HI threshold)
Entries 5598592
ADC
QT Input Channel

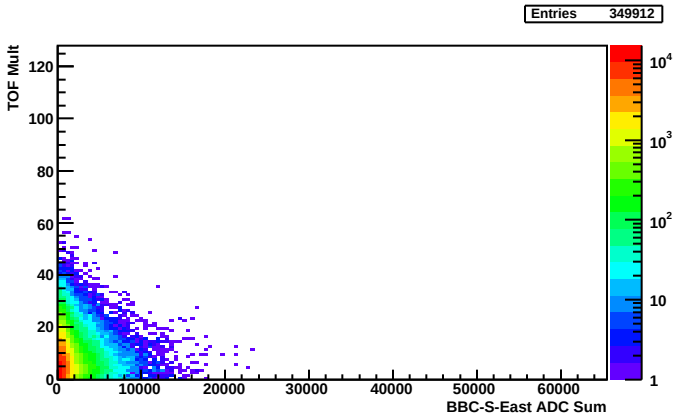

BBQ-VP003 (HI threshold)
Entries 5598592
TAC
QT Input Channel

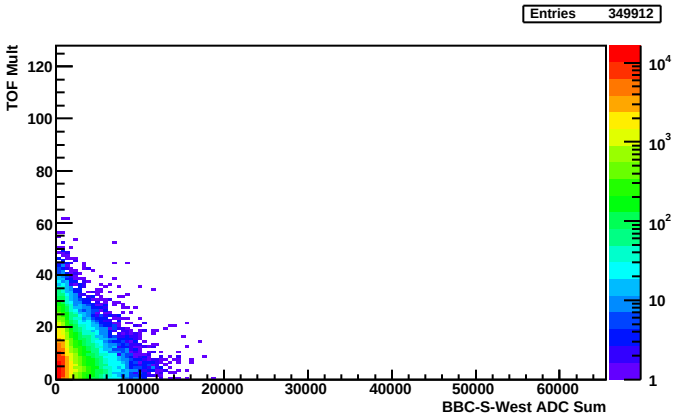

BBQ-VP004 (HI threshold)
Entries 5598592
ADC
QT Input Channel

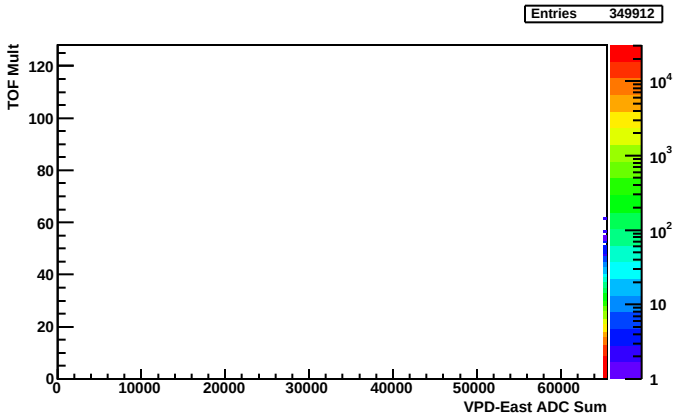

BBQ-VP004 (HI threshold)
Entries 5598592
TAC
QT Input Channel

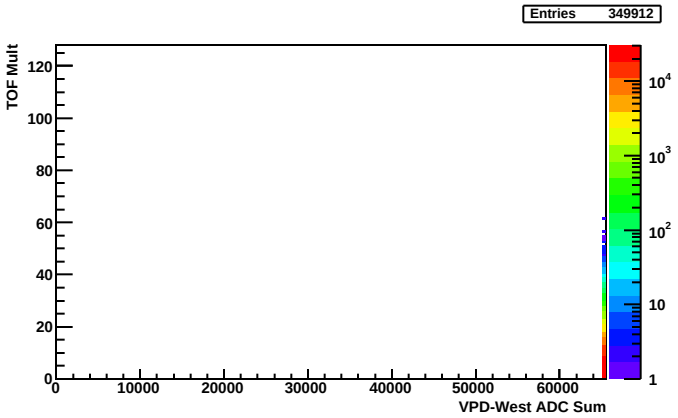

Entries 349912
TOF Mult
BBC-L-East ADC Sum

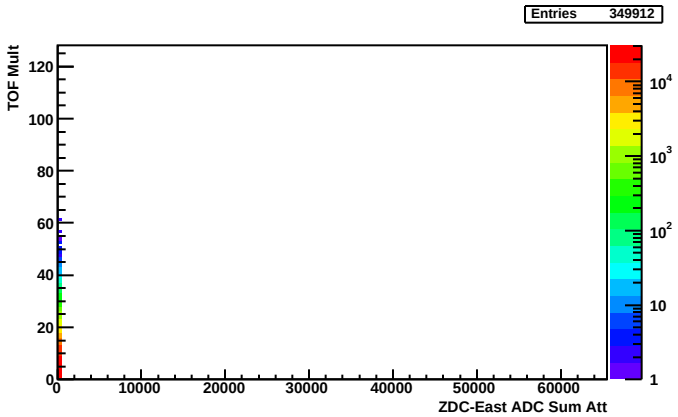

Entries 349912
TOF Mult
BBC-L-West ADC Sum

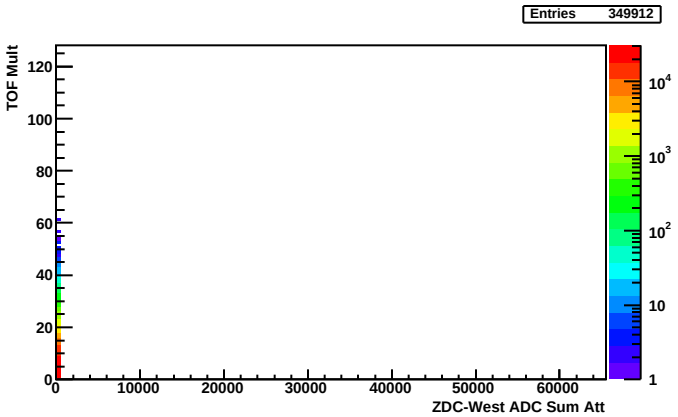

Entries 349912
TOF Mult
BBC-S-East ADC Sum

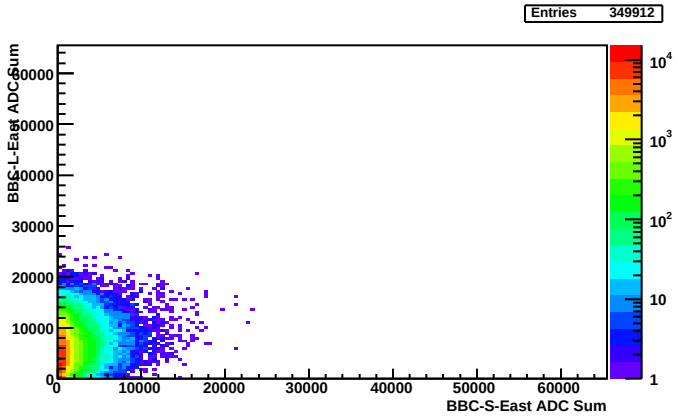

Entries 349912
TOF Mult
BBC-S-West ADC Sum

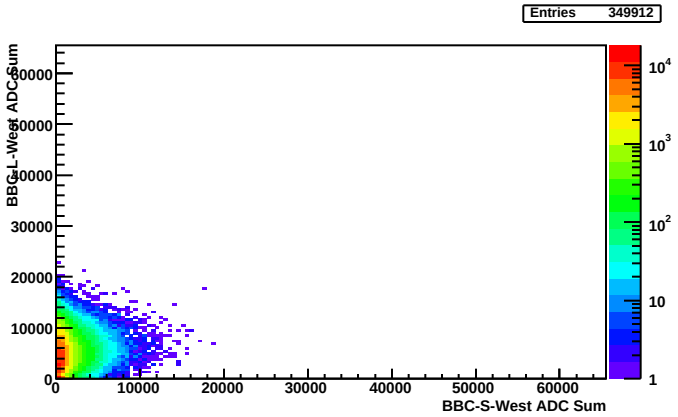

Entries 349912
TOF Mult
VPD-East ADC Sum

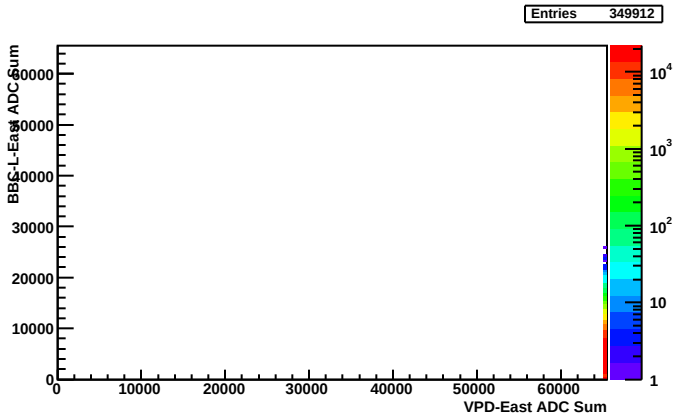

Entries 349912
TOF Mult
VPD-West ADC Sum

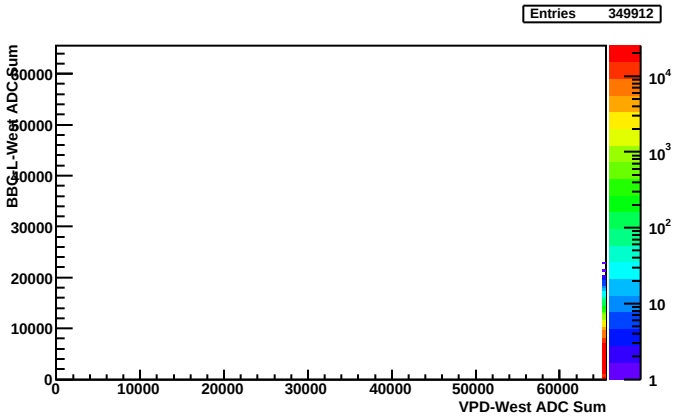

Entries 349912
TOF Mult
ZDC-East ADC Sum Att

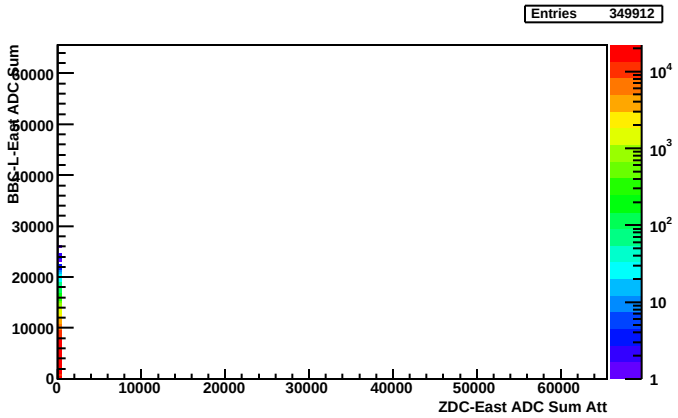

Entries 349912
TOF Mult
ZDC-West ADC Sum Att


Entries 349912
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 349912
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 349912
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 349912
BBC-L-West ADC Sum
VPD-West ADC Sum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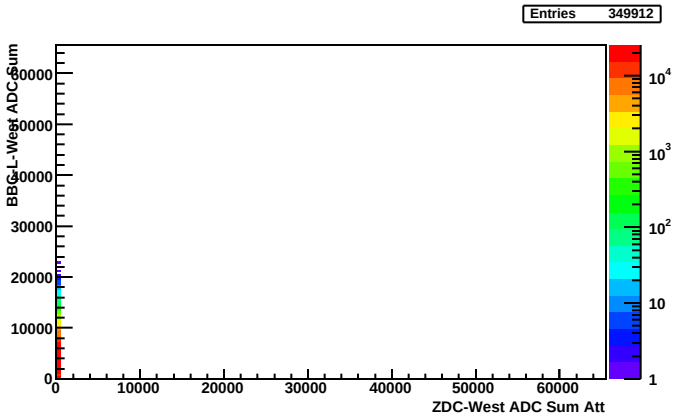
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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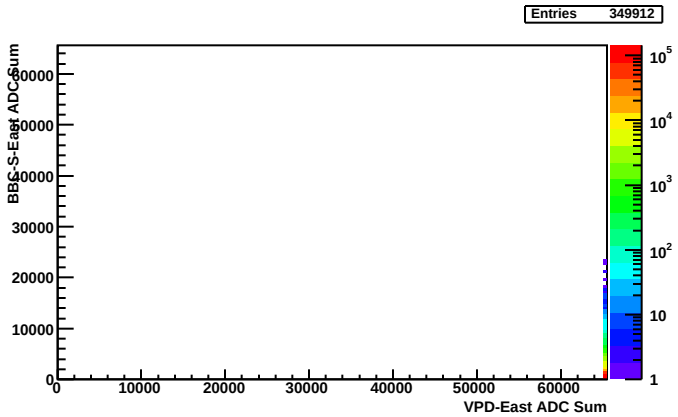
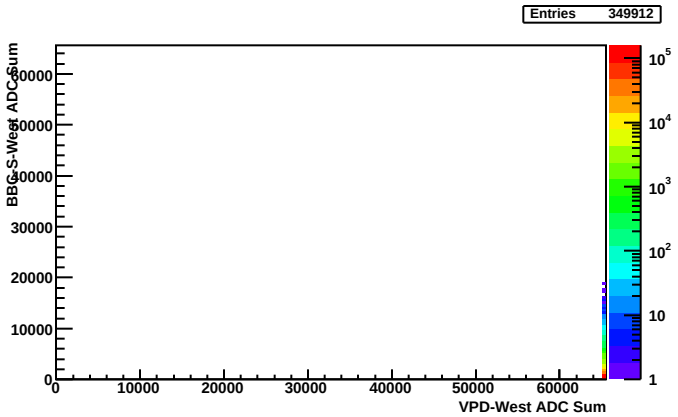
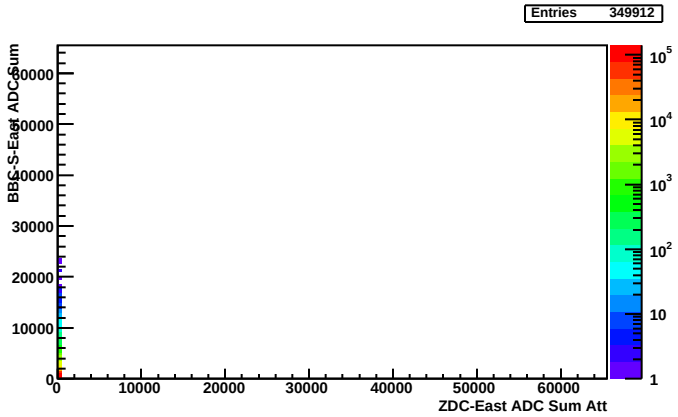
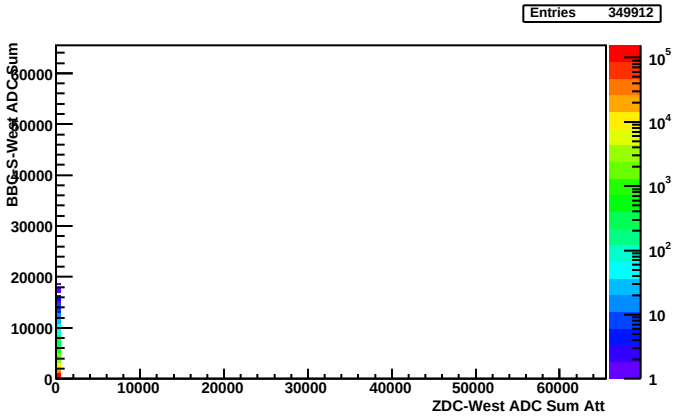
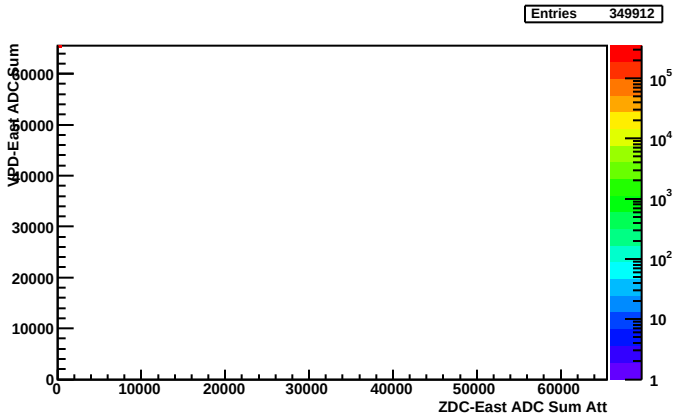
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

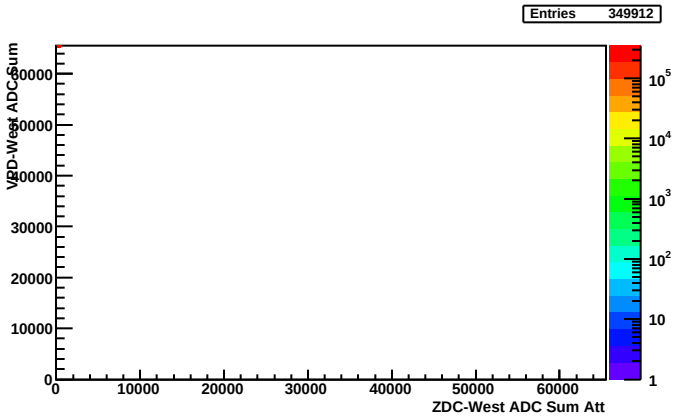

Entries 349912
BBC-S-East ADC Sum
VPD-East ADC Sum

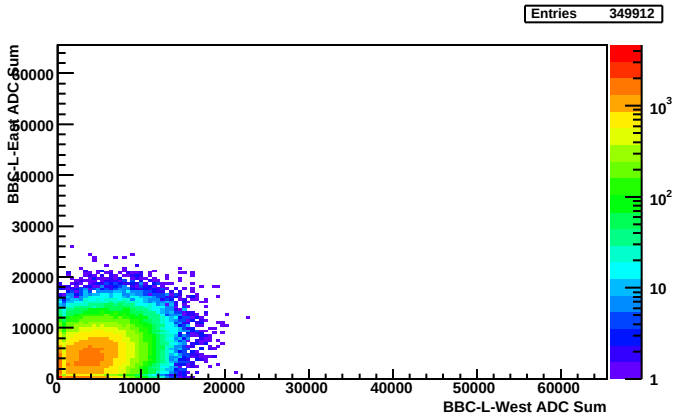

Entries 349912
BBC-S-West ADC Sum
VPD-West ADC Sum

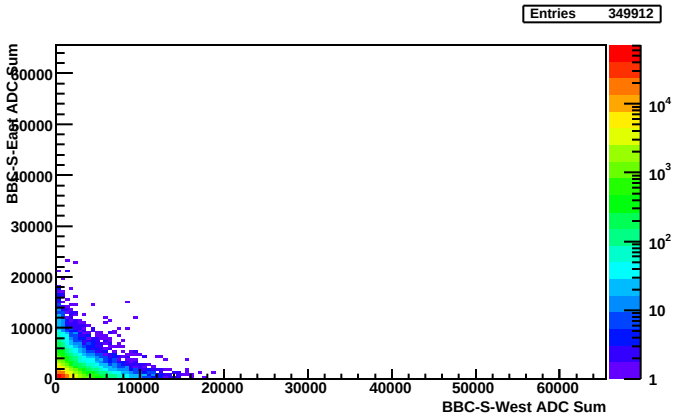

Entries 349912
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

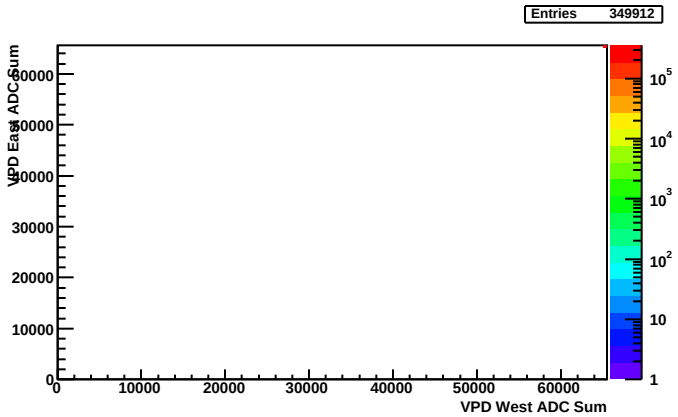

Entries 349912
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

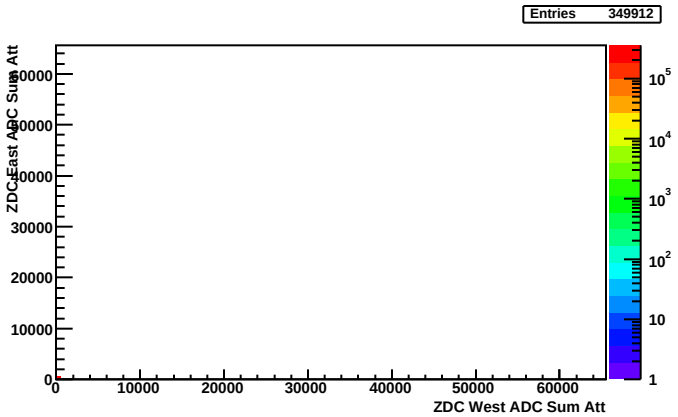

Entries 349912
VPD-East ADC Sum
ZDC-East ADC Sum Att

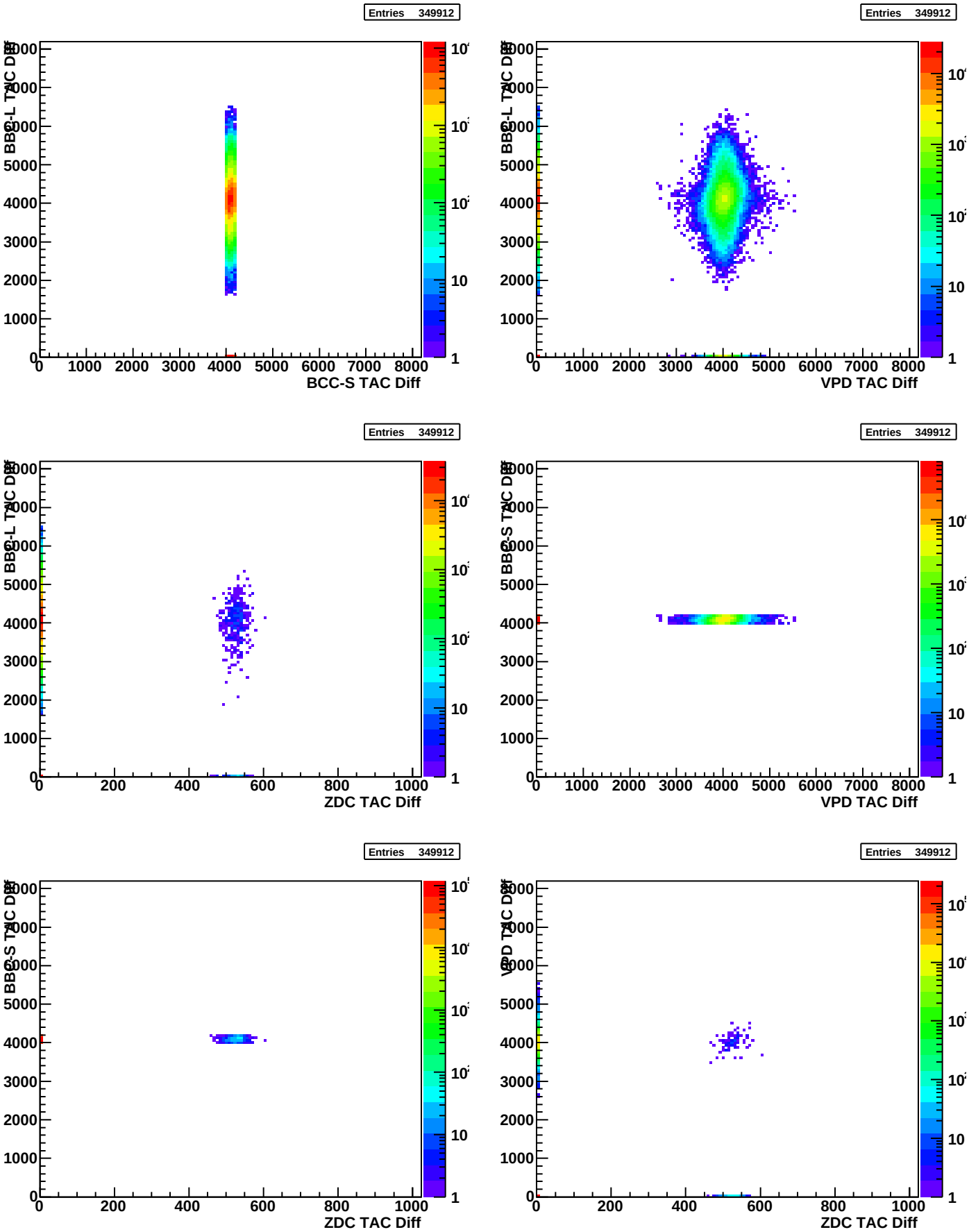

Entries 349912
VPD-West ADC Sum
ZDC-West ADC Sum Att

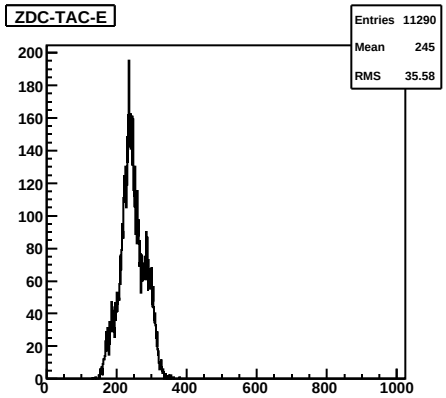

Entries 349912
BBC-L-East ADC Sum
BBC-L-West ADC Sum

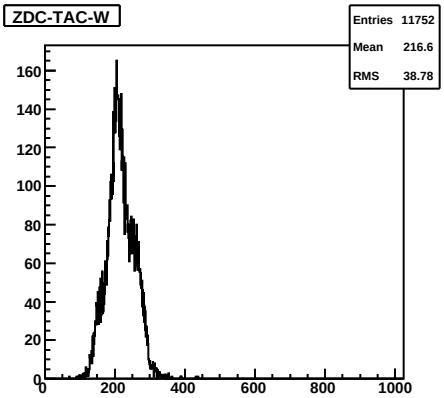

Entries 349912
BBC-S-East ADC Sum
BBC-S-West ADC Sum

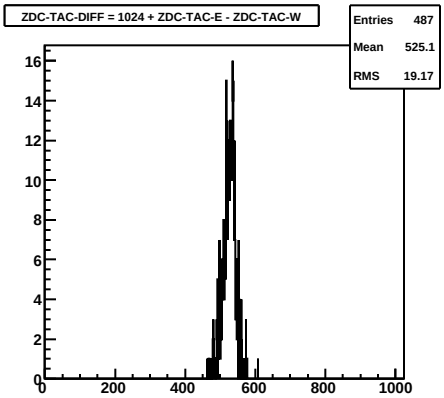

Entries 349912
VPD East ADC Sum
VPD West ADC Sum

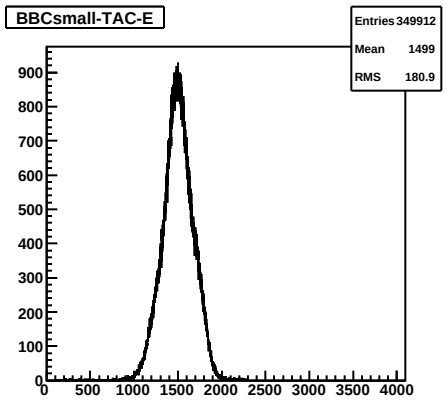

Entries 349912
ZDC East ADC Sum Att
ZDC West ADC Sum Att

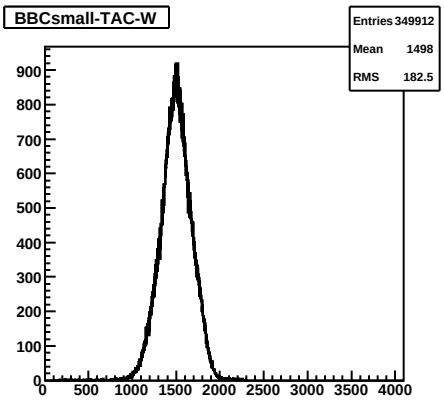

Entries 349912
BBC-L TAC Diff
BCC-S TAC Diff
Entries 349912
BBC-L TAC Diff
VPD TAC Diff
Entries 349912
BBC-L TAC Diff
ZDC TAC Diff
Entries 349912
BBC-S TAC Diff
VPD TAC Diff
Entries 349912
BBC-S TAC Diff
ZDC TAC Diff
Entries 349912
VPD TAC Diff
ZDC TAC Diff

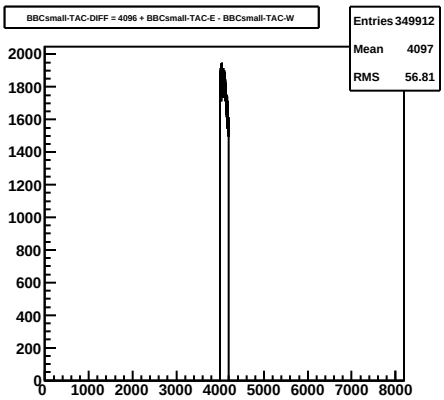

ZDC-TAC-E
Entries 11290
Mean 245
RMS 35.58

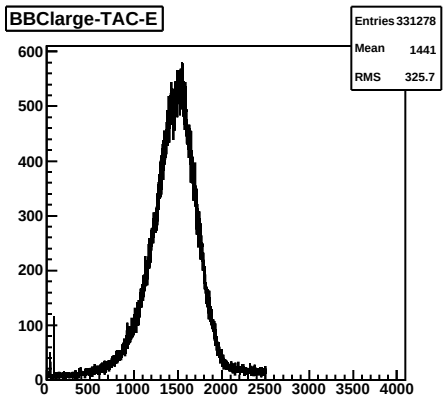

ZDC-TAC-W
Entries 11752
Mean 216.6
RMS 38.78

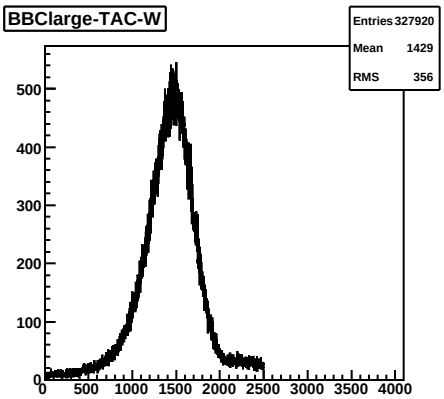

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 487
Mean 525.1
RMS 19.17

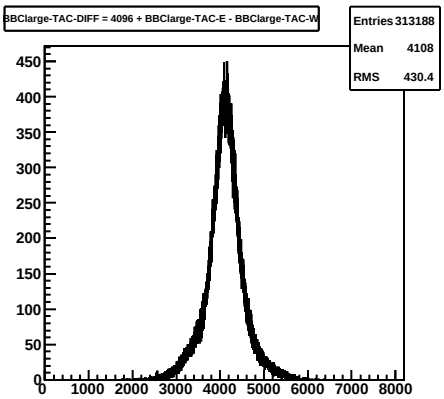

BBCsmall-TAC-E
Entries 349912
Mean 1499
RMS 180.9

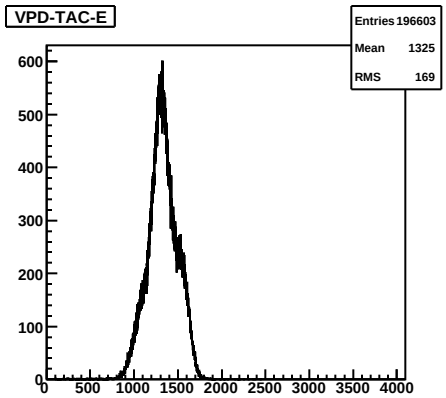

BBCsmall-TAC-W
Entries 349912
Mean 1498
RMS 182.5

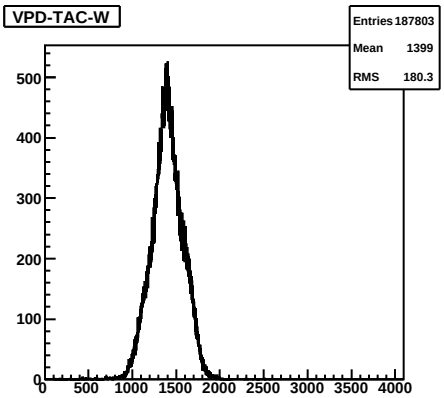

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 349912
Mean 4097
RMS 56.81

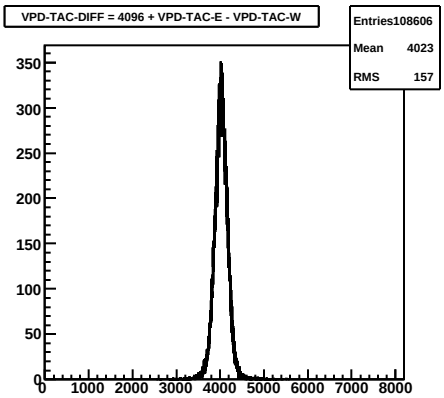

BBClarge-TAC-E
Entries 331278
Mean 1441
RMS 325.7

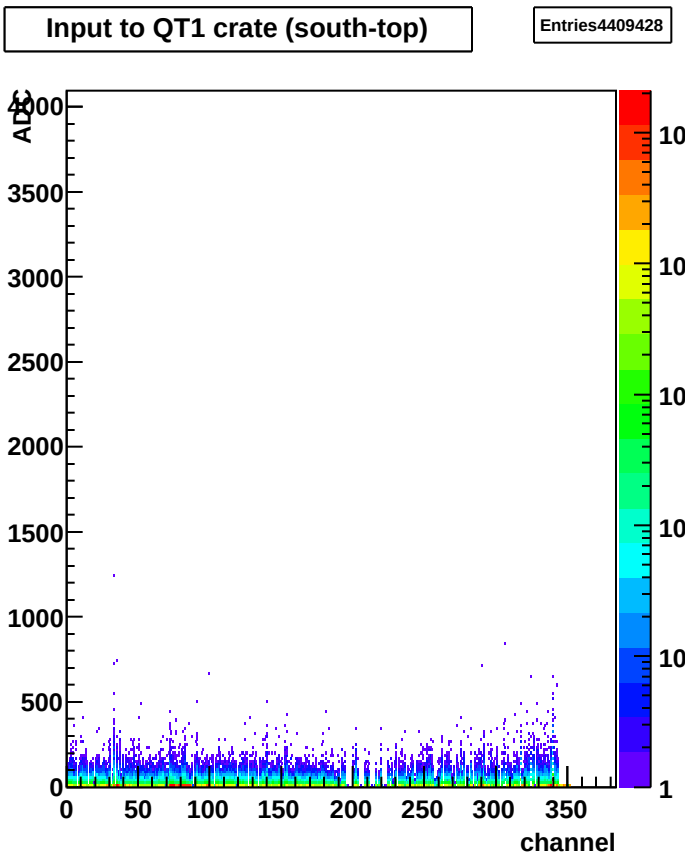

BBClarge-TAC-W
Entries 327920
Mean 1429
RMS 356

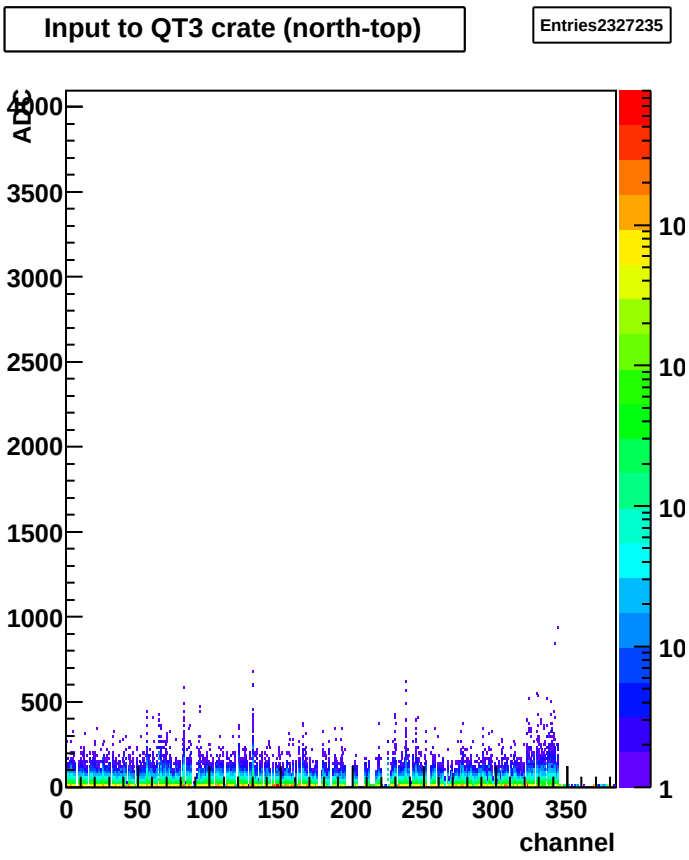

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 313188
Mean 4108
RMS 430.4

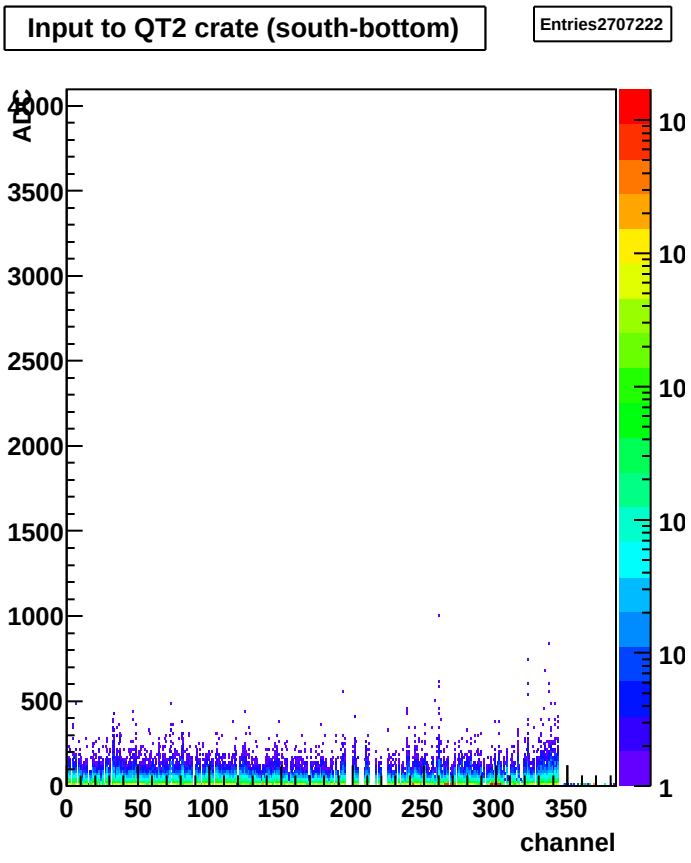

VPD-TAC-E
Entries 196603
Mean 1325
RMS 169

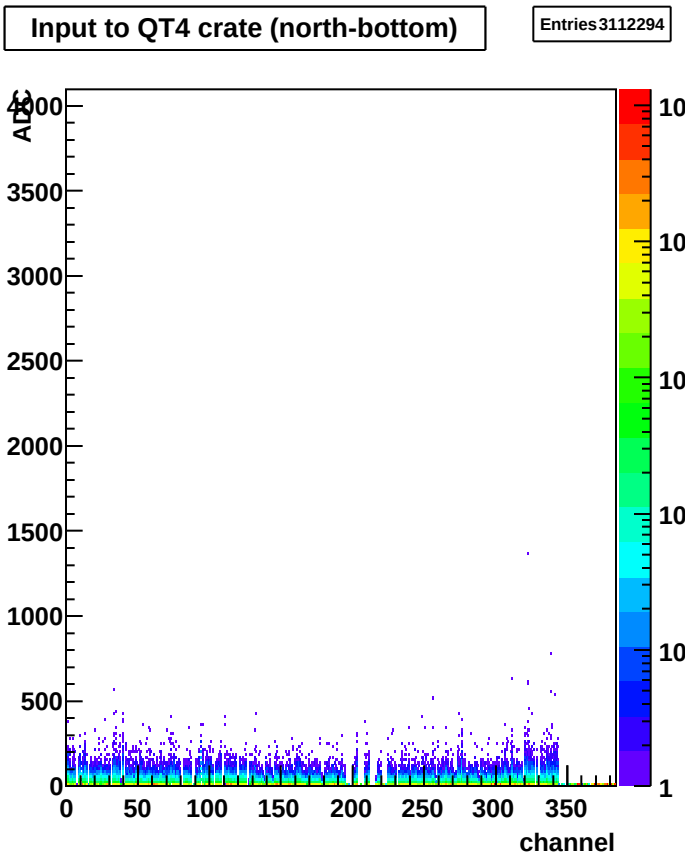

VPD-TAC-W
Entries 187803
Mean 1399
RMS 180.3

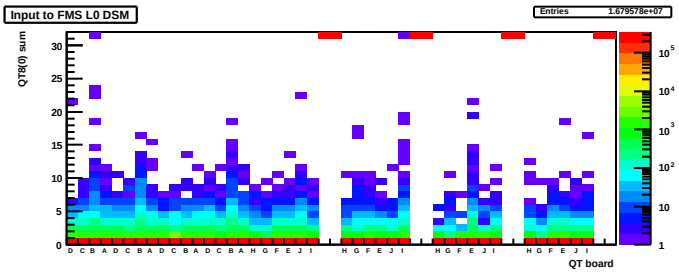

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 108606
Mean 4023
RMS 157

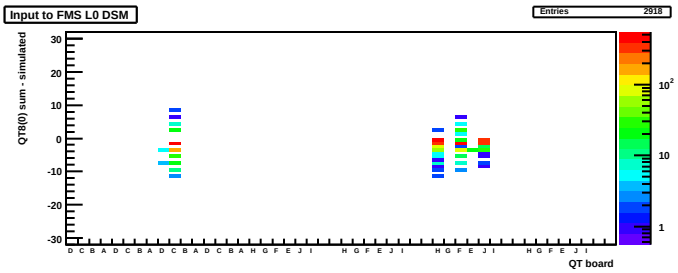

Input to QT1 crate (south-top)
Entries4409428
ADC
channel

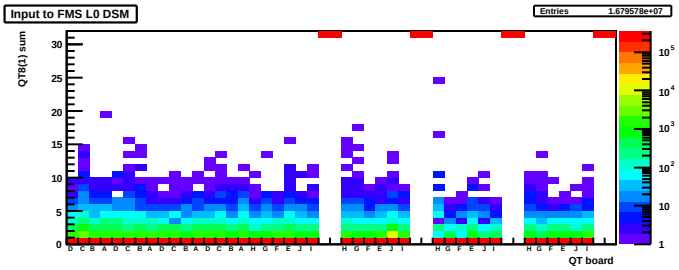

Input to QT3 crate (north-top)
Entries2327235
ADC
channel

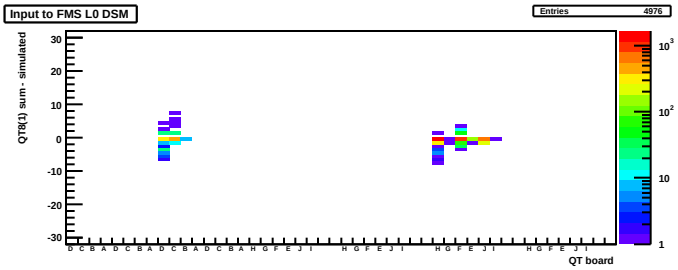

Input to QT2 crate (south-bottom)
Entries2707222
ADC
channel

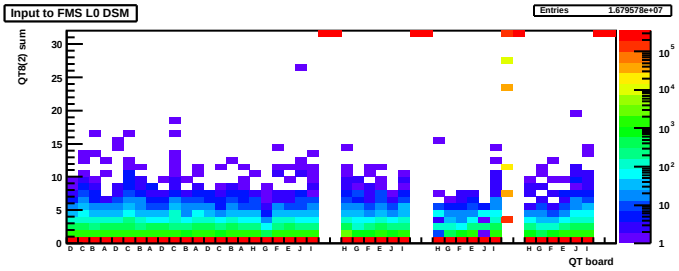

Input to QT4 crate (north-bottom)
Entries3112294
ADC
channel

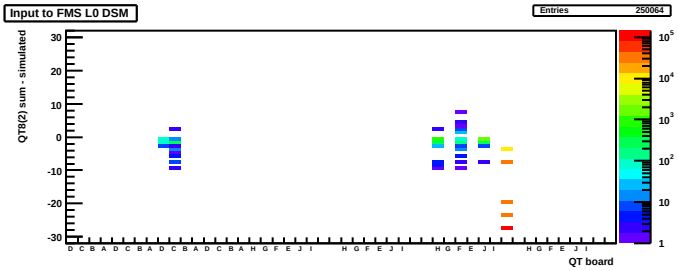

Input to FMS L0 DSM
Entries 1.679578e+07
QT8(0) sum
QT board

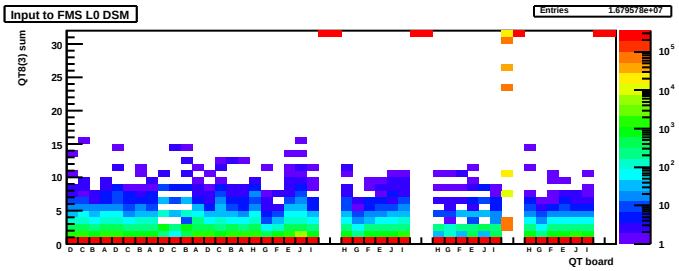

Input to FMS L0 DSM
Entries 2918
QT8(0) sum - simulated
QT board

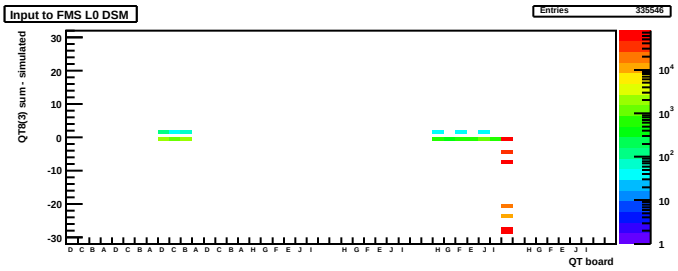

Input to FMS L0 DSM
Entries 1.679578e+07
QT8(1) sum
QT board

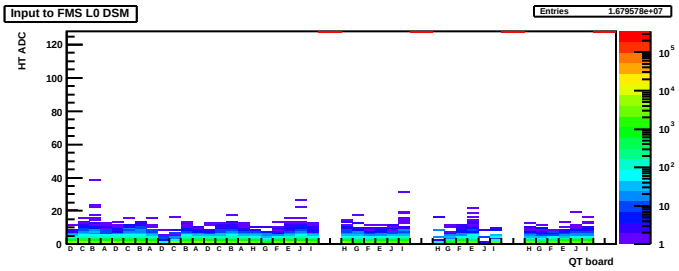

Input to FMS L0 DSM
Entries 4976
QT8(1) sum - simulated
QT board

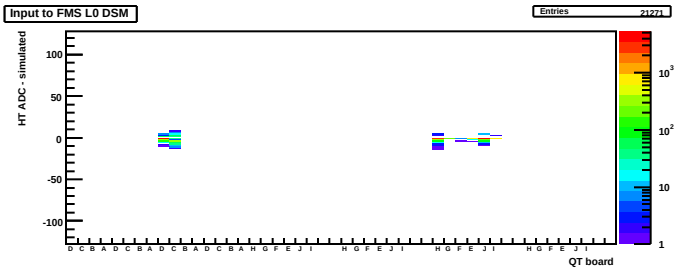

Input to FMS L0 DSM
Entries 1.679578e+07
QT8(2) sum
QT board

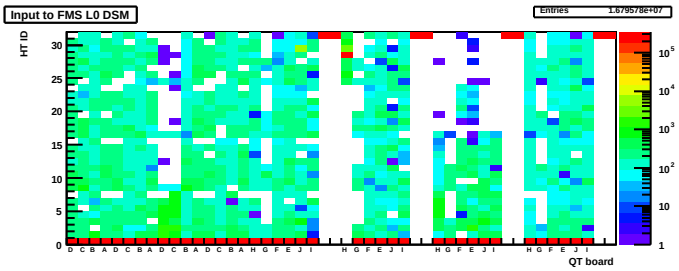

Input to FMS L0 DSM
Entries 250064
QT8(2) sum - simulated
QT board

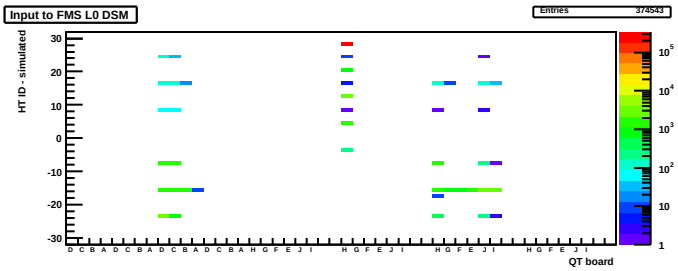

Input to FMS L0 DSM
Entries 1.679578e+07
QT8(3) sum
QT board

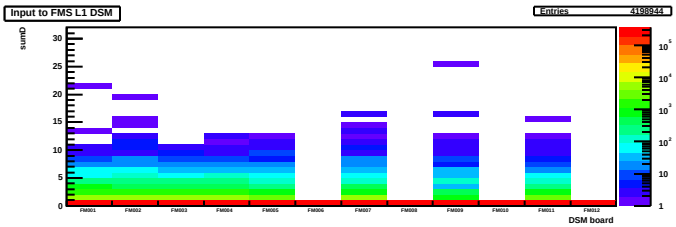

Input to FMS L0 DSM
Entries 335546
QT8(3) sum - simulated
QT board

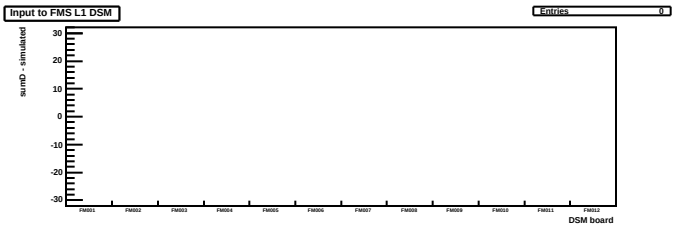

Input to FMS L0 DSM
Entries 1.679578e+07
HT ADC
QT board

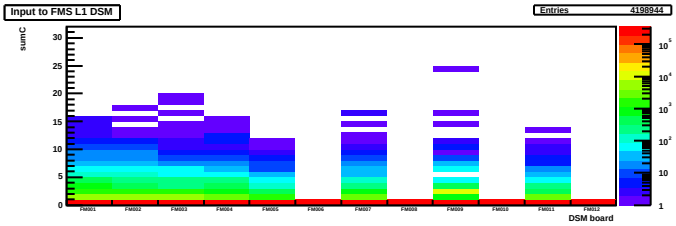

Input to FMS L0 DSM
Entries 21271
HT ADC - simulated
QT board

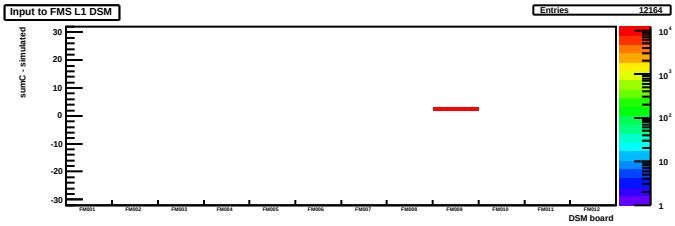

Input to FMS L0 DSM
Entries 1.679578e+07
HT ID
QT board

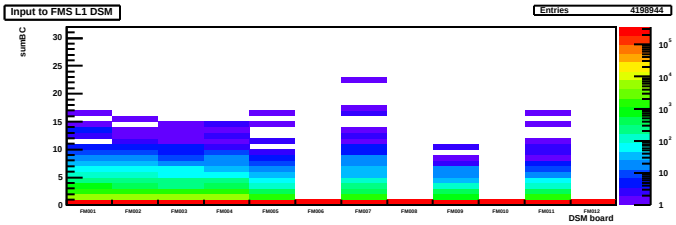

Input to FMS L0 DSM
Entries 374543
HT ID - simulated
QT board

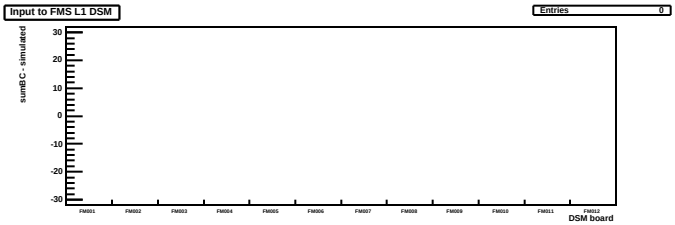

Input to FMS L1 DSM
Entries 4198944
sumD
DSM board

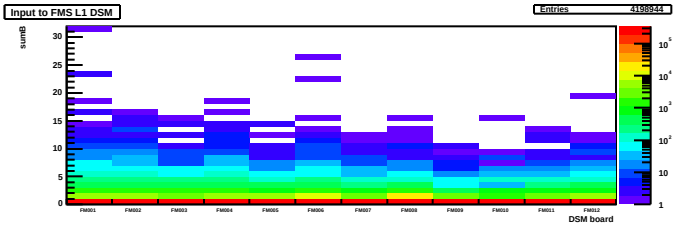

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

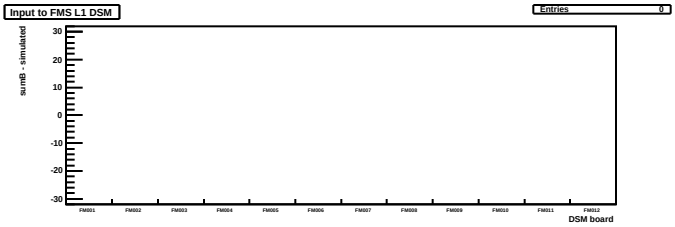

Input to FMS L1 DSM
Entries 4198944
sumC
DSM board

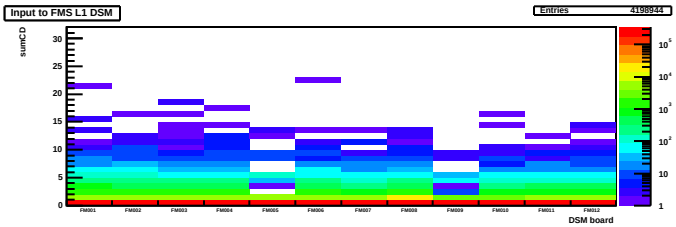

Input to FMS L1 DSM
Entries 12164
sumC - simulated
DSM board

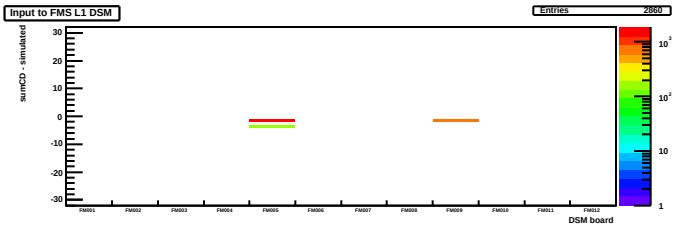

Input to FMS L1 DSM
Entries 4198944
sumBC
DSM board

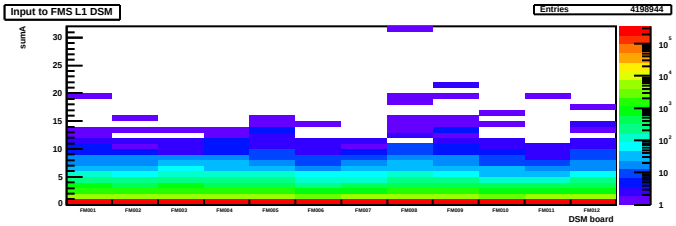

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

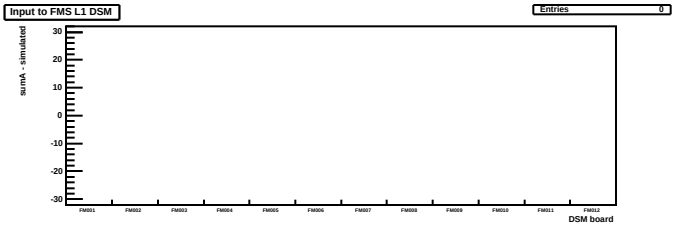

Input to FMS L1 DSM
Entries 4198944
sumB
DSM board

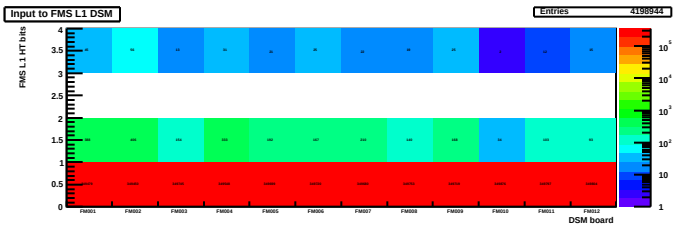

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

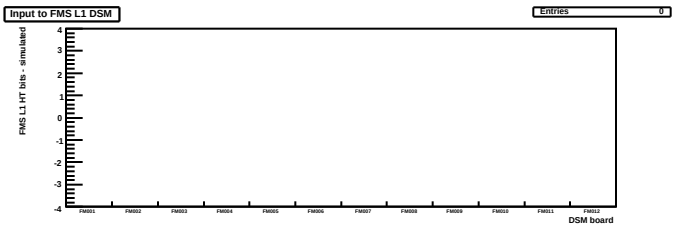

Input to FMS L1 DSM
Entries 4198944
sumCD
DSM board

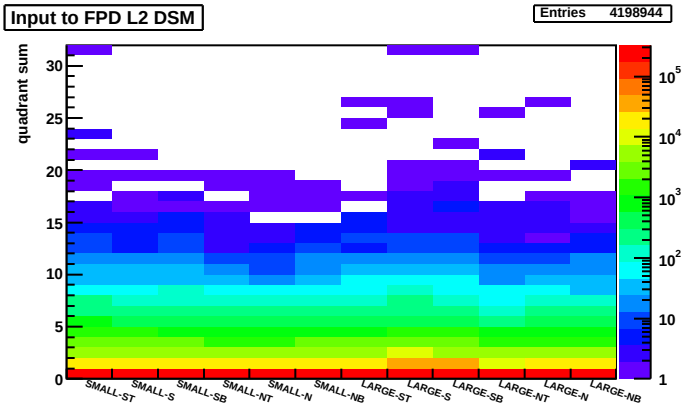

Input to FMS L1 DSM
Entries 2860
sumCD - simulated
DSM board

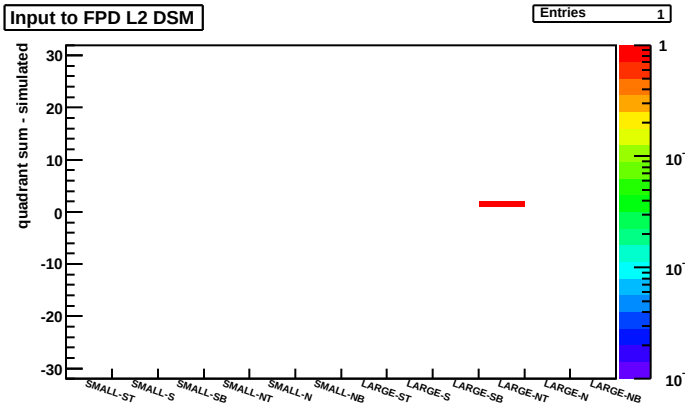

Input to FMS L1 DSM
Entries 4198944
sumA
DSM board

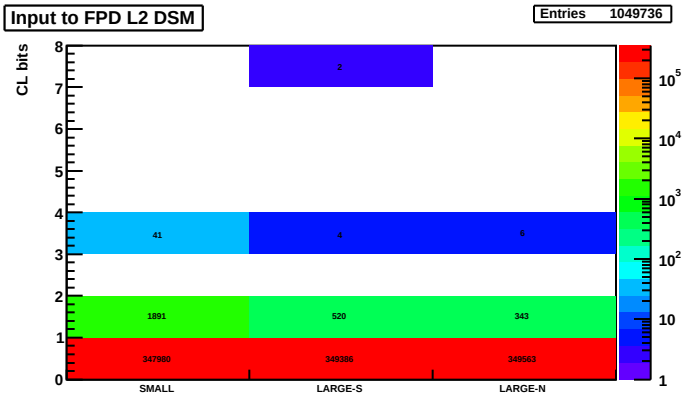

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

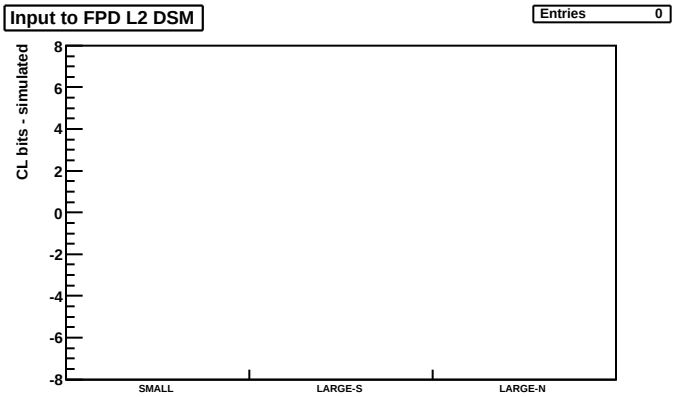

Input to FMS L1 DSM
Entries 4198944
FMS L1 HT bits
DSM board

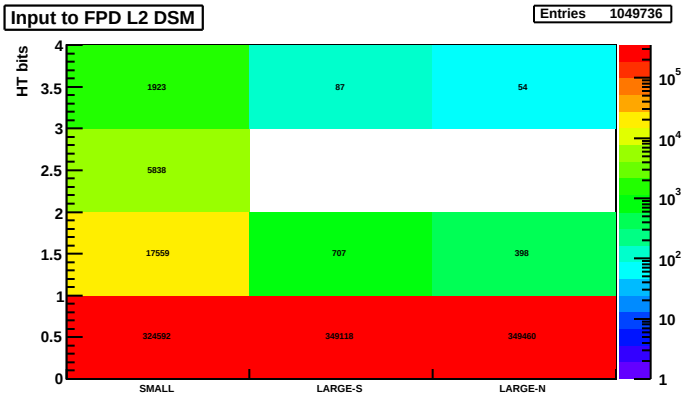

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

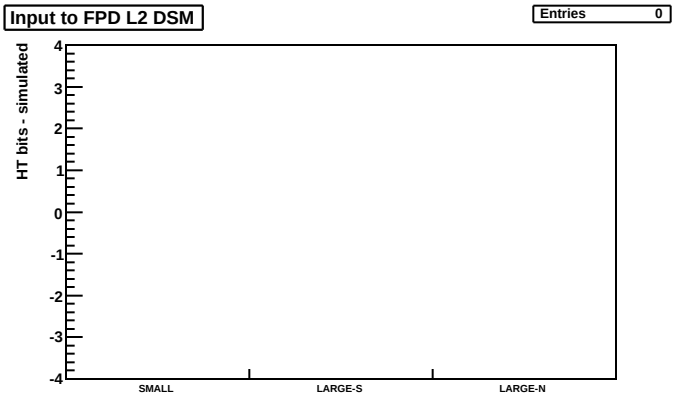

Input to FPD L2 DSM
Entries 4198944
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

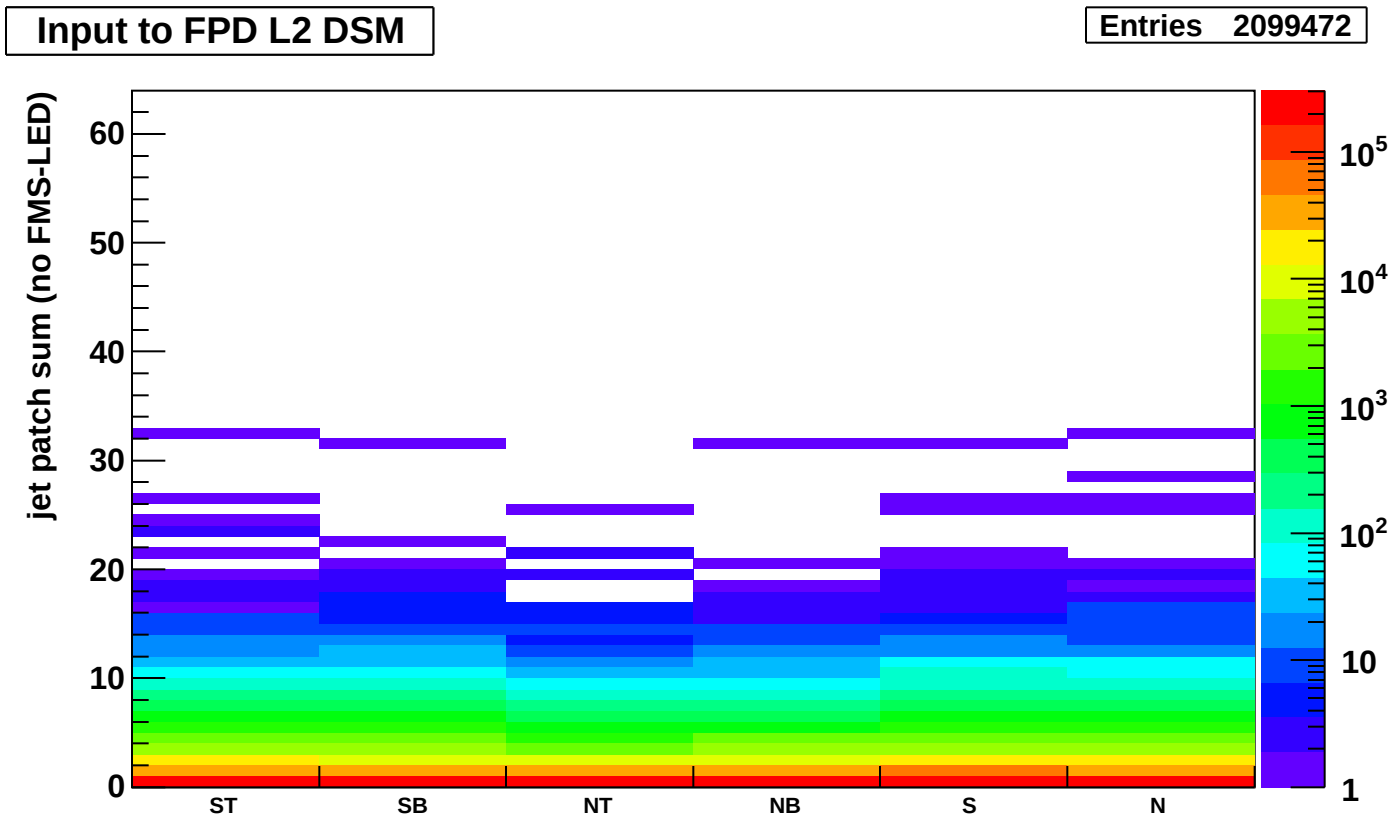

Input to FPD L2 DSM
Entries 1
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

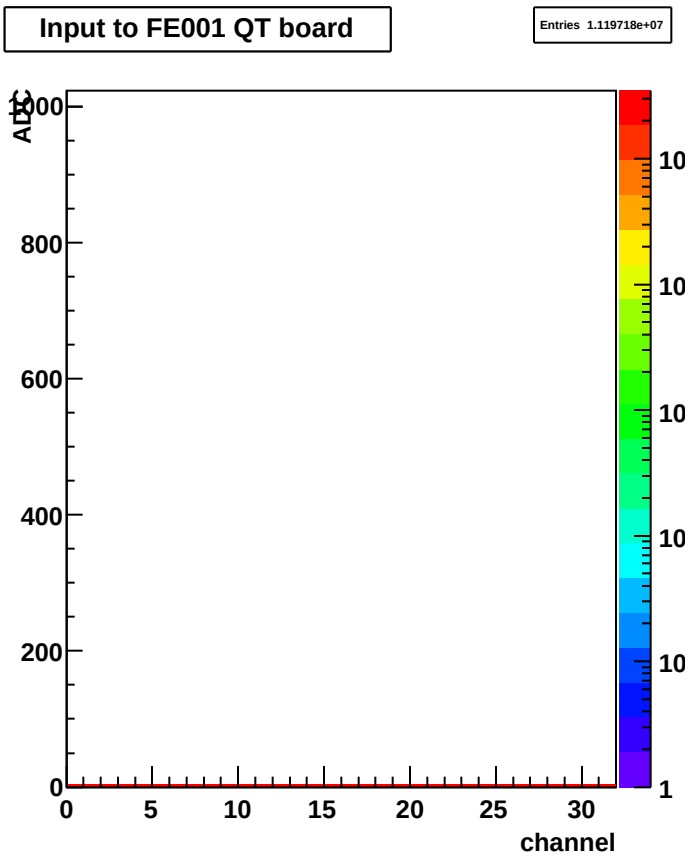

Input to FPD L2 DSM
Entries 1049736
CL bits
2
41
4
6
1891
520
343
347980
349386
349563
SMALL
LARGE-S
LARGE-N

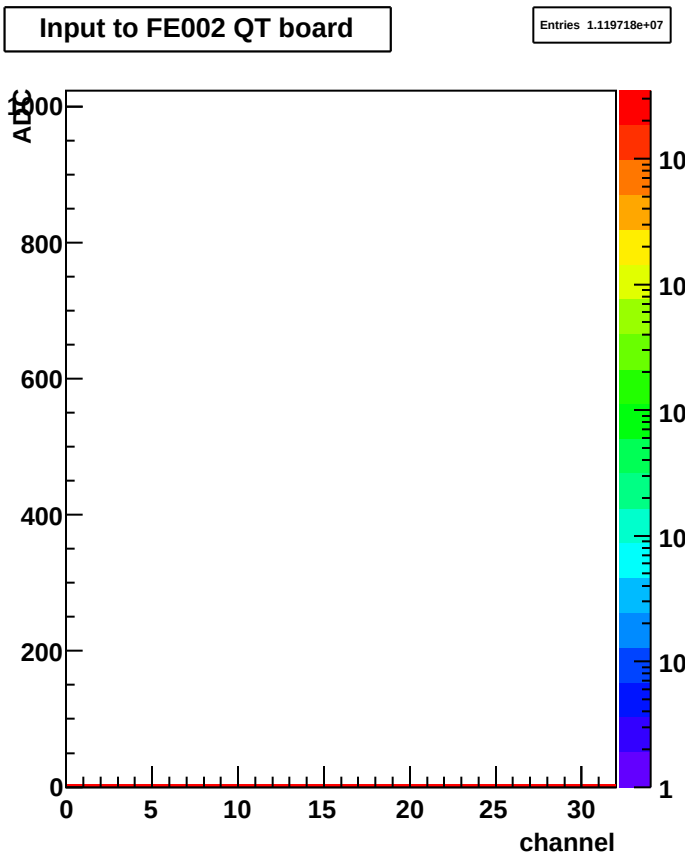

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

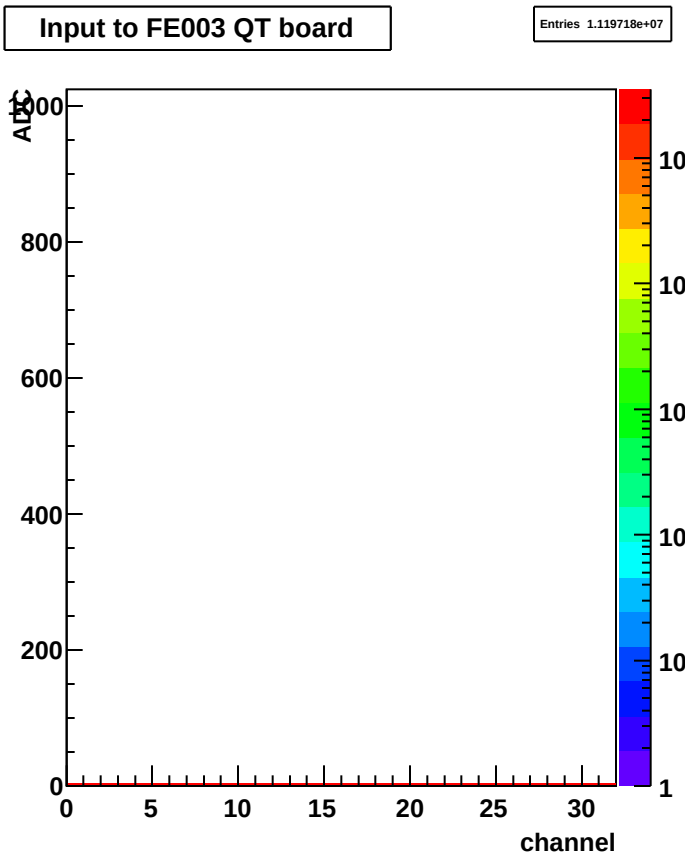

Input to FPD L2 DSM
Entries 1049736
HT bits
1923
87
54
5838
17559
707
398
324592
349118
349460
SMALL
LARGE-S
LARGE-N

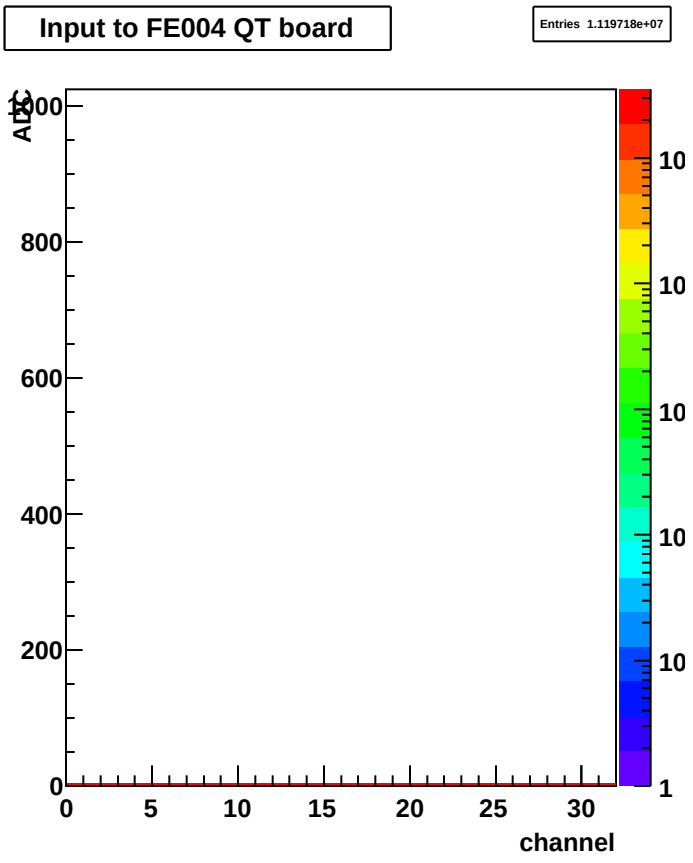

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

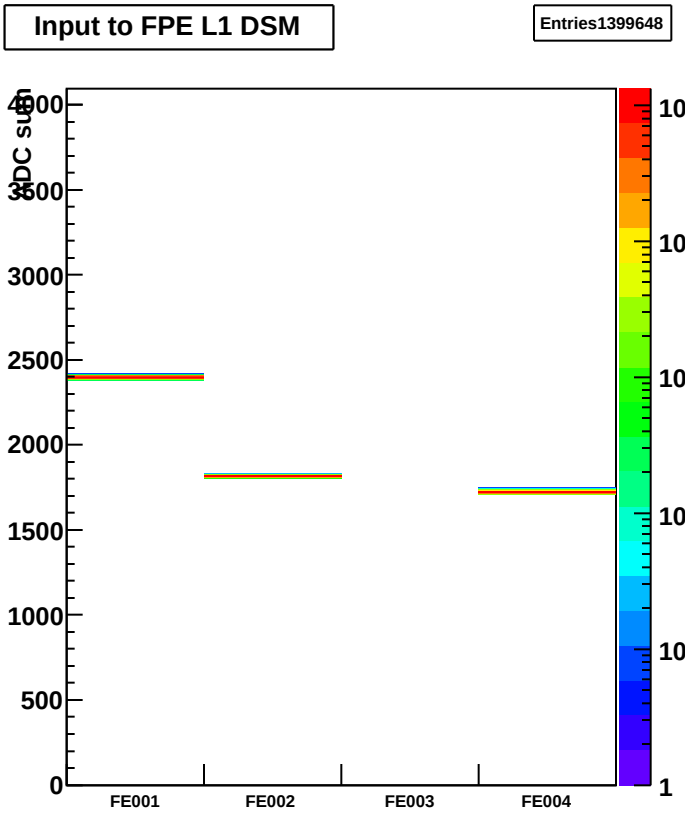

Input to FPD L2 DSM
Entries 2099472
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

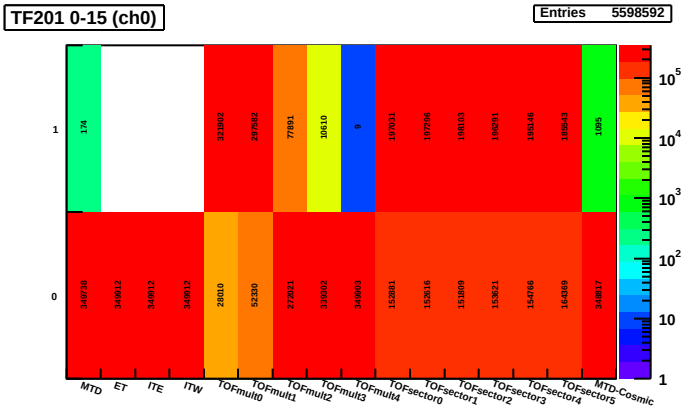

Input to FE001 QT board
Entries 1.119718e+07
ADC
channel

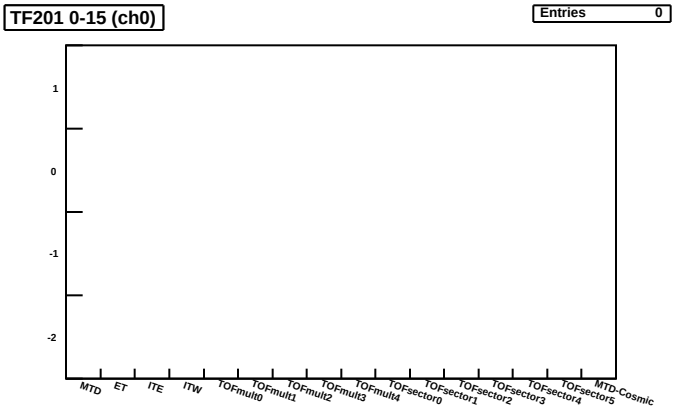

Input to FE002 QT board
Entries 1.119718e+07
ADC
channel

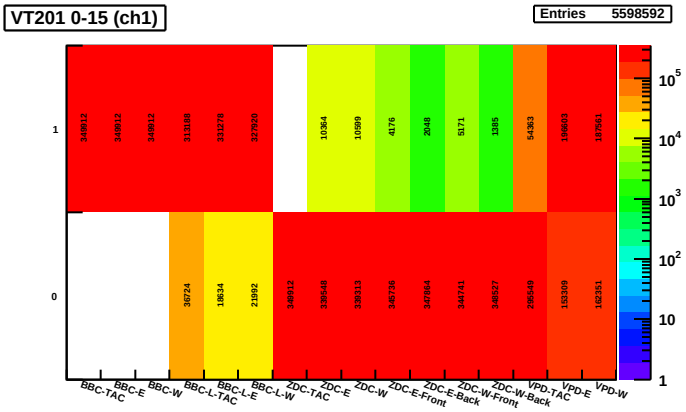

Input to FE003 QT board
Entries 1.119718e+07
ADC
channel

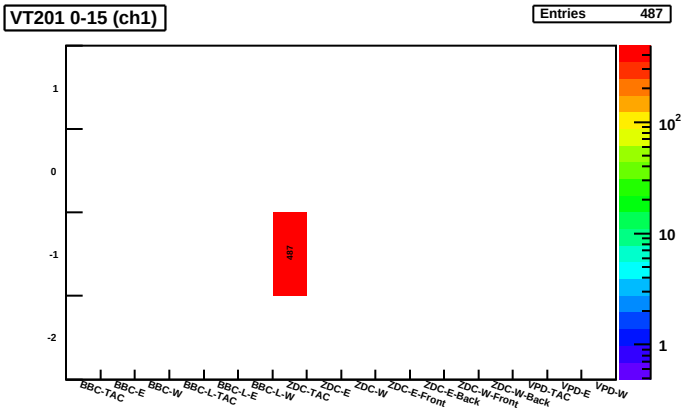

Input to FE004 QT board
Entries 1.119718e+07
ADC
channel

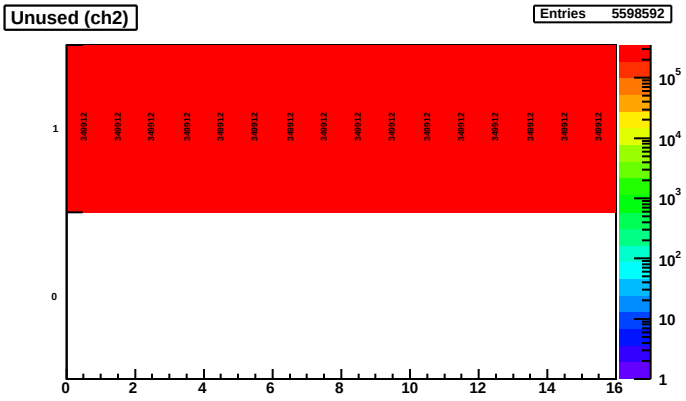

Input to FPE L1 DSM
Entries1399648
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

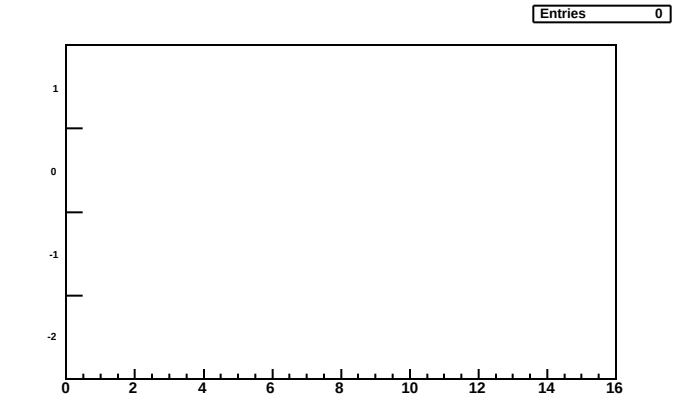

TF201 0-15 (ch0)
Entries 5598592
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 5598592
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 487
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 5598592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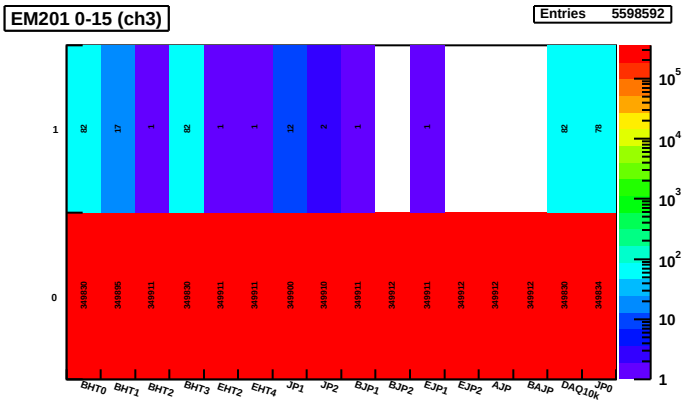
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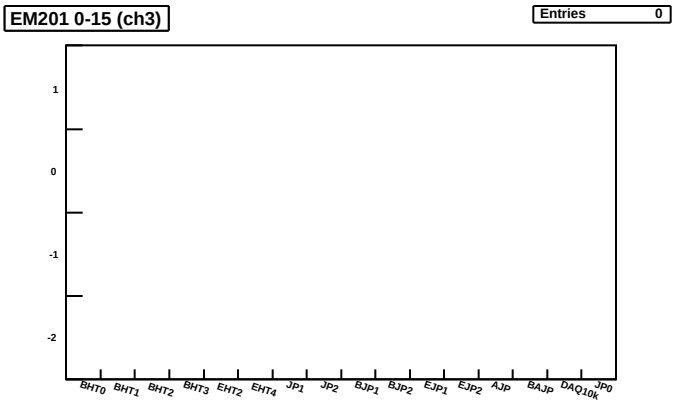
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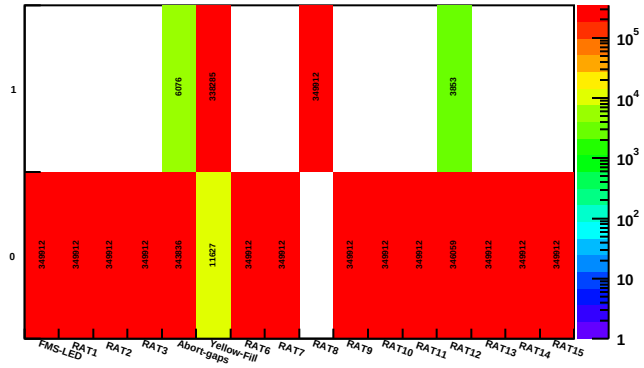
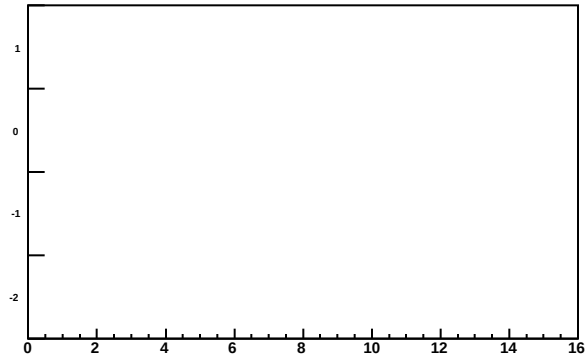
RAT board (ch4)





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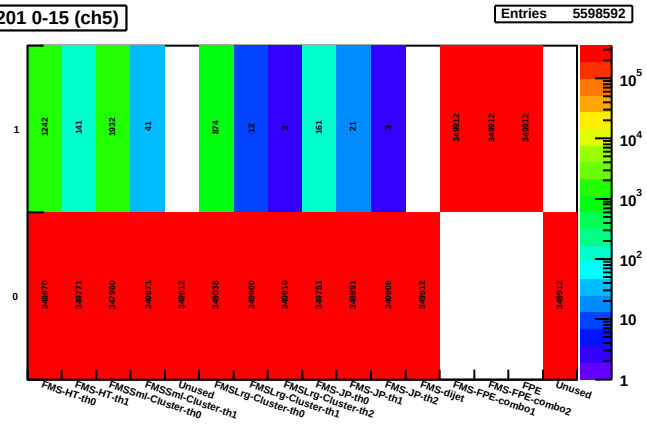

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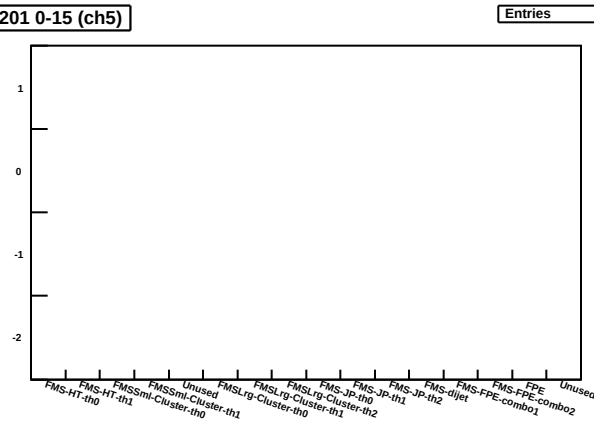

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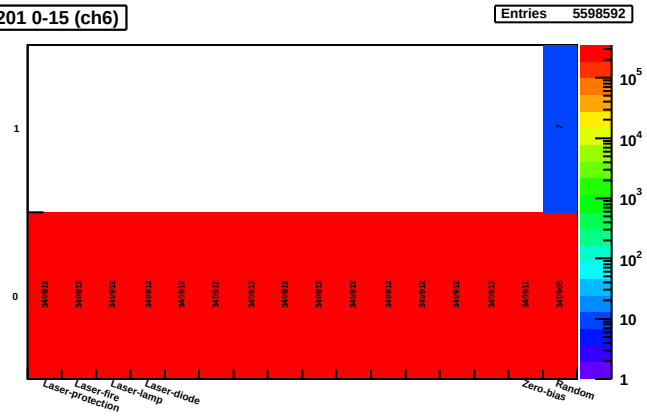

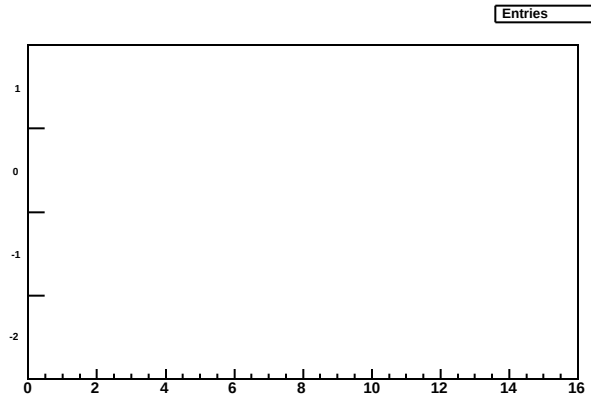
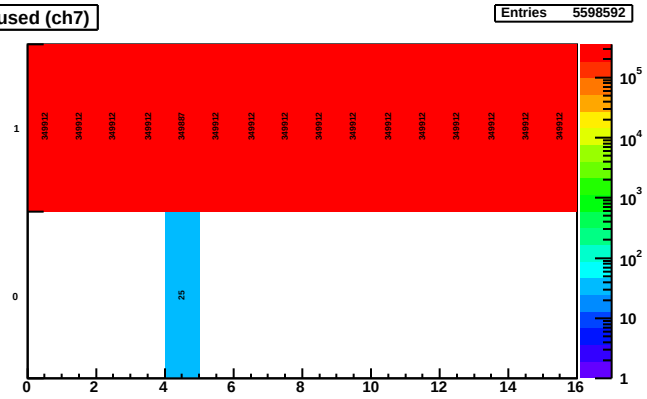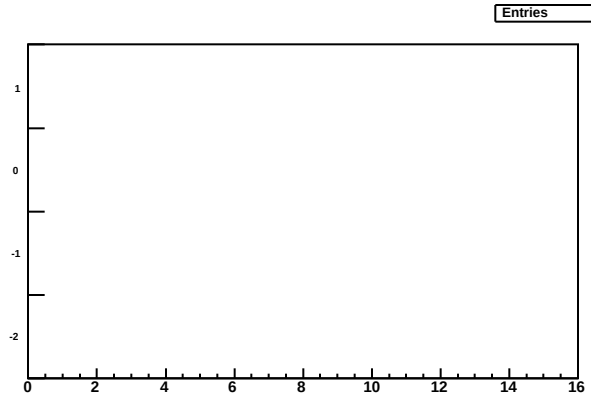
Unused (ch7)