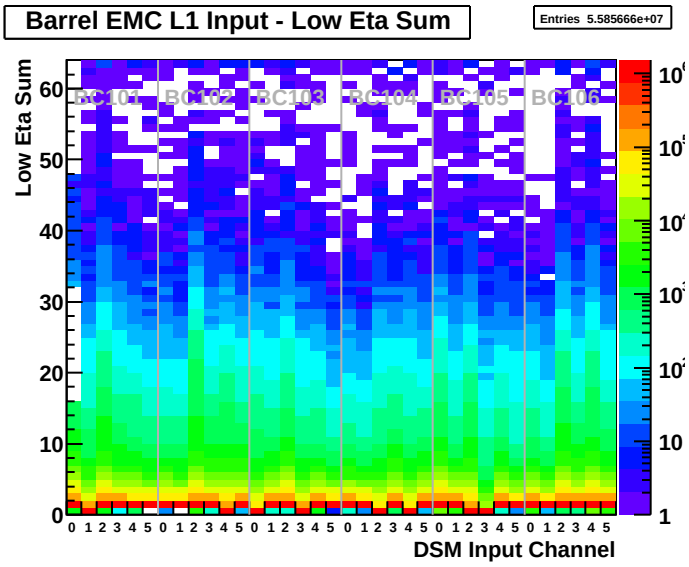
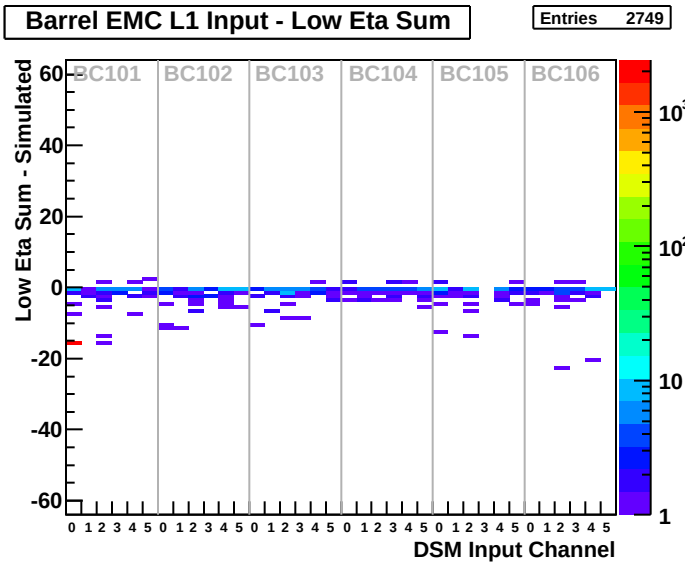
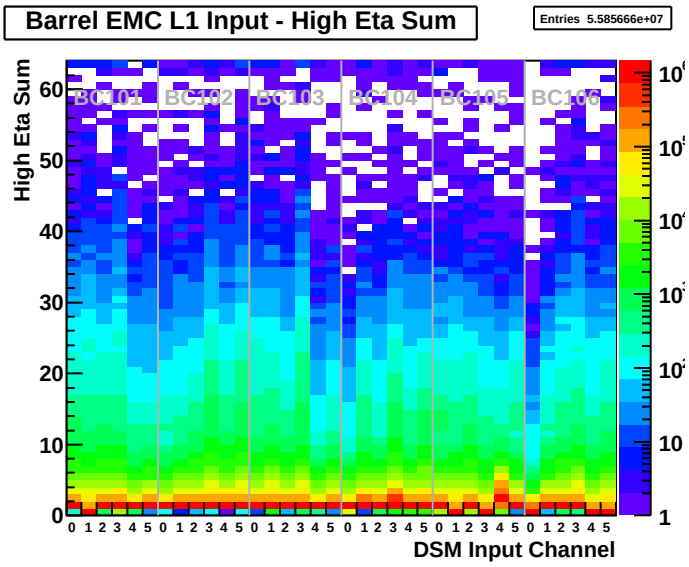
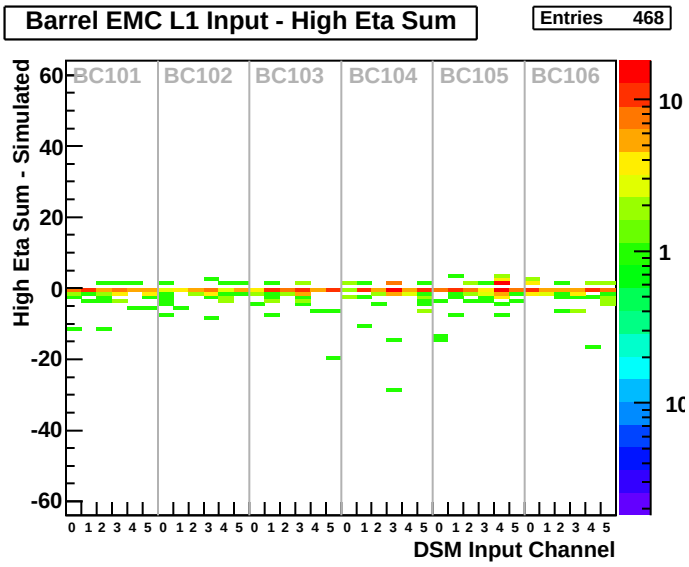
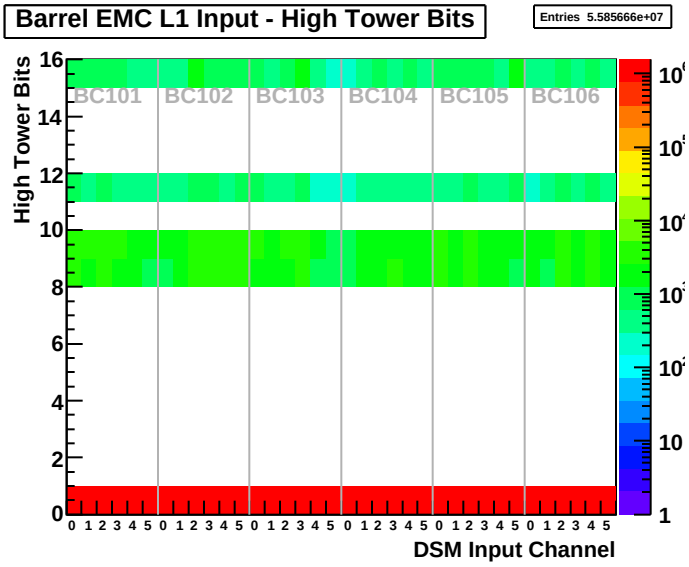
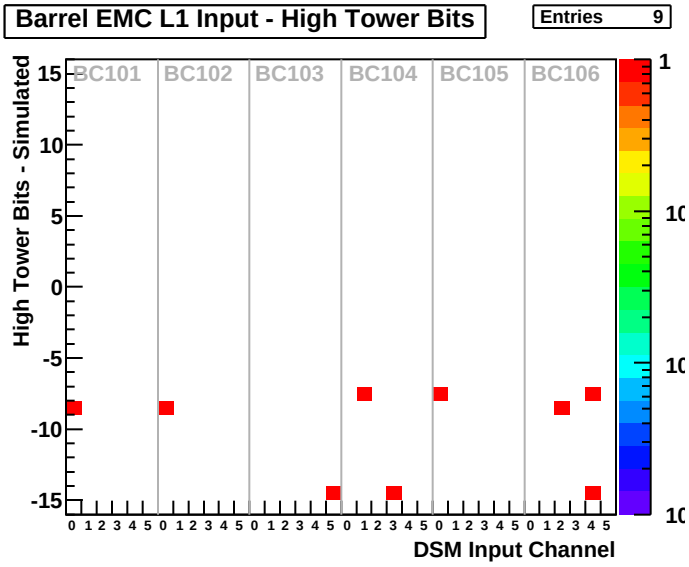

Barrel EMC L1 Input - Low Eta Sum
Entries 5.585666e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

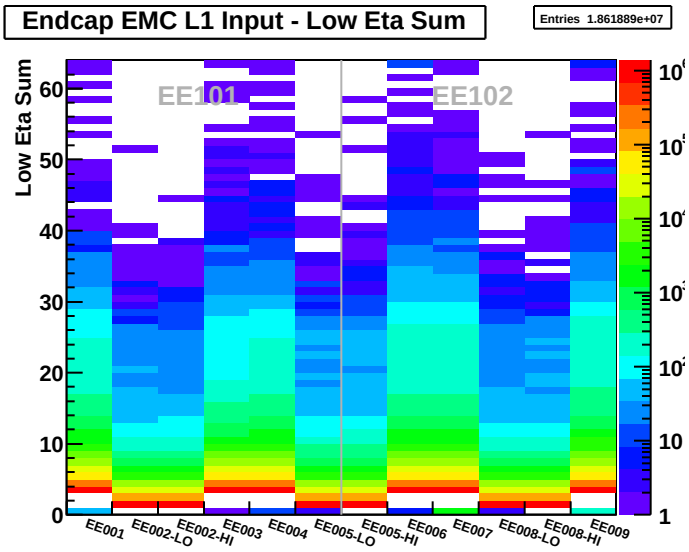

Barrel EMC L1 Input - Low Eta Sum
Entries 2749
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

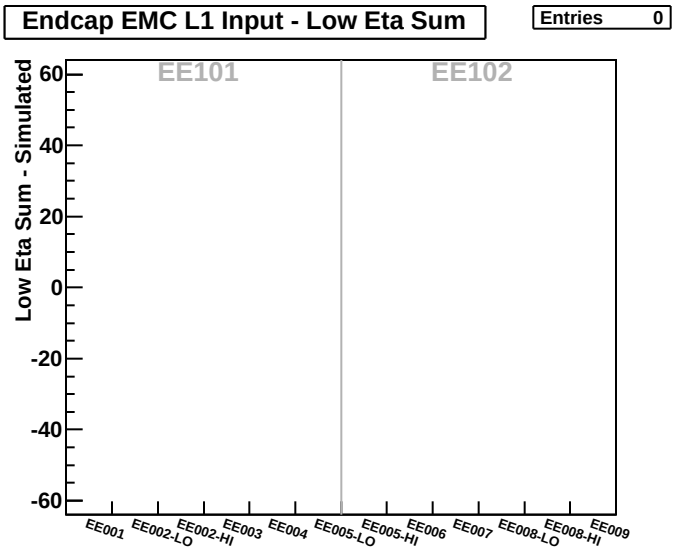

Barrel EMC L1 Input - High Eta Sum
Entries 5.585666e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

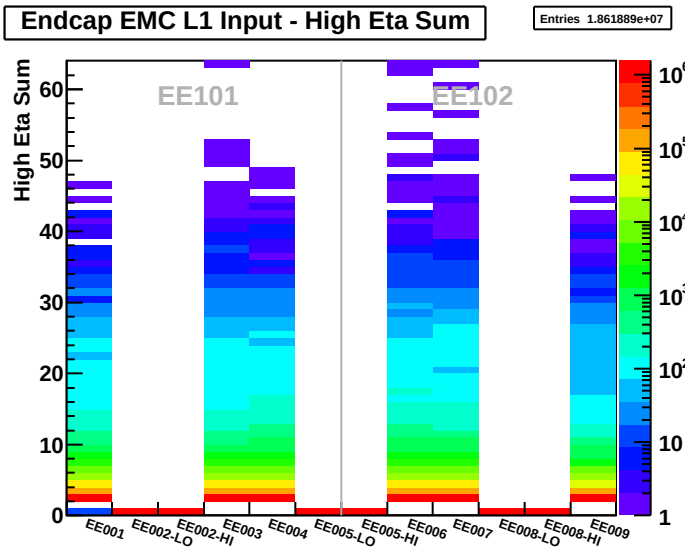

Barrel EMC L1 Input - High Eta Sum
Entries 468
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

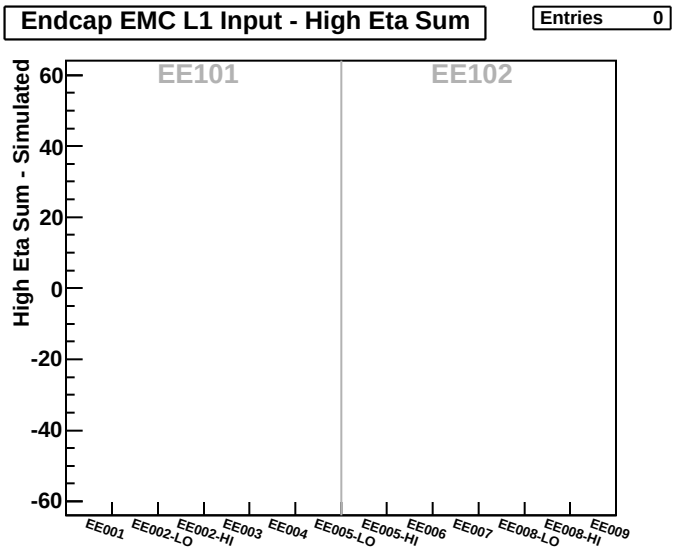

Barrel EMC L1 Input - High Tower Bits
Entries 5.585666e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

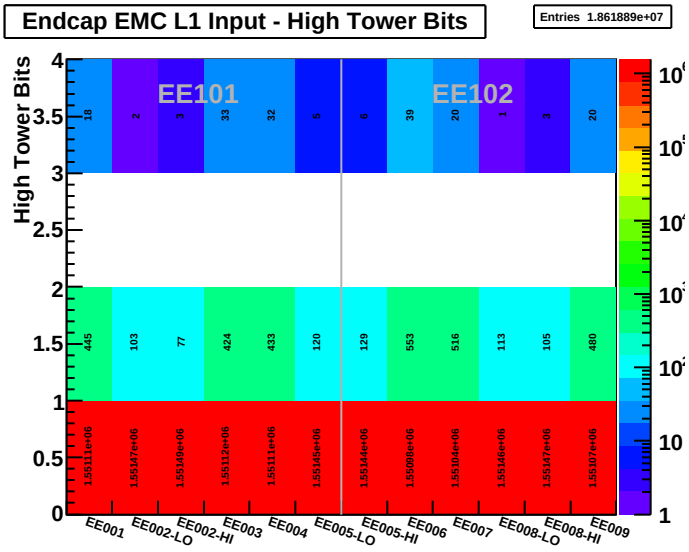

Barrel EMC L1 Input - High Tower Bits
Entries 9
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

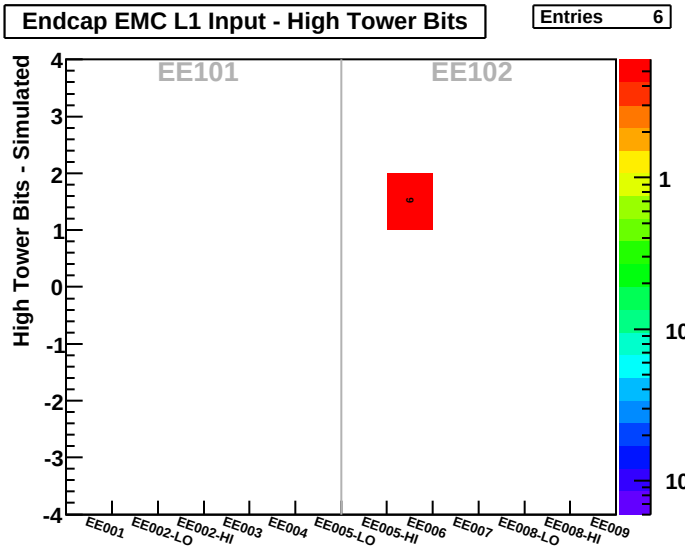

Endcap EMC L1 Input - Low Eta Sum
Entries 1.861889e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

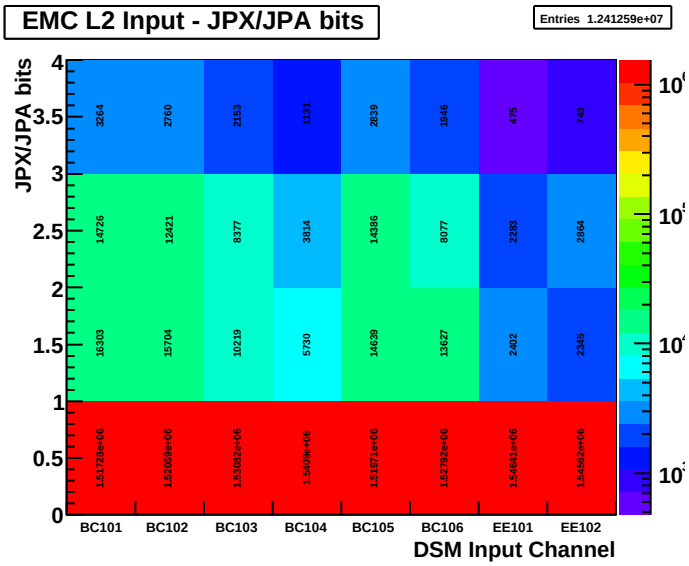

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

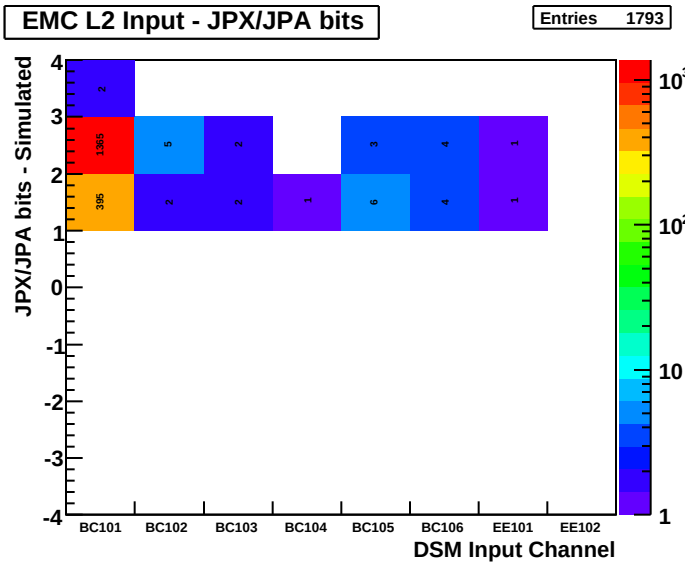

Endcap EMC L1 Input - High Eta Sum
Entries 1.861889e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

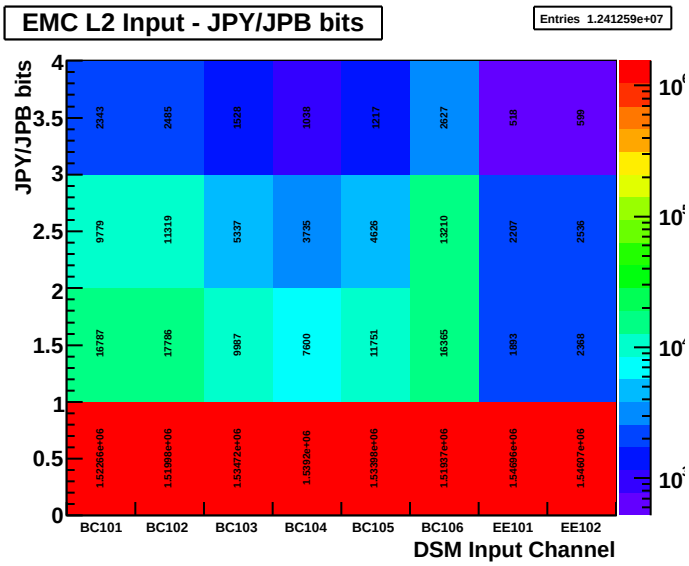

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

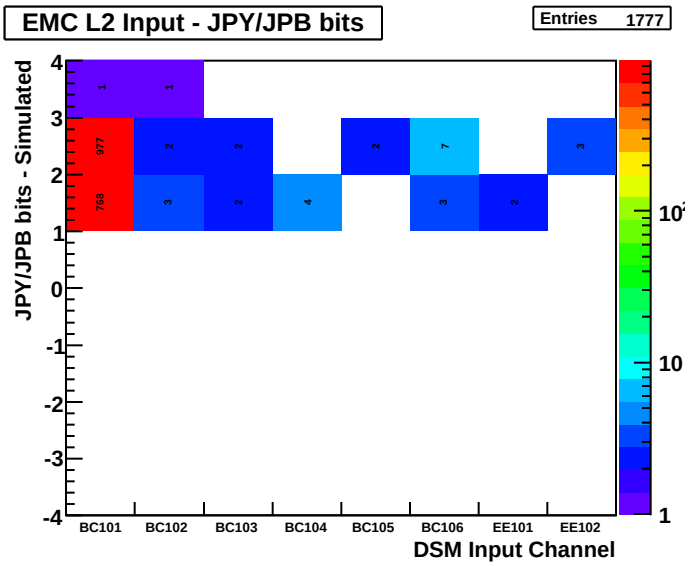

Endcap EMC L1 Input - High Tower Bits
Entries 1.861889e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

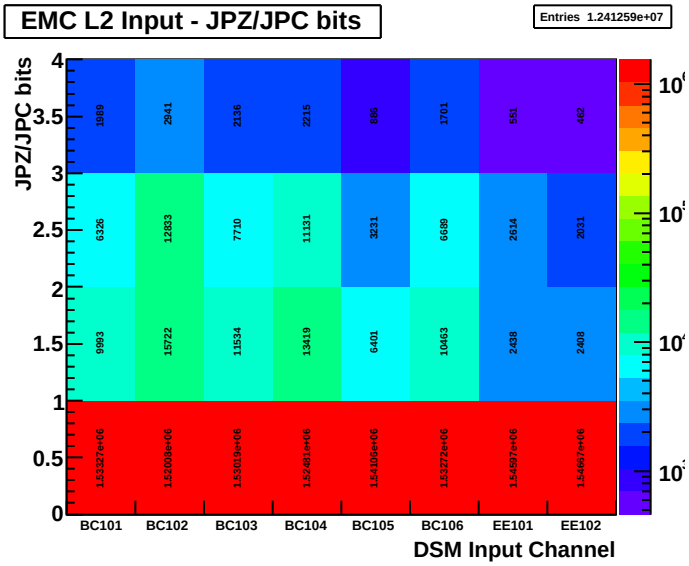

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

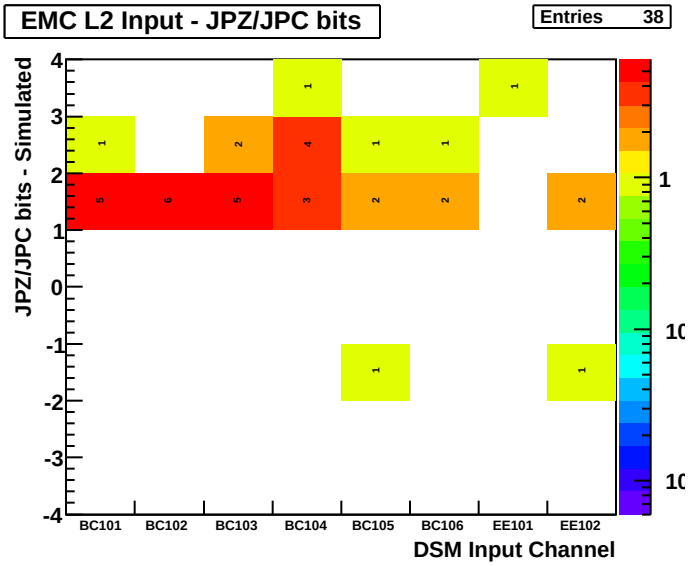

EMC L2 Input - JPX/JPA bits
Entries 1.241259e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

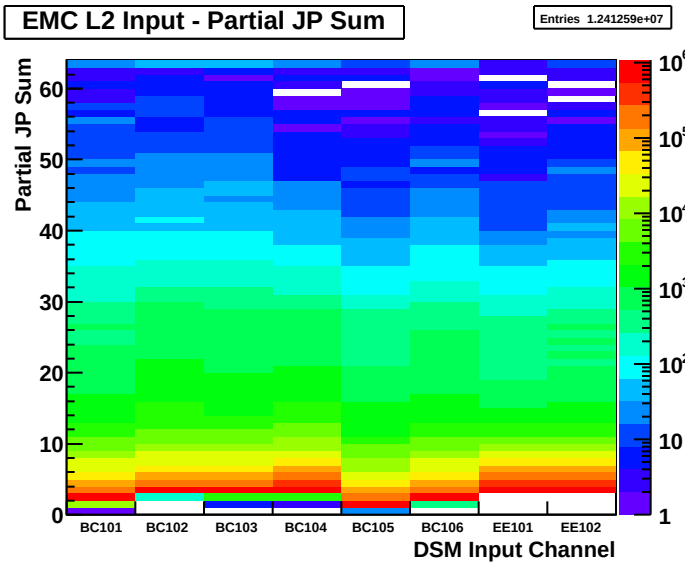

EMC L2 Input - JPX/JPA bits
Entries 1793
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

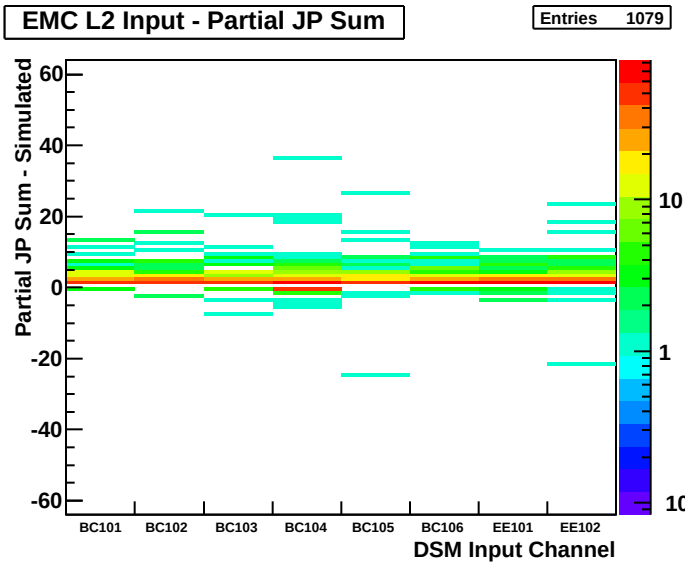

EMC L2 Input - JPY/JPB bits
Entries 1.241259e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

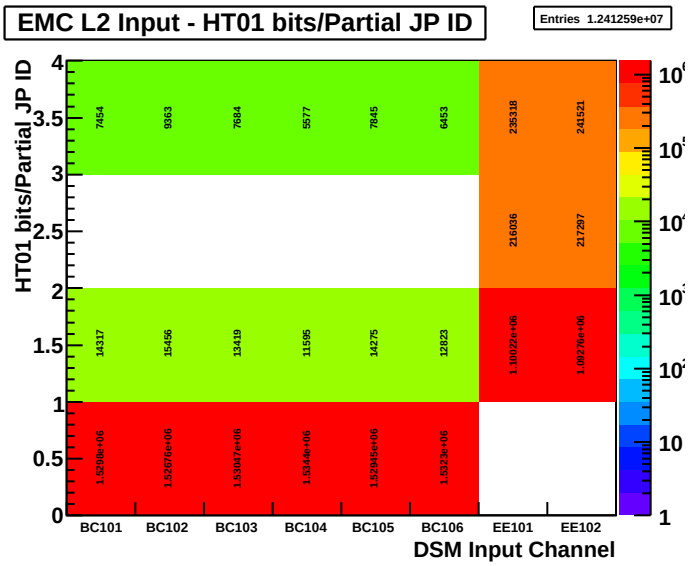

EMC L2 Input - JPY/JPB bits
Entries 1777
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

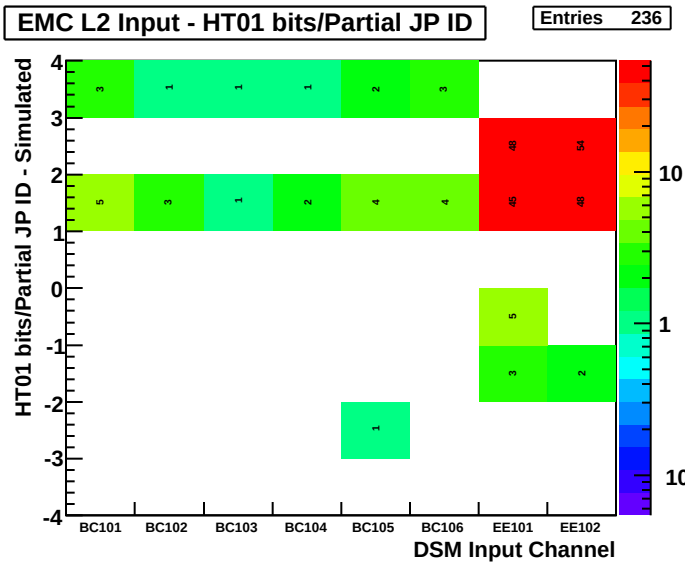

EMC L2 Input - JPZ/JPC bits
Entries 1.241259e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

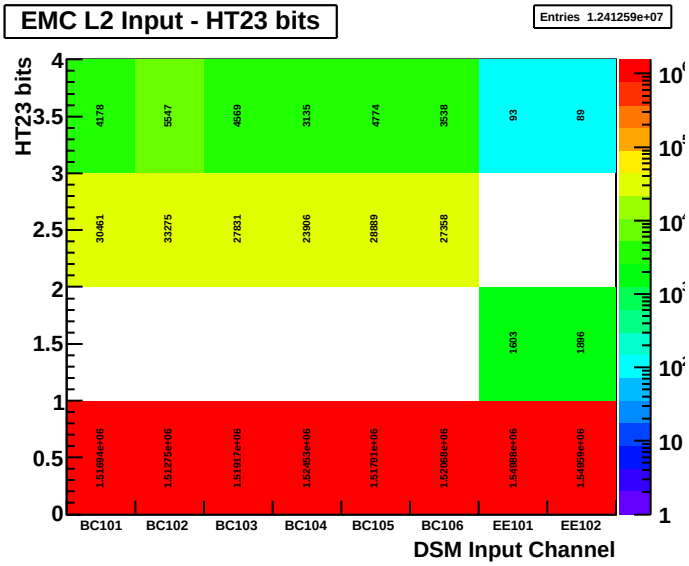

EMC L2 Input - JPZ/JPC bits
Entries 38
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

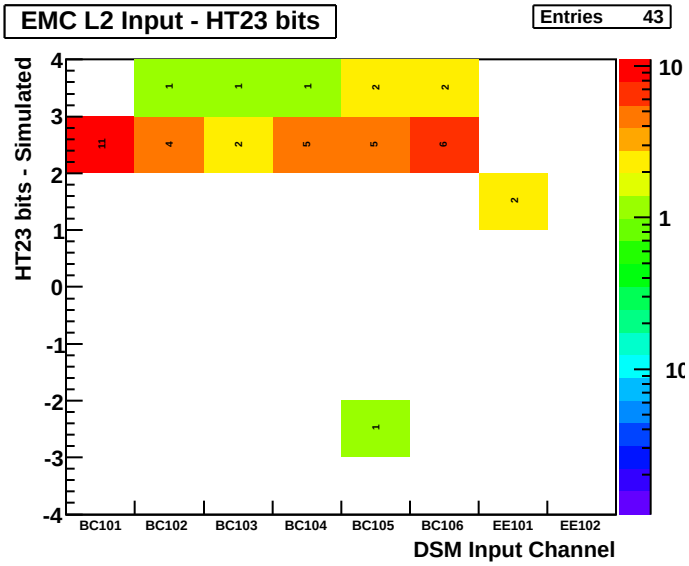

EMC L2 Input - Partial JP Sum
Entries 1.241259e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

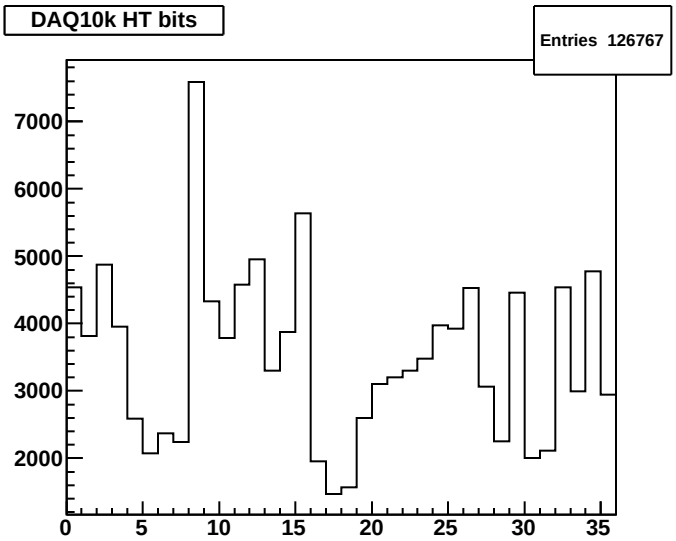

EMC L2 Input - Partial JP Sum
Entries 1079
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

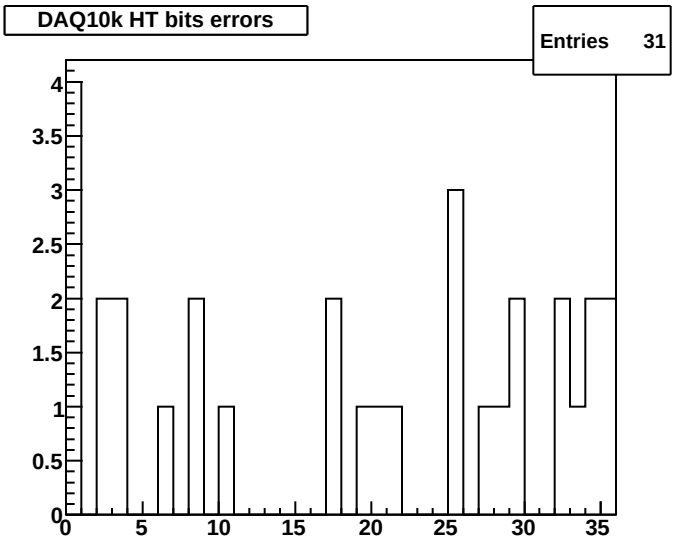

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.241259e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

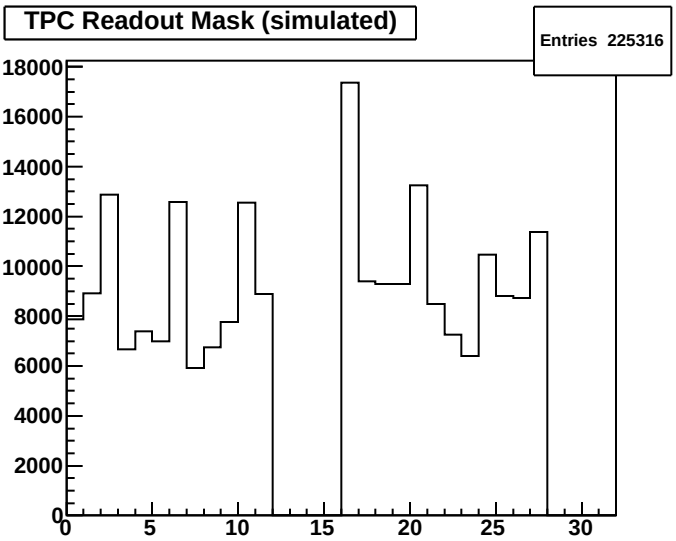

EMC L2 Input - HT01 bits/Partial JP ID
Entries 236
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

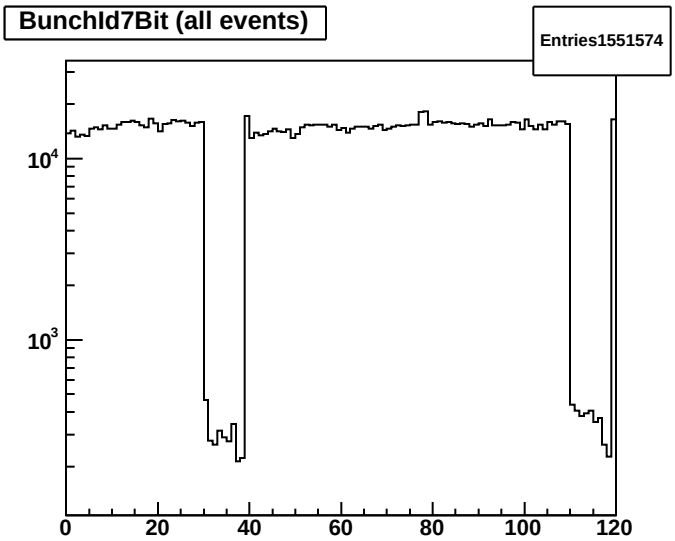

EMC L2 Input - HT23 bits
Entries 1.241259e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

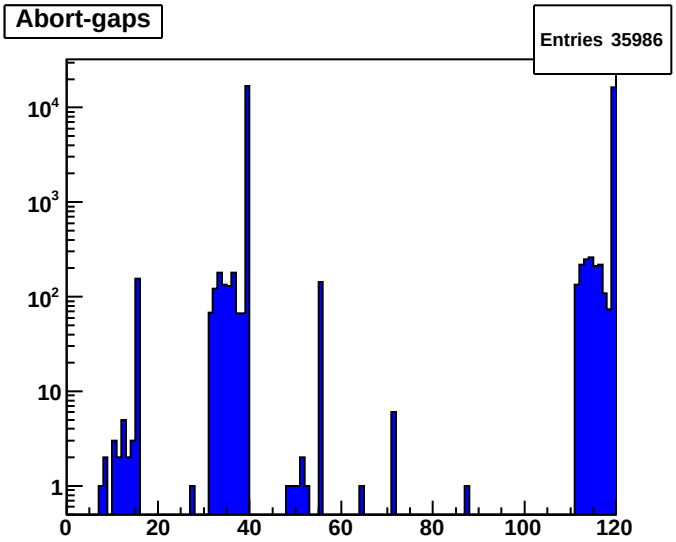

EMC L2 Input - HT23 bits
Entries 43
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

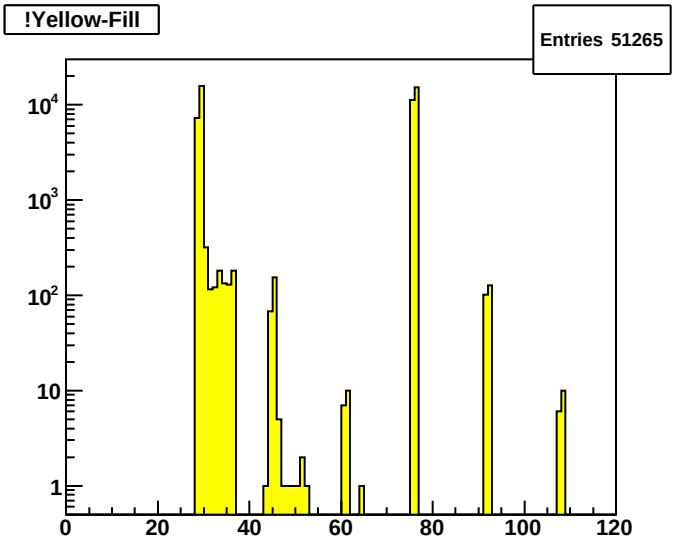

DAQ10k HT bits
Entries 126767

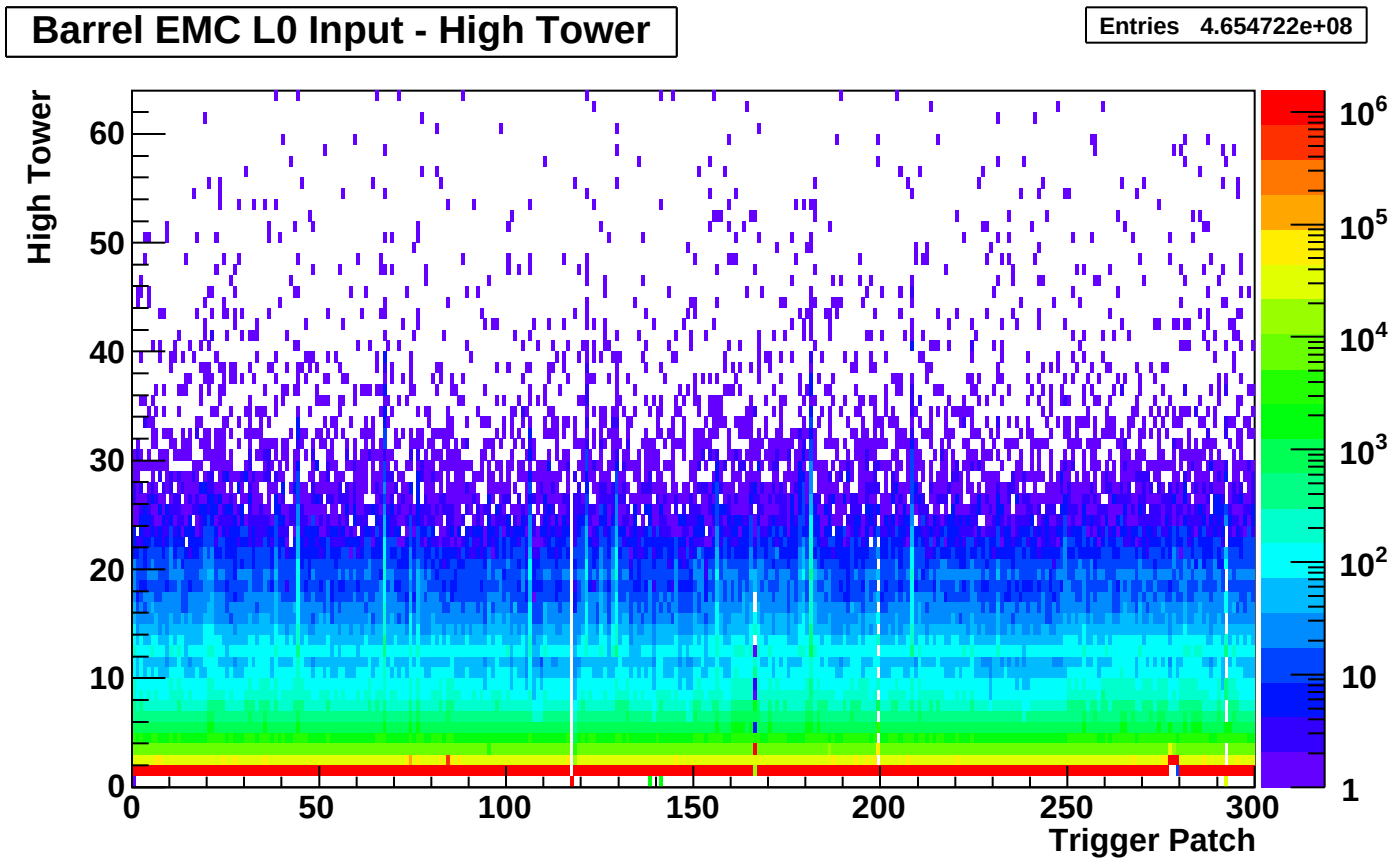

DAQ10k HT bits errors
Entries 31

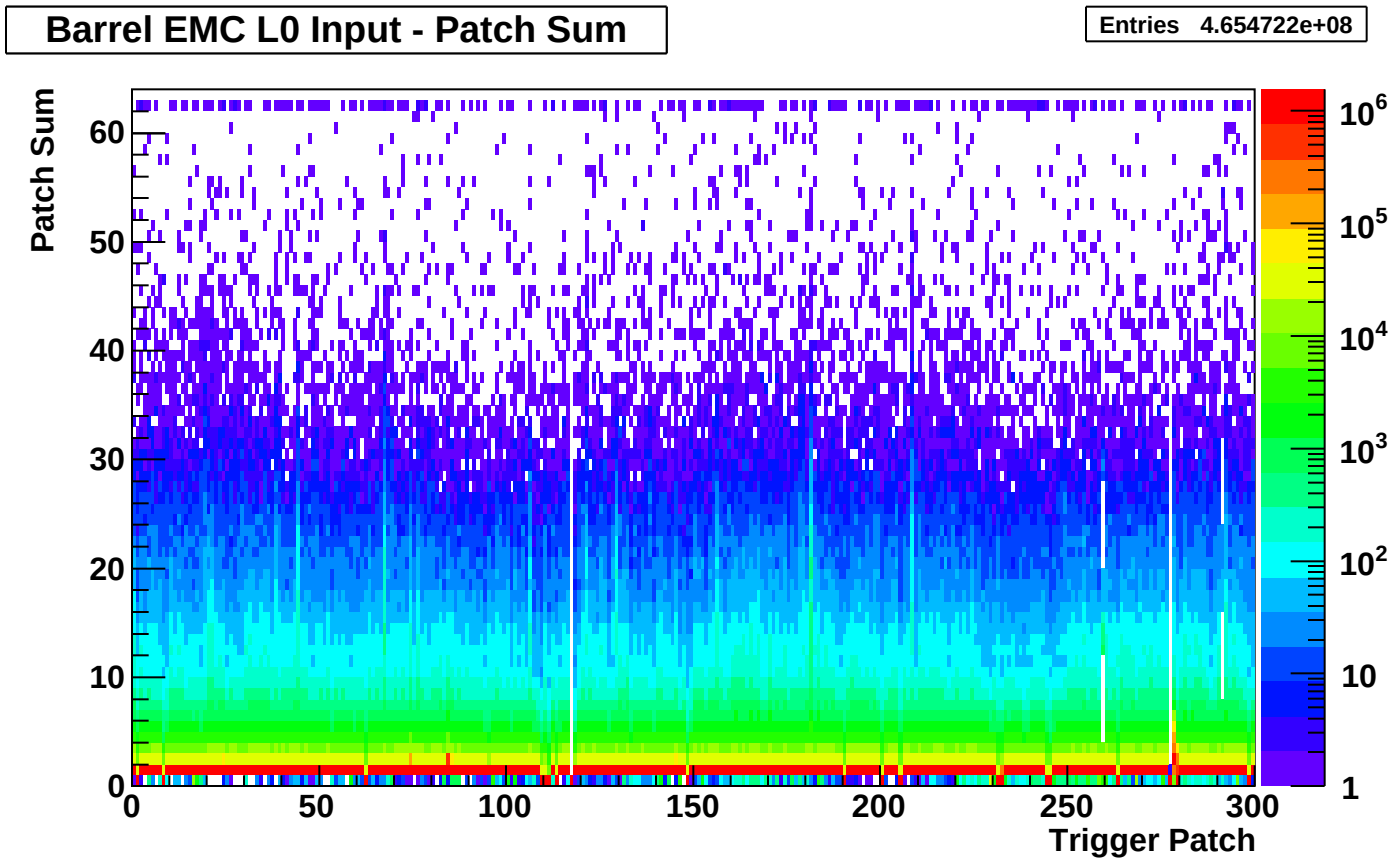

TPC Readout Mask (simulated)
Entries 225316

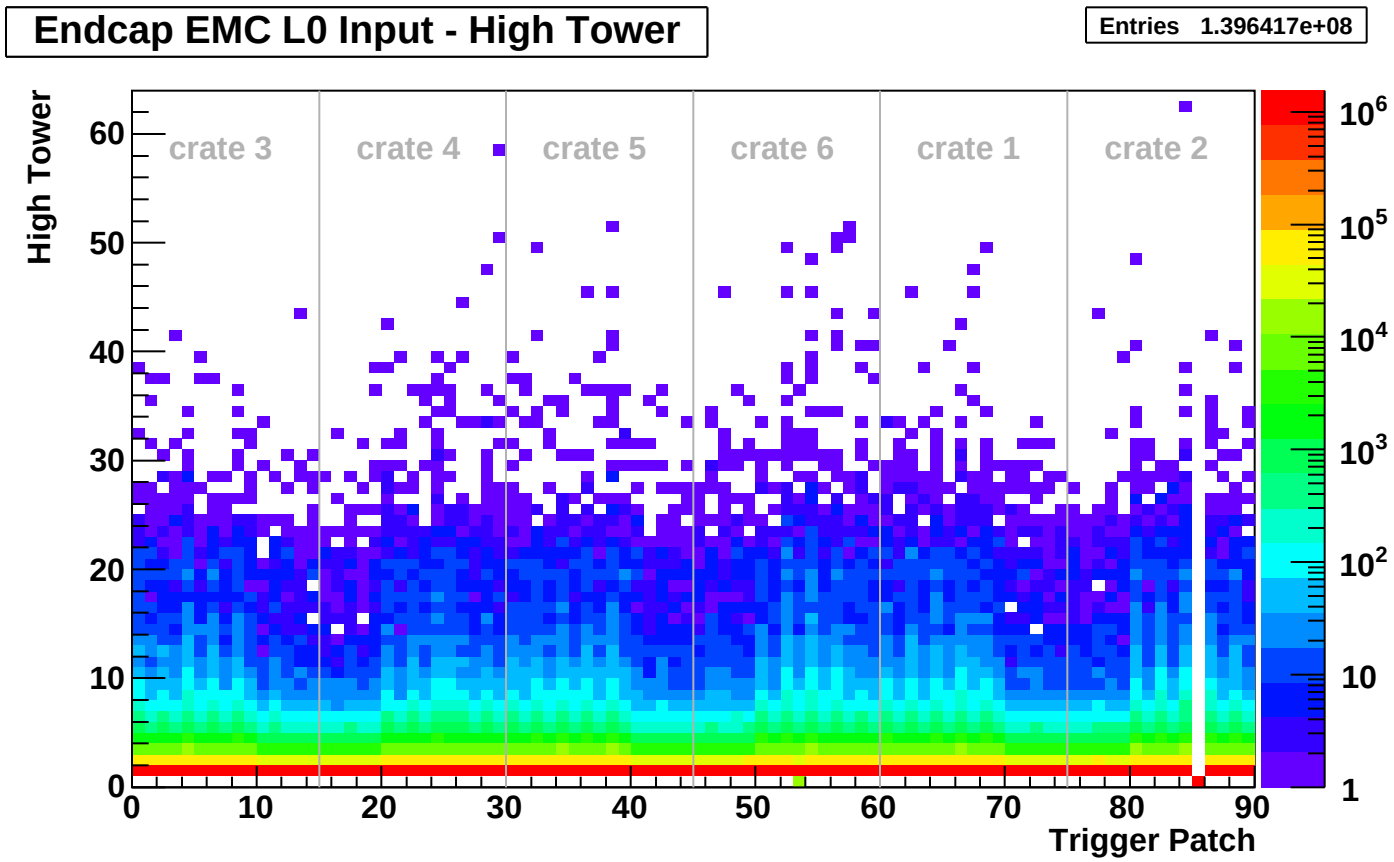

BunchId7Bit (all events)
Entries1551574

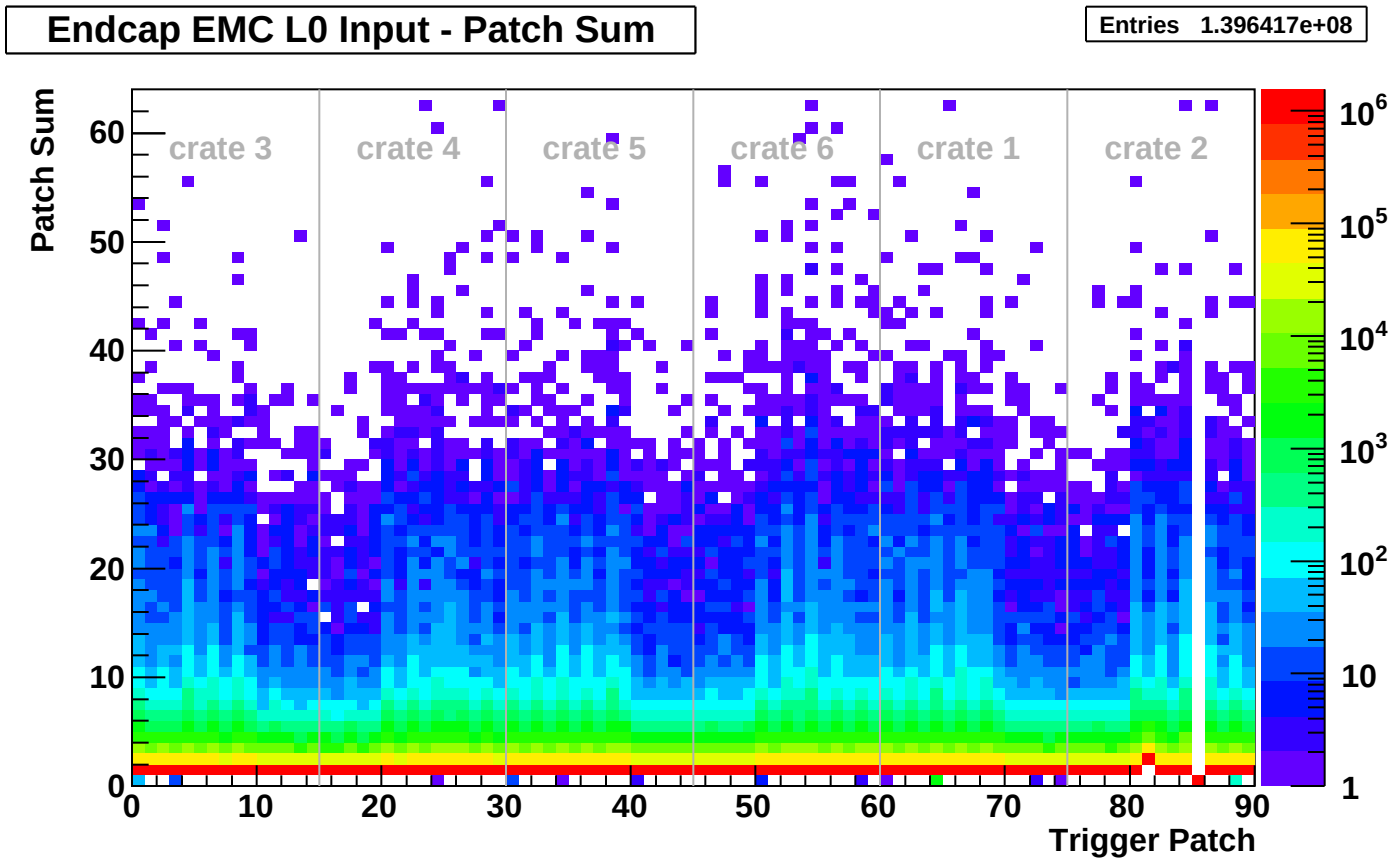

Abort-gaps
Entries 35986

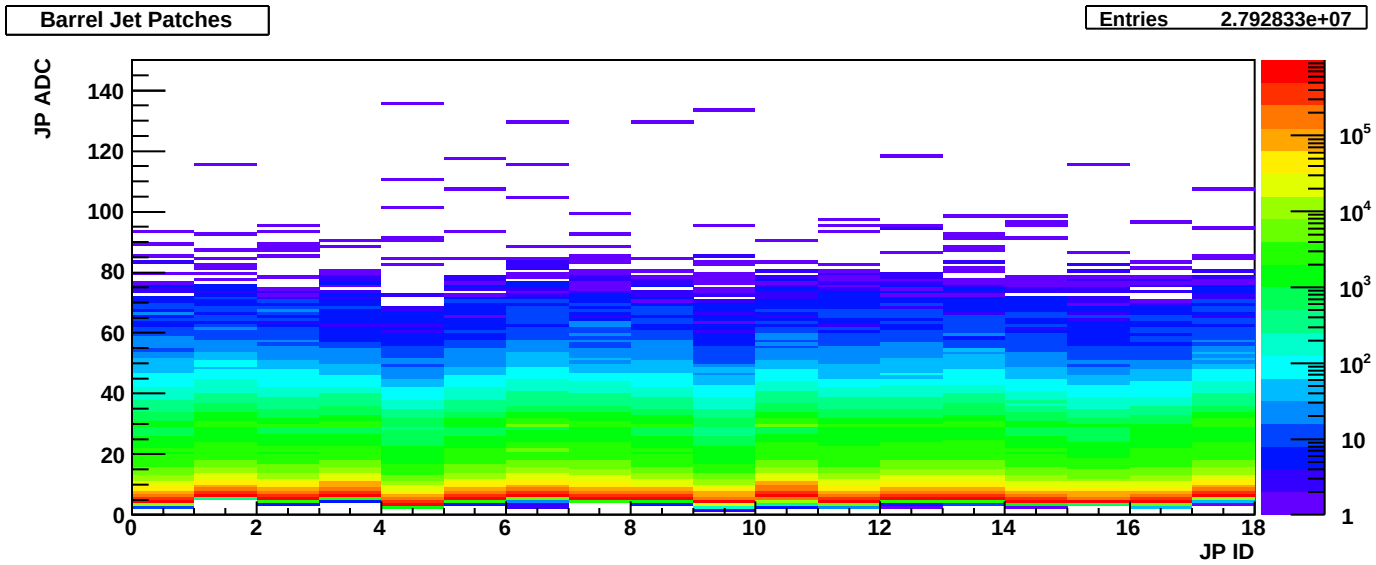

!Yellow-Fill
Entries 51265

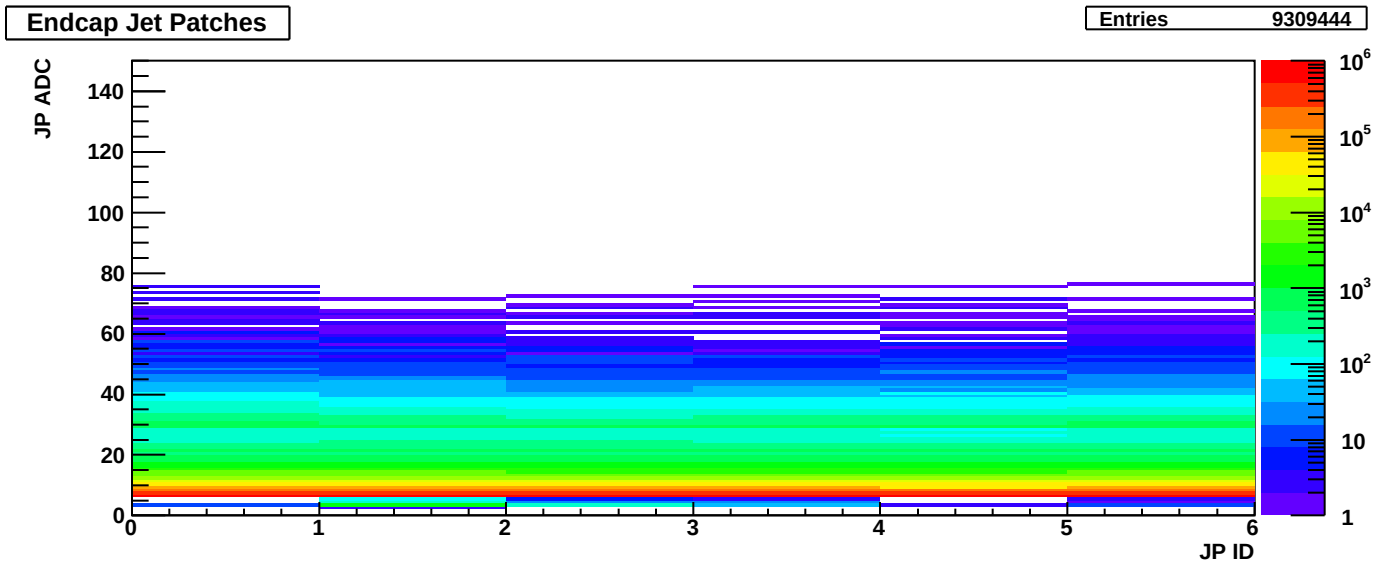

Barrel EMC L0 Input - High Tower
Entries 4.654722e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

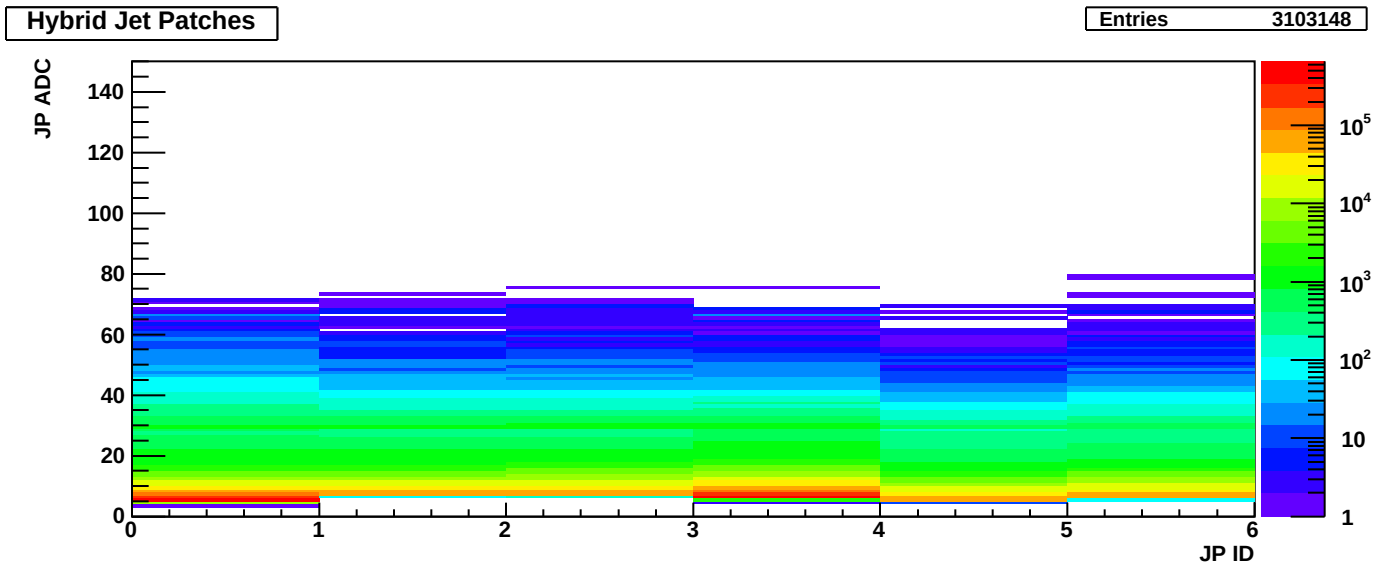

Barrel EMC L0 Input - Patch Sum
Entries 4.654722e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

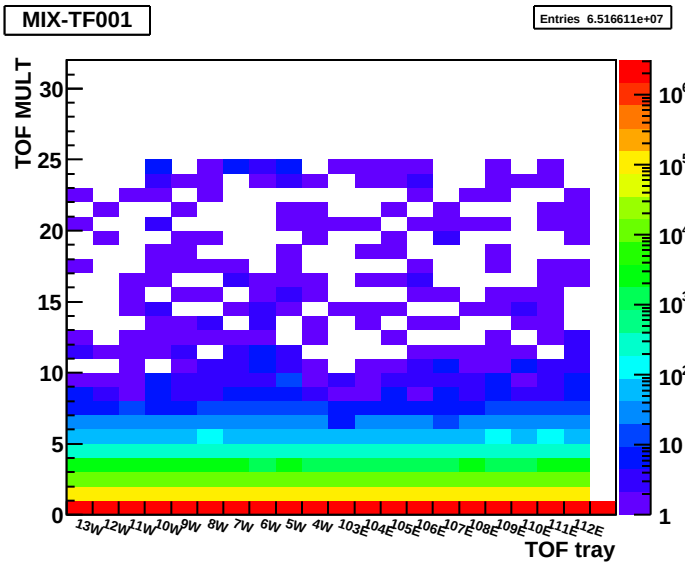

Endcap EMC L0 Input - High Tower
Entries 1.396417e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

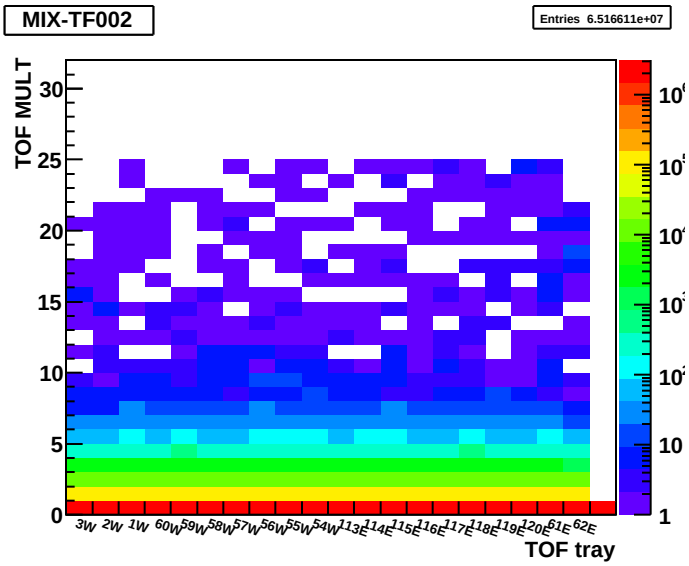

Endcap EMC L0 Input - Patch Sum
Entries 1.396417e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

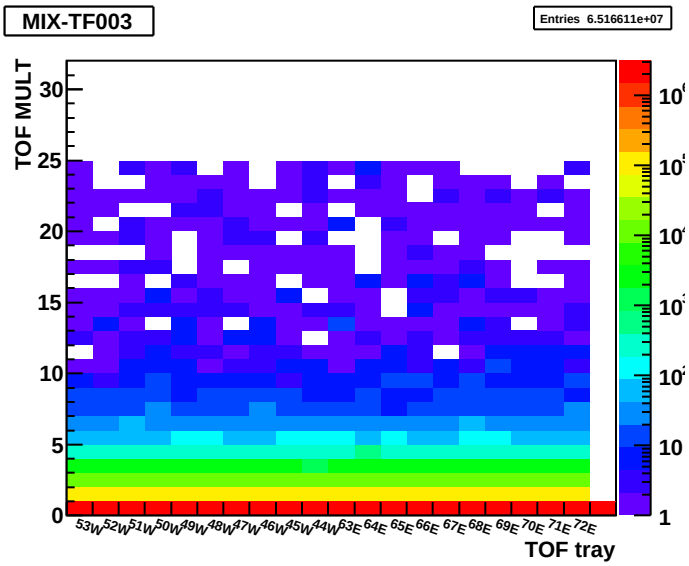

Barrel Jet Patches
Entries 2.792833e+07
JP ADC
JP ID

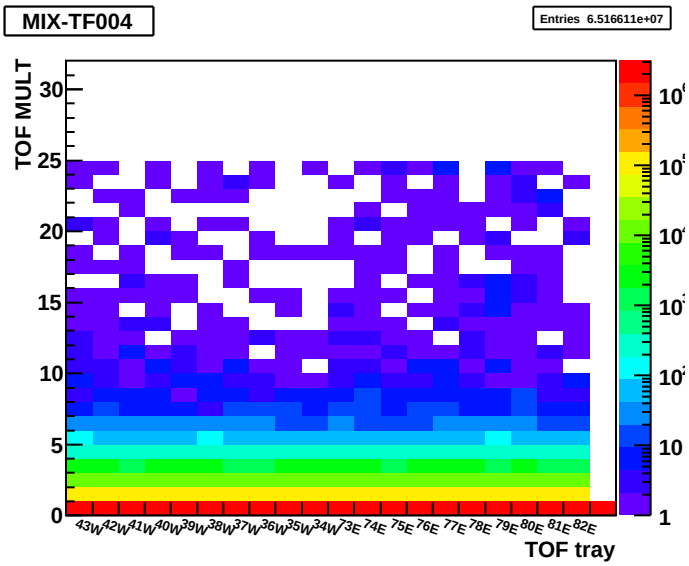

Endcap Jet Patches
Entries 9309444
JP ADC
JP ID

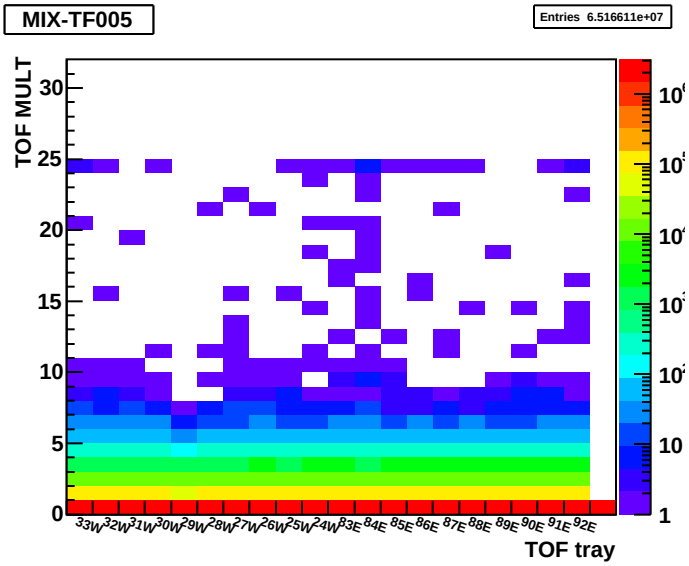

Hybrid Jet Patches
Entries 3103148
JP ADC
JP ID

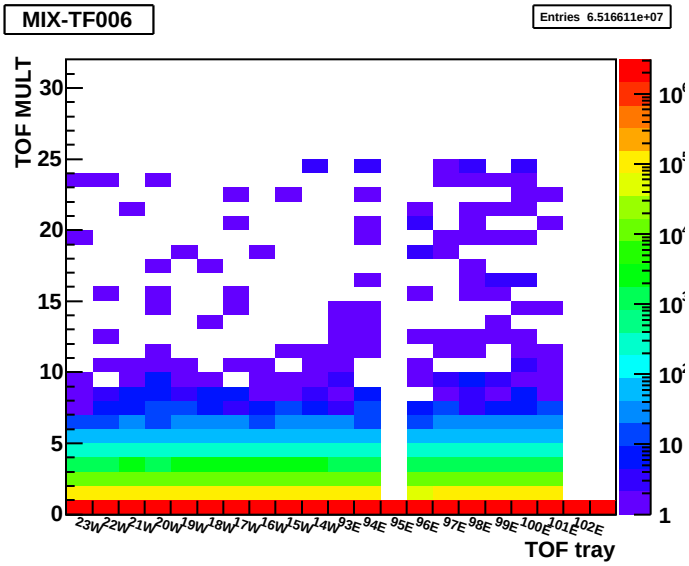

MIX-TF001
Entries 6.516611e+07
TOF MULT
TOF tray

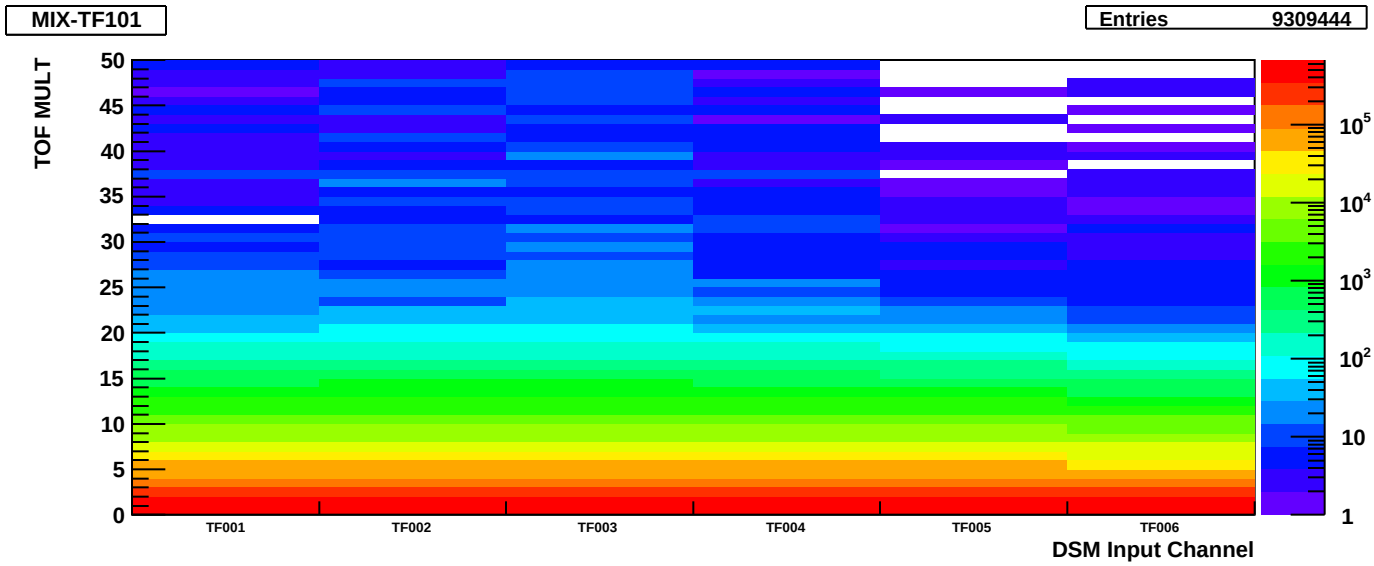

MIX-TF002
Entries 6.516611e+07
TOF MULT
TOF tray

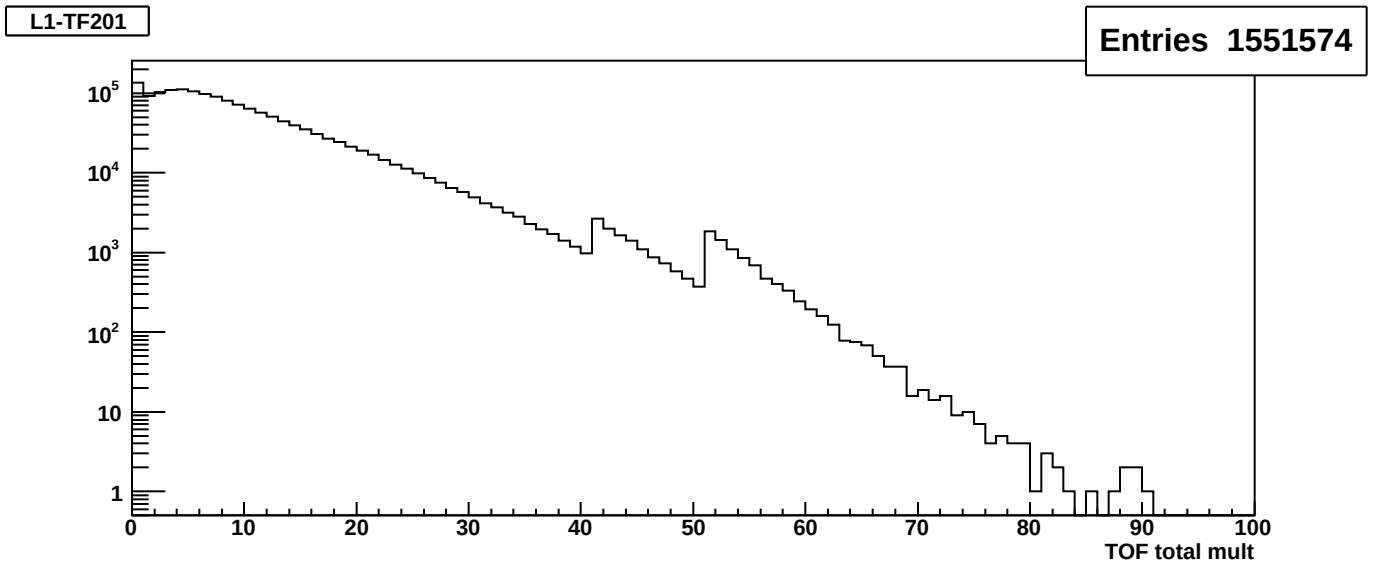

MIX-TF003
Entries 6.516611e+07
TOF MULT
TOF tray

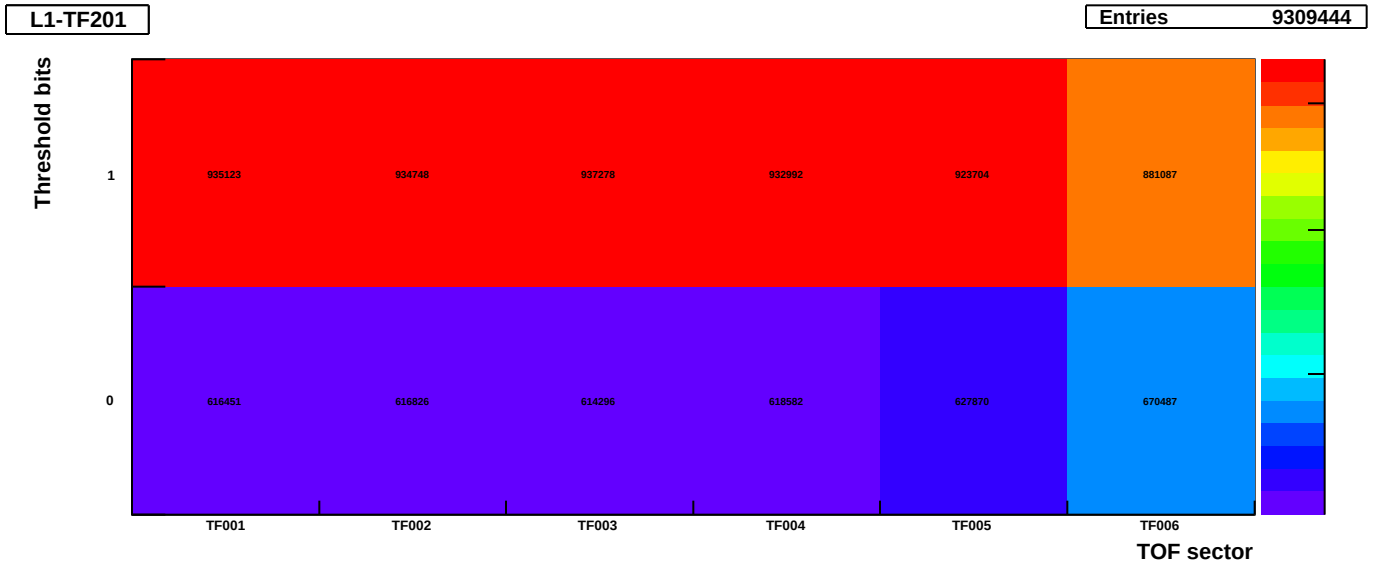

MIX-TF004
Entries 6.516611e+07
TOF MULT
TOF tray

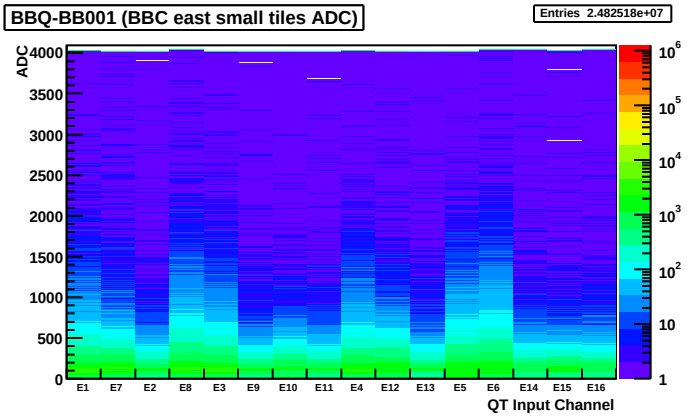

MIX-TF005
Entries 6.516611e+07
TOF MULT
TOF tray

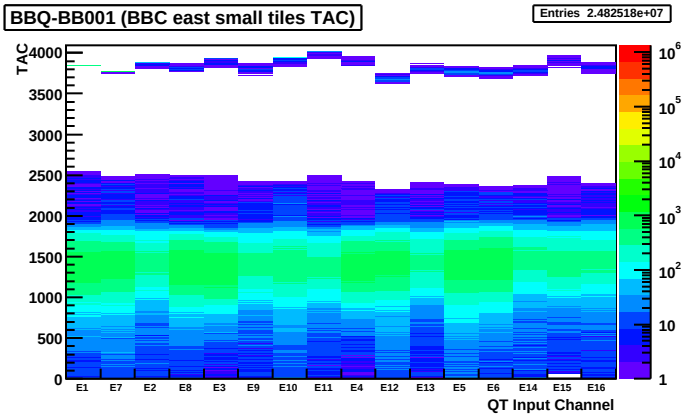

MIX-TF006
Entries 6.516611e+07
TOF MULT
TOF tray

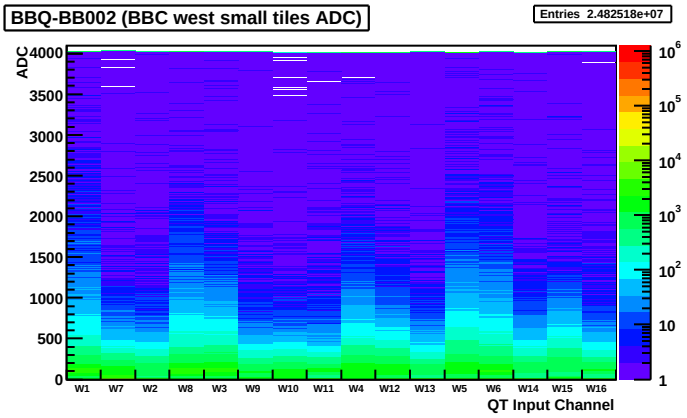

MIX-TF101
Entries 9309444
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

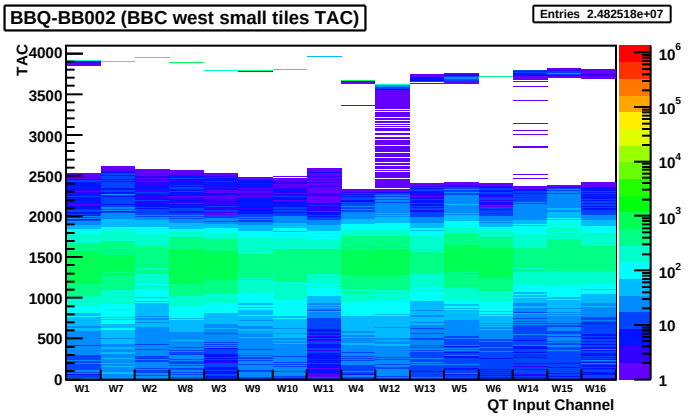

L1-TF201
Entries 1551574
TOF total mult

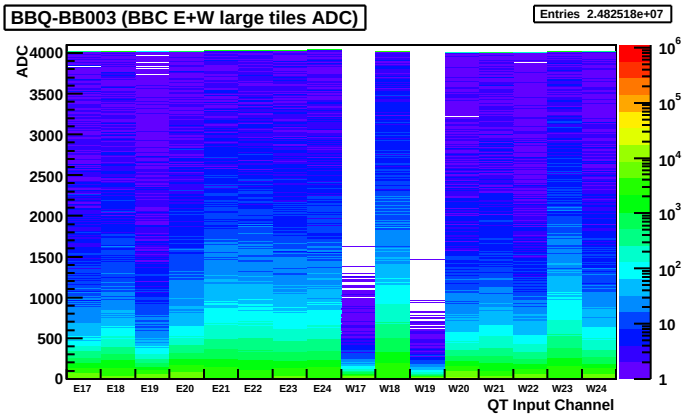

L1-TF201
Entries 9309444
Threshold bits
935123
934748
937278
932992
923704
881087
616451
616826
614296
618582
627870
670487
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

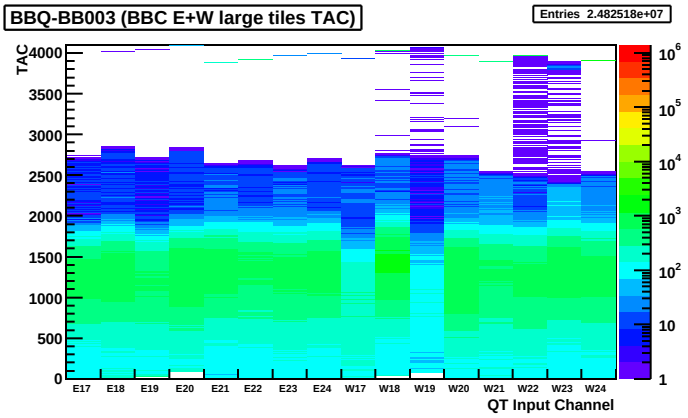

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.482518e+07
ADC
QT Input Channel

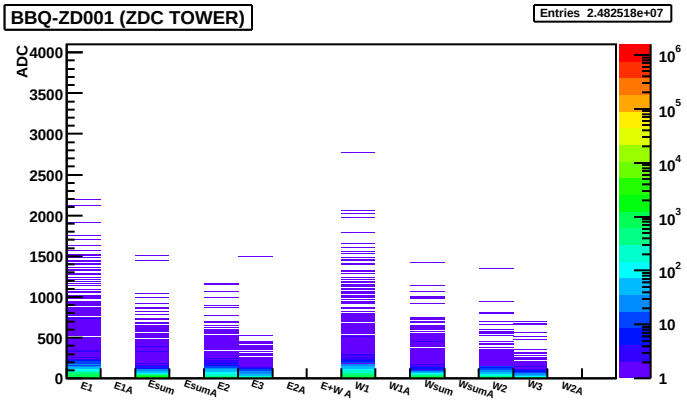

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.482518e+07
TAC
QT Input Channel

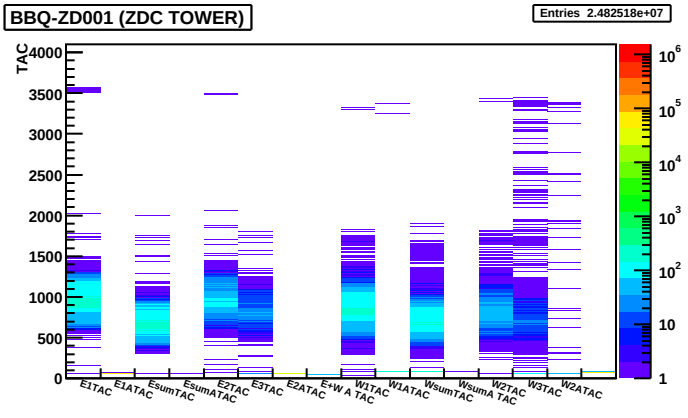

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.482518e+07
ADC
QT Input Channel

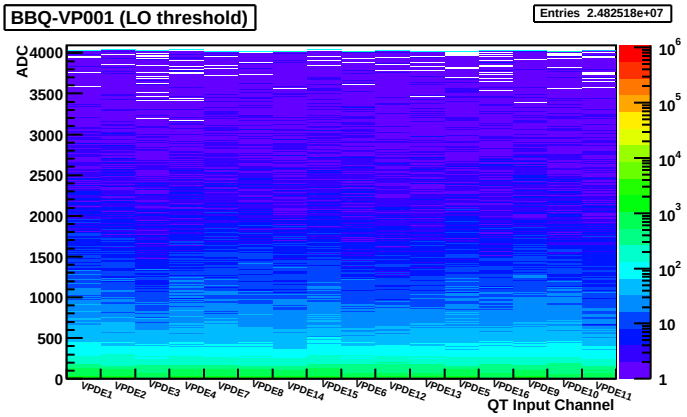

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.482518e+07
TAC
QT Input Channel

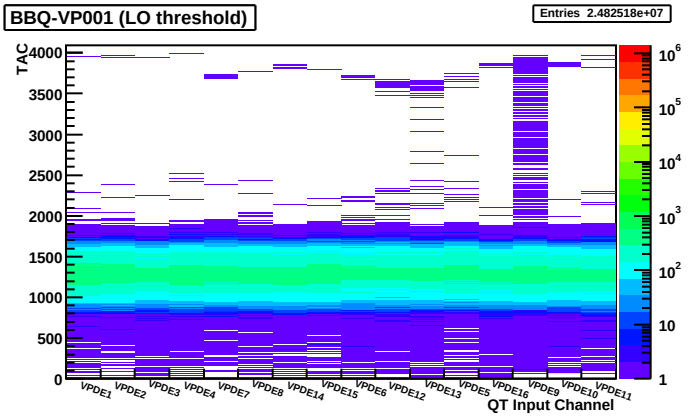

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.482518e+07
ADC
QT Input Channel

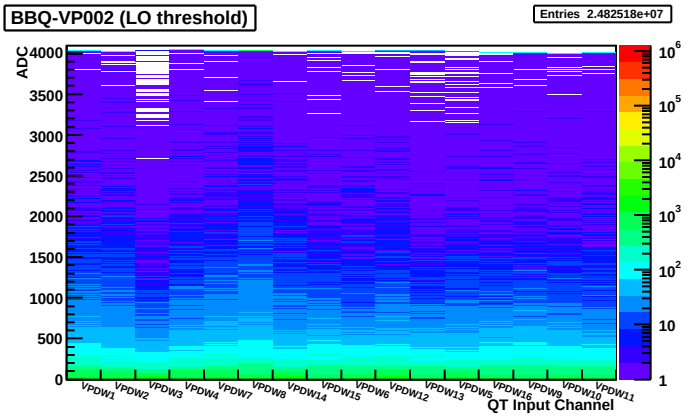

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.482518e+07
TAC
QT Input Channel

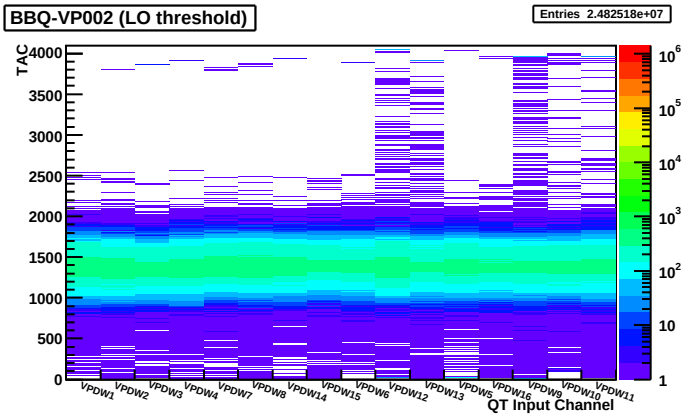

BBQ-ZD001 (ZDC TOWER)
Entries 2.482518e+07
ADC

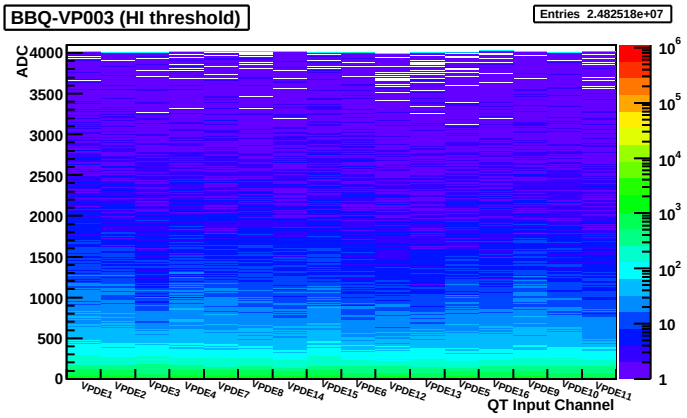

BBQ-ZD001 (ZDC TOWER)
Entries 2.482518e+07
TAC

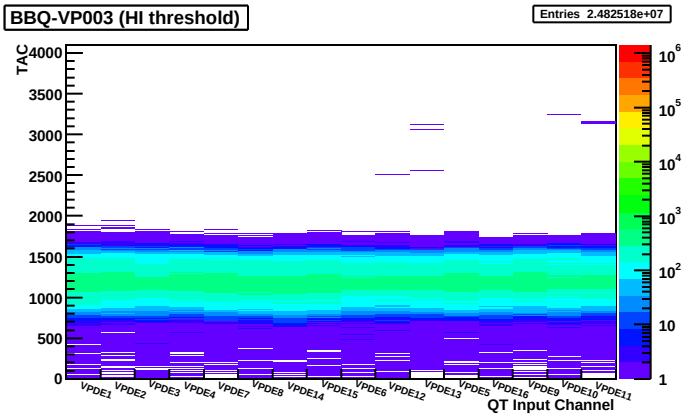

BBQ-VP001 (LO threshold)
Entries 2.482518e+07
ADC
QT Input Channel

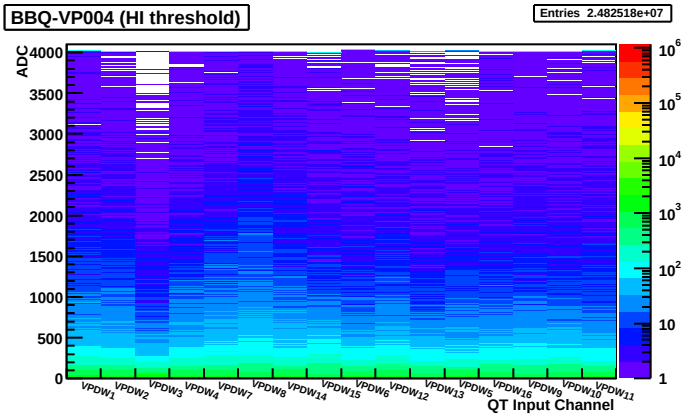

BBQ-VP001 (LO threshold)
Entries 2.482518e+07
TAC
QT Input Channel

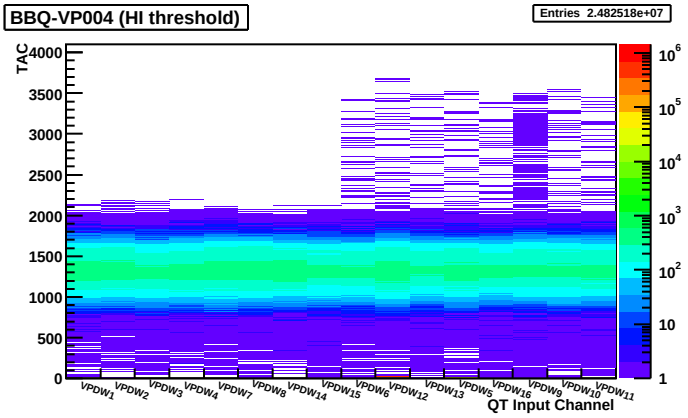

BBQ-VP002 (LO threshold)
Entries 2.482518e+07
ADC
QT Input Channel

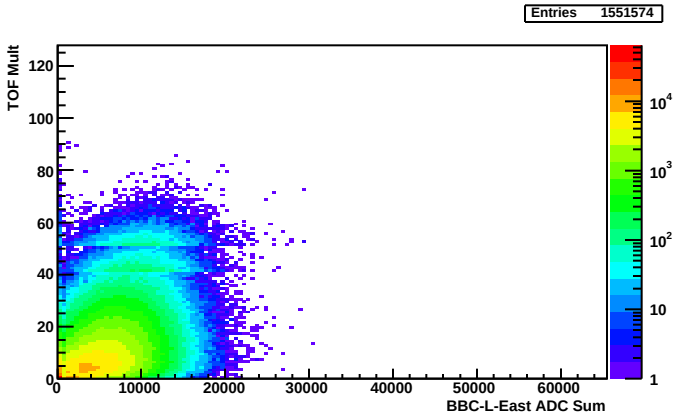

BBQ-VP002 (LO threshold)
Entries 2.482518e+07
TAC
QT Input Channel

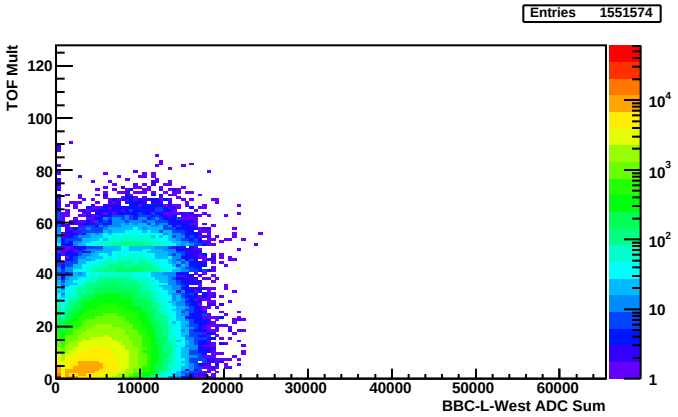

BBQ-VP003 (HI threshold)
Entries 2.482518e+07
ADC
QT Input Channel

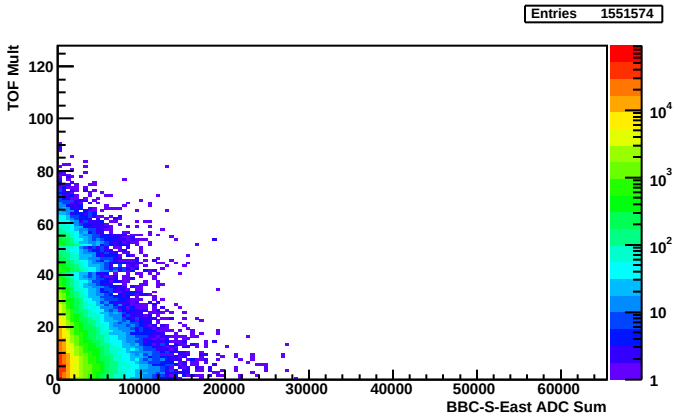

BBQ-VP003 (HI threshold)
Entries 2.482518e+07
TAC
QT Input Channel

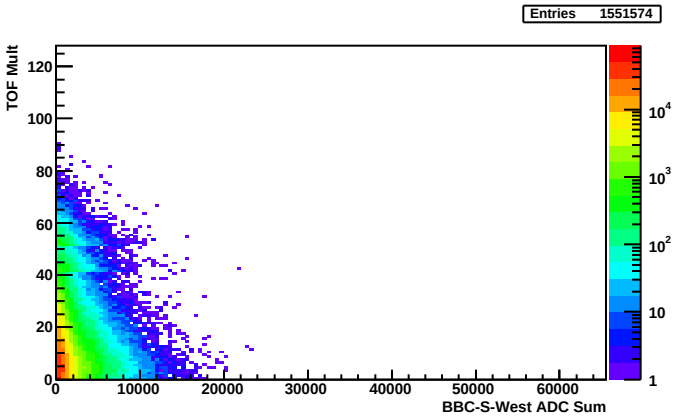

BBQ-VP004 (HI threshold)
Entries 2.482518e+07
ADC
QT Input Channel

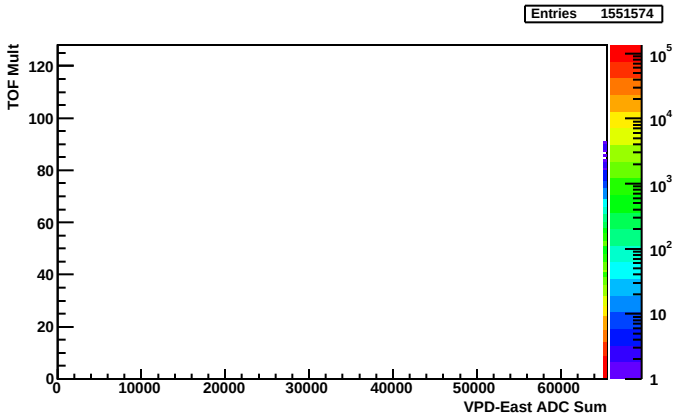

BBQ-VP004 (HI threshold)
Entries 2.482518e+07
TAC
QT Input Channel

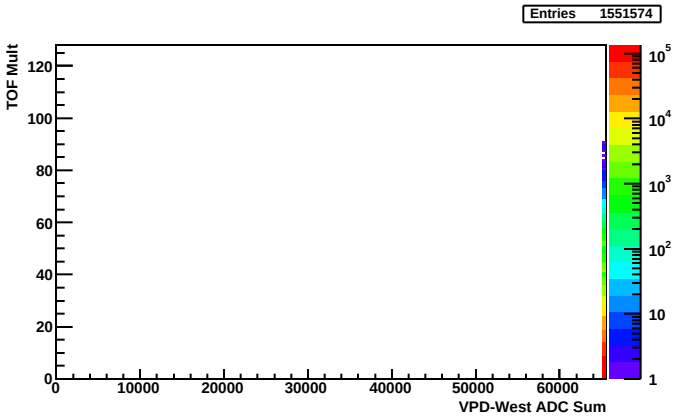

Entries 1551574
TOF Mult
BBC-L-East ADC Sum

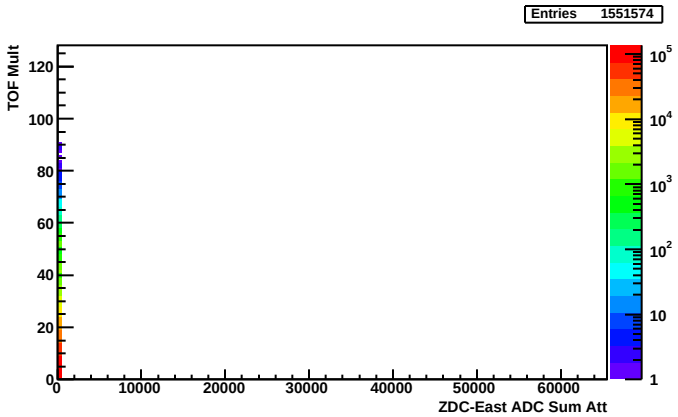

Entries 1551574
TOF Mult
BBC-L-West ADC Sum

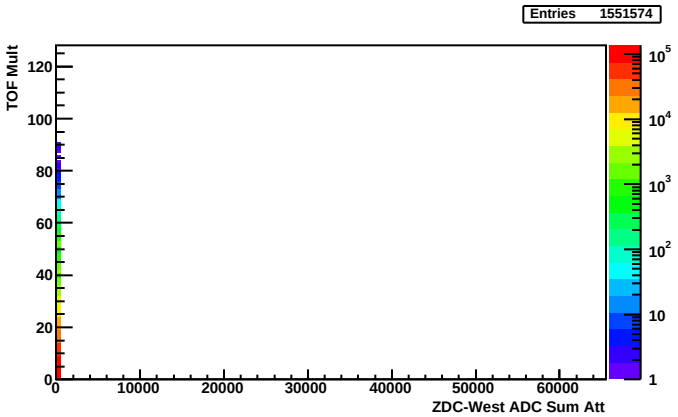

Entries 1551574
TOF Mult
BBC-S-East ADC Sum

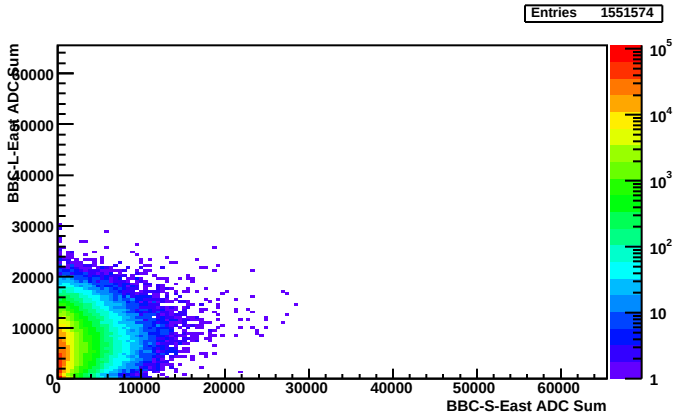

Entries 1551574
TOF Mult
BBC-S-West ADC Sum


Entries 1551574
TOF Mult
VPD-East ADC Sum


Entries 1551574
TOF Mult
VPD-West ADC Sum


Entries 1551574
TOF Mult
ZDC-East ADC Sum Att


Entries 1551574
TOF Mult
ZDC-West ADC Sum Att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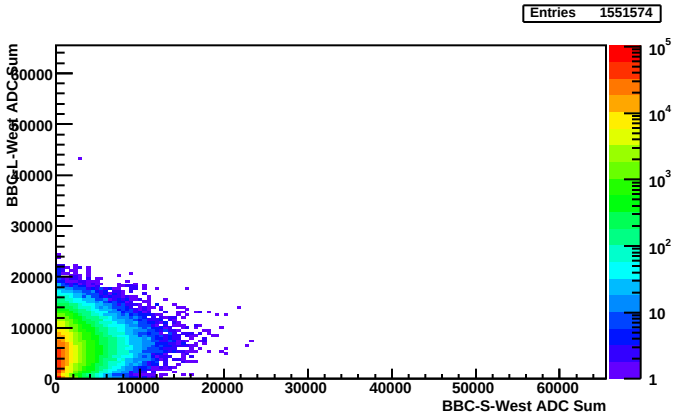
BBC-L-East ADC Sum
BBC-S-East ADC Sum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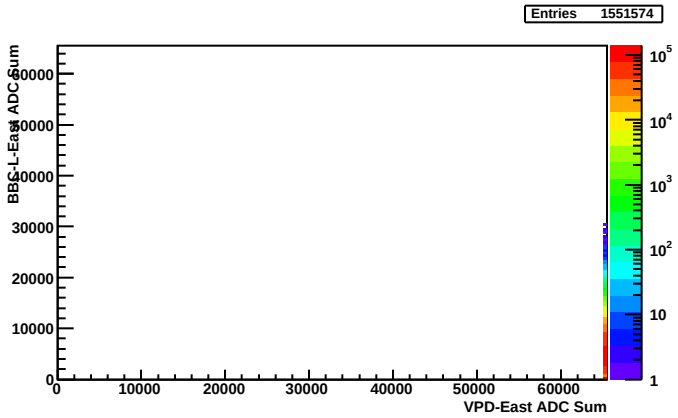
BBC-L-West ADC Sum
BBC-S-West ADC Sum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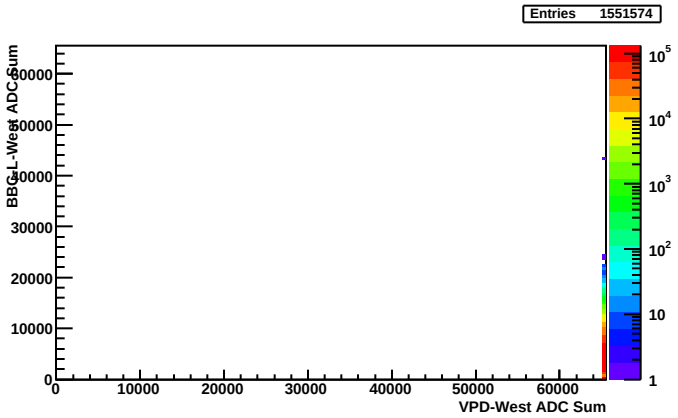
BBC-L-East ADC Sum
VPD-East ADC Sum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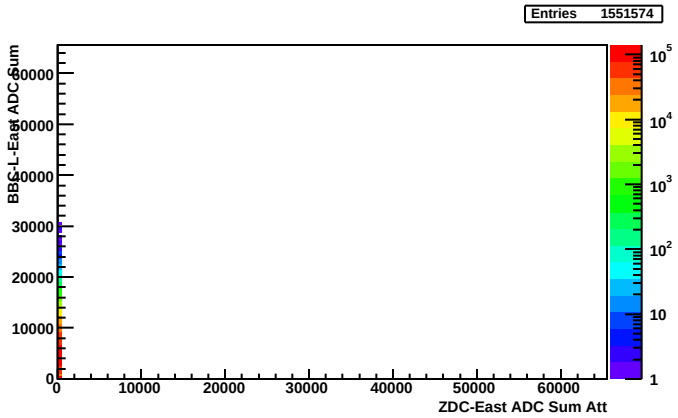
BBC-L-West ADC Sum
VPD-West ADC Sum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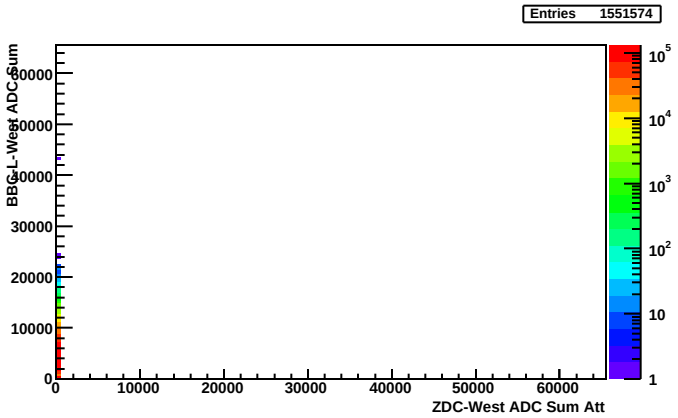
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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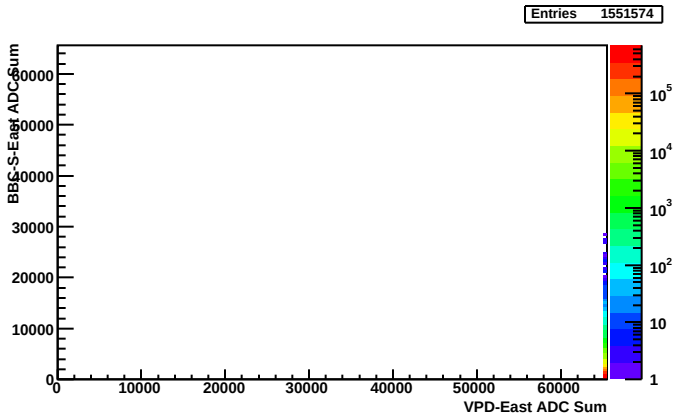
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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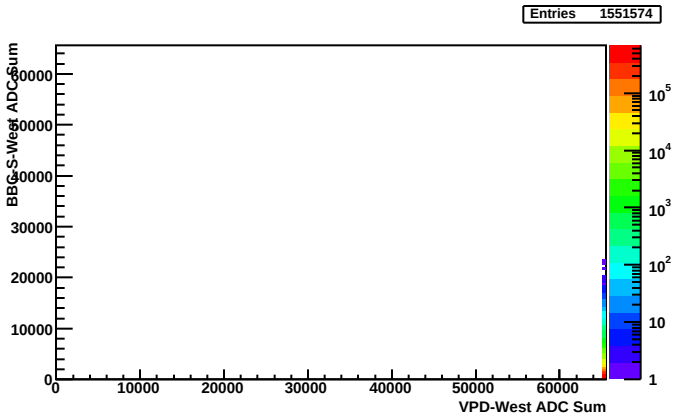
BBC-S-East ADC Sum
VPD-East ADC Sum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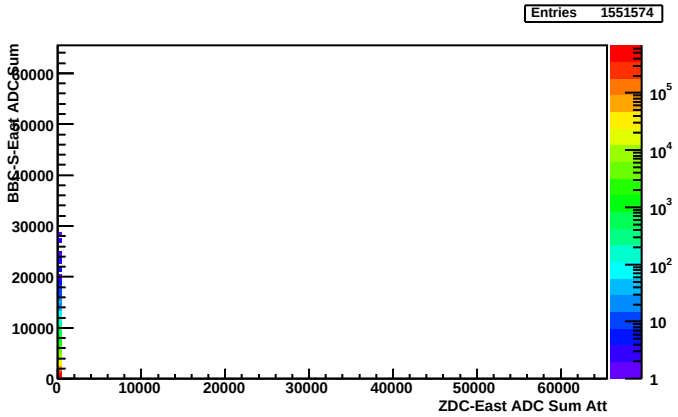
BBC-S-West ADC Sum
VPD-West ADC Sum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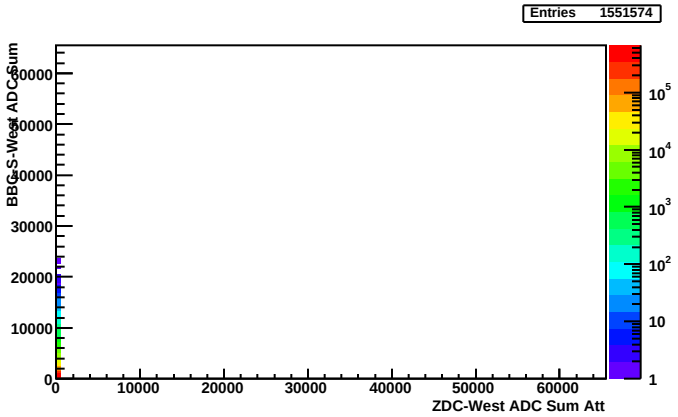
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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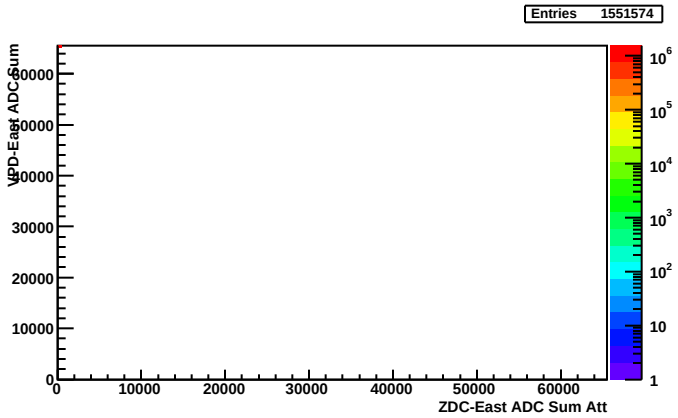
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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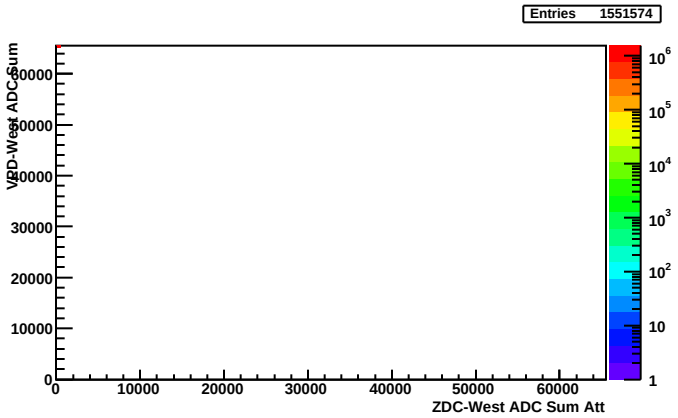
VPD-East ADC Sum
ZDC-East ADC Sum Att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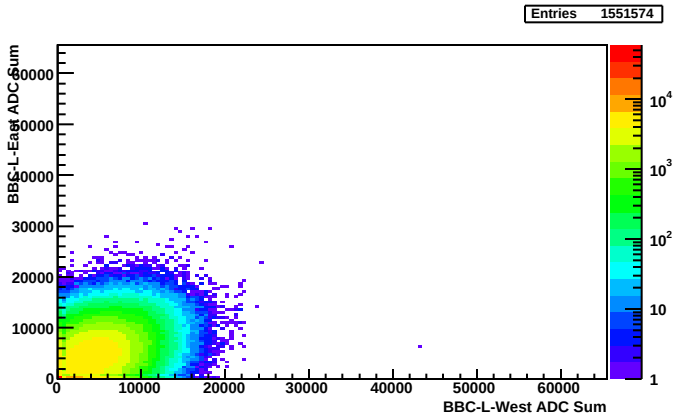
VPD-West ADC Sum
ZDC-West ADC Sum Att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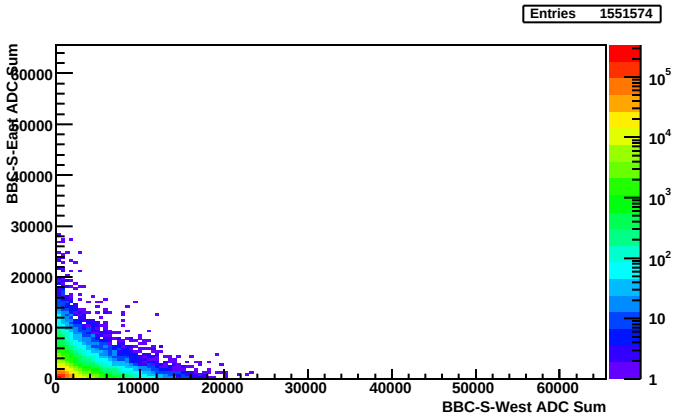
BBC-L-East ADC Sum
BBC-L-West ADC Sum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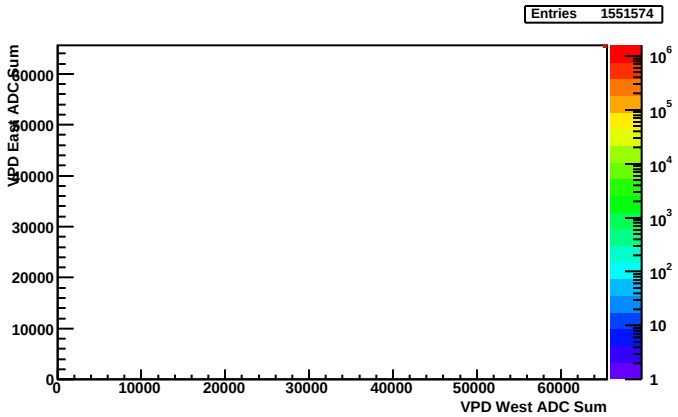
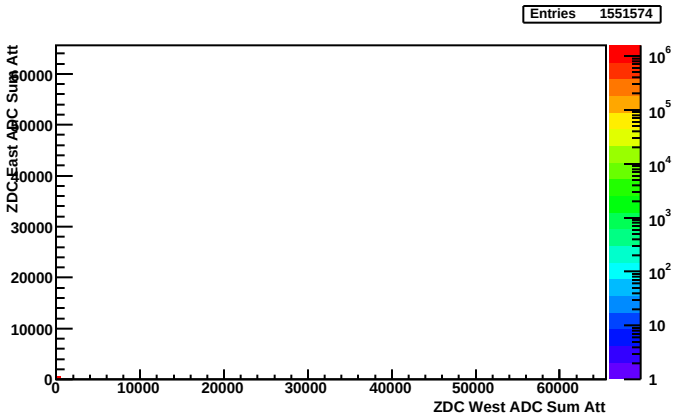
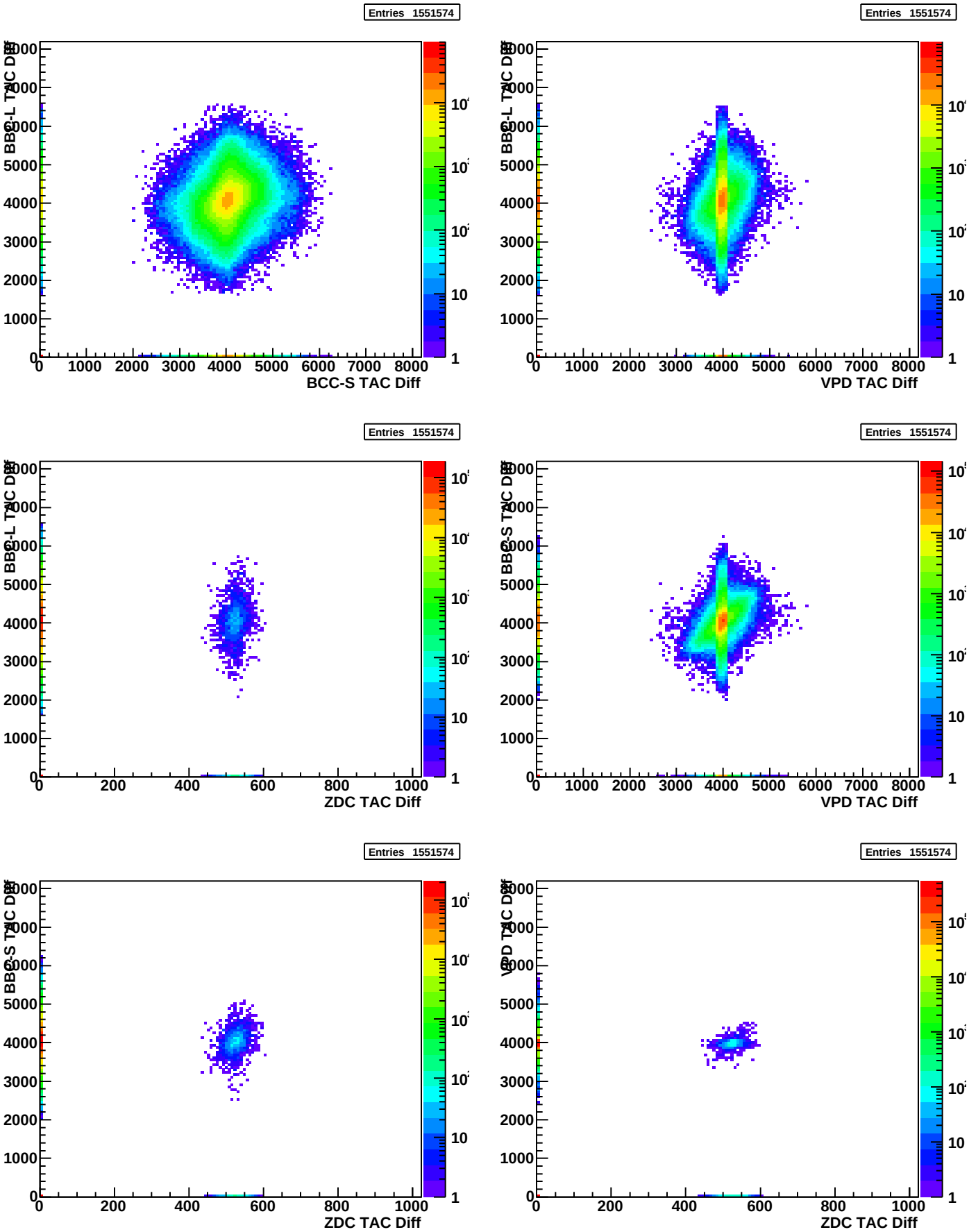
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 1551574
VPD East ADC Sum
VPD West ADC Sum


Entries 1551574
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 1551574
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1551574
BBC-L TAC Diff
VPD TAC Diff
Entries 1551574
BBC-L TAC Diff
ZDC TAC Diff
Entries 1551574
BBC-S TAC Diff
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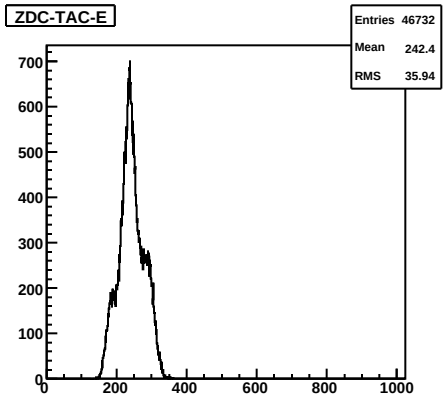
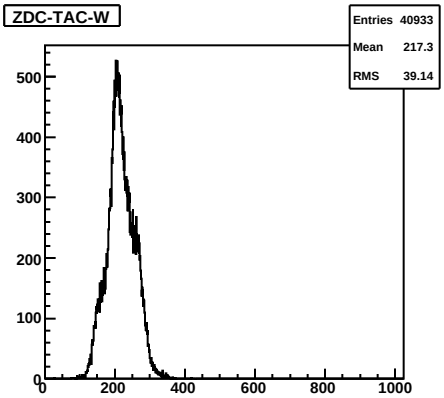
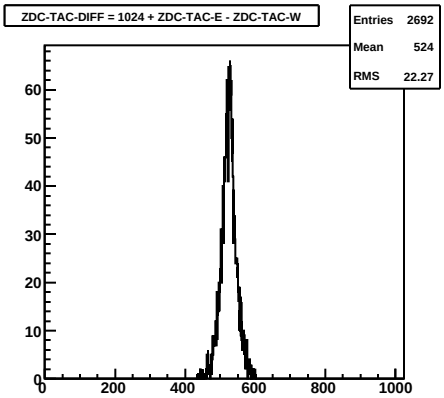
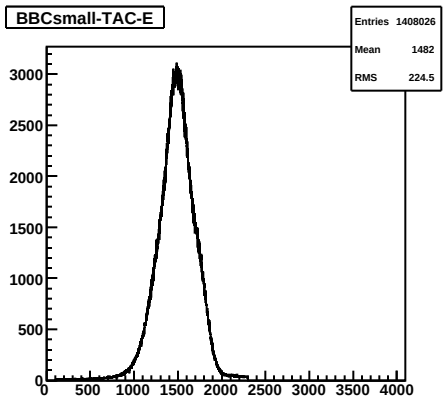
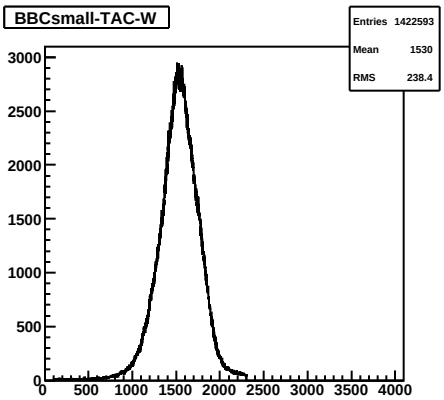
Entries 1551574
BBC-S TAC Diff
ZDC TAC Diff
Entries 1551574
VPD TAC Diff
ZDC TAC Diff

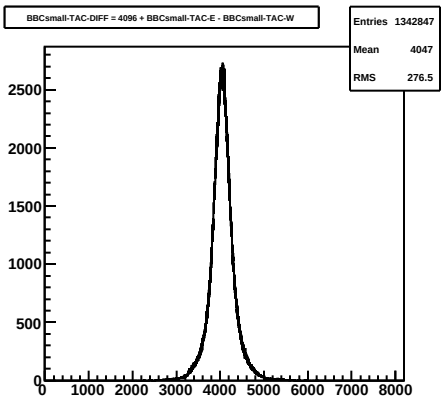

ZDC-TAC-E
Entries 46732
Mean 242.4
RMS 35.94

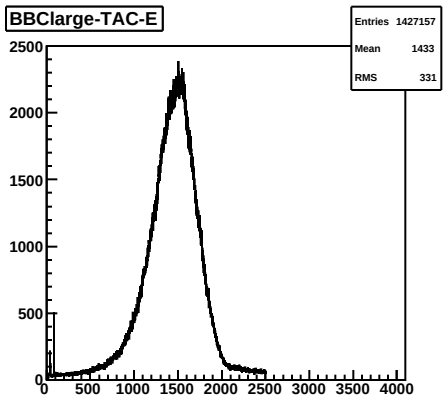

ZDC-TAC-W
Entries 40933
Mean 217.3
RMS 39.14

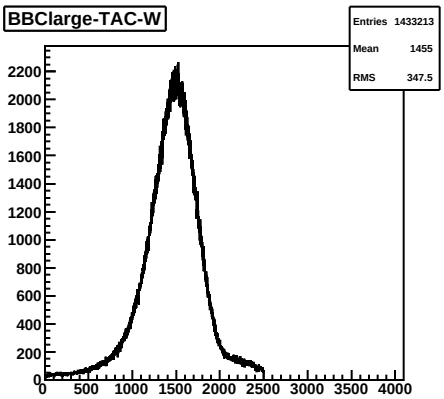

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2692
Mean 524
RMS 22.27

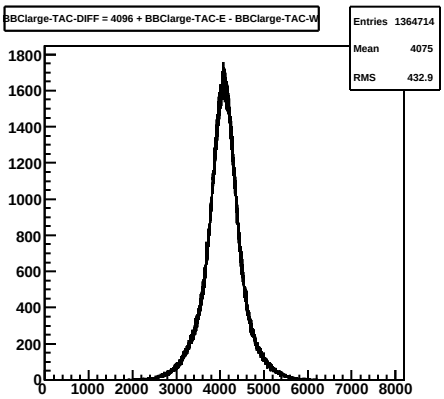

BBCsmall-TAC-E
Entries 1408026
Mean 1482
RMS 224.5

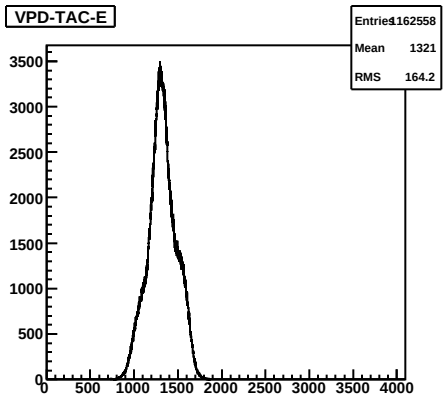

BBCsmall-TAC-W
Entries 1422593
Mean 1530
RMS 238.4

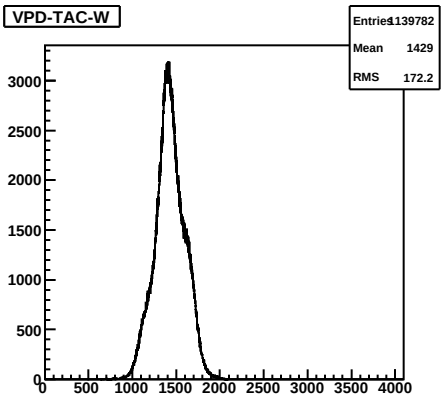

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1342847
Mean 4047
RMS 276.5

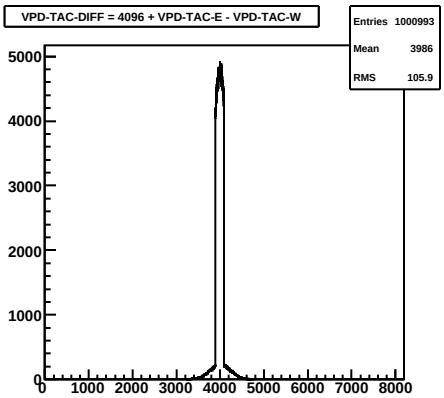

BBClarge-TAC-E
Entries 1427157
Mean 1433
RMS 331

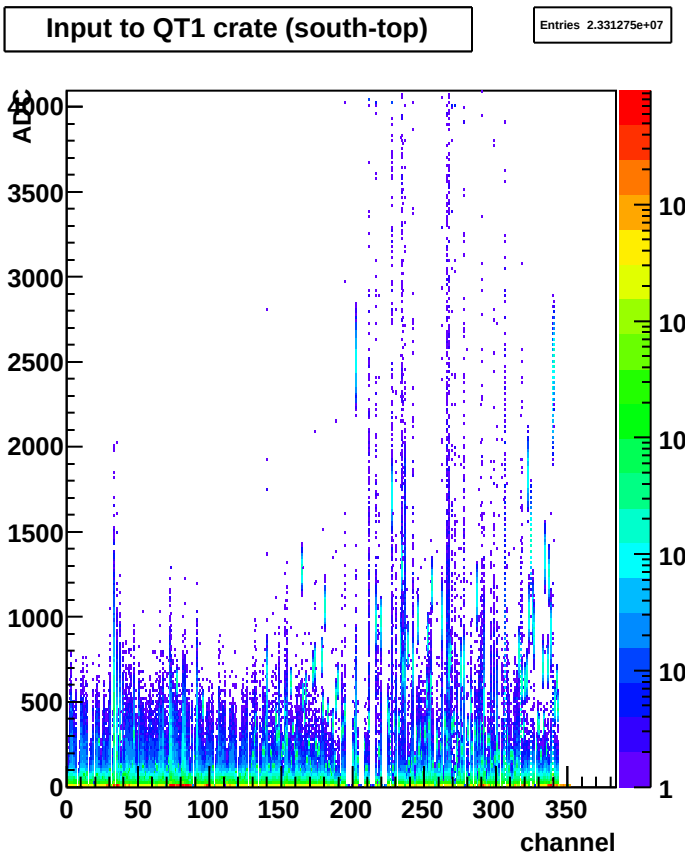

BBClarge-TAC-W
Entries 1433213
Mean 1455
RMS 347.5

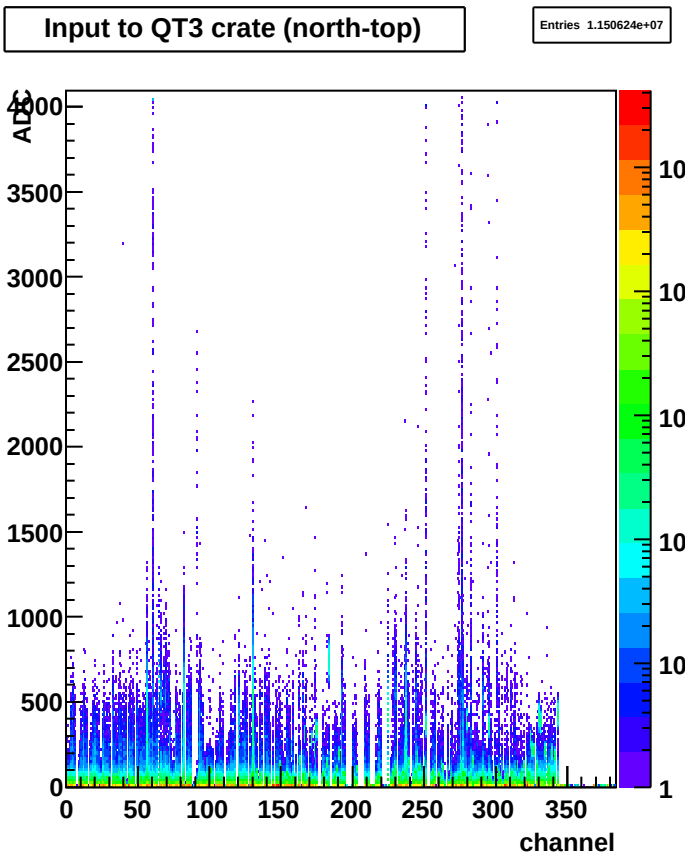

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1364714
Mean 4075
RMS 432.9

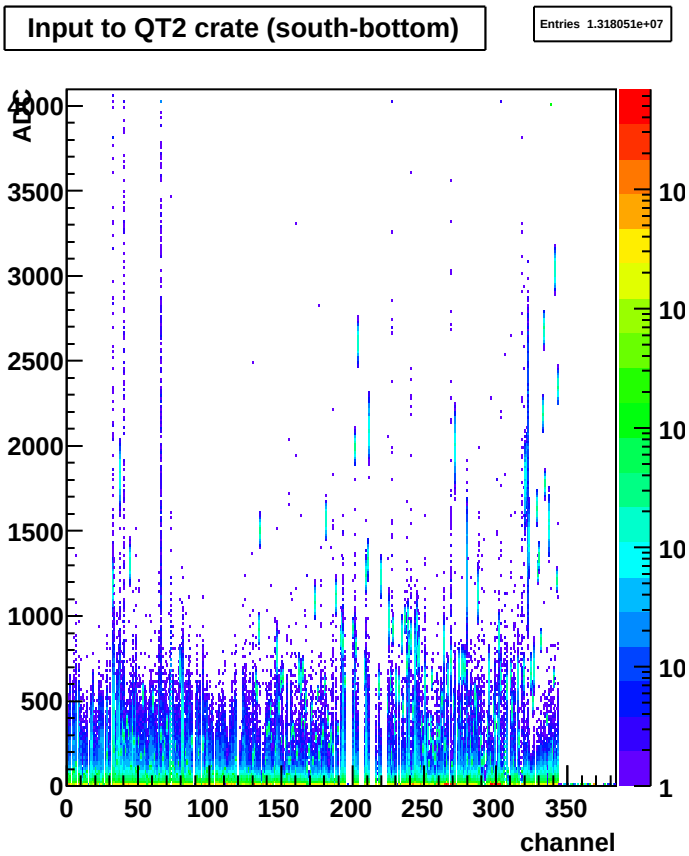

VPD-TAC-E
Entries 1162558
Mean 1321
RMS 164.2

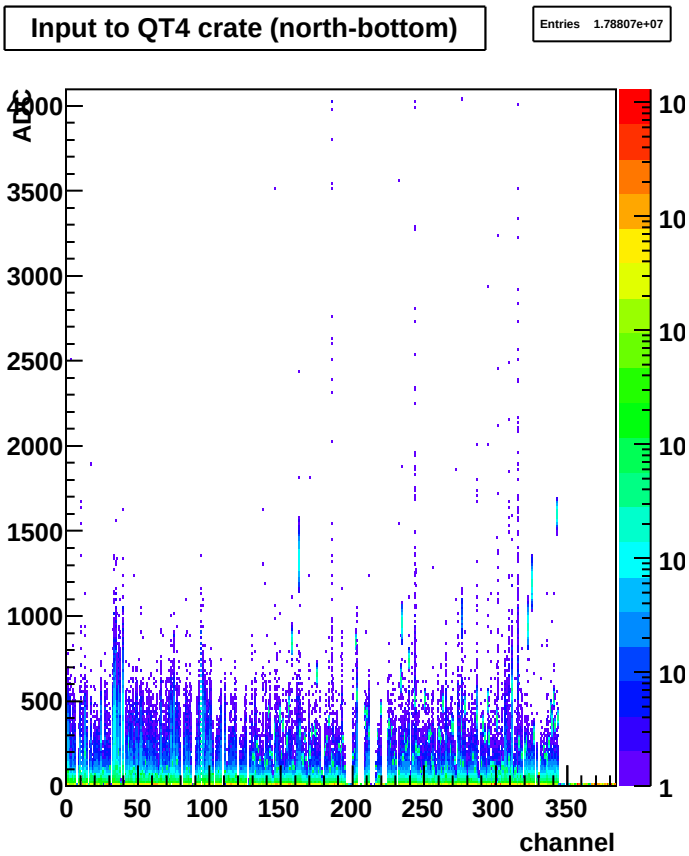

VPD-TAC-W
Entries 1139782
Mean 1429
RMS 172.2

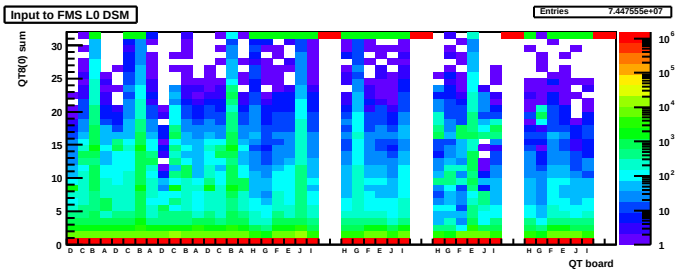

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1000993
Mean 3986
RMS 105.9

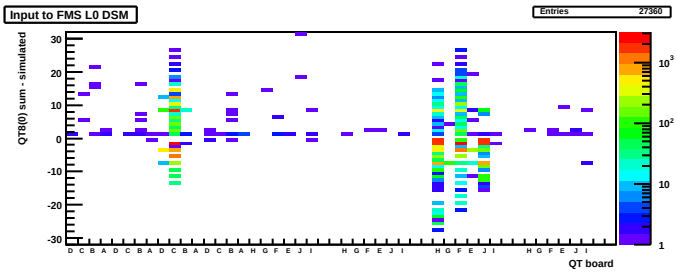

Input to QT1 crate (south-top)
Entries 2.331275e+07
ADC
channel

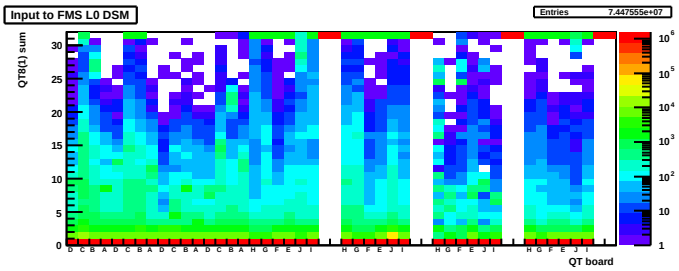

Input to QT3 crate (north-top)
Entries 1.150624e+07
ADC
channel

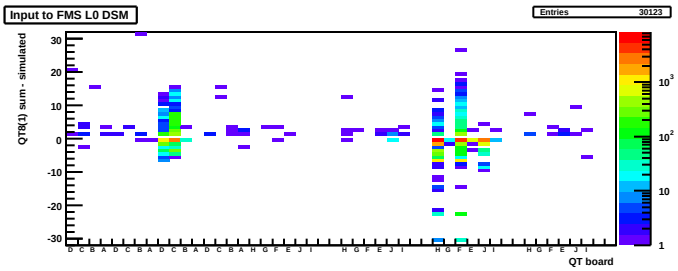

Input to QT2 crate (south-bottom)
Entries 1.318051e+07
ADC
channel

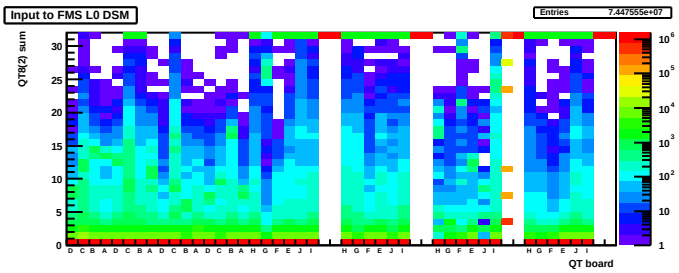

Input to QT4 crate (north-bottom)
Entries 1.78807e+07
ADC
channel

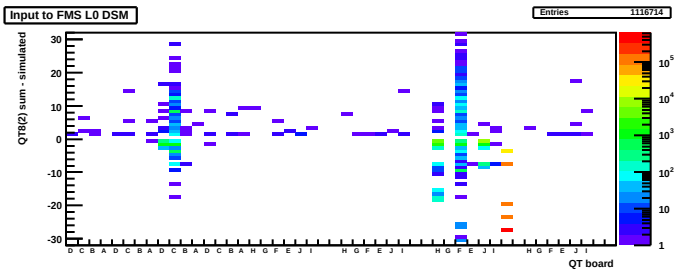

Input to FMS L0 DSM
Entries 7.447555e+07
QT8(0) sum
QT board

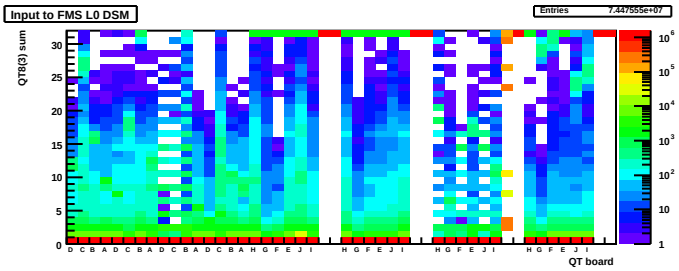

Input to FMS L0 DSM
Entries 27360
QT8(0) sum - simulated
QT board

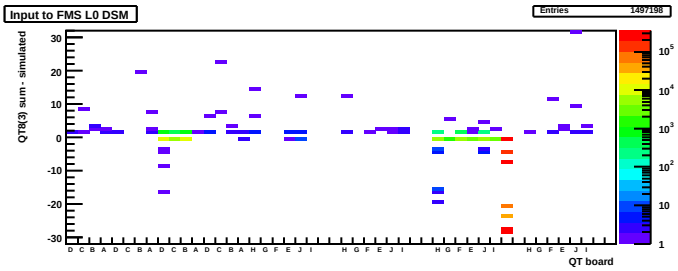

Input to FMS L0 DSM
Entries 7.447555e+07
QT8(1) sum
QT board

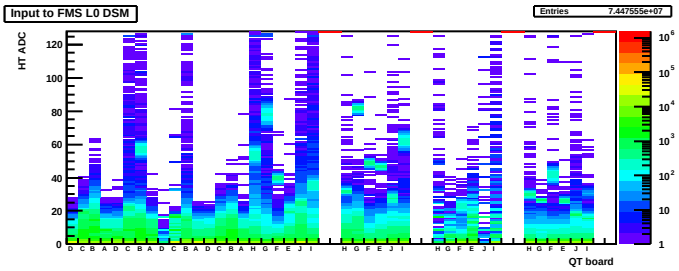

Input to FMS L0 DSM
Entries 30123
QT8(1) sum - simulated
QT board

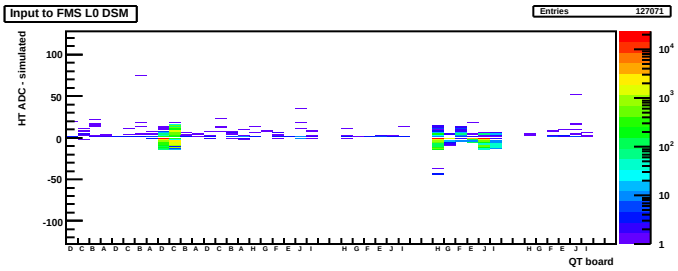

Input to FMS L0 DSM
Entries 7.447555e+07
QT8(2) sum
QT board

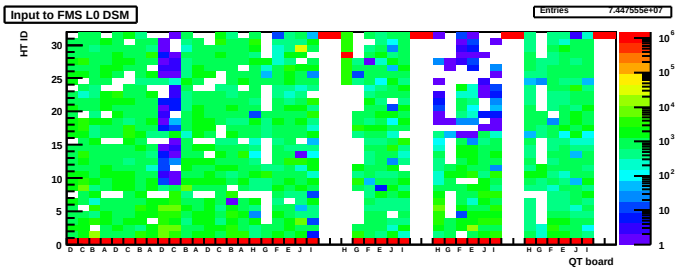

Input to FMS L0 DSM
Entries 1116714
QT8(2) sum - simulated
QT board

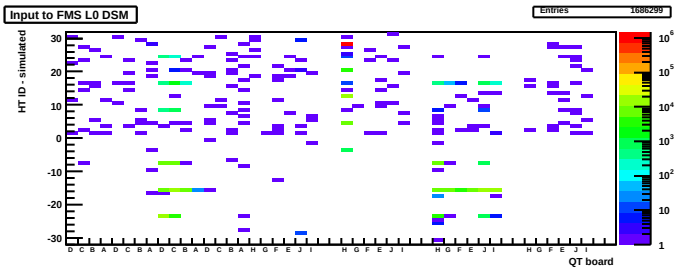

Input to FMS L0 DSM
Entries 7.447555e+07
QT8(3) sum
QT board

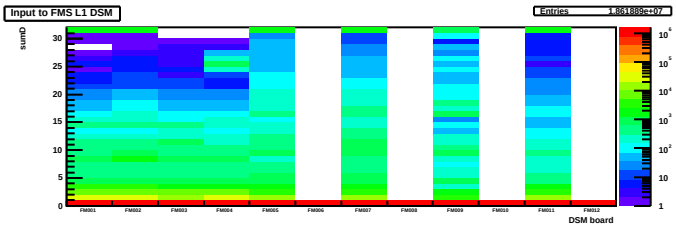

Input to FMS L0 DSM
Entries 1497198
QT8(3) sum - simulated
QT board

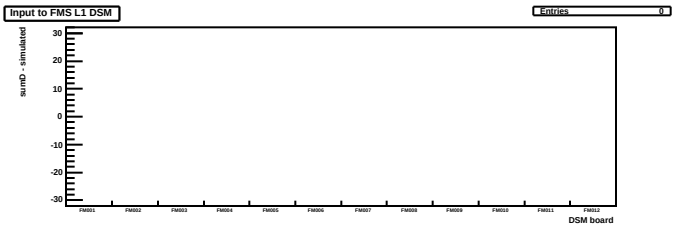

Input to FMS L0 DSM
Entries 7.447555e+07
HT ADC
QT board

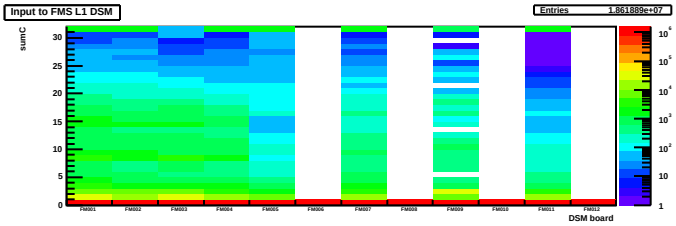

Input to FMS L0 DSM
Entries 127071
HT ADC - simulated
QT board

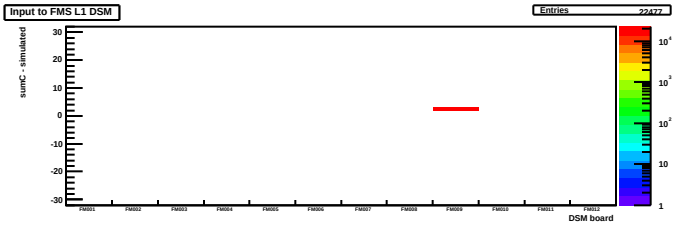

Input to FMS L0 DSM
Entries 7.447555e+07
HT ID
QT board

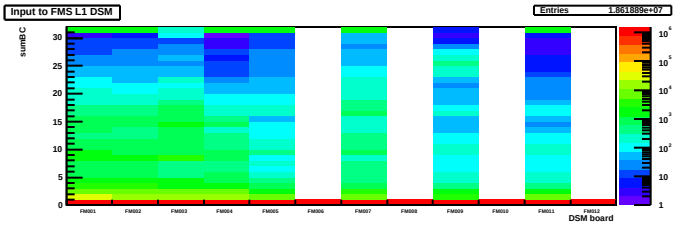

Input to FMS L0 DSM
Entries 1686299
HT ID - simulated
QT board

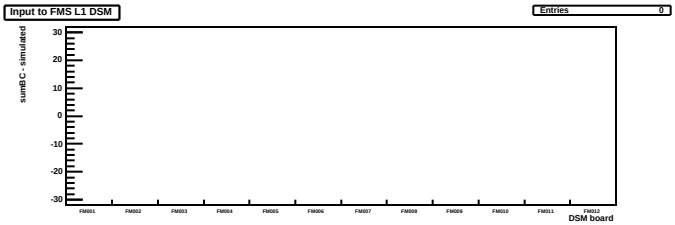

Input to FMS L1 DSM
Entries 1.861889e+07
sumD
DSM board

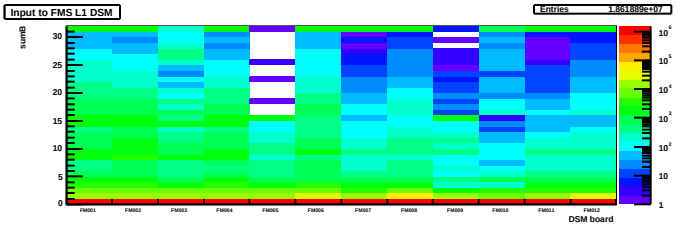

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

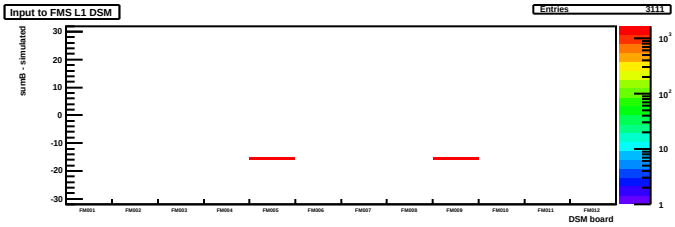

Input to FMS L1 DSM
Entries 1.861889e+07
sumC
DSM board

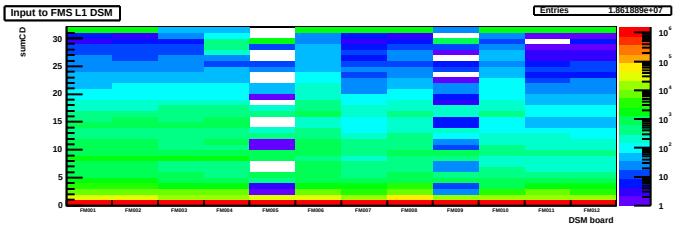

Input to FMS L1 DSM
Entries 22477
sumC - simulated
DSM board

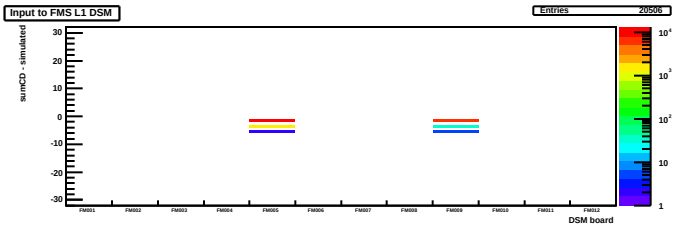

Input to FMS L1 DSM
Entries 1.861889e+07
sumBC
DSM board

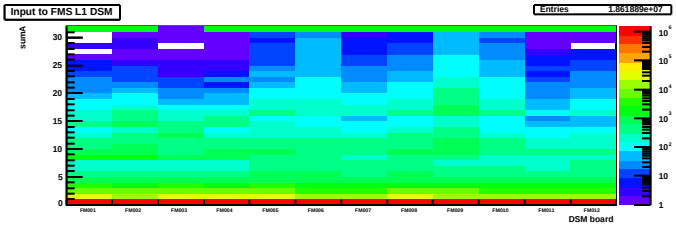

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

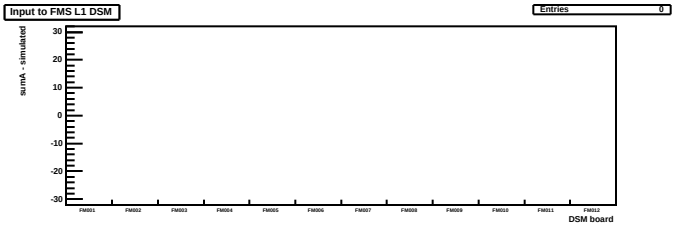

Input to FMS L1 DSM
Entries 1.861889e+07
sumB
DSM board

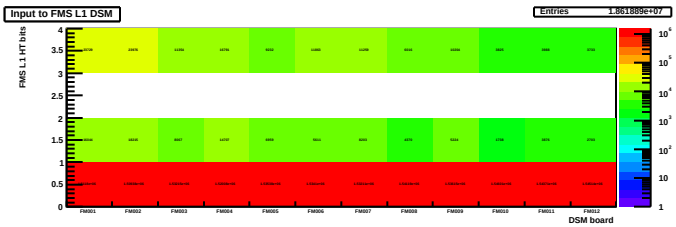

Input to FMS L1 DSM
Entries 3111
sumB - simulated
DSM board

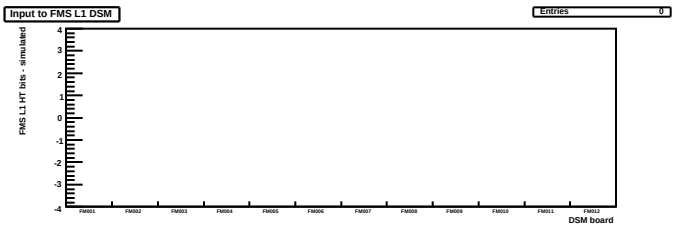

Input to FMS L1 DSM
Entries 1.861889e+07
sumCD
DSM board

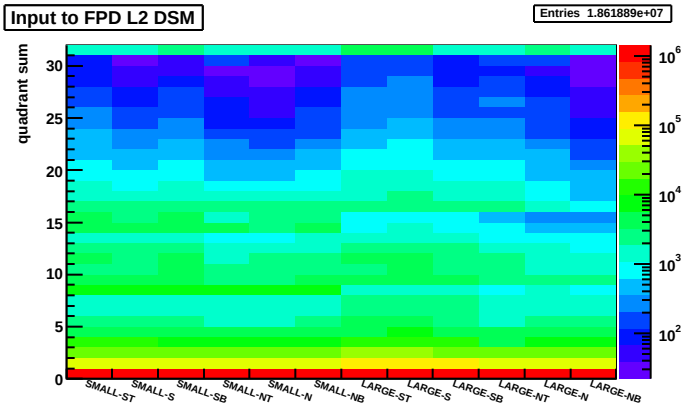

Input to FMS L1 DSM
Entries 20506
sumCD - simulated
DSM board

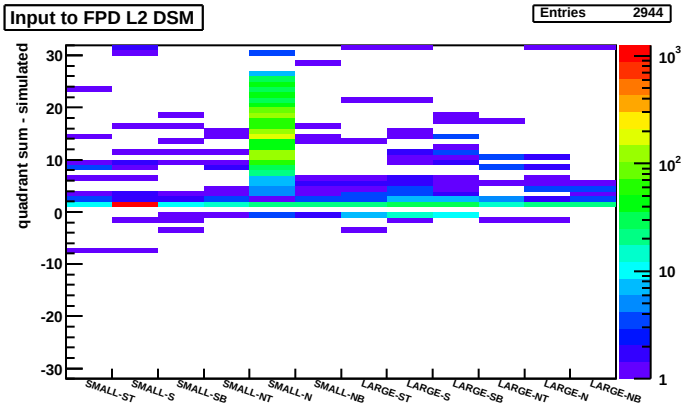

Input to FMS L1 DSM
Entries 1.861889e+07
sumA
DSM board

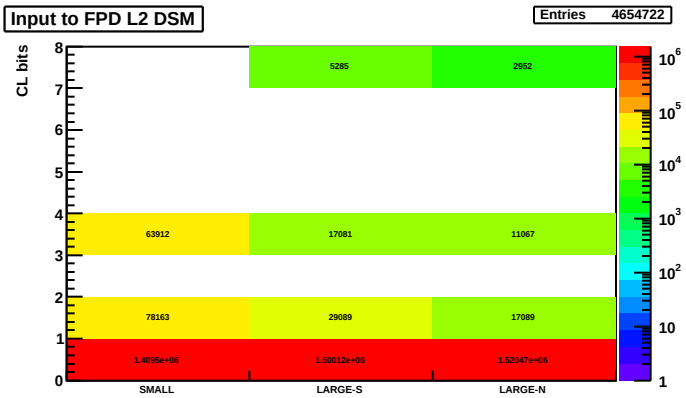

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

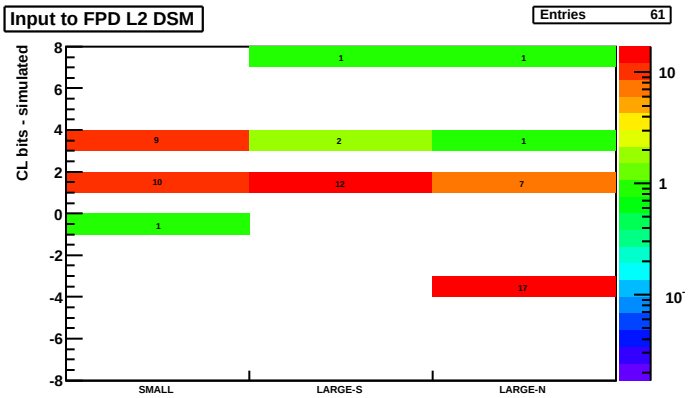

Input to FMS L1 DSM
Entries 1.861889e+07
FMS L1 HT bits
DSM board

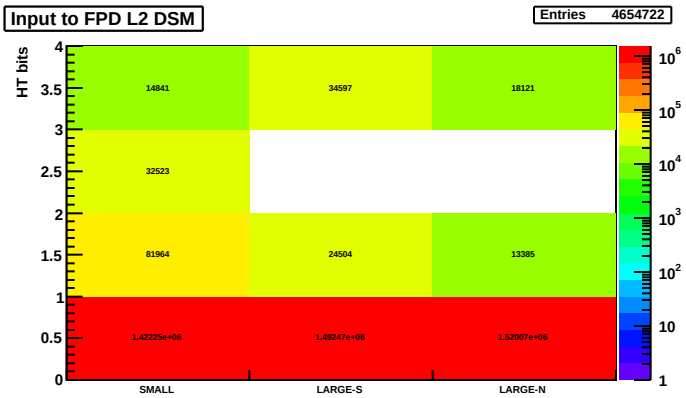

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

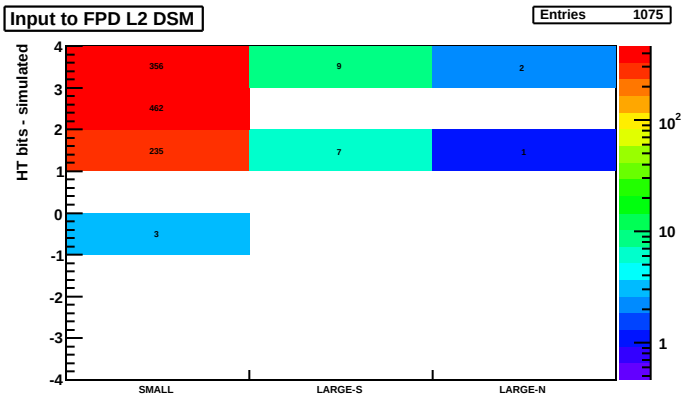

Input to FPD L2 DSM
Entries 1.861889e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

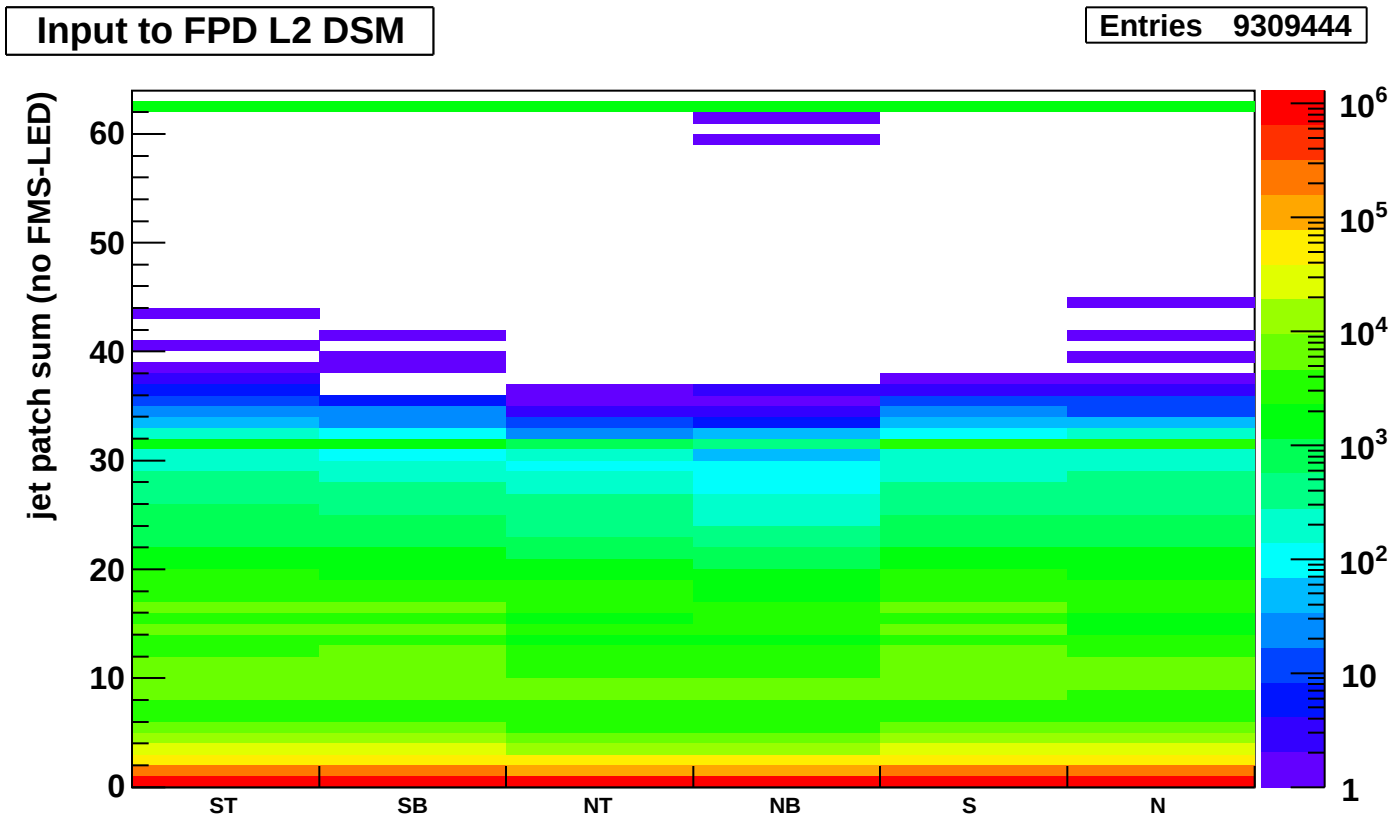

Input to FPD L2 DSM
Entries 2944
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

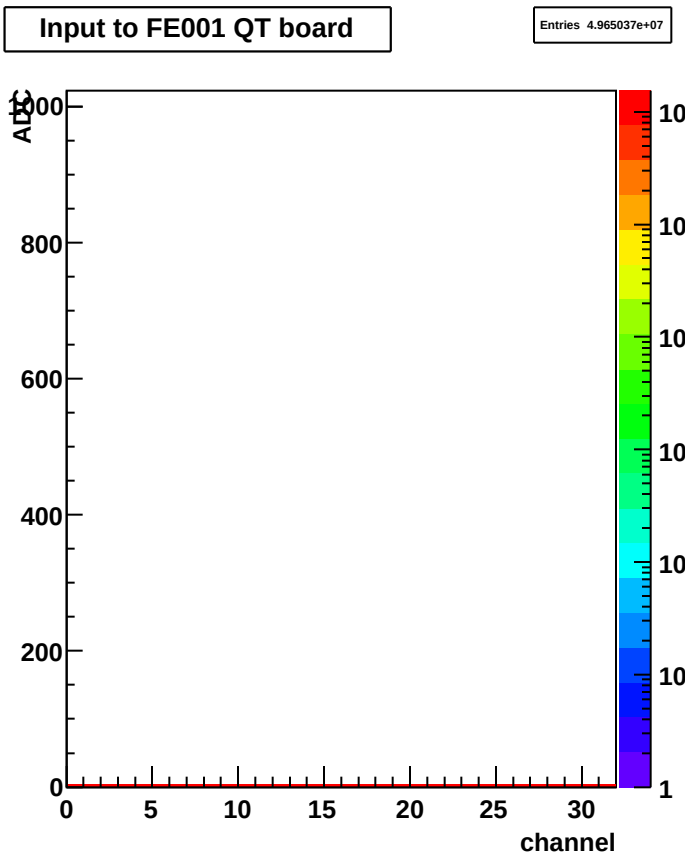

Input to FPD L2 DSM
Entries 4654722
CL bits
5285
2952
63912
17081
11067
78163
29089
17089
1.4095e+06
1.50012e+06
1.52047e+06
SMALL
LARGE-S
LARGE-N

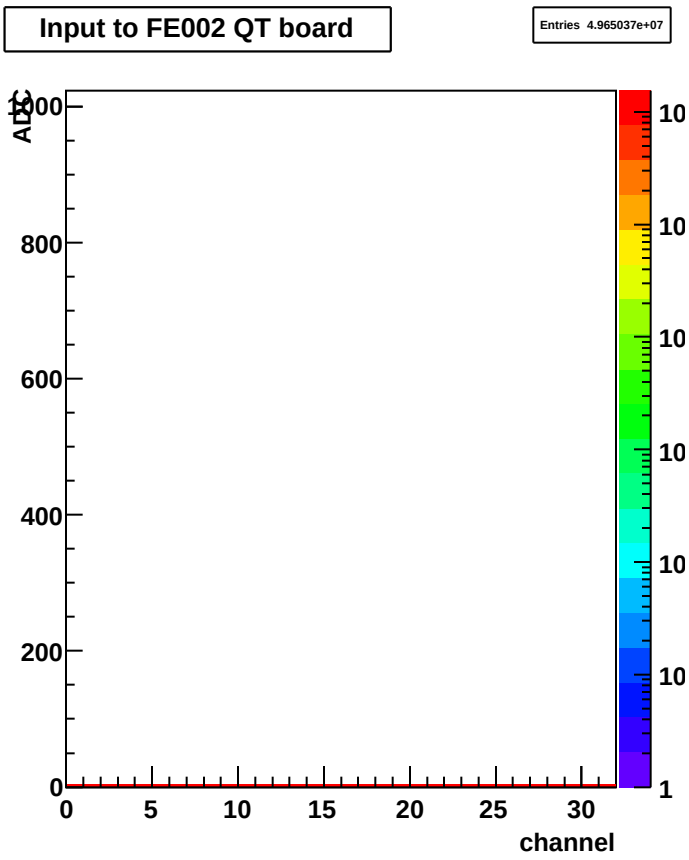

Input to FPD L2 DSM
Entries 61
CL bits - simulated
1
1
9
2
1
10
12
7
1
17
SMALL
LARGE-S
LARGE-N

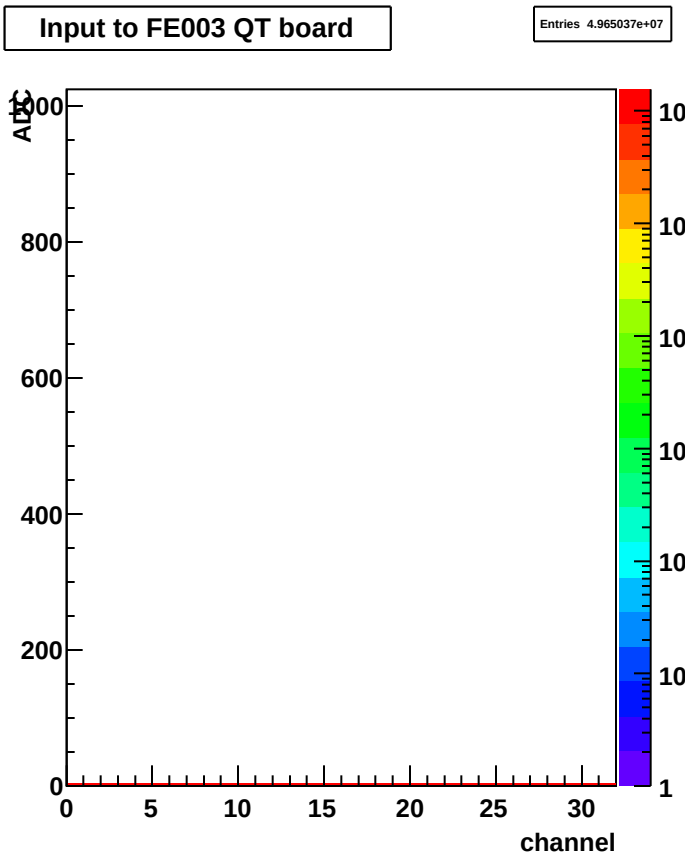

Input to FPD L2 DSM
Entries 4654722
HT bits
14841
34597
18121
32523
81964
24504
13385
1.42225e+06
1.49247e+06
1.52007e+06
SMALL
LARGE-S
LARGE-N

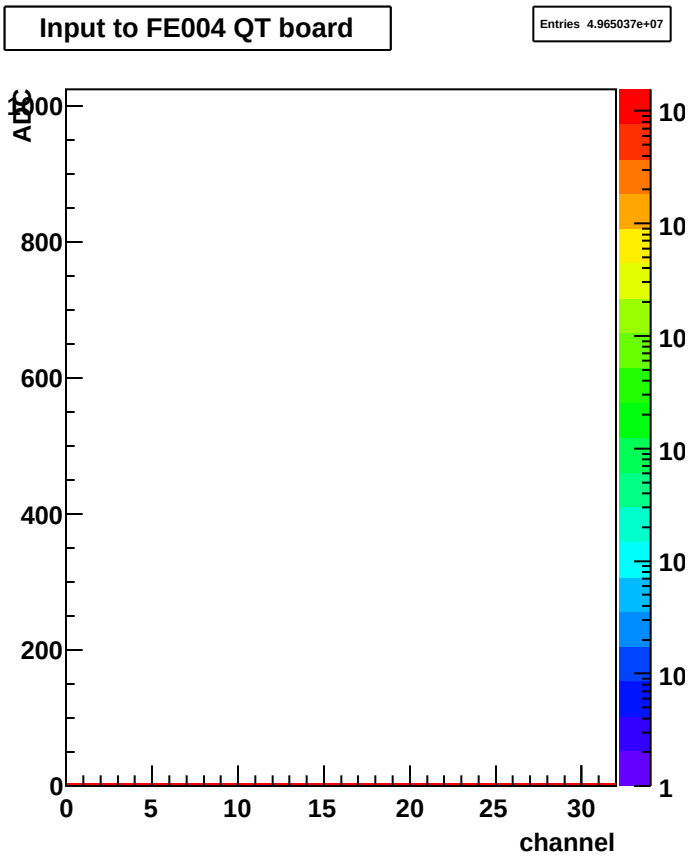

Input to FPD L2 DSM
Entries 1075
HT bits - simulated
356
9
2
462
235
7
1
3
SMALL
LARGE-S
LARGE-N

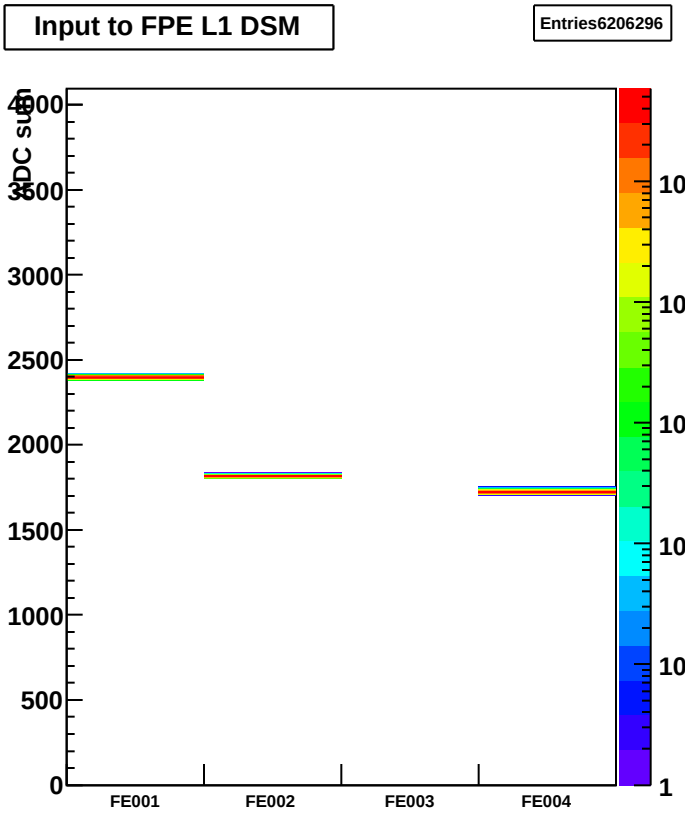

Input to FPD L2 DSM
Entries 9309444
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

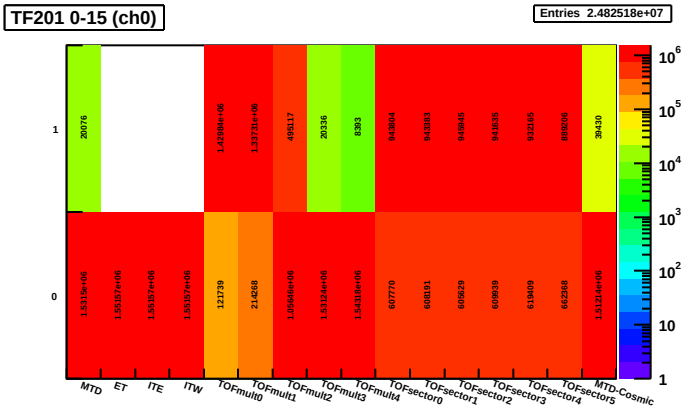

Input to FE001 QT board
Entries 4.965037e+07
ADC
channel

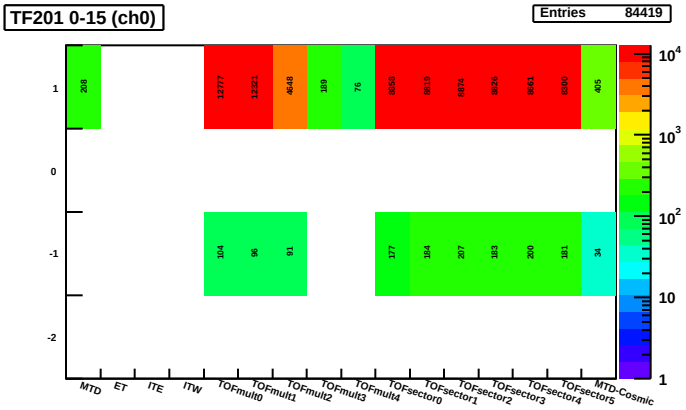

Input to FE002 QT board
Entries 4.965037e+07
ADC
channel

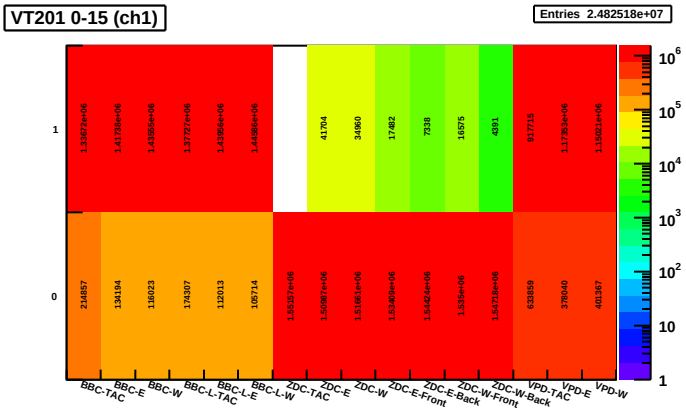

Input to FE003 QT board
Entries 4.965037e+07
ADC
channel

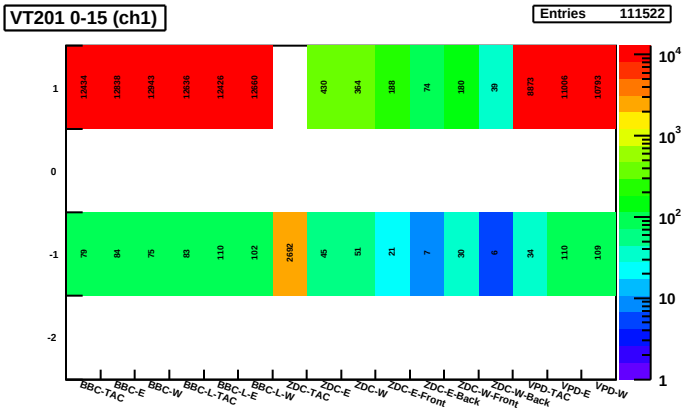

Input to FE004 QT board
Entries 4.965037e+07
ADC
channel

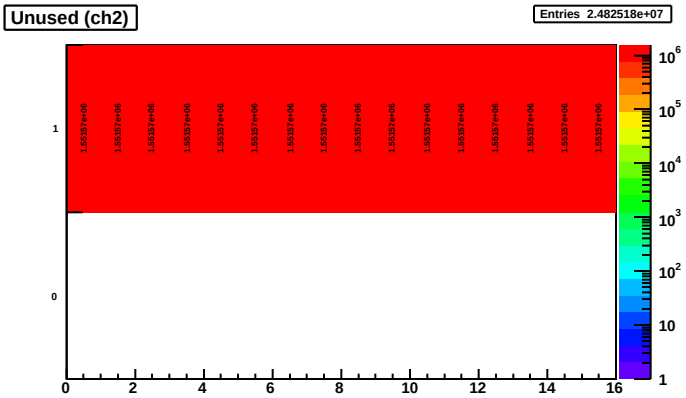

Input to FPE L1 DSM
Entries6206296
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.482518e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 84419
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 2.482518e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 111522
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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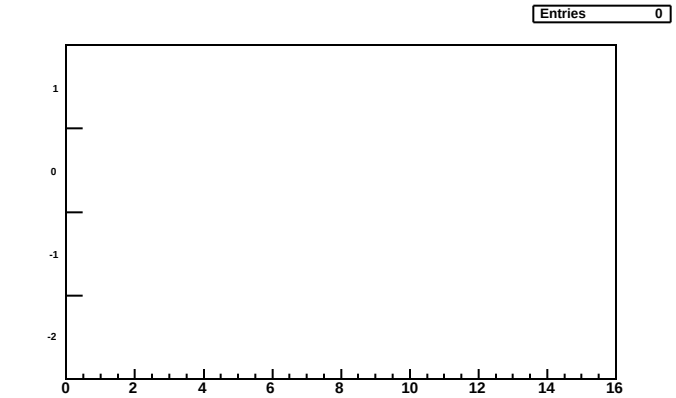
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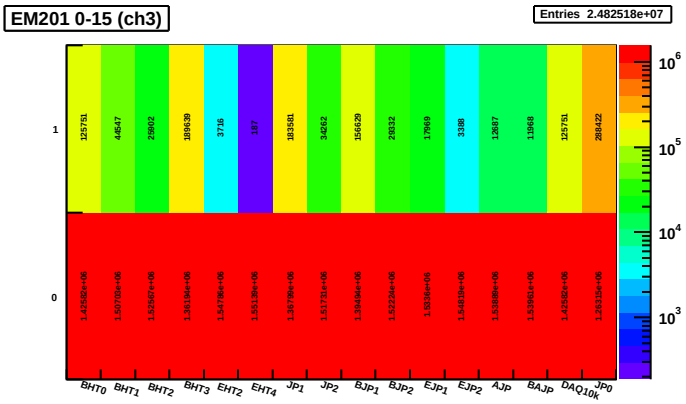
EM201 0-15 (ch3)
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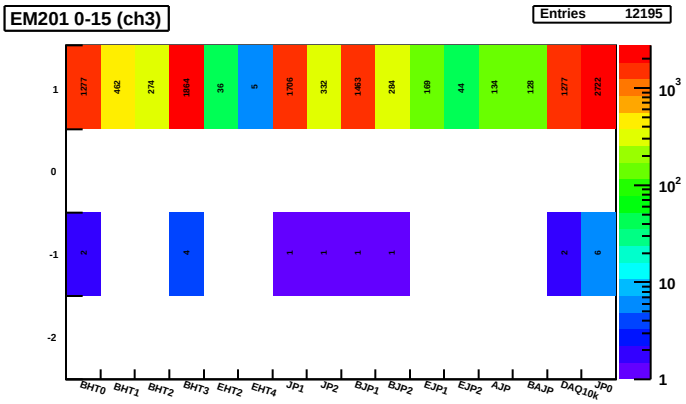
EM201 0-15 (ch3)
Entries 12195
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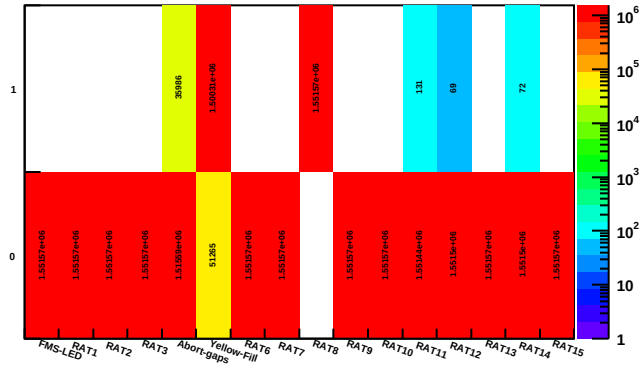
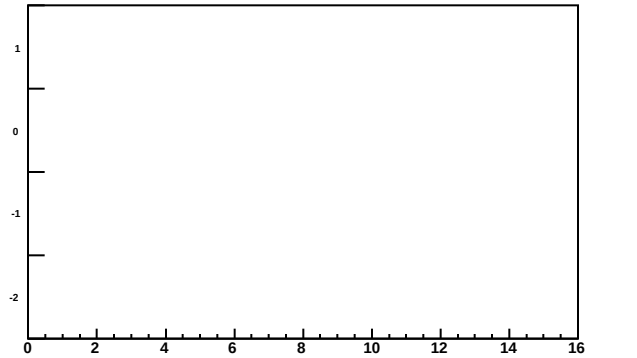
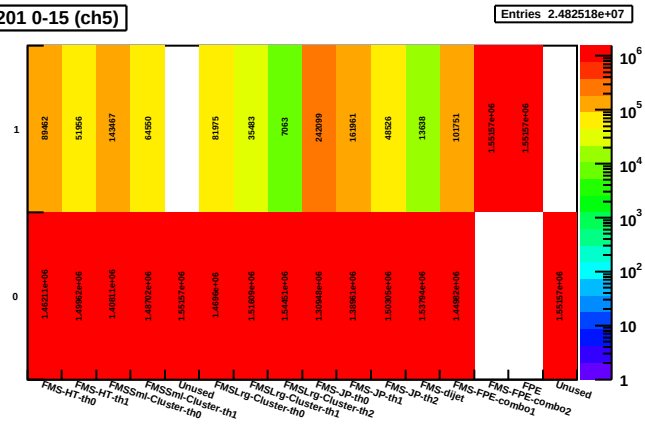
RAT board (ch4)

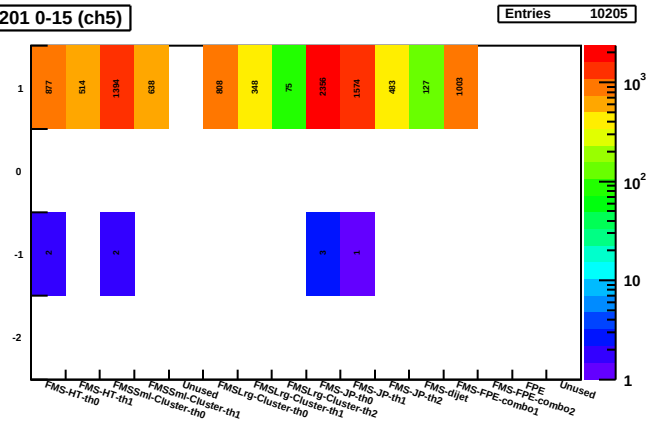




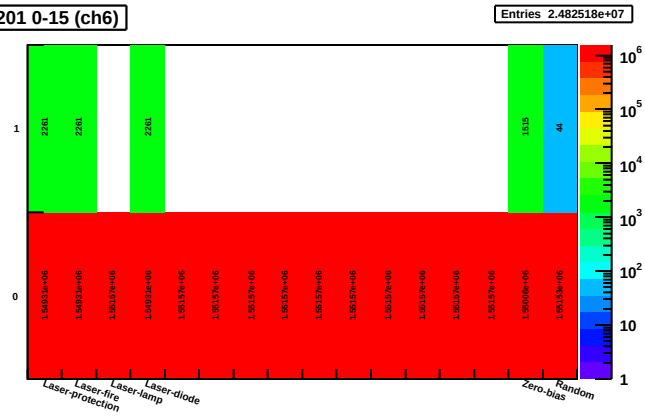
FP201 0-15 (ch5)

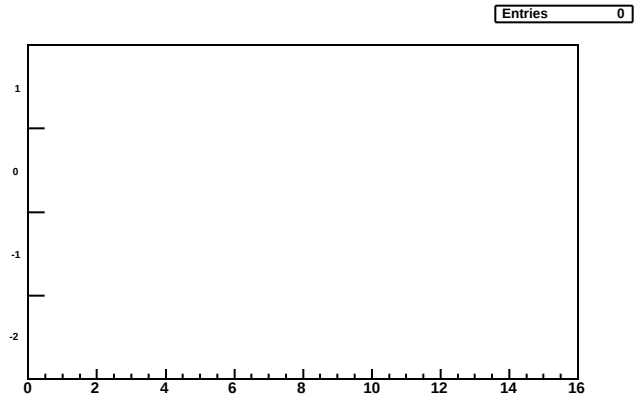
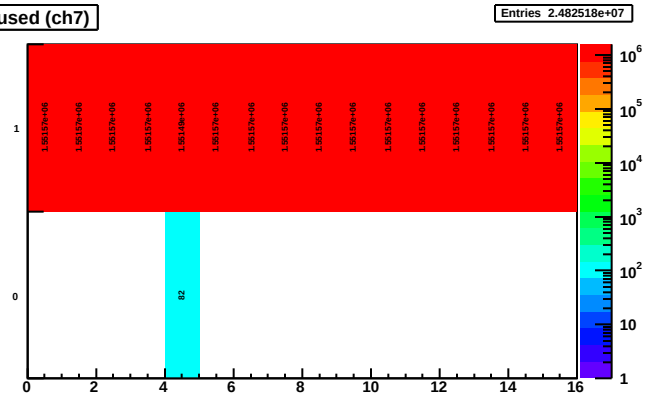
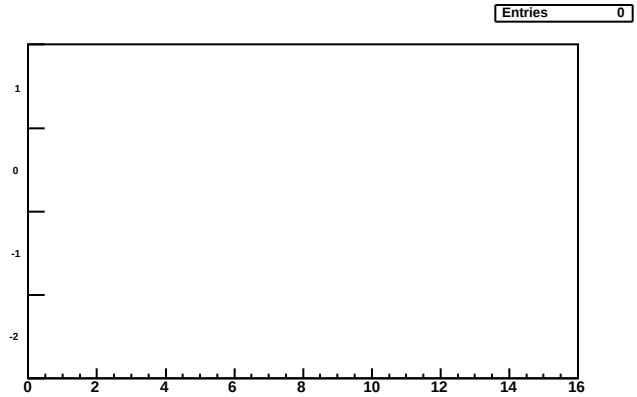


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)