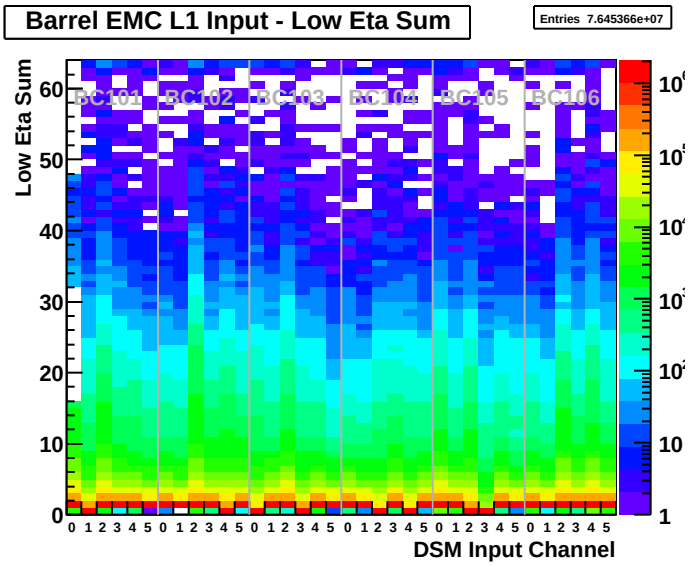
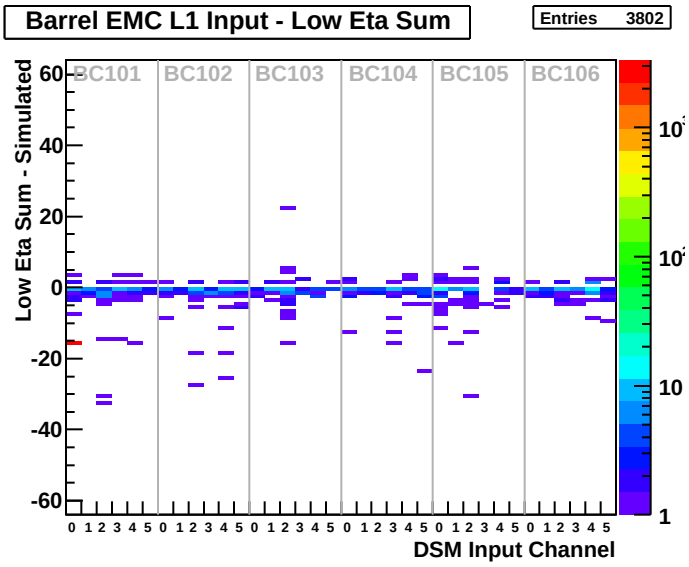
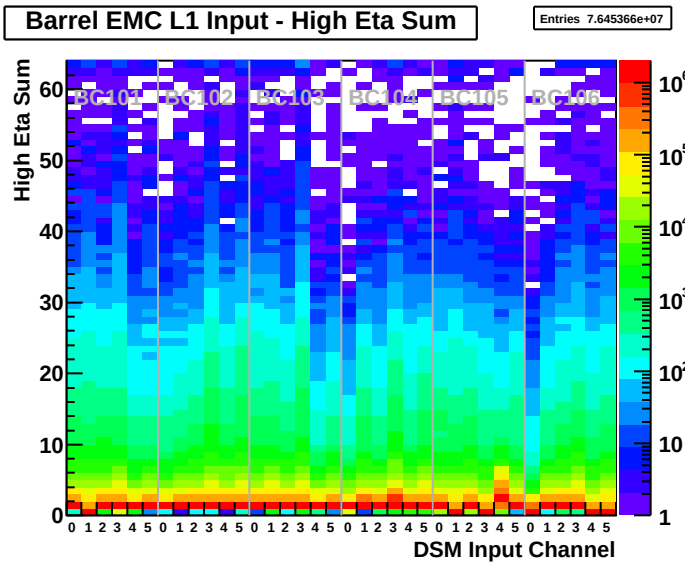
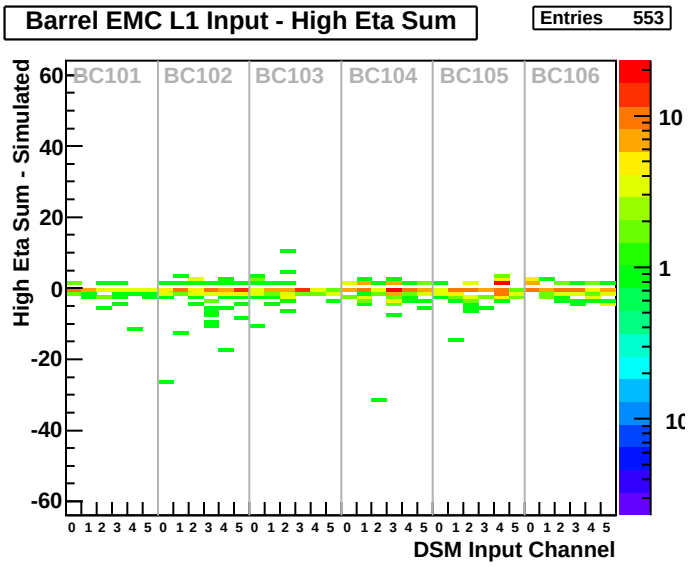
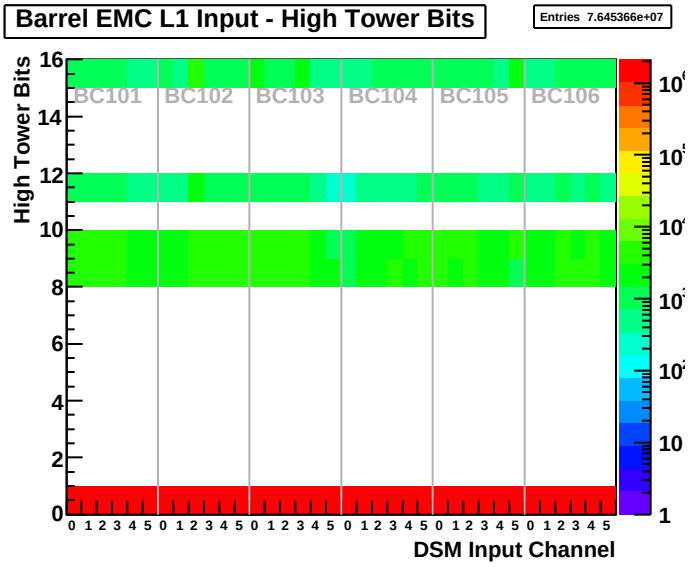
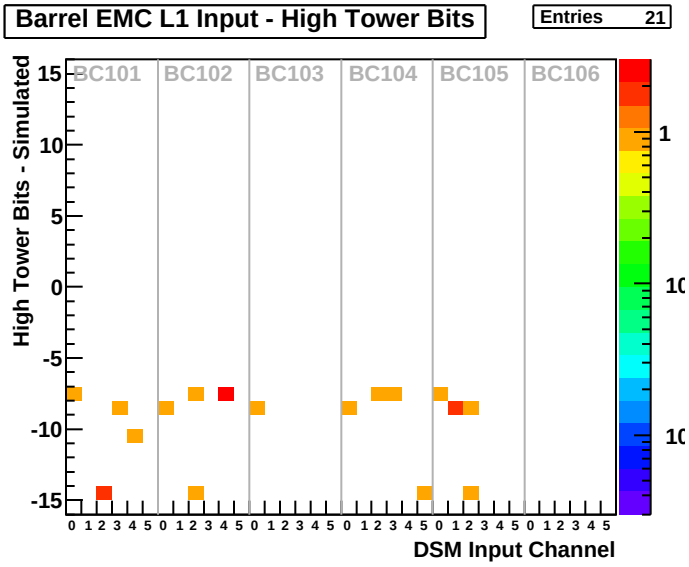

Barrel EMC L1 Input - Low Eta Sum
Entries 7.645366e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

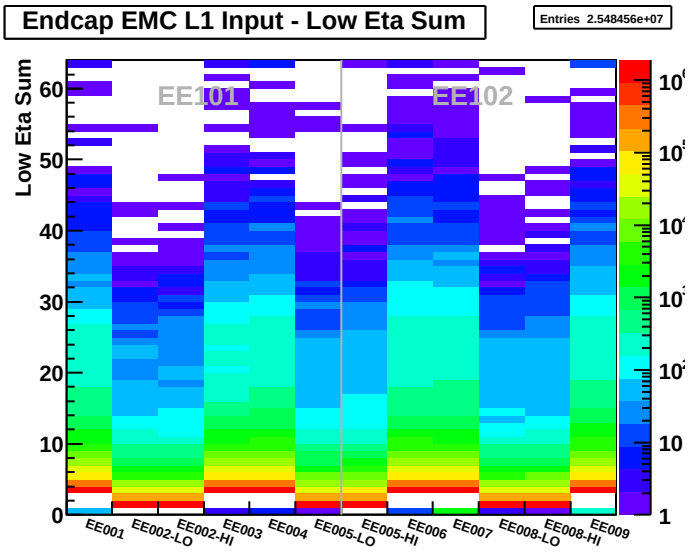

Barrel EMC L1 Input - Low Eta Sum
Entries 3802
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

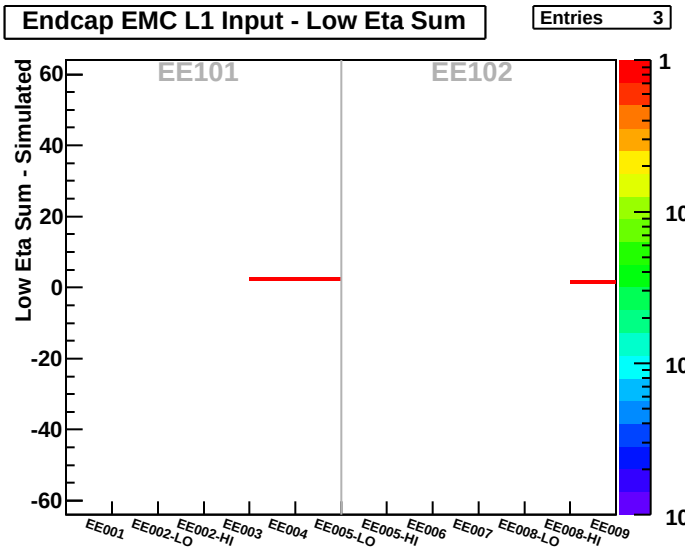

Barrel EMC L1 Input - High Eta Sum
Entries 7.645366e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

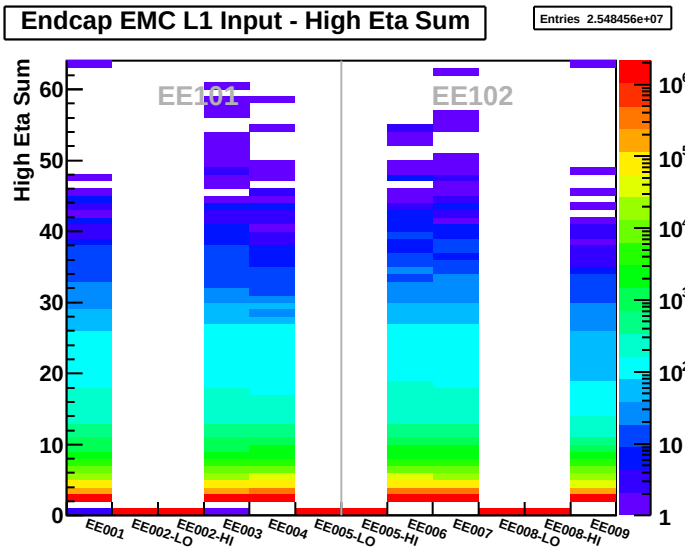

Barrel EMC L1 Input - High Eta Sum
Entries 553
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

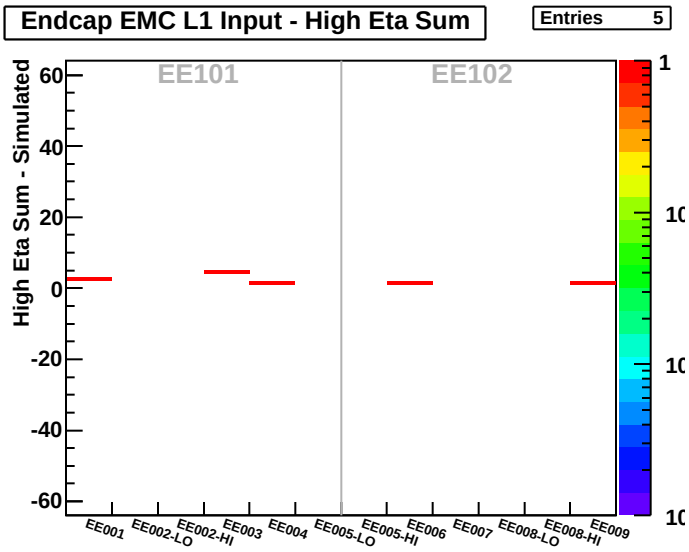

Barrel EMC L1 Input - High Tower Bits
Entries 7.645366e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

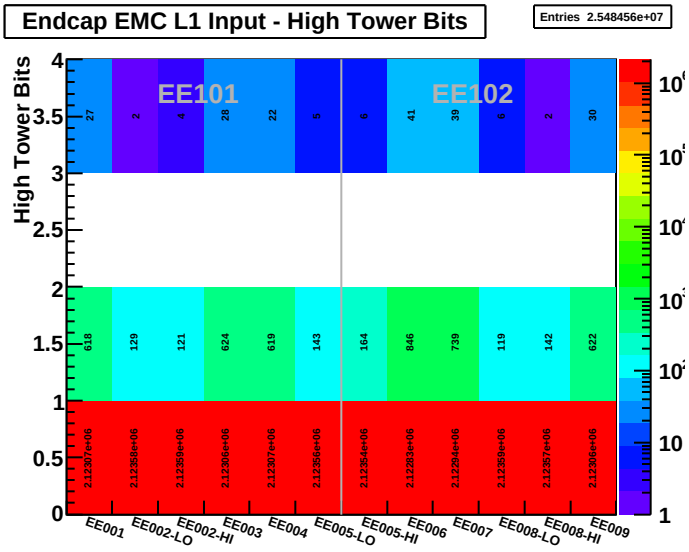

Barrel EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

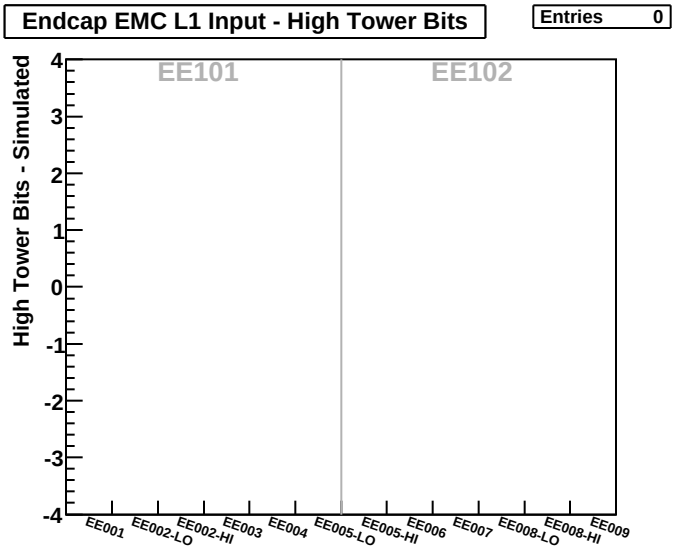

Endcap EMC L1 Input - Low Eta Sum
Entries 2.548456e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

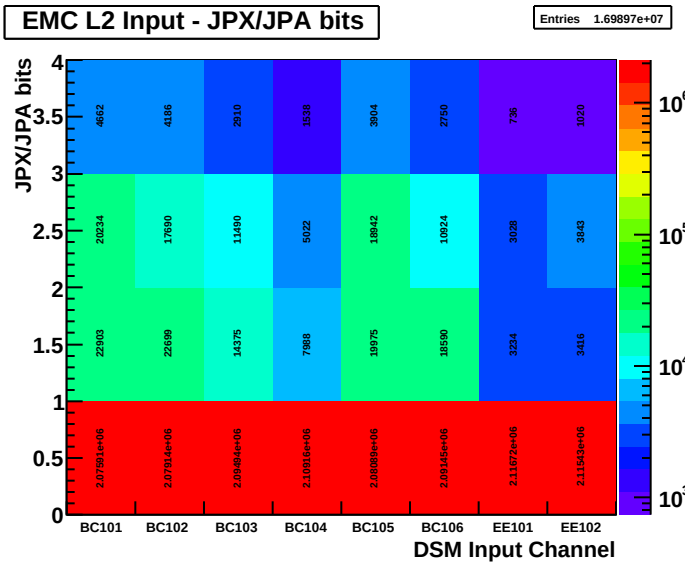

Endcap EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

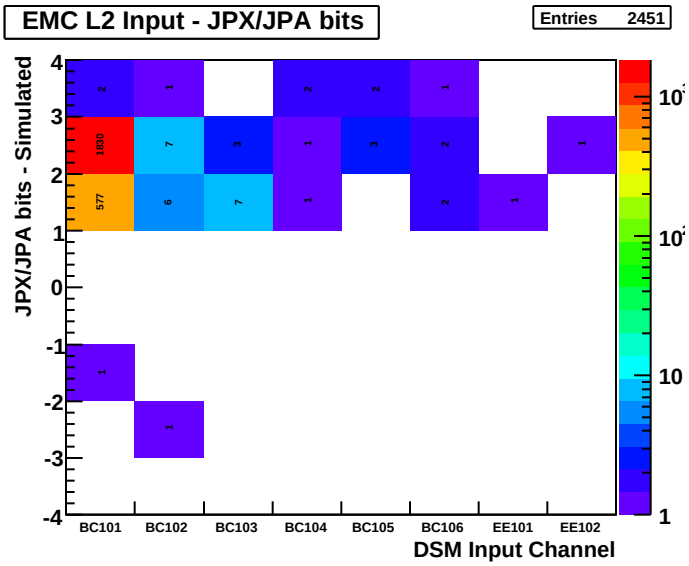

Endcap EMC L1 Input - High Eta Sum
Entries 2.548456e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

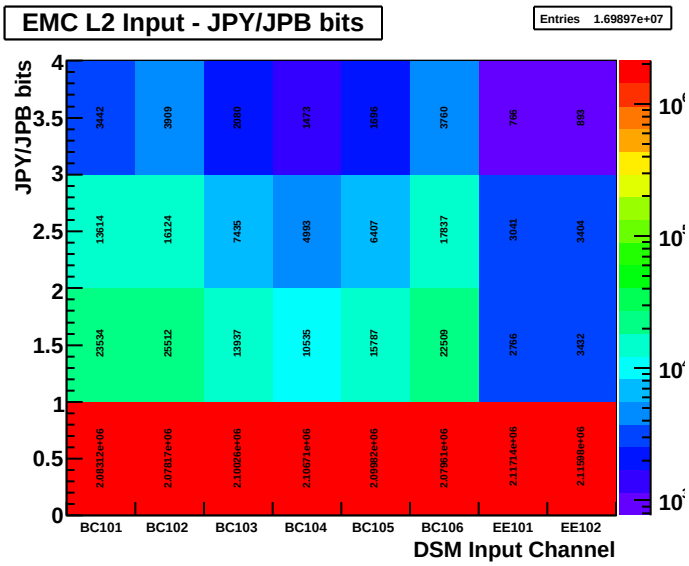

Endcap EMC L1 Input - High Eta Sum
Entries 5
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

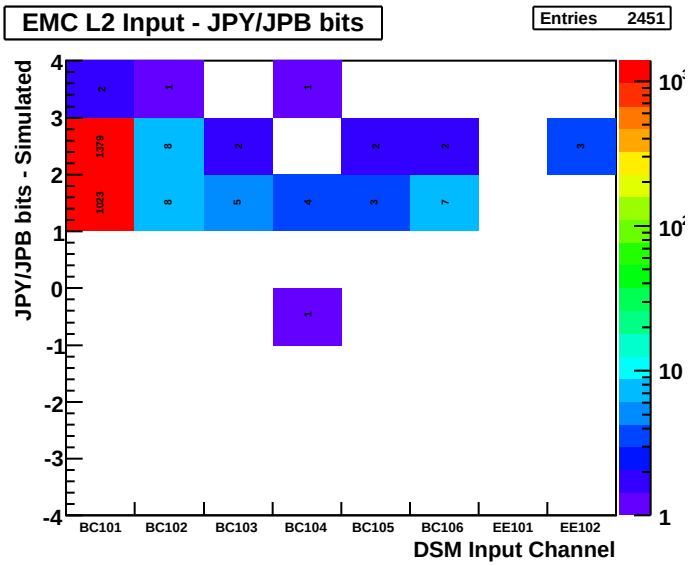

Endcap EMC L1 Input - High Tower Bits
Entries 2.548456e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

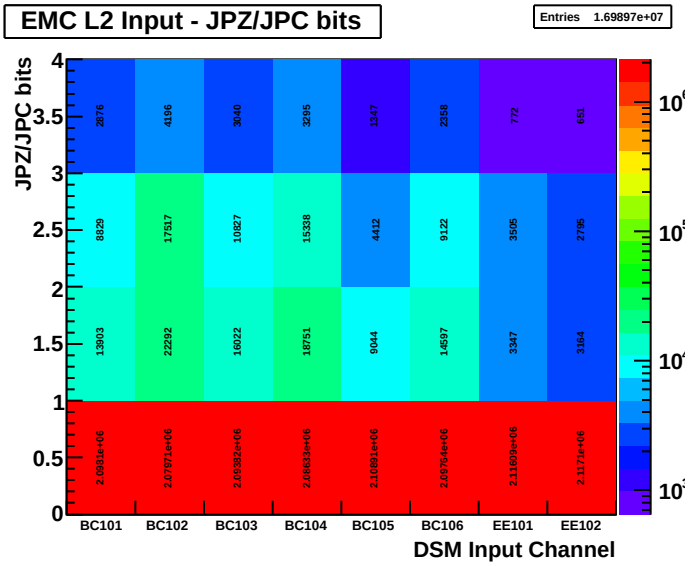

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

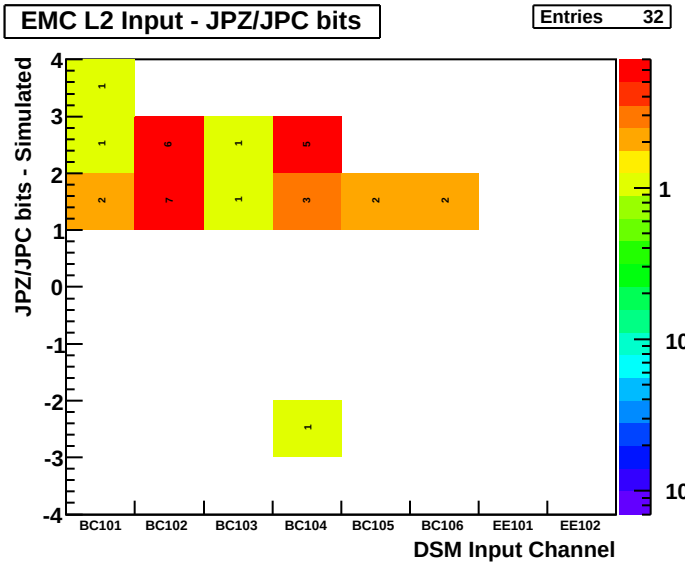

EMC L2 Input - JPX/JPA bits
Entries 1.69897e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

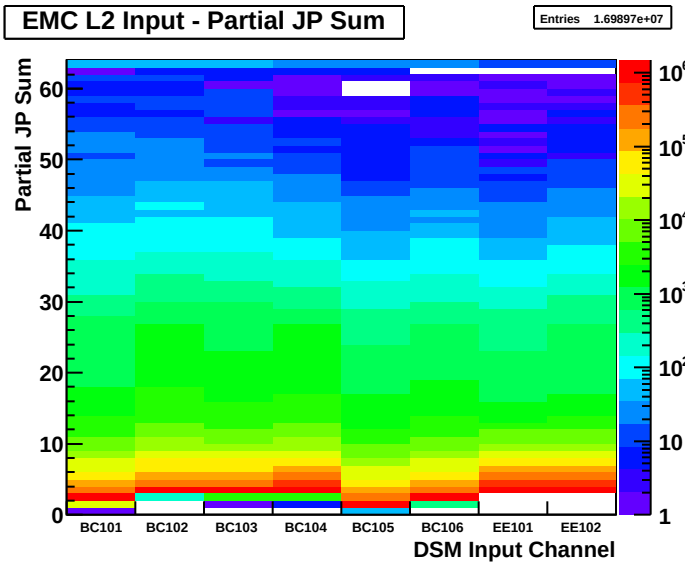

EMC L2 Input - JPX/JPA bits
Entries 2451
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

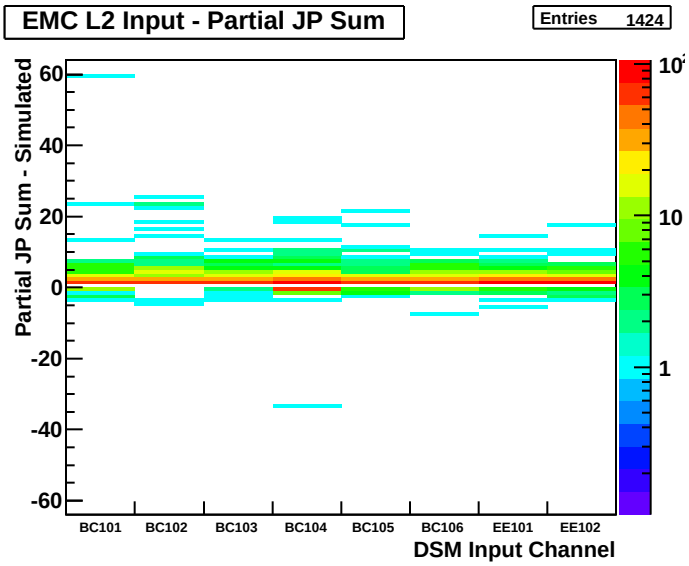

EMC L2 Input - JPY/JPB bits
Entries 1.69897e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

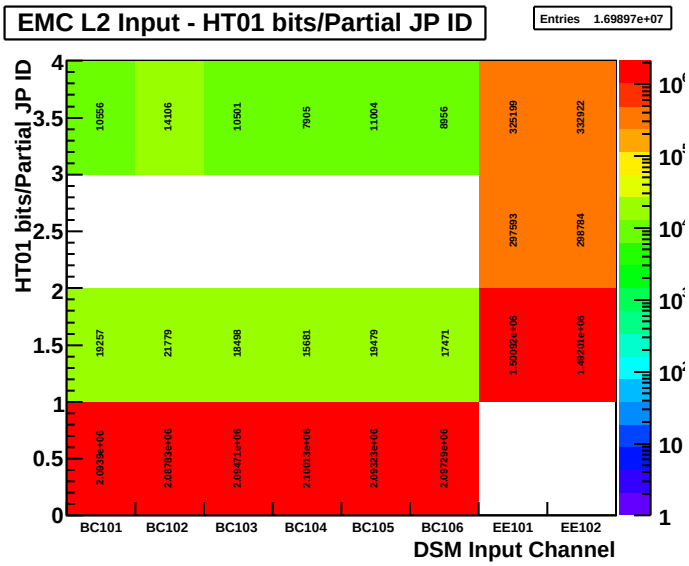

EMC L2 Input - JPY/JPB bits
Entries 2451
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

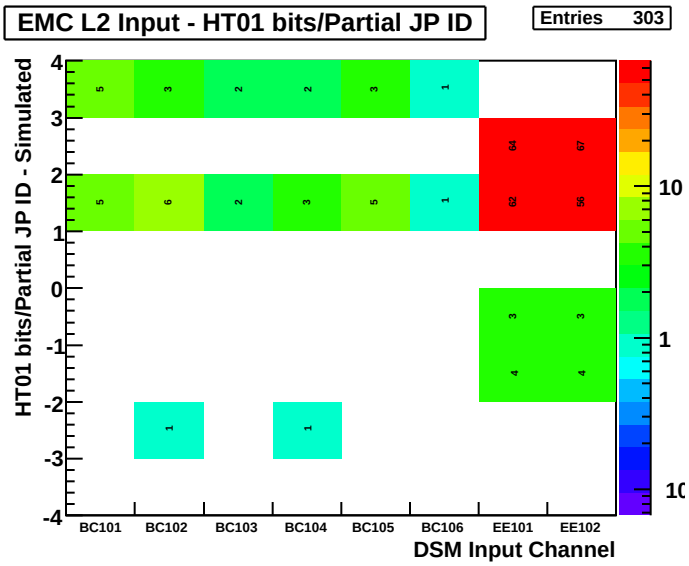

EMC L2 Input - JPZ/JPC bits
Entries 1.69897e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

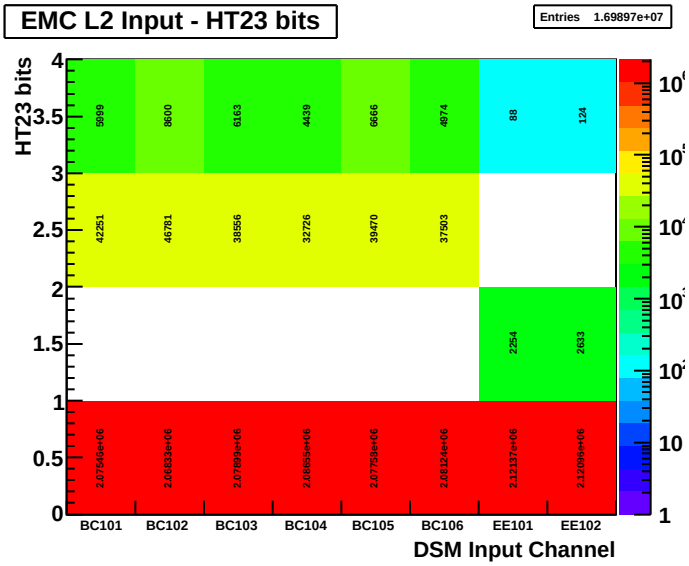

EMC L2 Input - JPZ/JPC bits
Entries 32
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

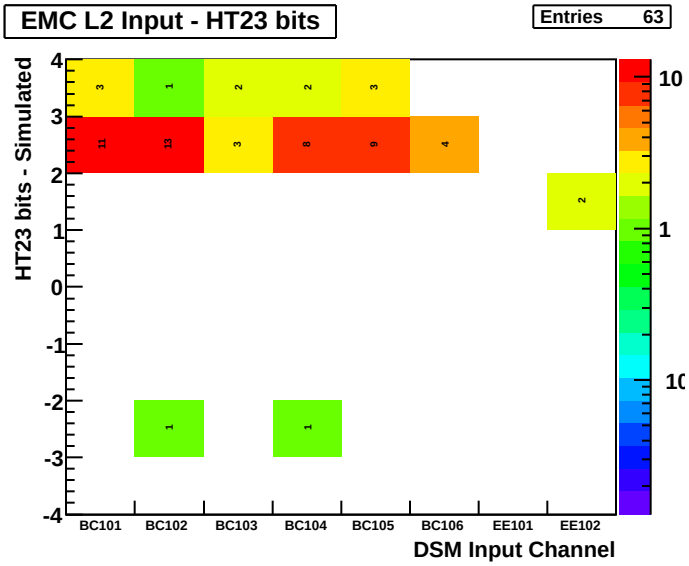

EMC L2 Input - Partial JP Sum
Entries 1.69897e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

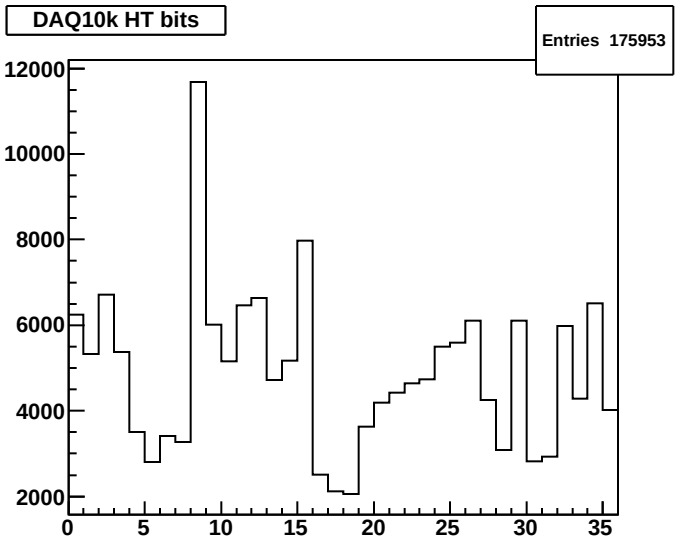

EMC L2 Input - Partial JP Sum
Entries 1424
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

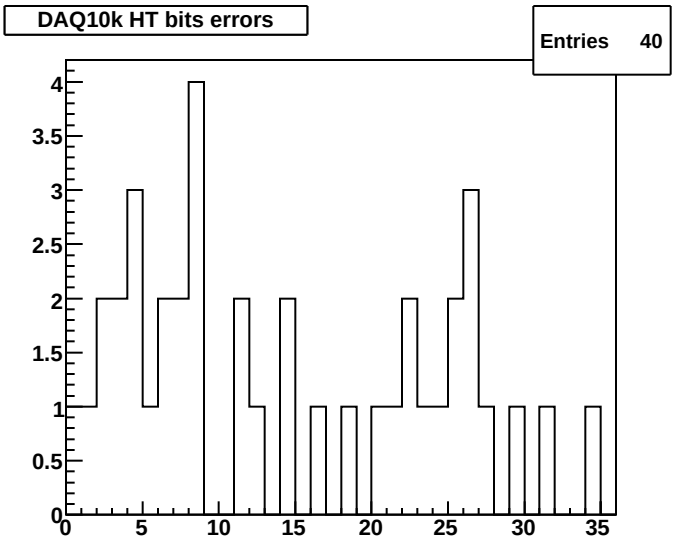

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.69897e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

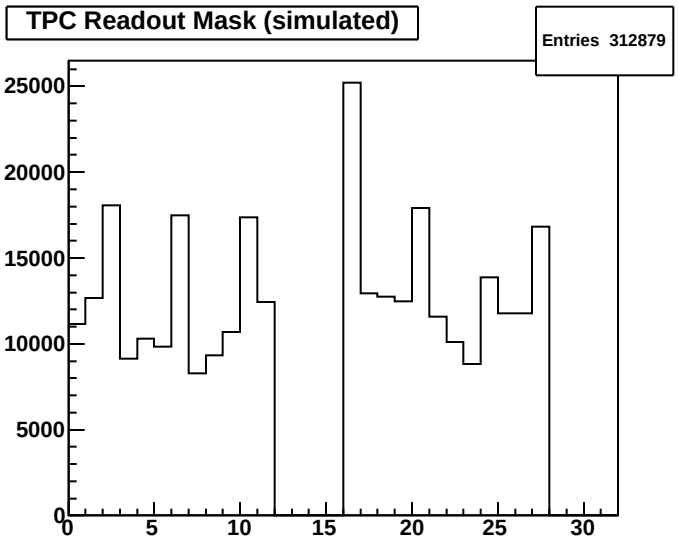

EMC L2 Input - HT01 bits/Partial JP ID
Entries 303
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

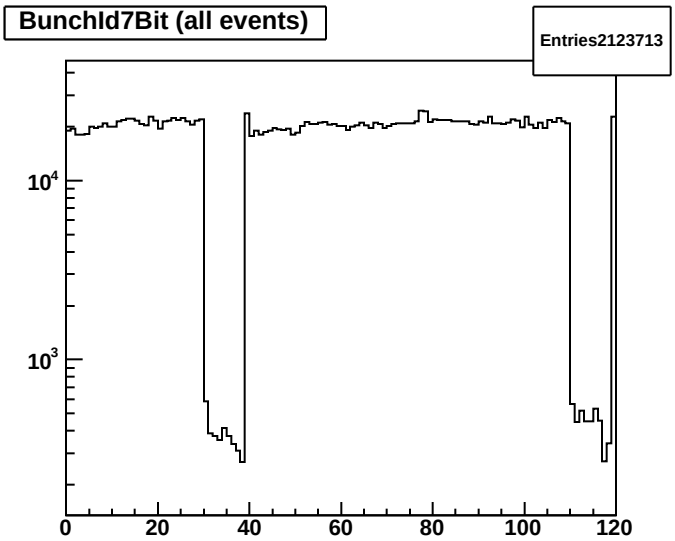

EMC L2 Input - HT23 bits
Entries 1.69897e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

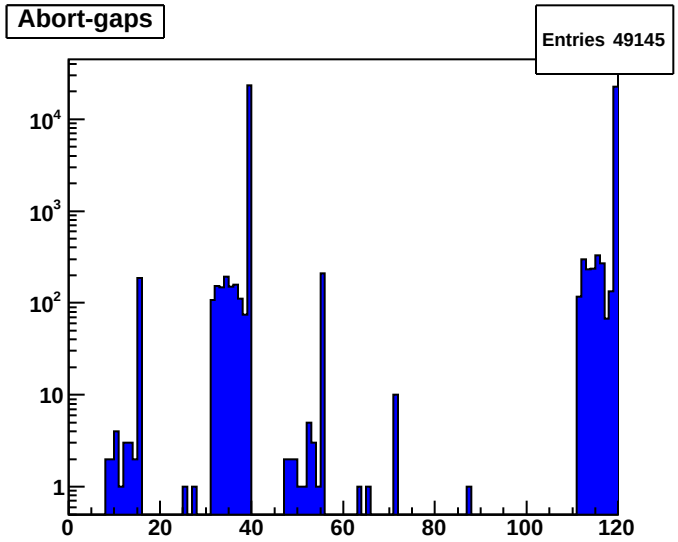

EMC L2 Input - HT23 bits
Entries 63
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

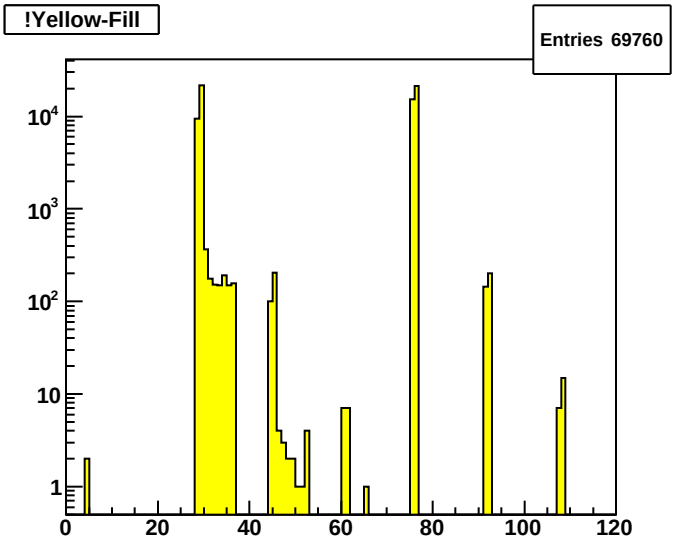

DAQ10k HT bits
Entries 175953

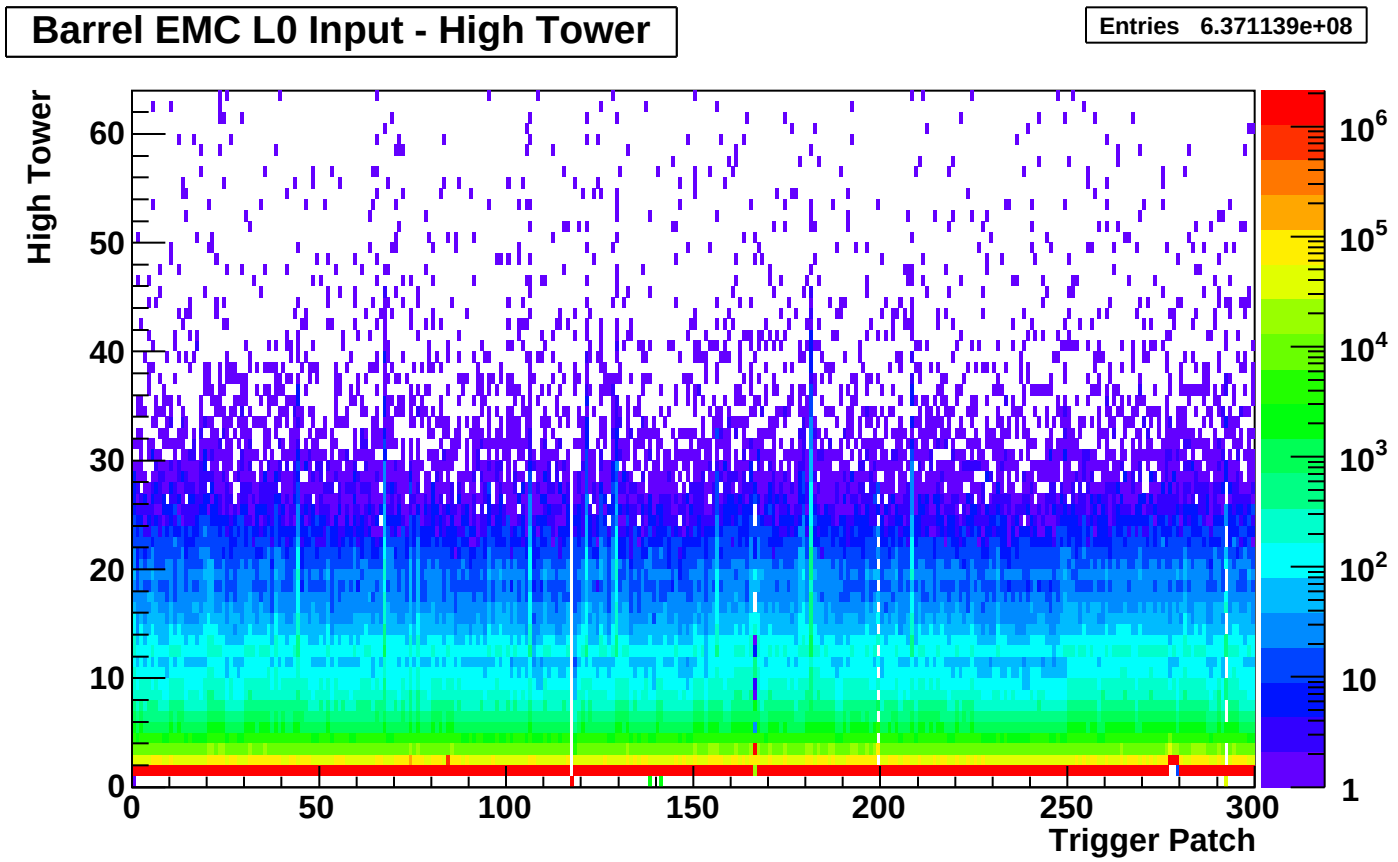

DAQ10k HT bits errors
Entries 40

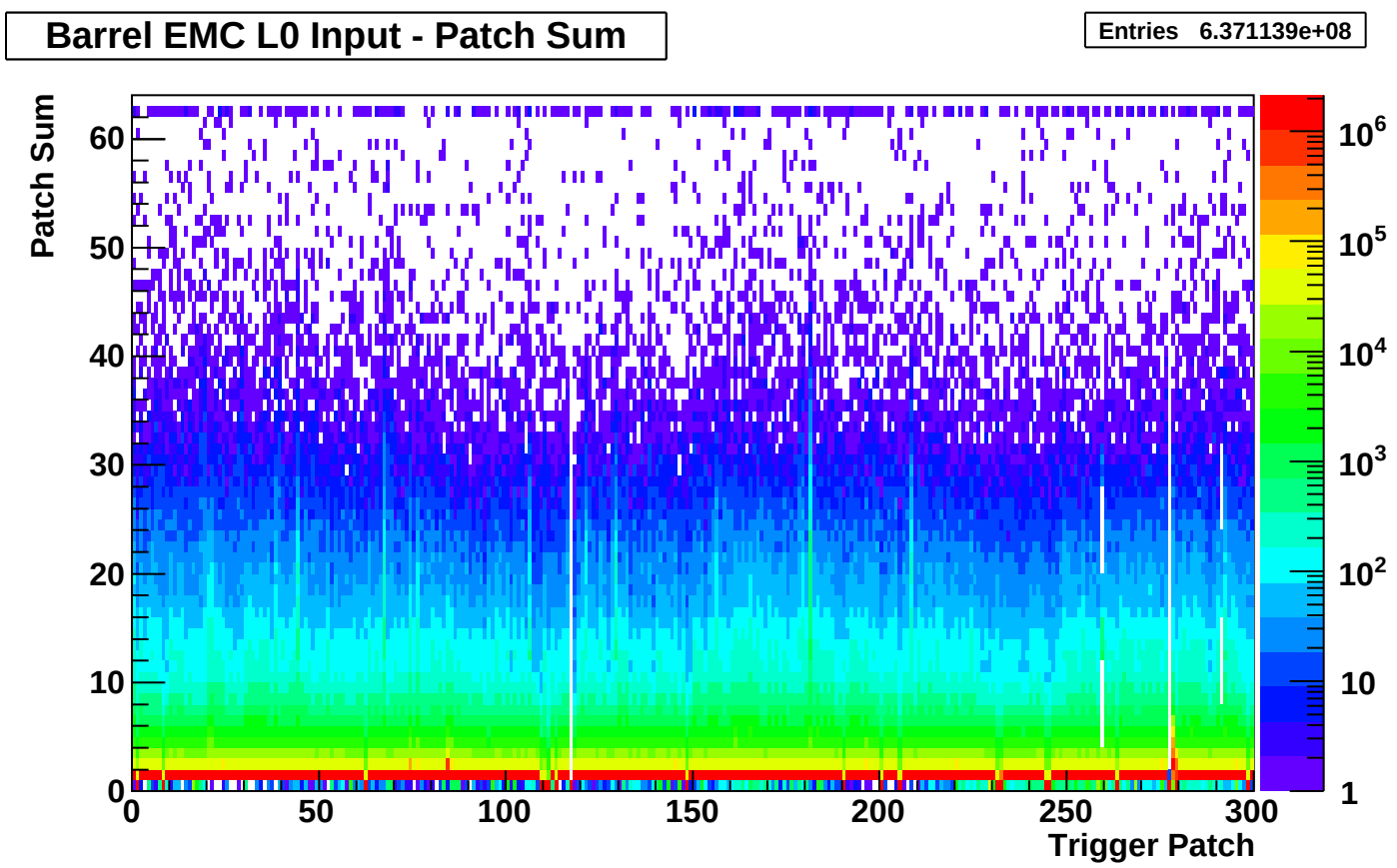

TPC Readout Mask (simulated)
Entries 312879

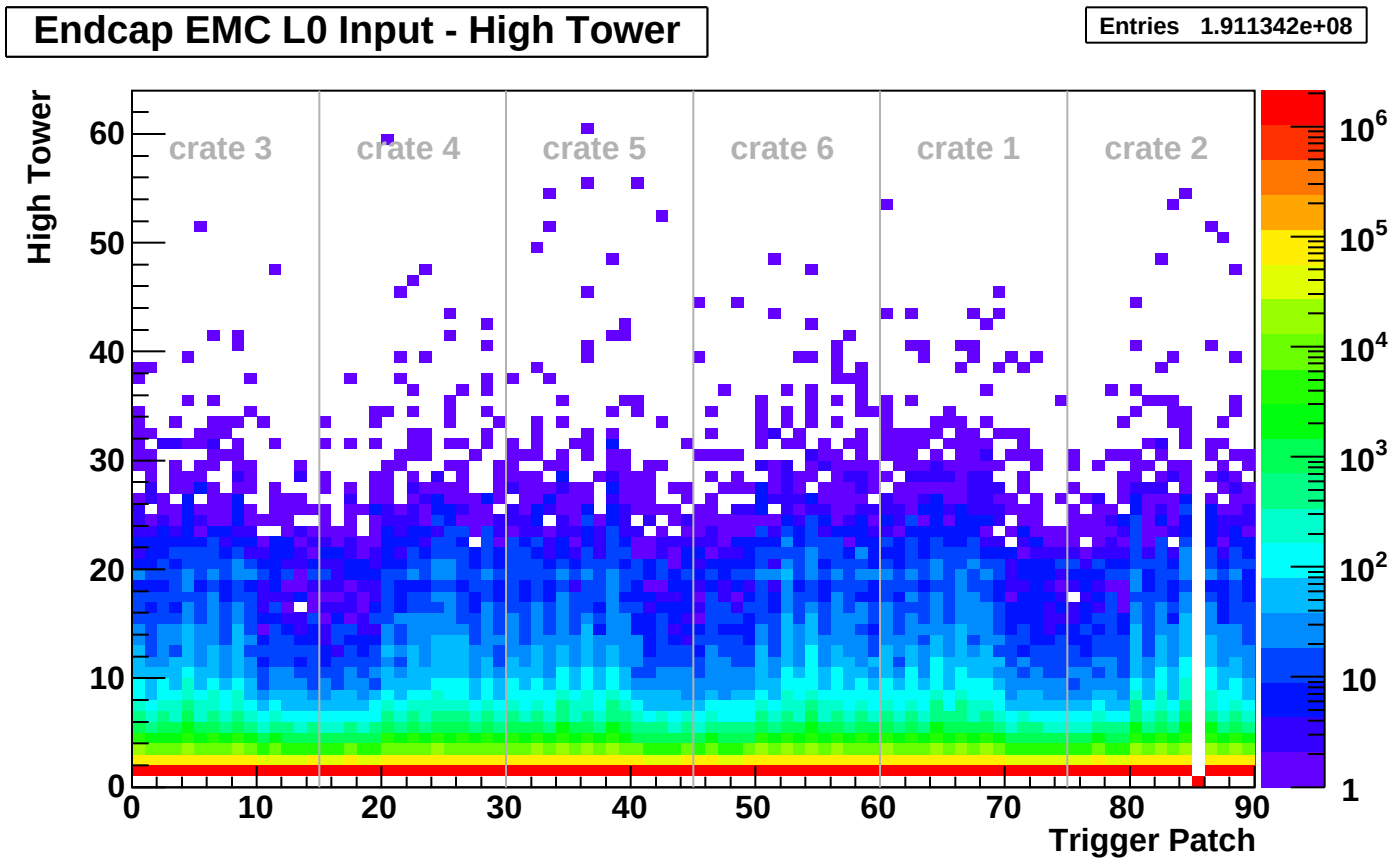

BunchId7Bit (all events)
Entries2123713

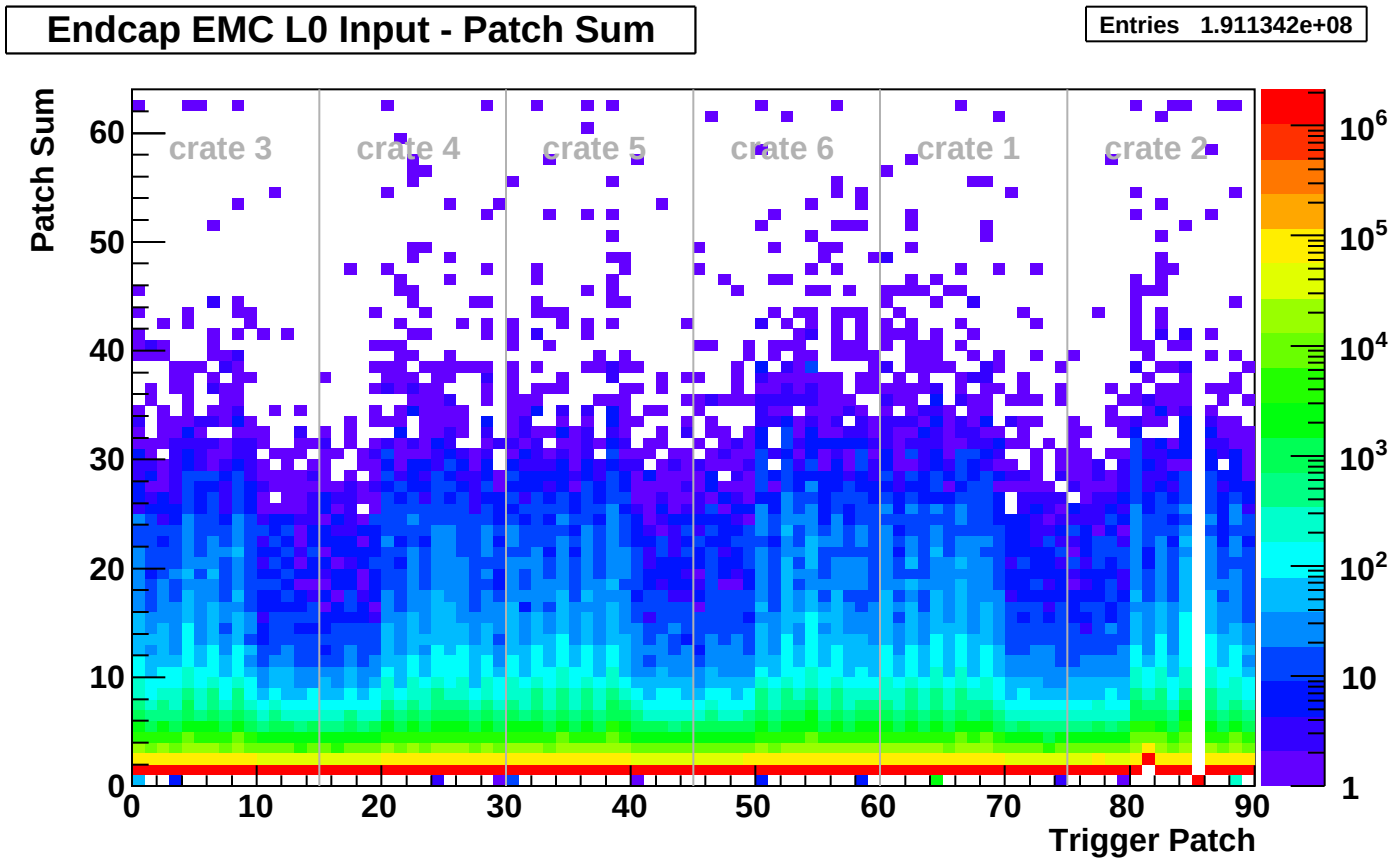

Abort-gaps
Entries 49145

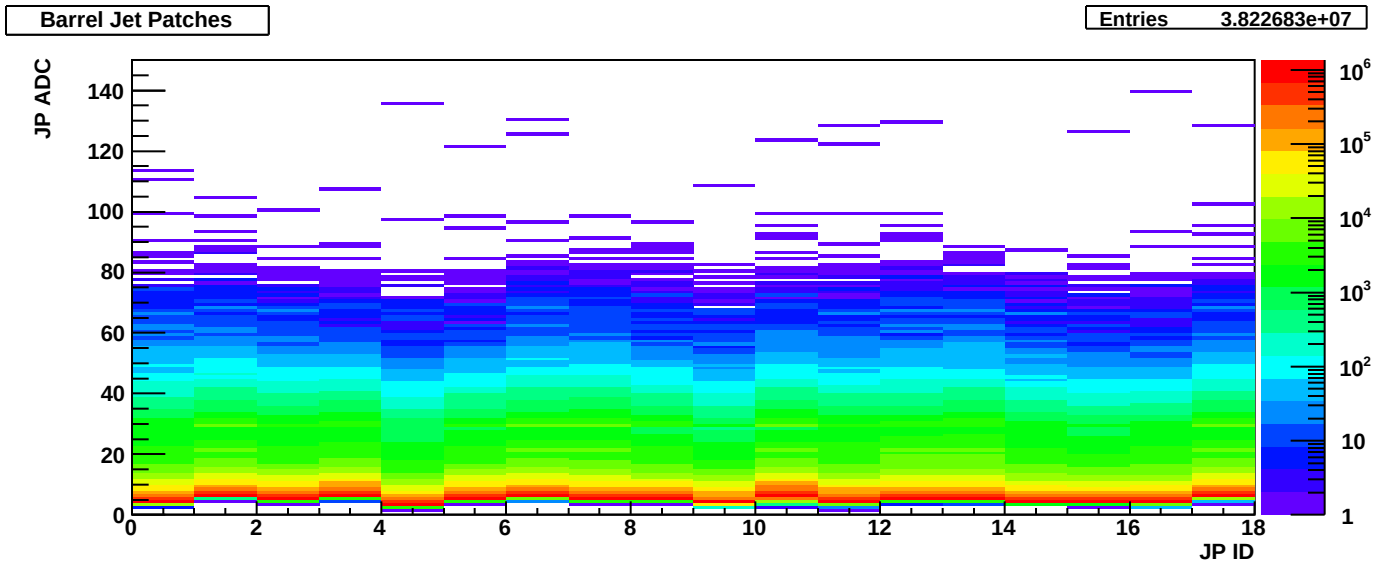

!Yellow-Fill
Entries 69760

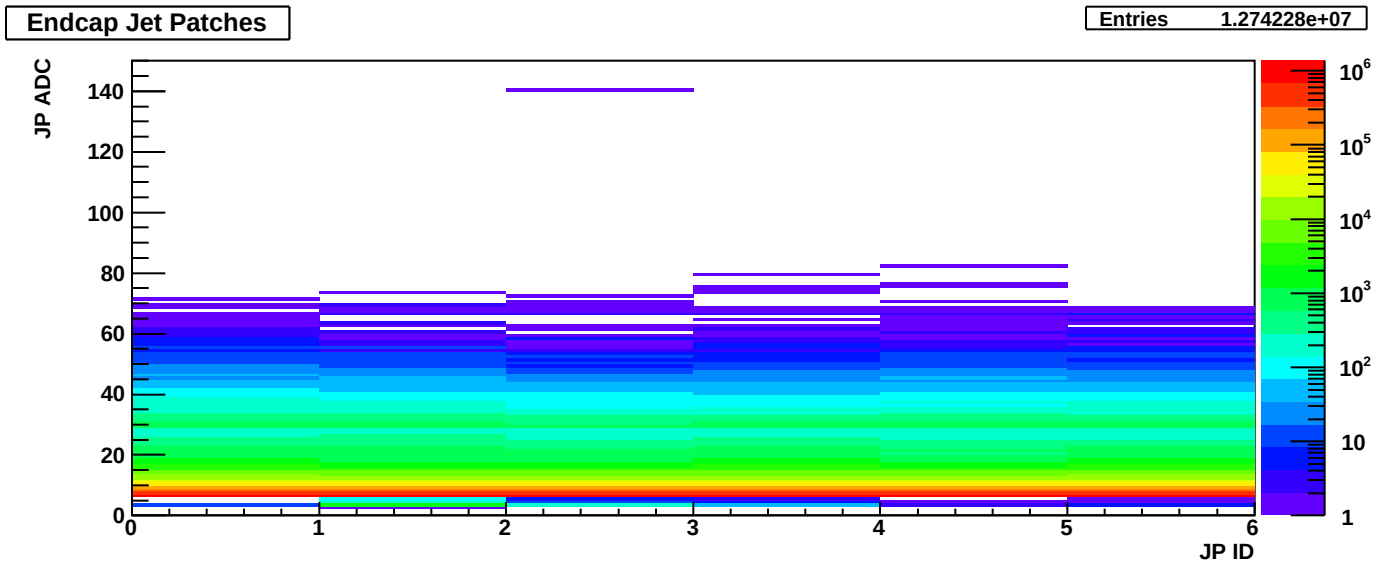

Barrel EMC L0 Input - High Tower
Entries 6.371139e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

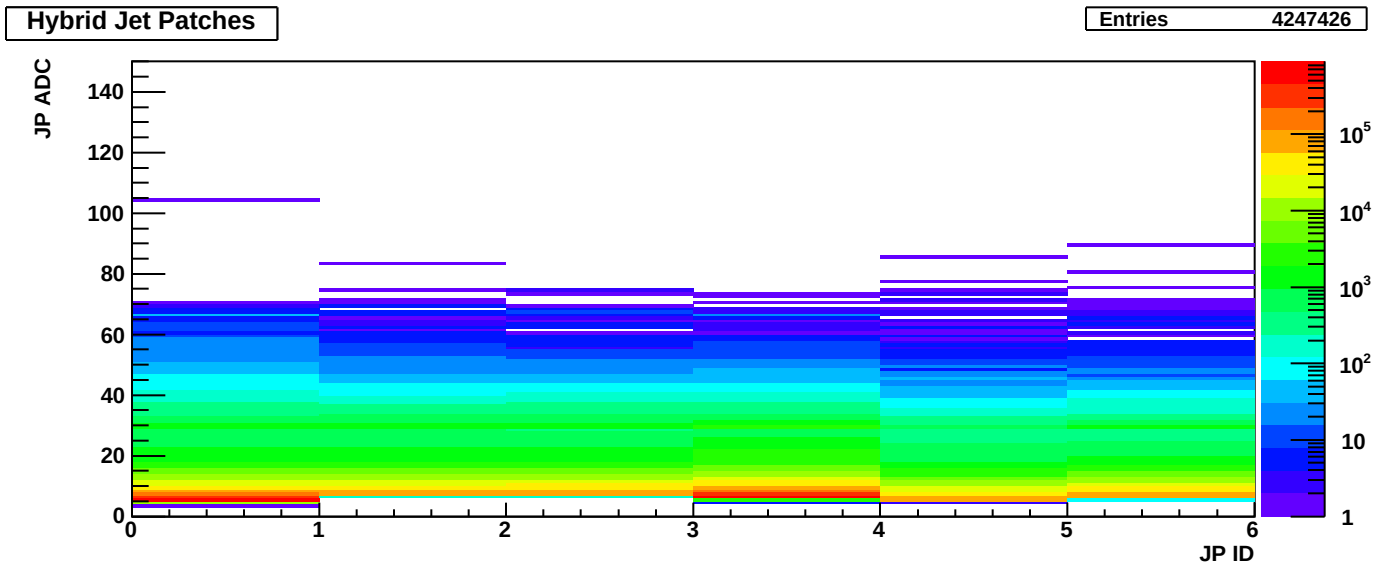

Barrel EMC L0 Input - Patch Sum
Entries 6.371139e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

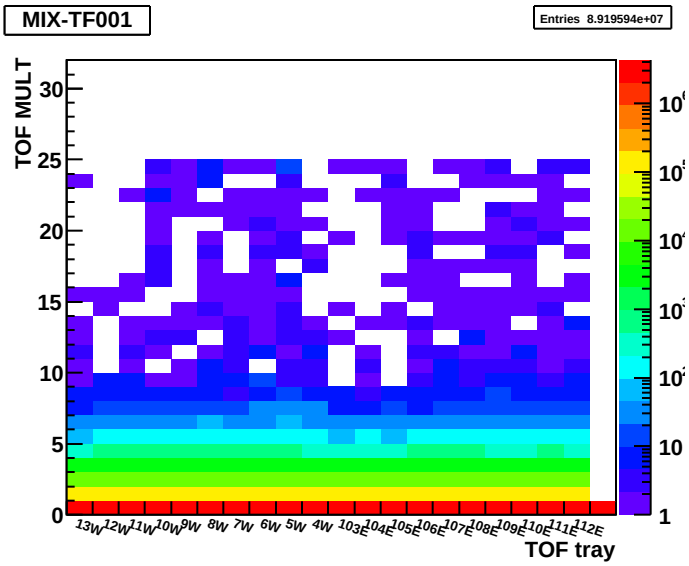

Endcap EMC L0 Input - High Tower
Entries 1.911342e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

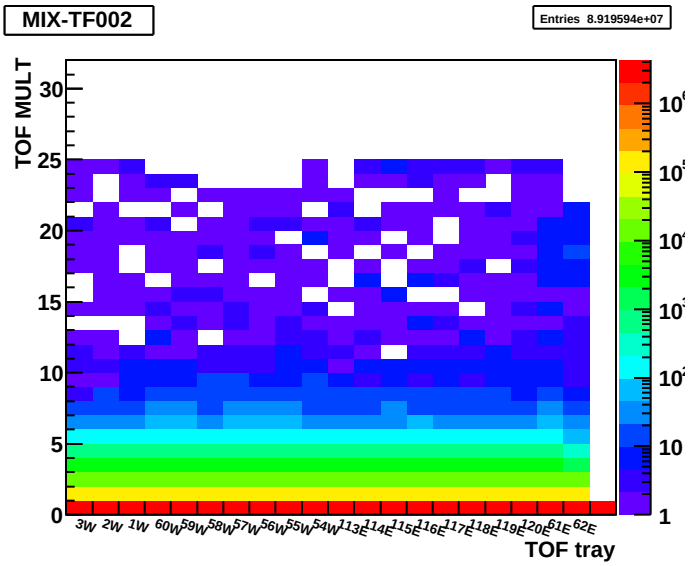

Endcap EMC L0 Input - Patch Sum
Entries 1.911342e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

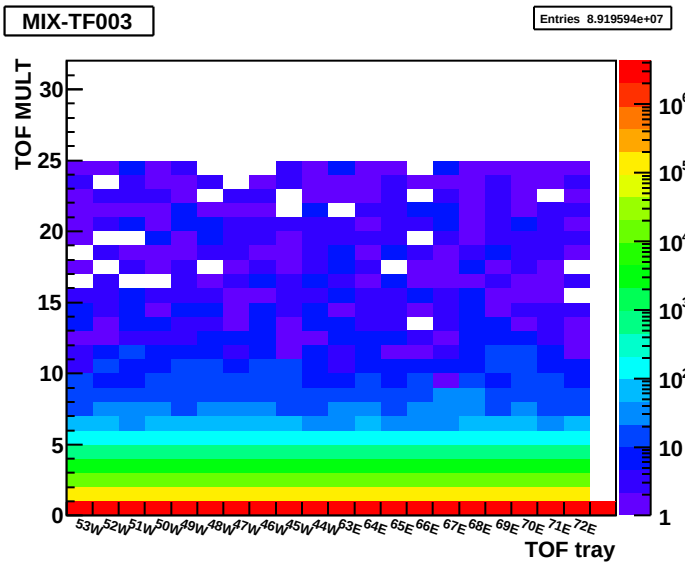

Barrel Jet Patches
Entries 3.822683e+07
JP ADC
JP ID

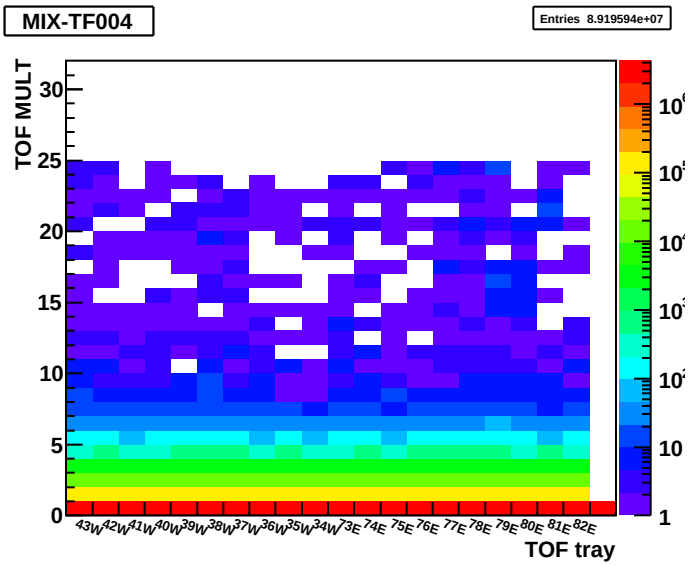

Endcap Jet Patches
Entries 1.274228e+07
JP ADC
JP ID

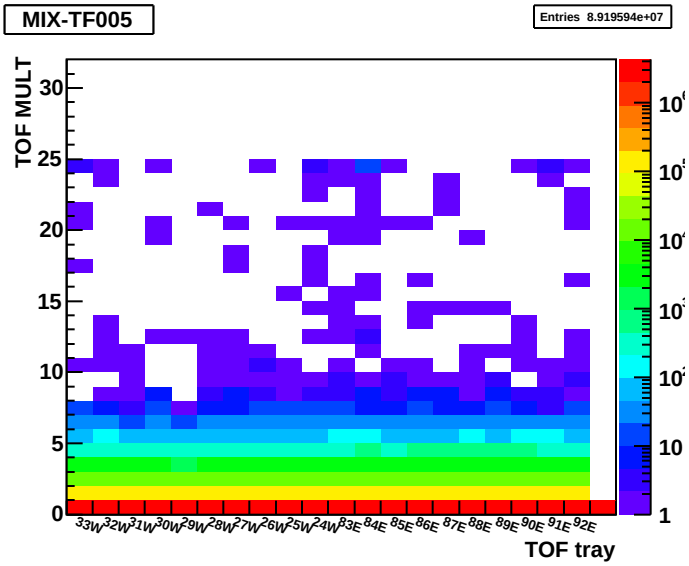

Hybrid Jet Patches
Entries 4247426
JP ADC
JP ID

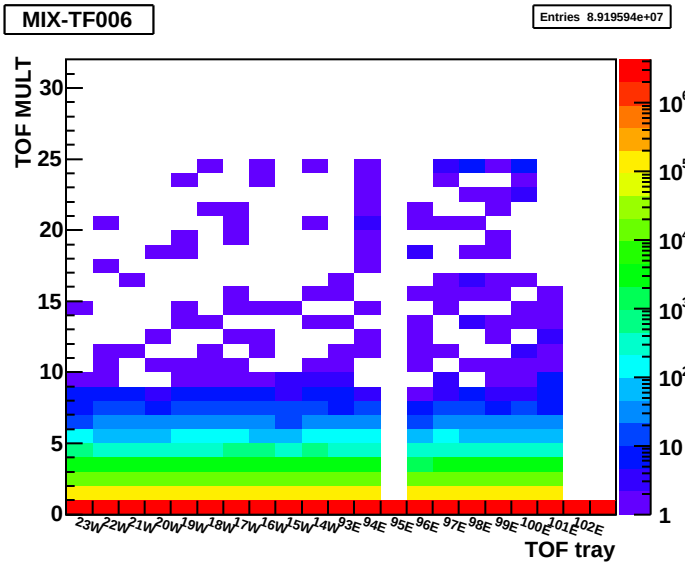

MIX-TF001
Entries 8.919594e+07
TOF MULT
TOF tray

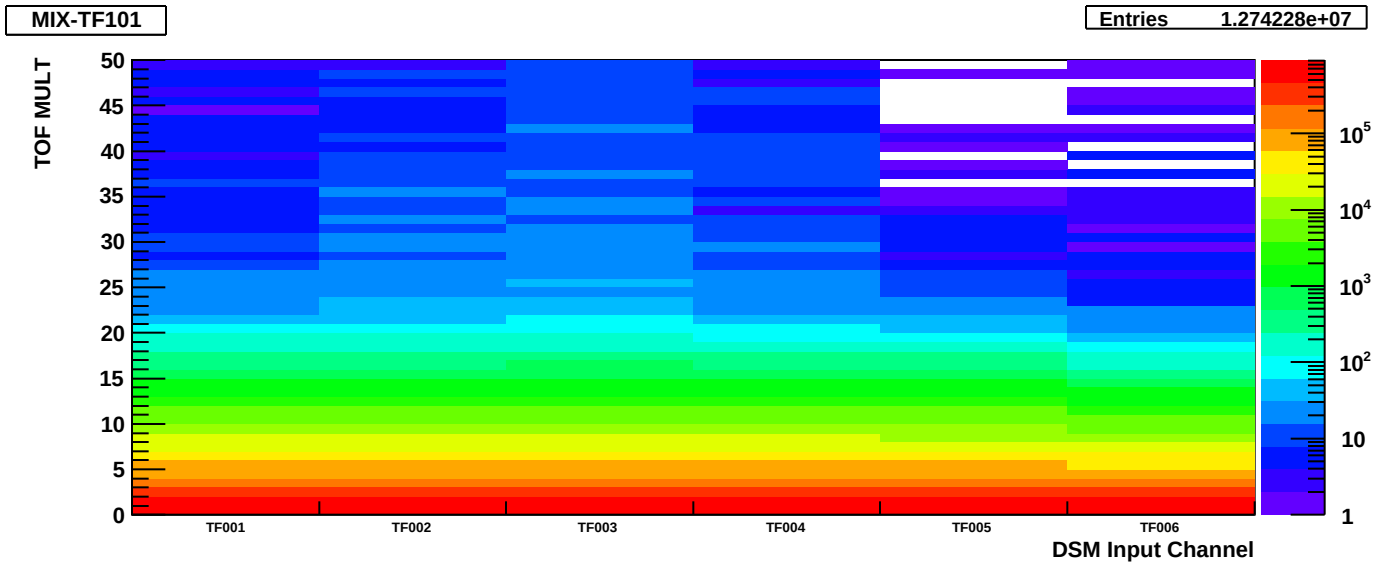

MIX-TF002
Entries 8.919594e+07
TOF MULT
TOF tray

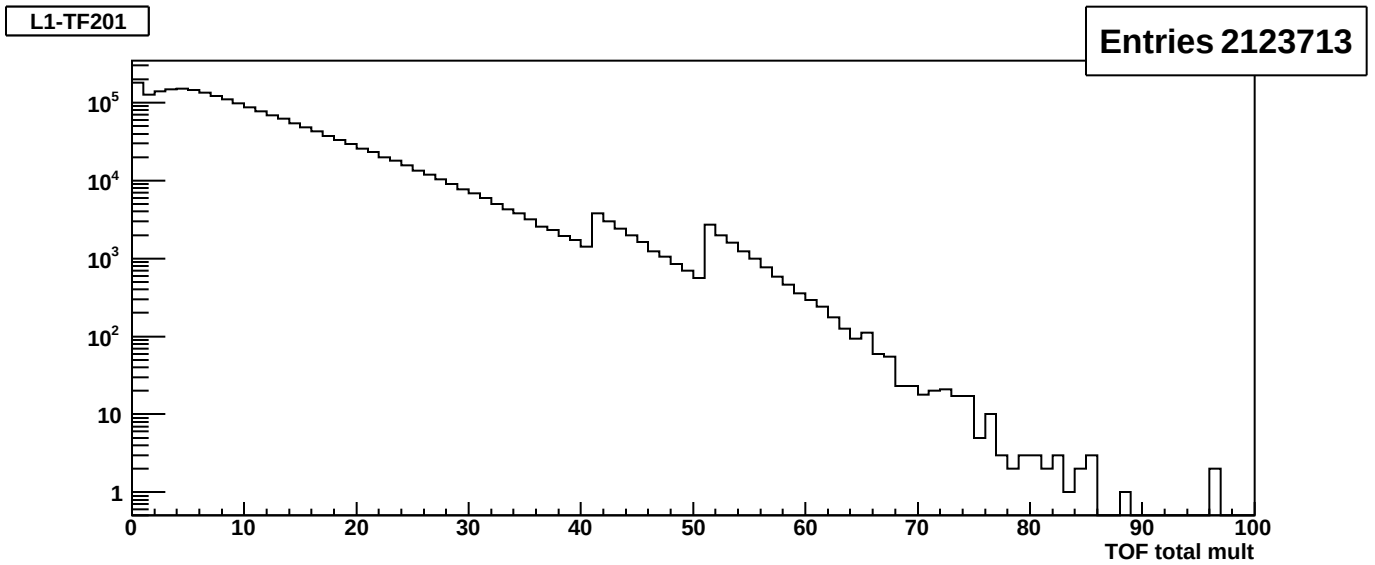

MIX-TF003
Entries 8.919594e+07
TOF MULT
TOF tray

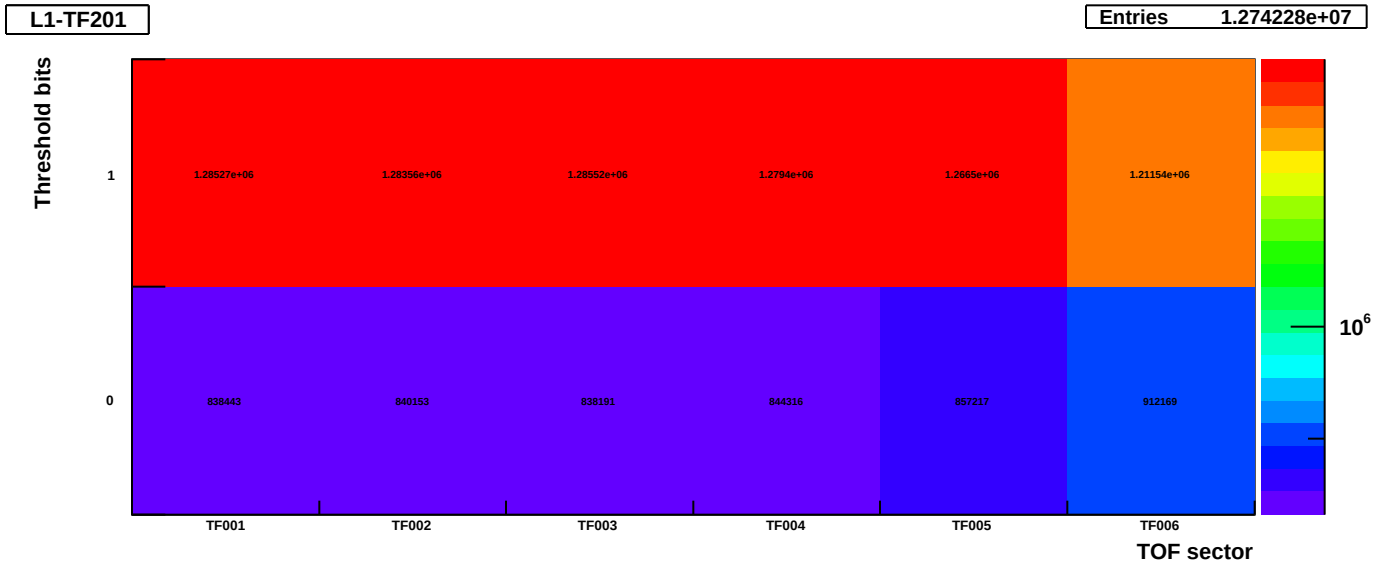

MIX-TF004
Entries 8.919594e+07
TOF MULT
TOF tray

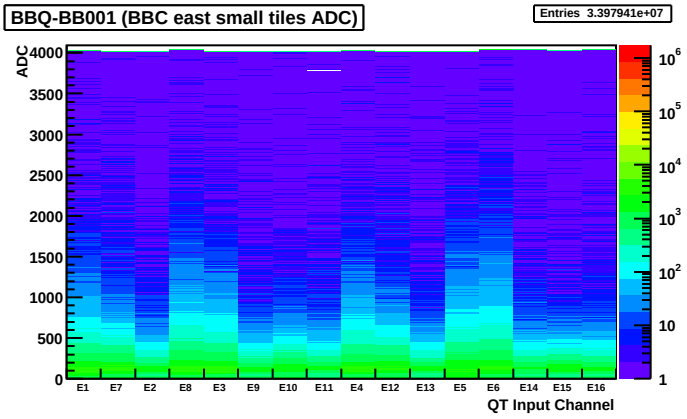

MIX-TF005
Entries 8.919594e+07
TOF MULT
TOF tray

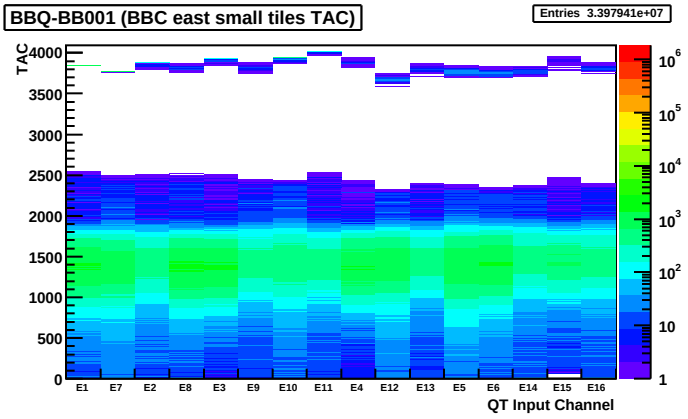

MIX-TF006
Entries 8.919594e+07
TOF MULT
TOF tray

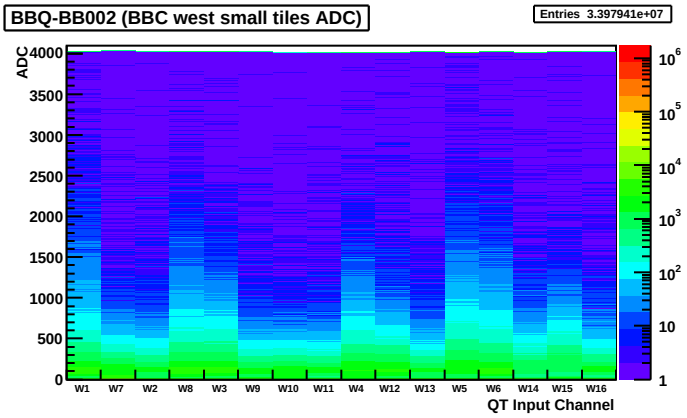

MIX-TF101
Entries 1.274228e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

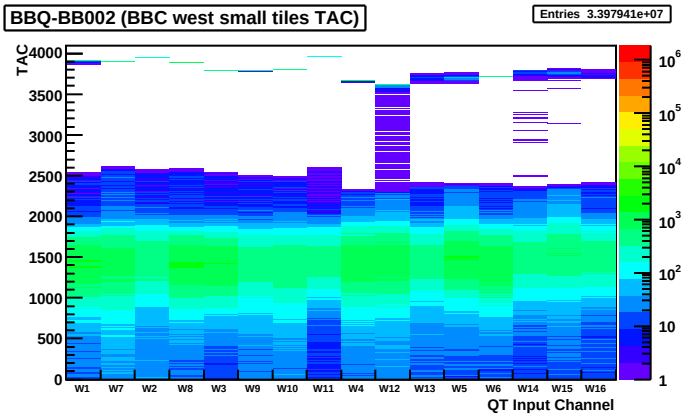

L1-TF201
Entries 2123713
TOF total mult

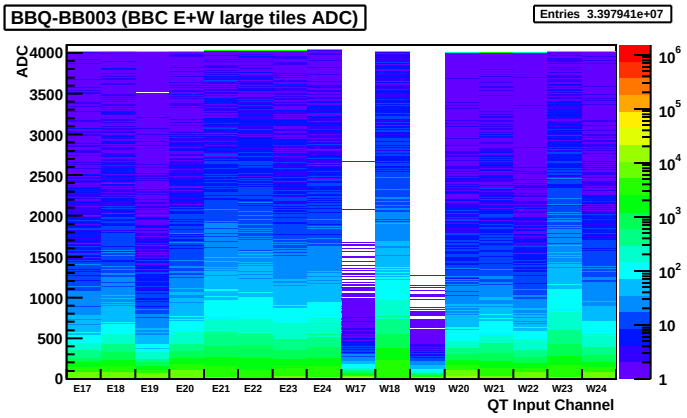

L1-TF201
Entries 1.274228e+07
Threshold bits
1.28527e+06
1.28356e+06
1.28552e+06
1.2794e+06
1.2665e+06
1.21154e+06
838443
840153
838191
844316
857217
912169
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

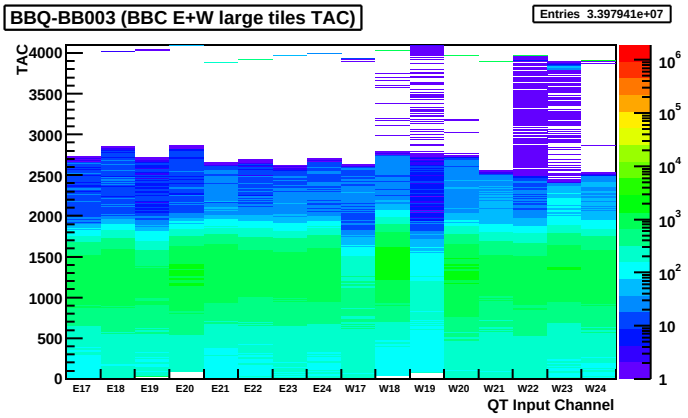

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.397941e+07
ADC
QT Input Channel

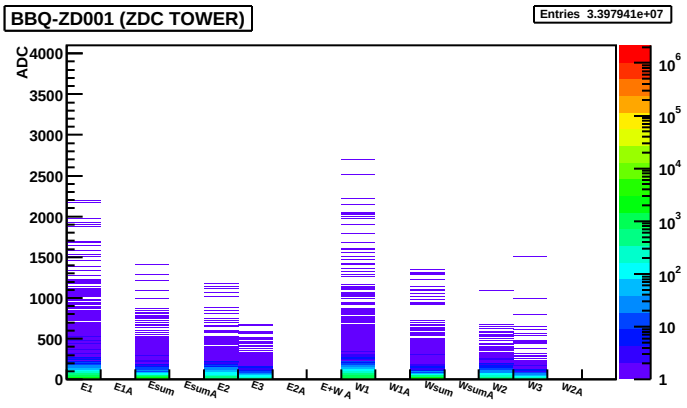

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.397941e+07
TAC
QT Input Channel

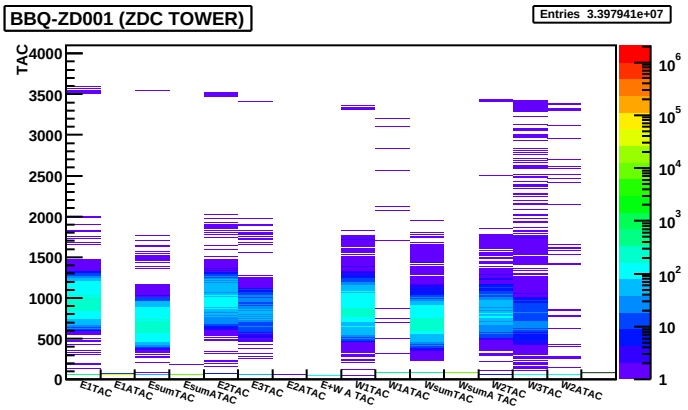

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.397941e+07
ADC
QT Input Channel

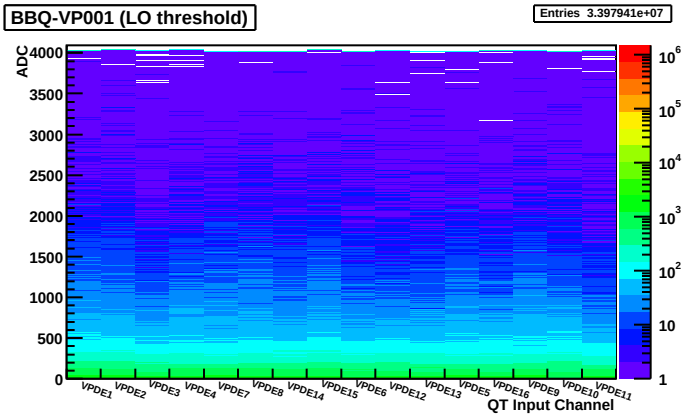

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.397941e+07
TAC
QT Input Channel

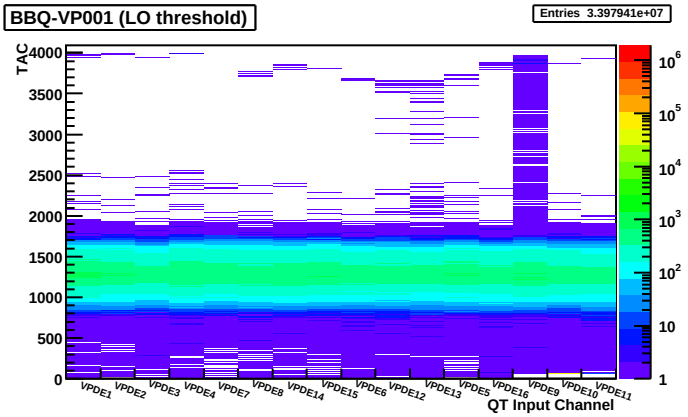

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.397941e+07
ADC
QT Input Channel

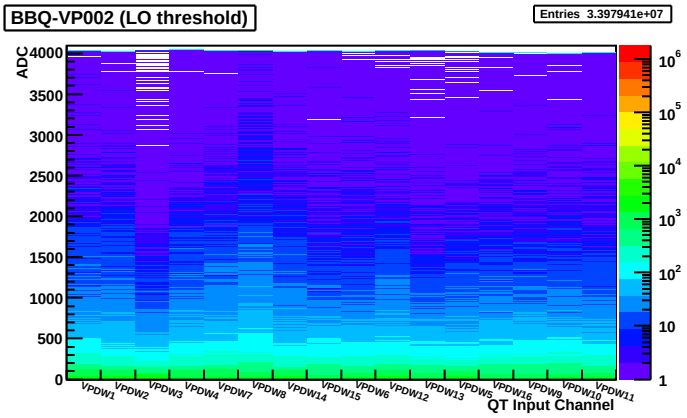

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.397941e+07
TAC
QT Input Channel

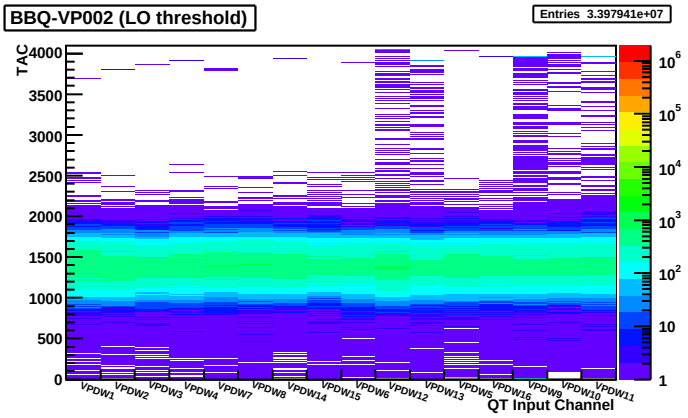

BBQ-ZD001 (ZDC TOWER)
Entries 3.397941e+07
ADC

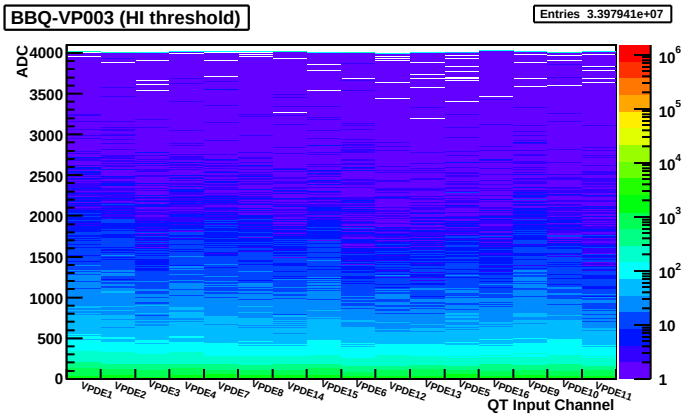

BBQ-ZD001 (ZDC TOWER)
Entries 3.397941e+07
TAC

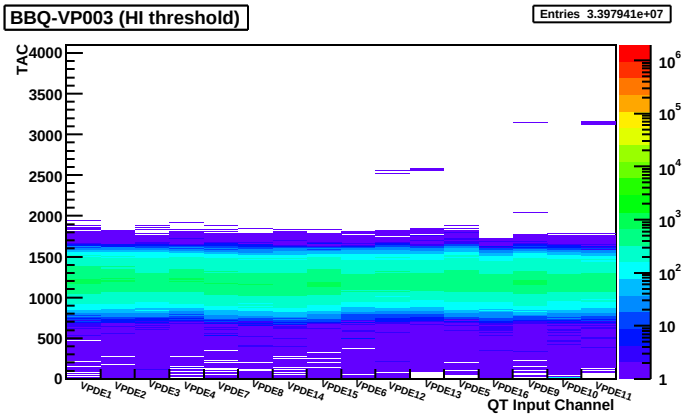

BBQ-VP001 (LO threshold)
Entries 3.397941e+07
ADC
QT Input Channel

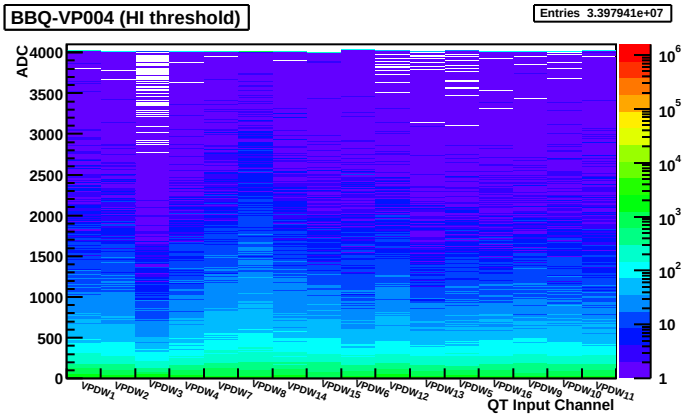

BBQ-VP001 (LO threshold)
Entries 3.397941e+07
TAC
QT Input Channel

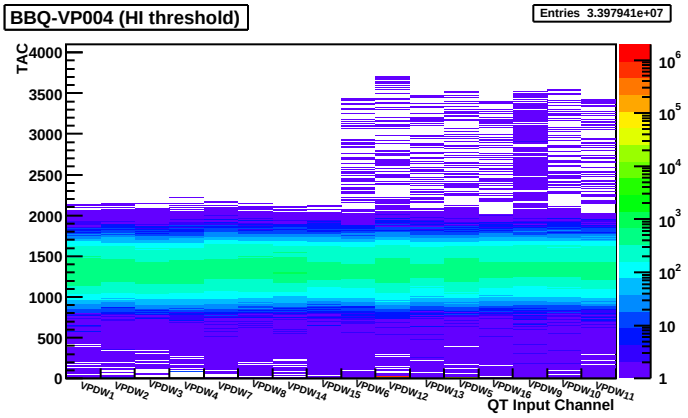

BBQ-VP002 (LO threshold)
Entries 3.397941e+07
ADC
QT Input Channel

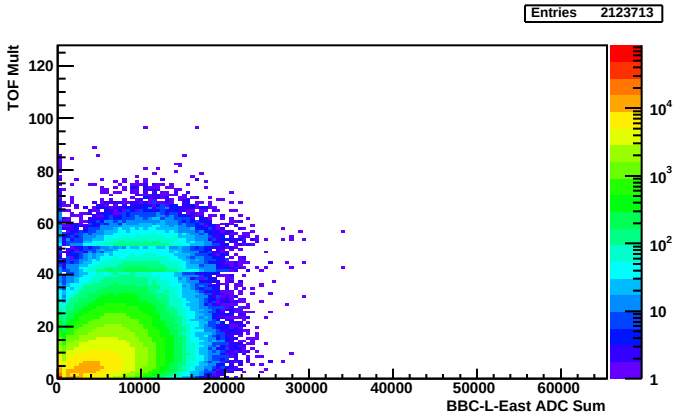

BBQ-VP002 (LO threshold)
Entries 3.397941e+07
TAC
QT Input Channel

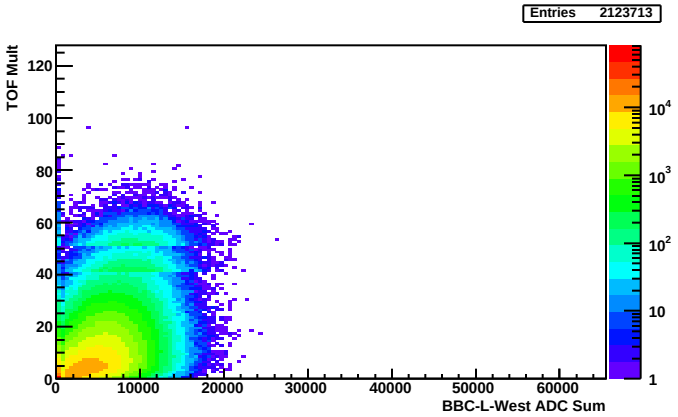

BBQ-VP003 (HI threshold)
Entries 3.397941e+07
ADC
QT Input Channel

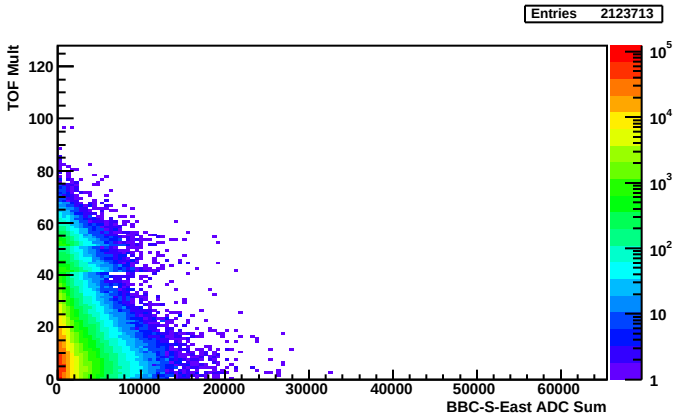

BBQ-VP003 (HI threshold)
Entries 3.397941e+07
TAC
QT Input Channel

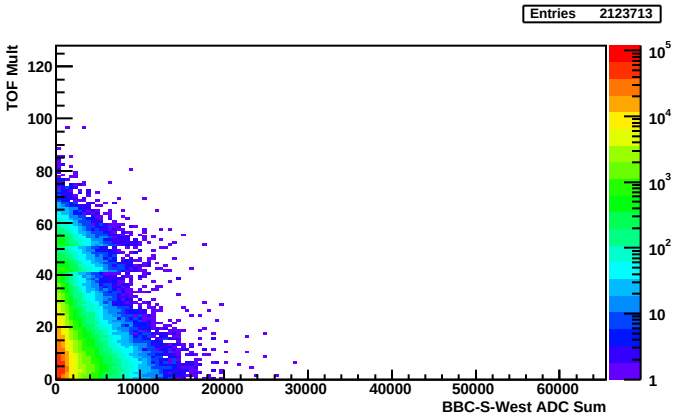

BBQ-VP004 (HI threshold)
Entries 3.397941e+07
ADC
QT Input Channel

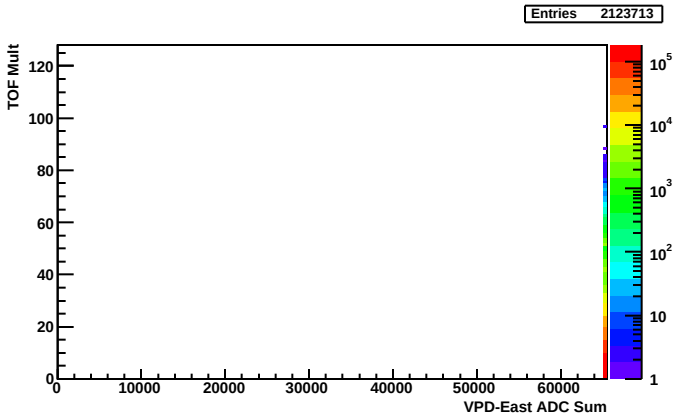

BBQ-VP004 (HI threshold)
Entries 3.397941e+07
TAC
QT Input Channel

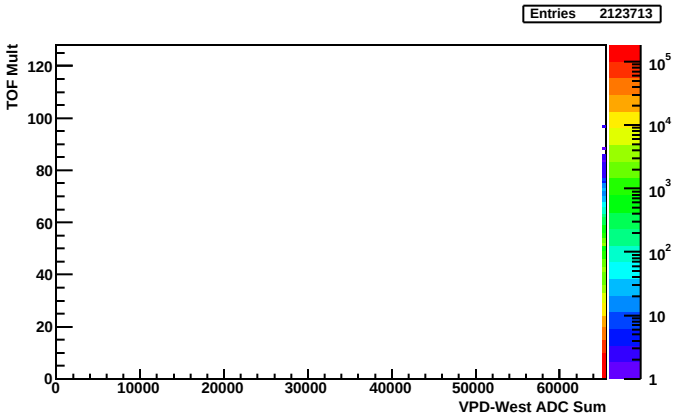

Entries 2123713
TOF Mult
BBC-L-East ADC Sum

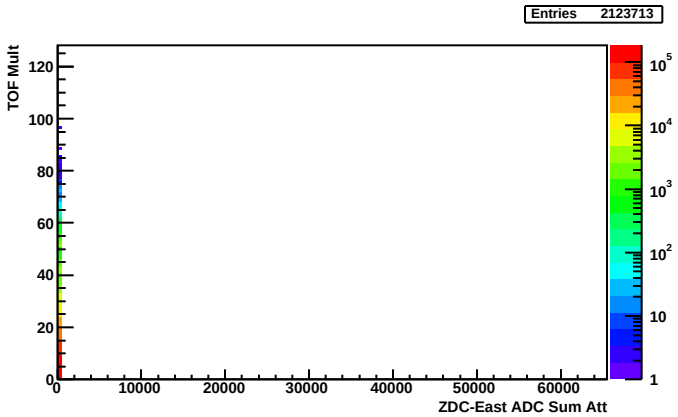

Entries 2123713
TOF Mult
BBC-L-West ADC Sum

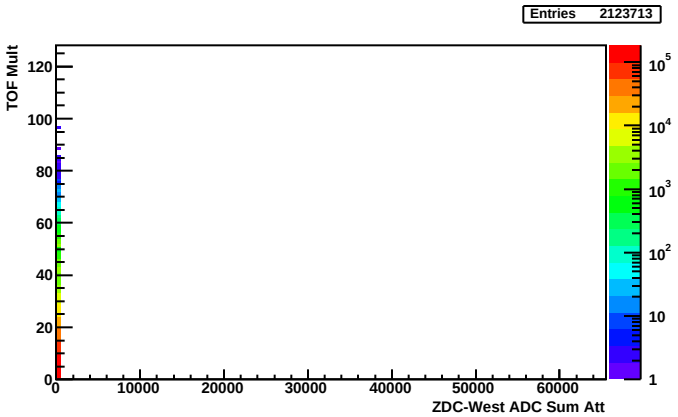

Entries 2123713
TOF Mult
BBC-S-East ADC Sum

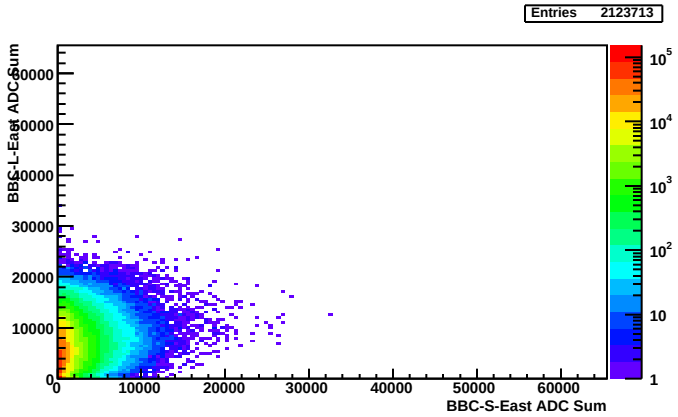

Entries 2123713
TOF Mult
BBC-S-West ADC Sum

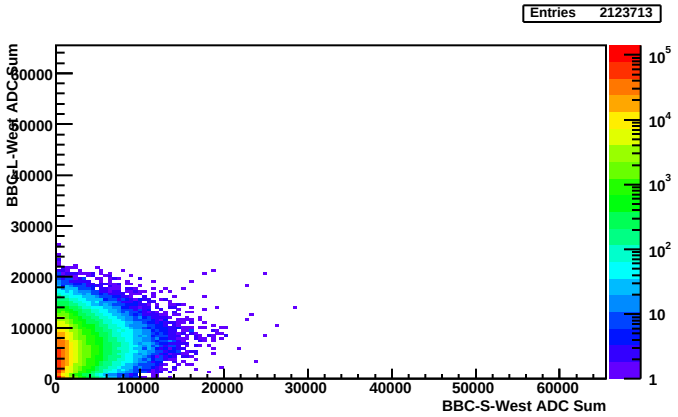

Entries 2123713
TOF Mult
VPD-East ADC Sum

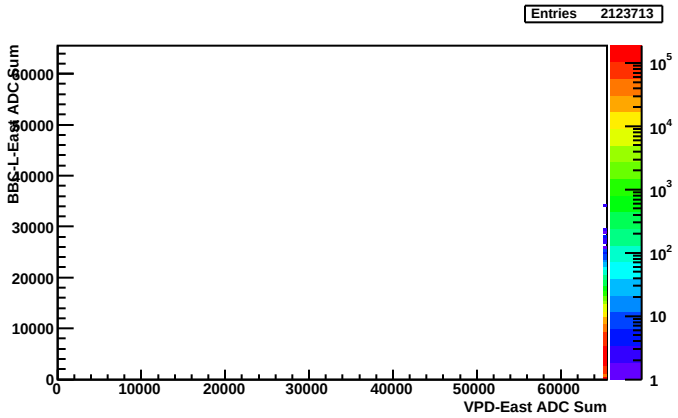

Entries 2123713
TOF Mult
VPD-West ADC Sum

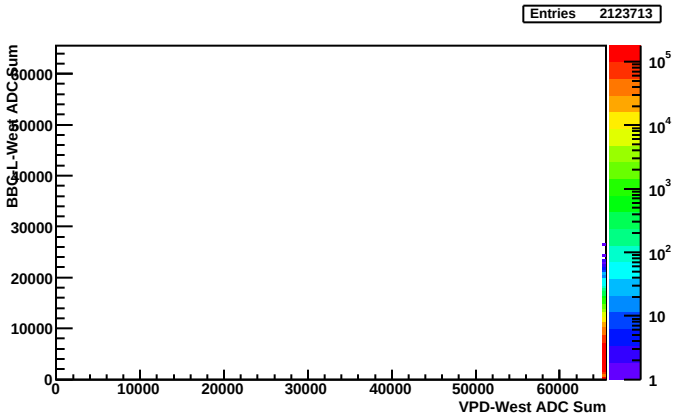

Entries 2123713
TOF Mult
ZDC-East ADC Sum Att

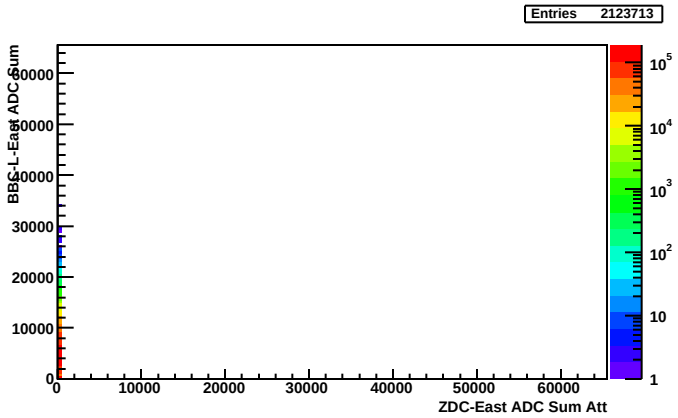

Entries 2123713
TOF Mult
ZDC-West ADC Sum Att


Entries 2123713
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2123713
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2123713
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2123713
BBC-L-West ADC Sum
VPD-West ADC Sum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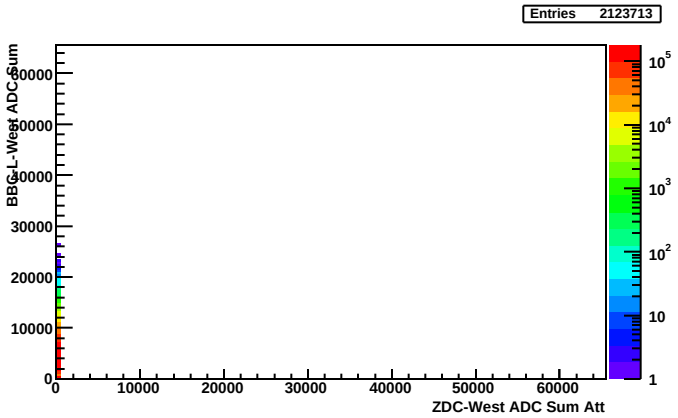
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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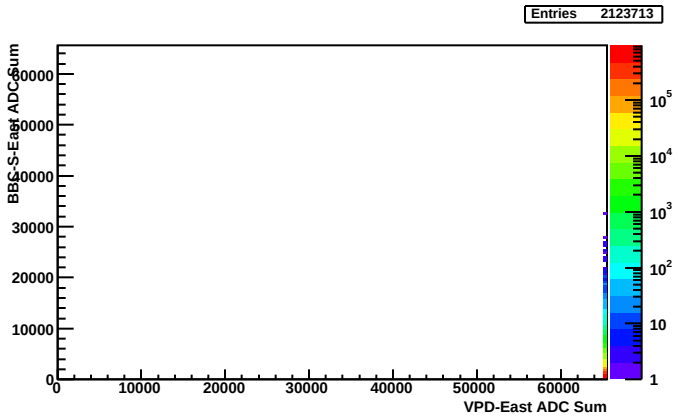
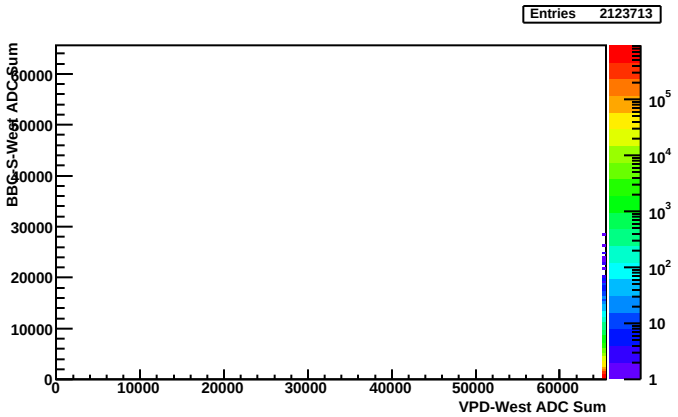
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2123713
BBC-S-East ADC Sum
VPD-East ADC Sum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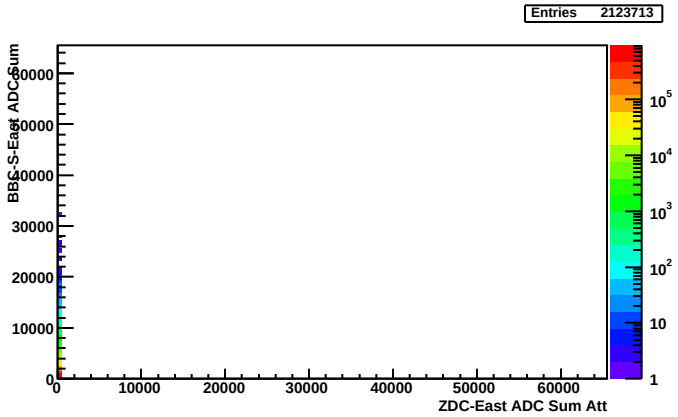
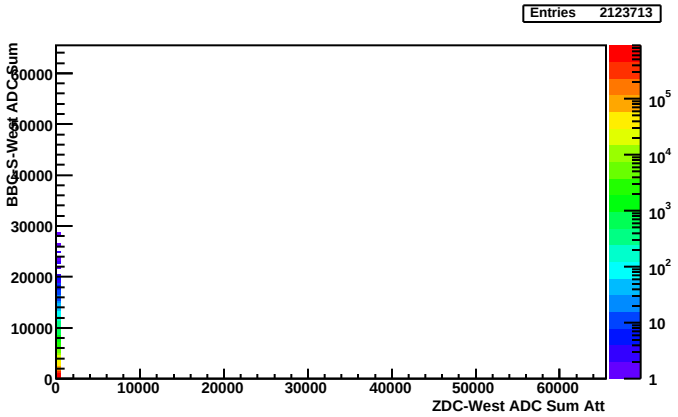
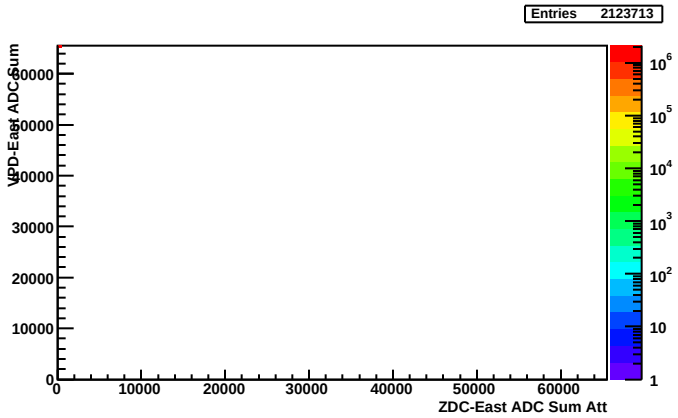
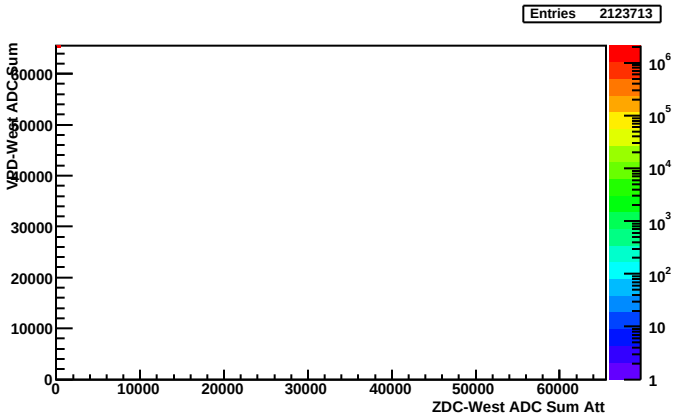
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2123713
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2123713
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2123713
VPD-East ADC Sum
ZDC-East ADC Sum Att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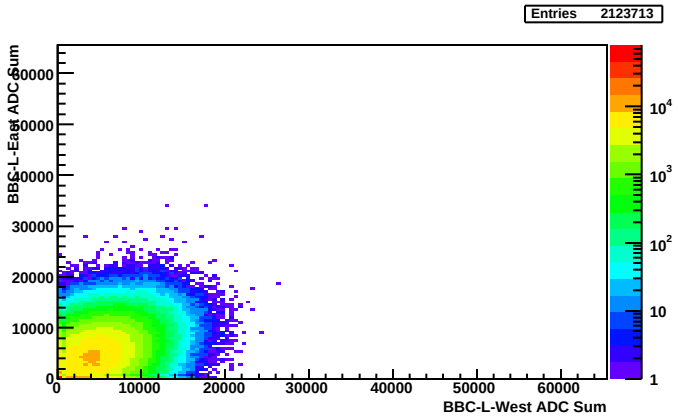
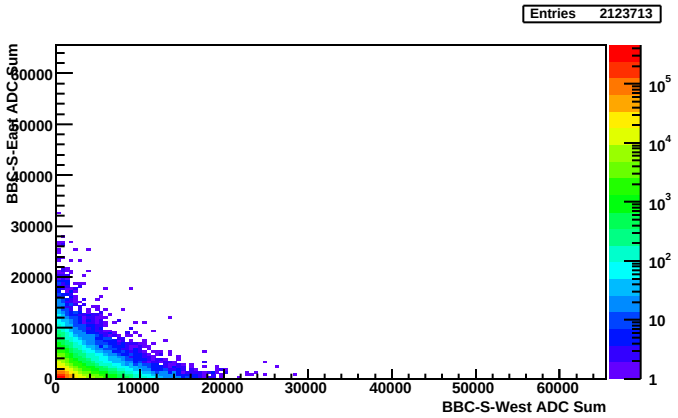
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2123713
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 2123713
BBC-S-East ADC Sum
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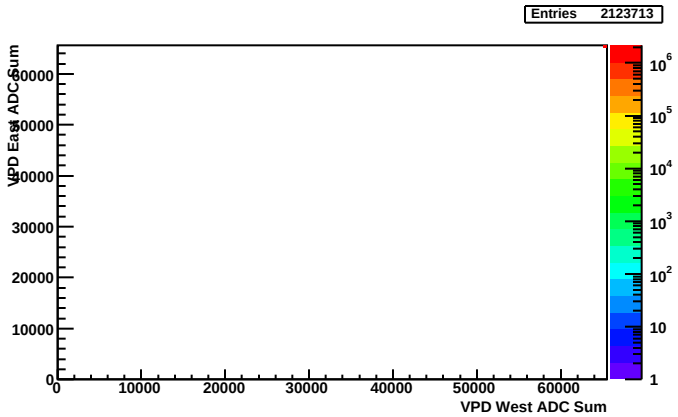
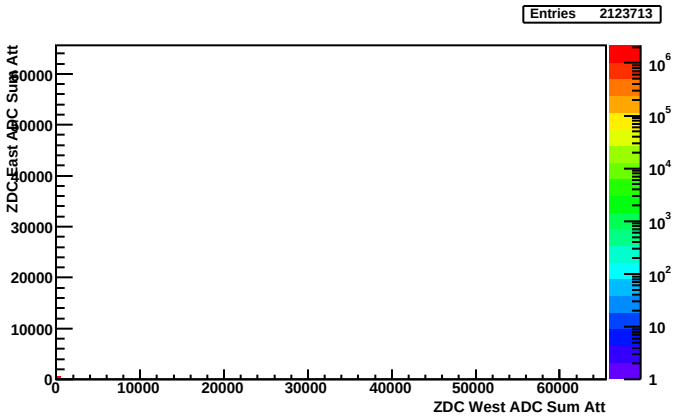
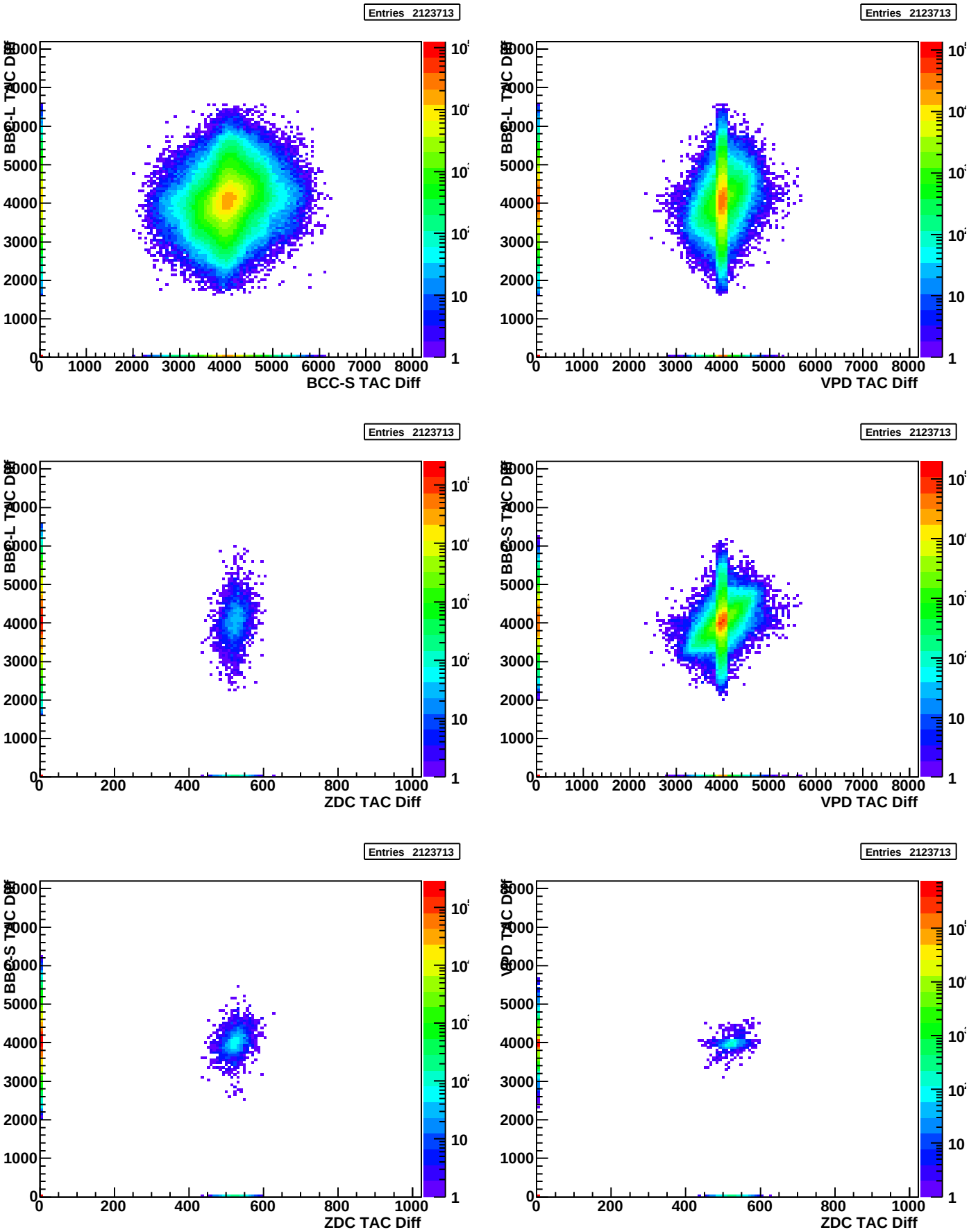

Entries 2123713
VPD East ADC Sum
VPD West ADC Sum


Entries 2123713
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2123713
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2123713
BBC-L TAC Diff
VPD TAC Diff
Entries 2123713
BBC-L TAC Diff
ZDC TAC Diff
Entries 2123713
BBC-S TAC Diff
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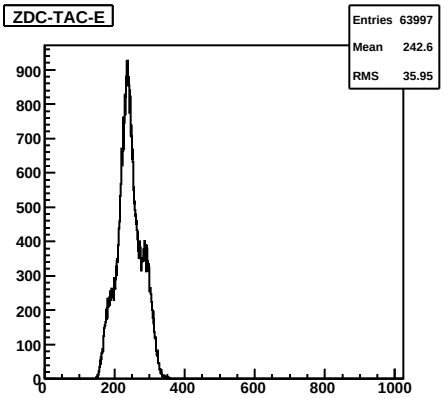
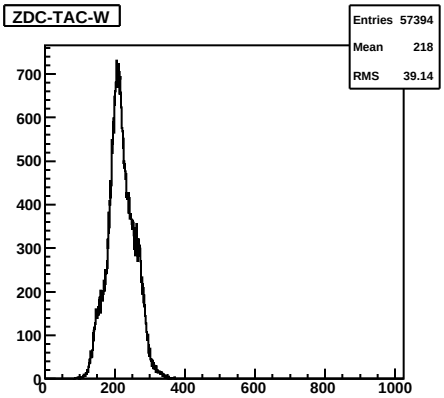
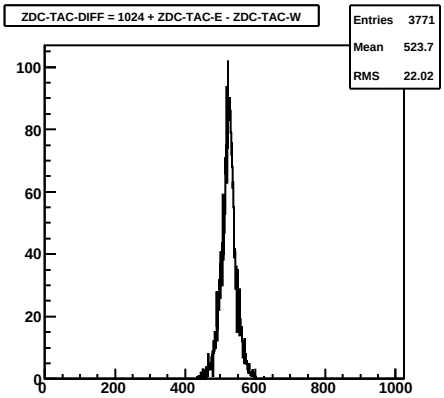
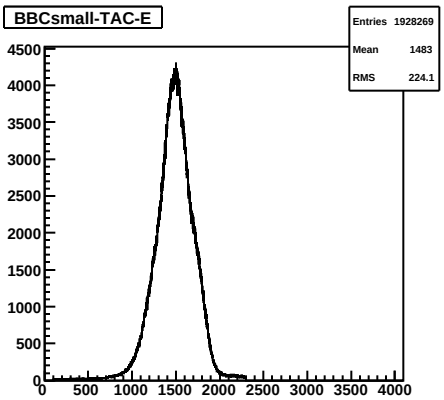
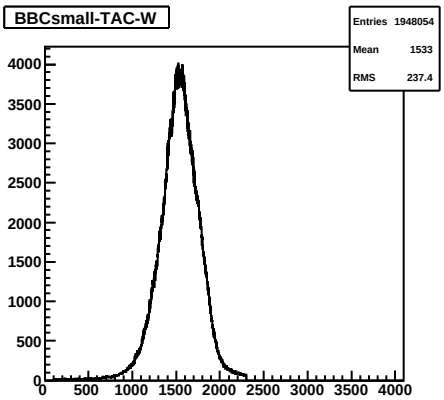
Entries 2123713
BBC-S TAC Diff
ZDC TAC Diff
Entries 2123713
VPD TAC Diff
ZDC TAC Diff

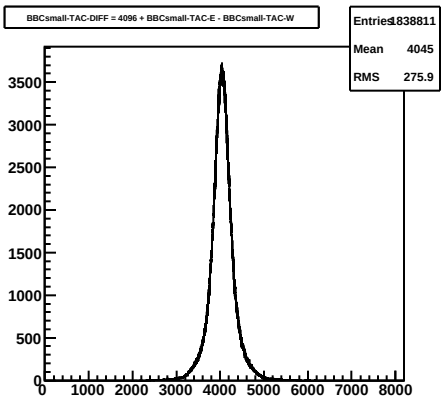

ZDC-TAC-E
Entries 63997
Mean 242.6
RMS 35.95

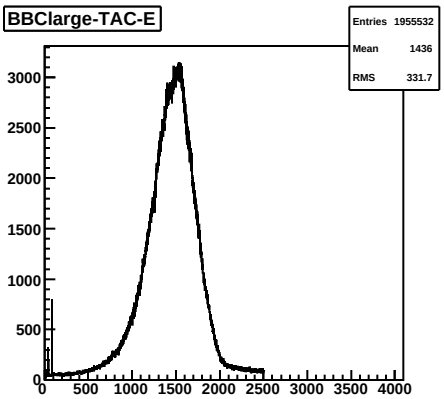

ZDC-TAC-W
Entries 57394
Mean 218
RMS 39.14

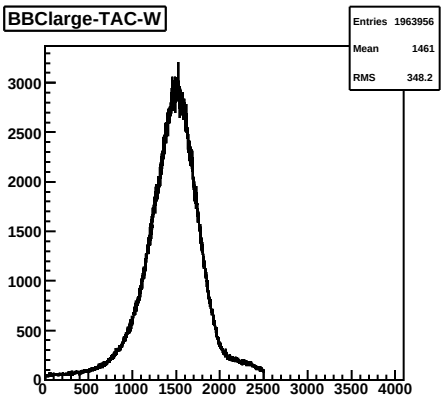

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3771
Mean 523.7
RMS 22.02

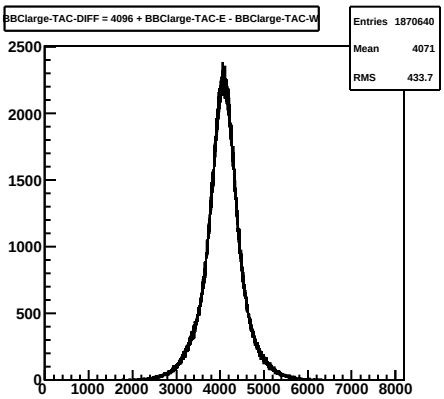

BBCsmall-TAC-E
Entries 1928269
Mean 1483
RMS 224.1

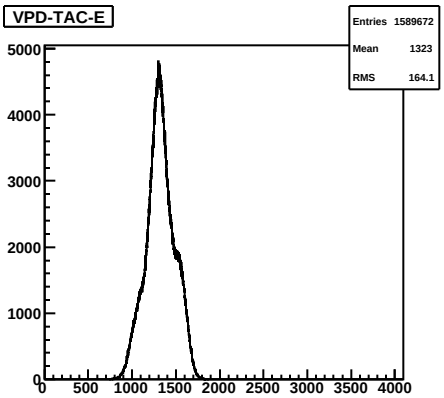

BBCsmall-TAC-W
Entries 1948054
Mean 1533
RMS 237.4

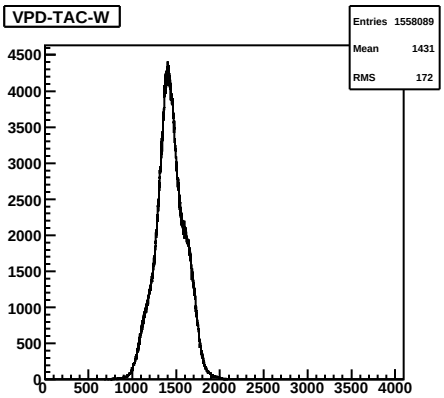

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1838811
Mean 4045
RMS 275.9

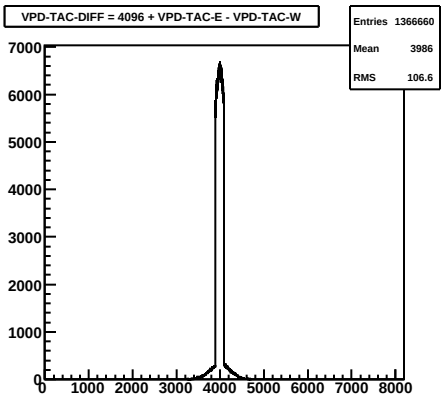

BBClarge-TAC-E
Entries 1955532
Mean 1436
RMS 331.7

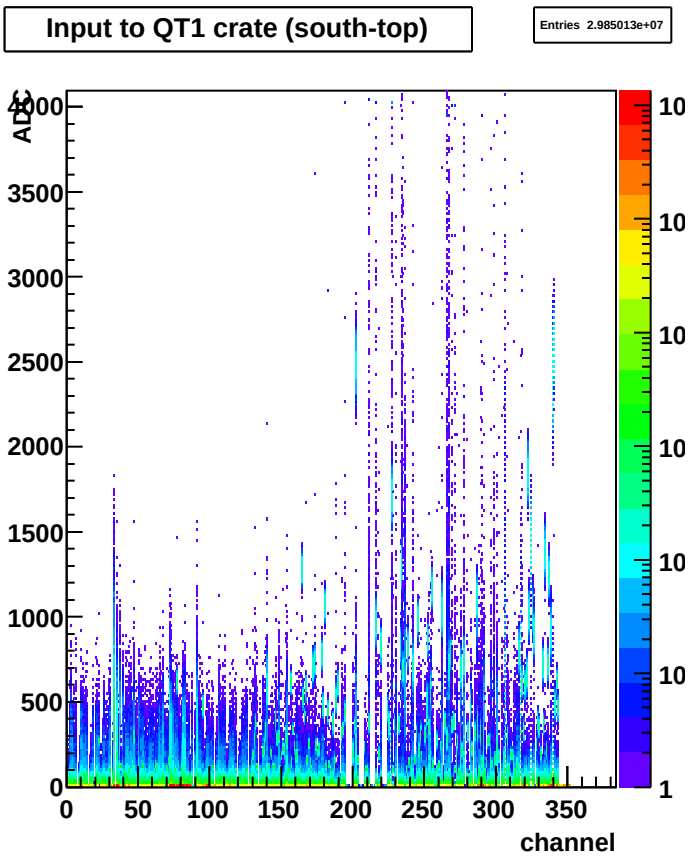

BBClarge-TAC-W
Entries 1963956
Mean 1461
RMS 348.2

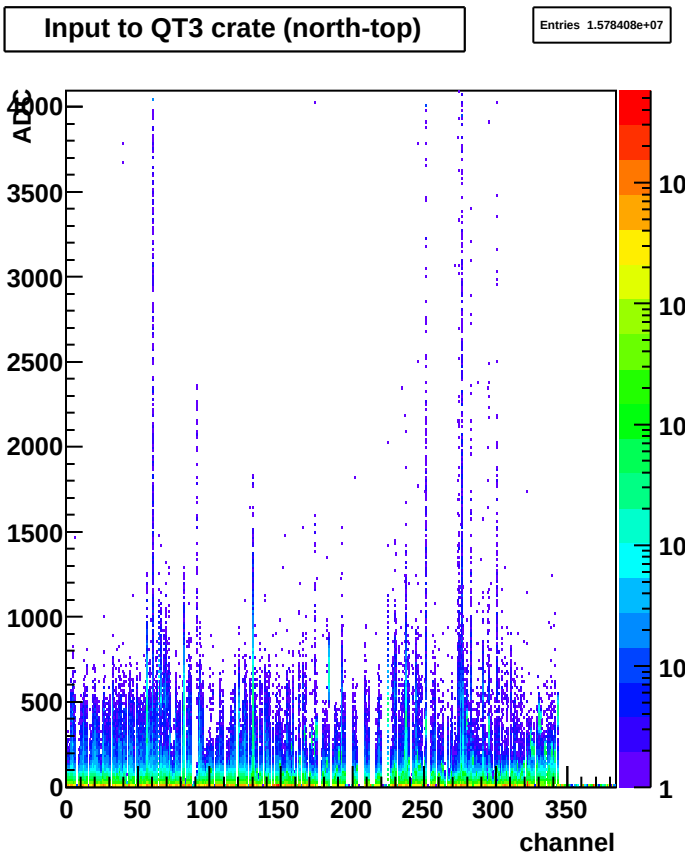

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1870640
Mean 4071
RMS 433.7

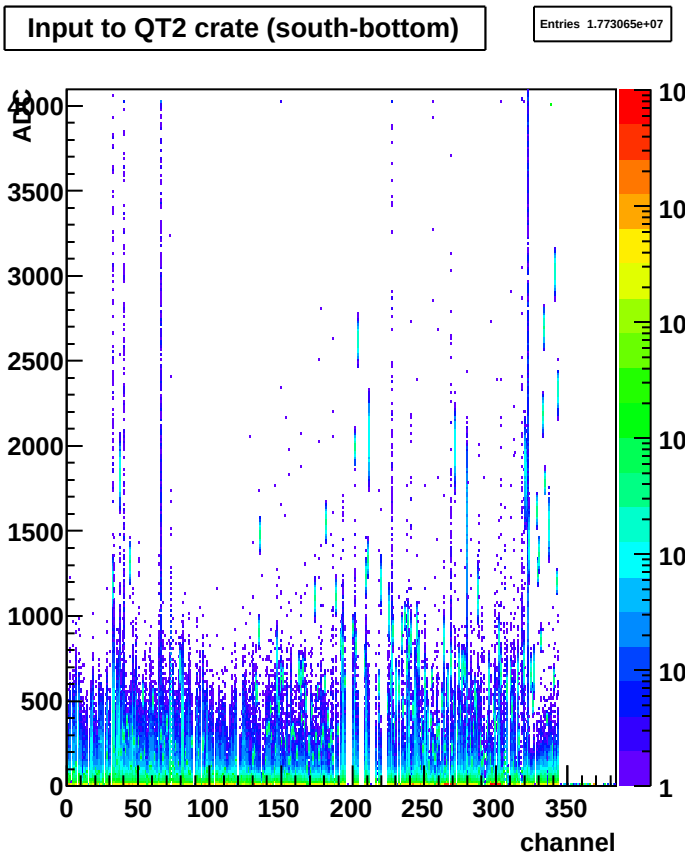

VPD-TAC-E
Entries 1589672
Mean 1323
RMS 164.1

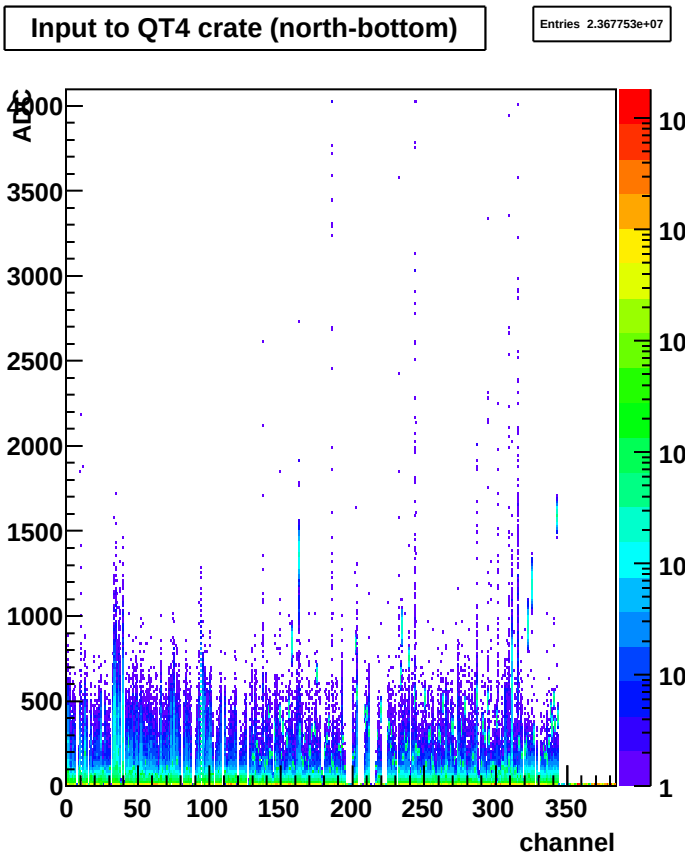

VPD-TAC-W
Entries 1558089
Mean 1431
RMS 172

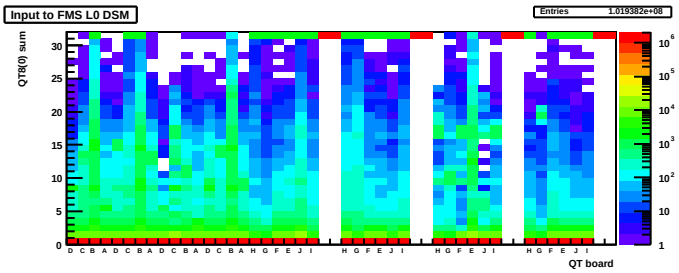

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1366660
Mean 3986
RMS 106.6

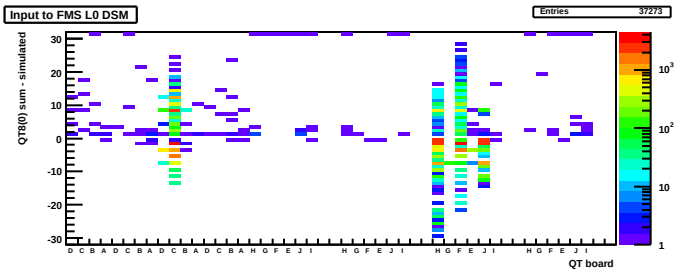

Input to QT1 crate (south-top)
Entries 2.985013e+07
ADC
channel

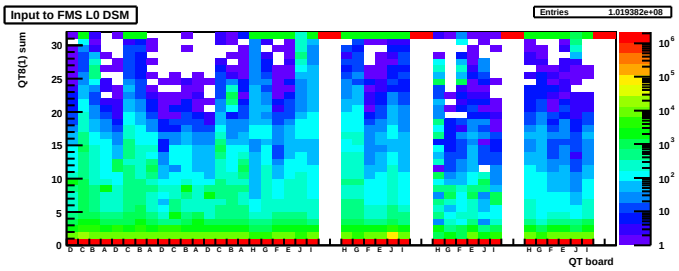

Input to QT3 crate (north-top)
Entries 1.578408e+07
ADC
channel

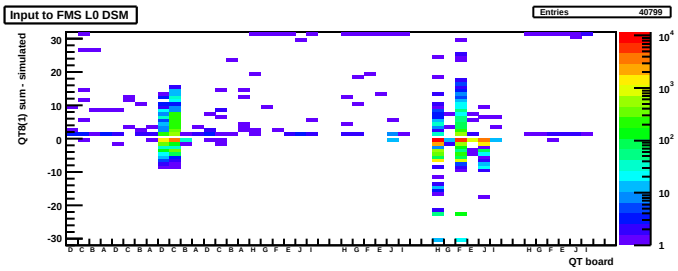

Input to QT2 crate (south-bottom)
Entries 1.773065e+07
ADC
channel

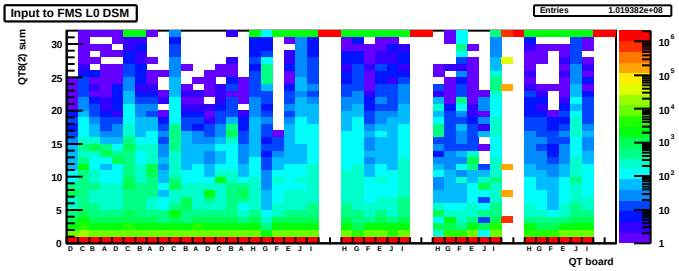

Input to QT4 crate (north-bottom)
Entries 2.367753e+07
ADC
channel

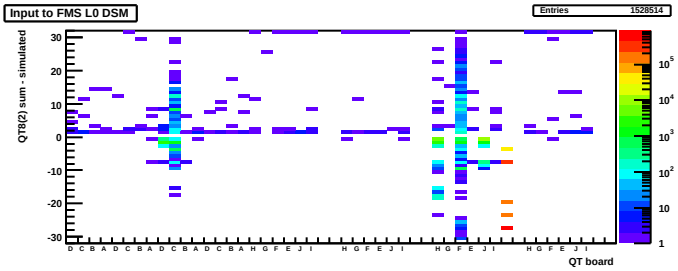

Input to FMS L0 DSM
Entries 1.019382e+08
QT8(0) sum
QT board

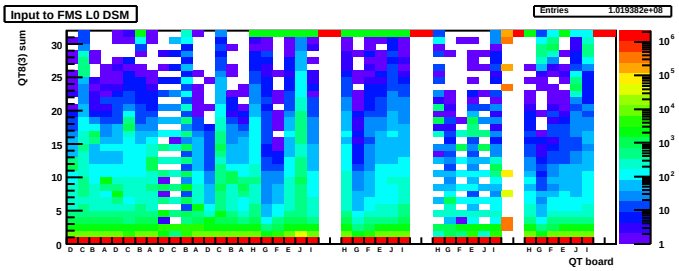

Input to FMS L0 DSM
Entries 37273
QT8(0) sum - simulated
QT board

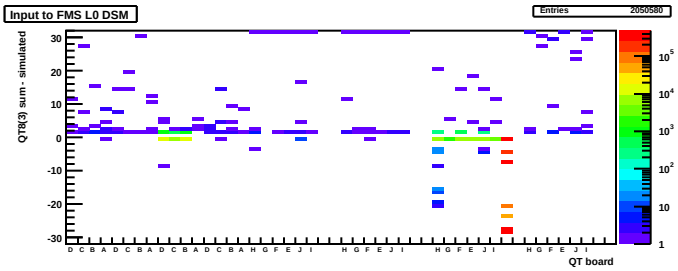

Input to FMS L0 DSM
Entries 1.019382e+08
QT8(1) sum
QT board

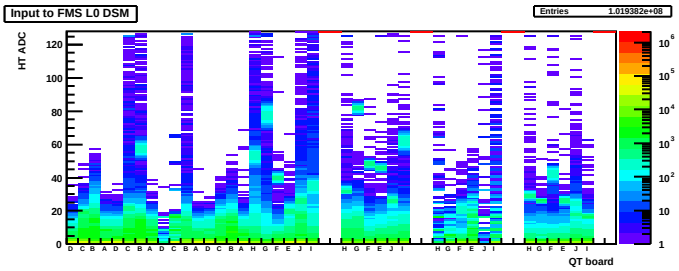

Input to FMS L0 DSM
Entries 40799
QT8(1) sum - simulated
QT board

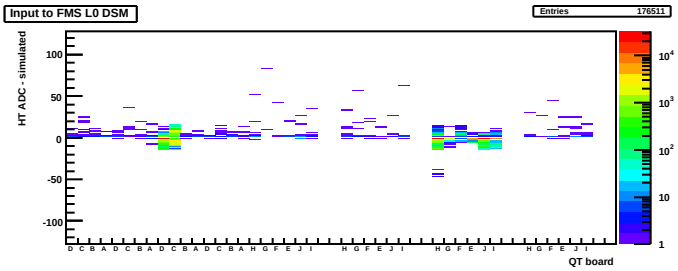

Input to FMS L0 DSM
Entries 1.019382e+08
QT8(2) sum
QT board

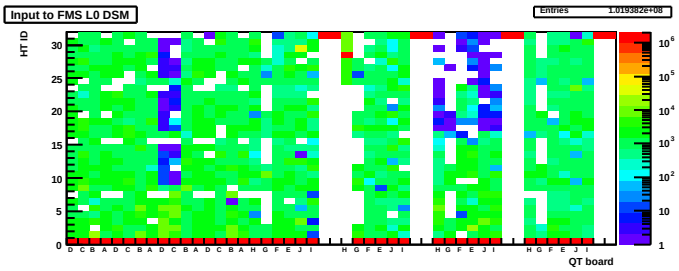

Input to FMS L0 DSM
Entries 1528514
QT8(2) sum - simulated
QT board

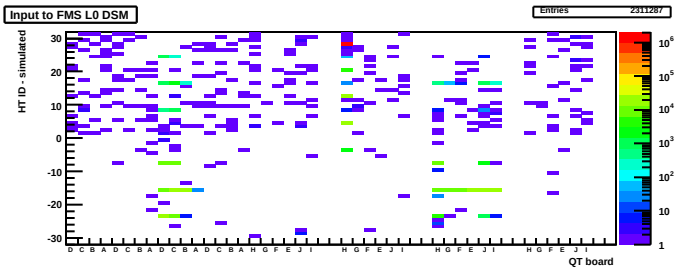

Input to FMS L0 DSM
Entries 1.019382e+08
QT8(3) sum
QT board

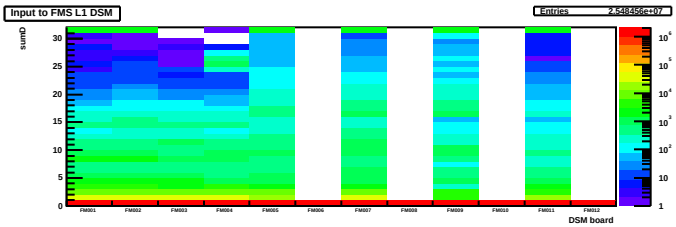

Input to FMS L0 DSM
Entries 2050580
QT8(3) sum - simulated
QT board

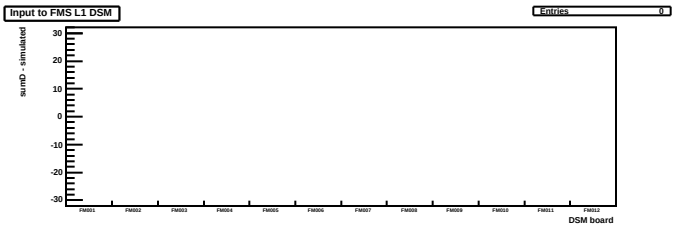

Input to FMS L0 DSM
Entries 1.019382e+08
HT ADC
QT board

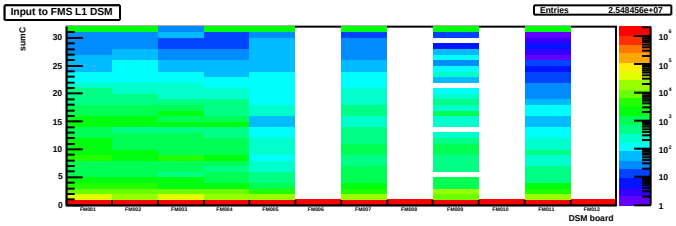

Input to FMS L0 DSM
Entries 176511
HT ADC - simulated
QT board

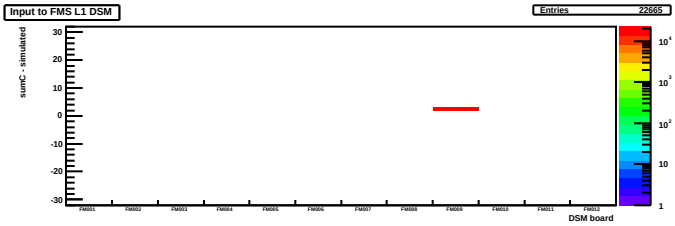

Input to FMS L0 DSM
Entries 1.019382e+08
HT ID
QT board

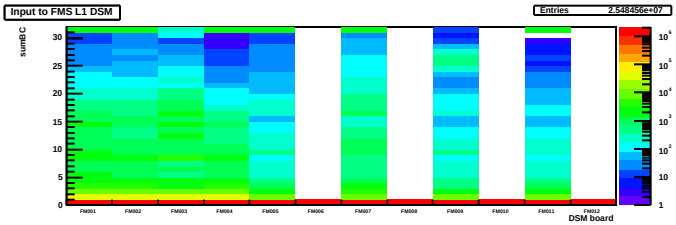

Input to FMS L0 DSM
Entries 2311287
HT ID - simulated
QT board

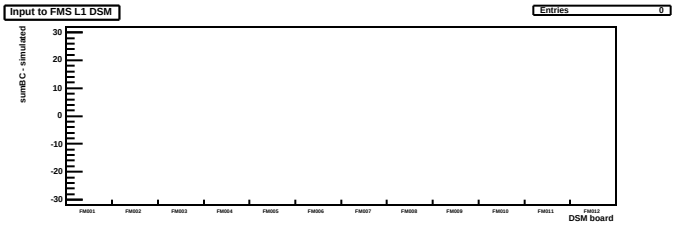

Input to FMS L1 DSM
Entries 2.548456e+07
sumD
DSM board

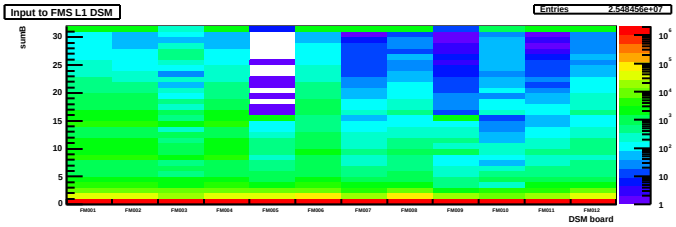

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

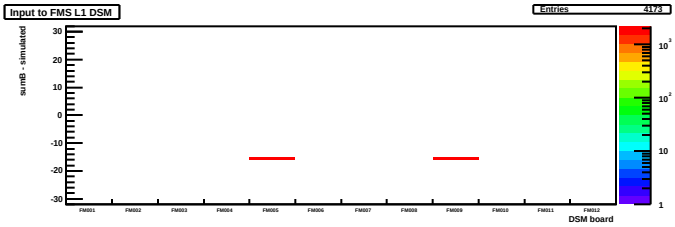

Input to FMS L1 DSM
Entries 2.548456e+07
sumC
DSM board

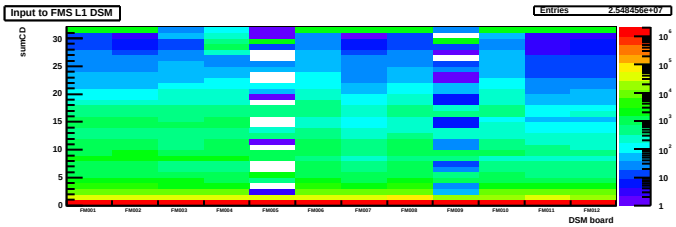

Input to FMS L1 DSM
Entries 22665
sumC - simulated
DSM board

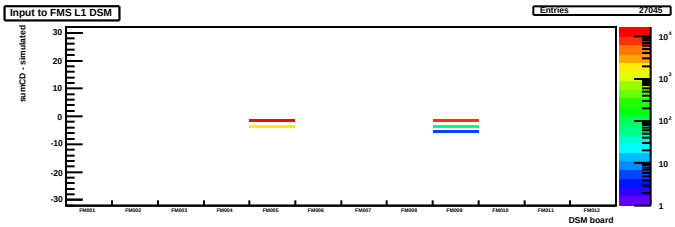

Input to FMS L1 DSM
Entries 2.548456e+07
sumBC
DSM board

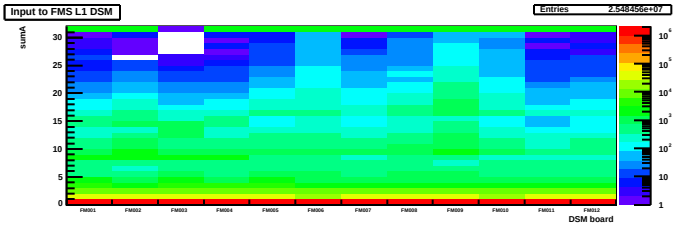

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

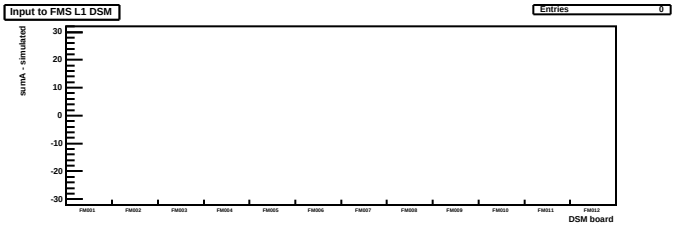

Input to FMS L1 DSM
Entries 2.548456e+07
sumB
DSM board

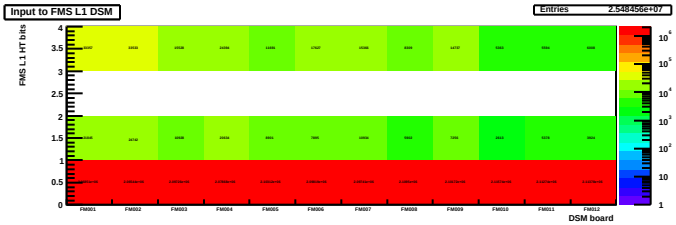

Input to FMS L1 DSM
Entries 4173
sumB - simulated
DSM board

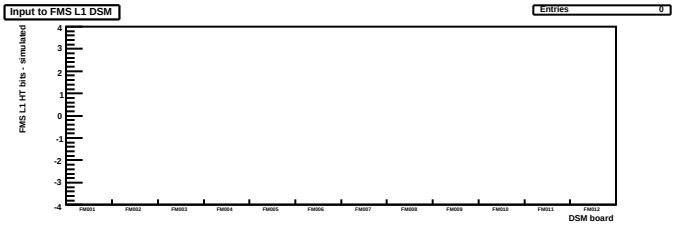

Input to FMS L1 DSM
Entries 2.548456e+07
sumCD
DSM board

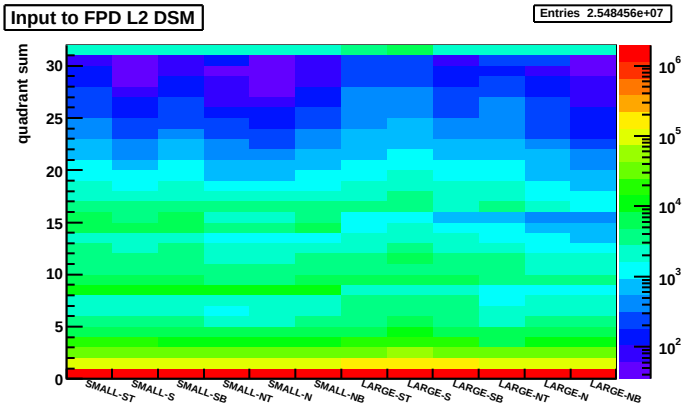

Input to FMS L1 DSM
Entries 27045
sumCD - simulated
DSM board

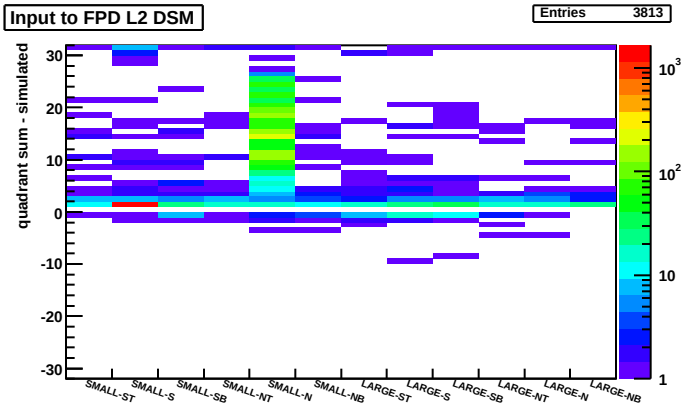

Input to FMS L1 DSM
Entries 2.548456e+07
sumA
DSM board

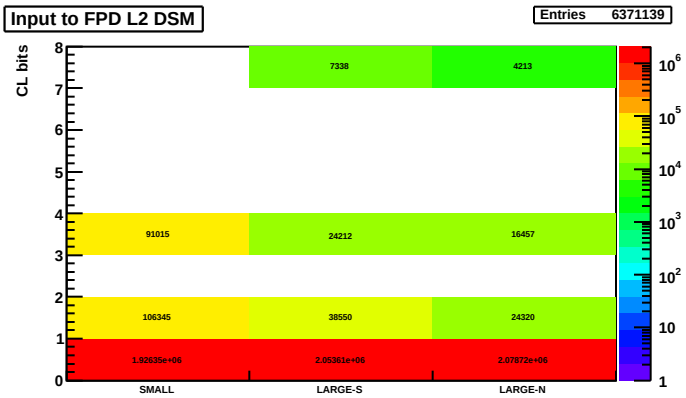

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

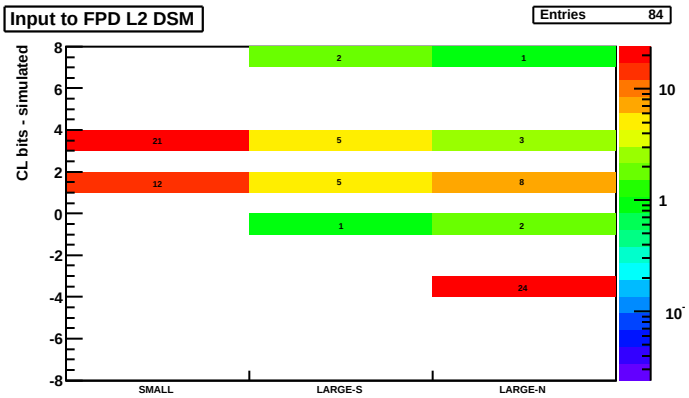

Input to FMS L1 DSM
Entries 2.548456e+07
FMS L1 HT bits
DSM board

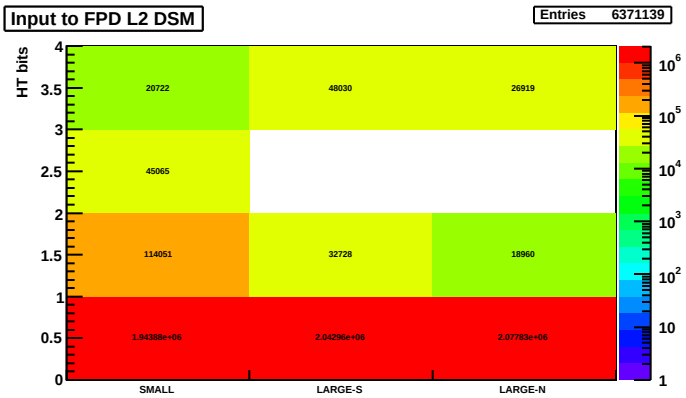

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

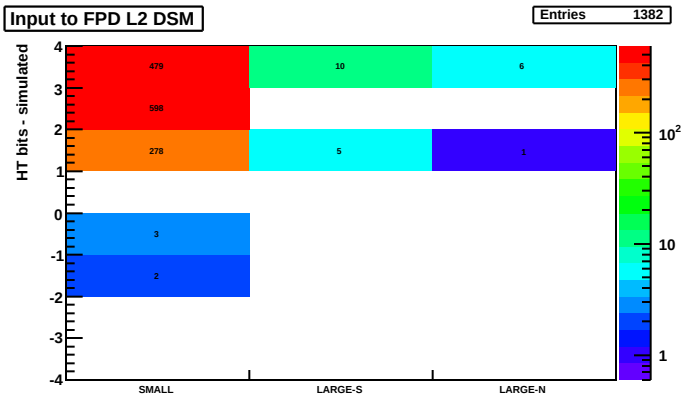

Input to FPD L2 DSM
Entries 2.548456e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

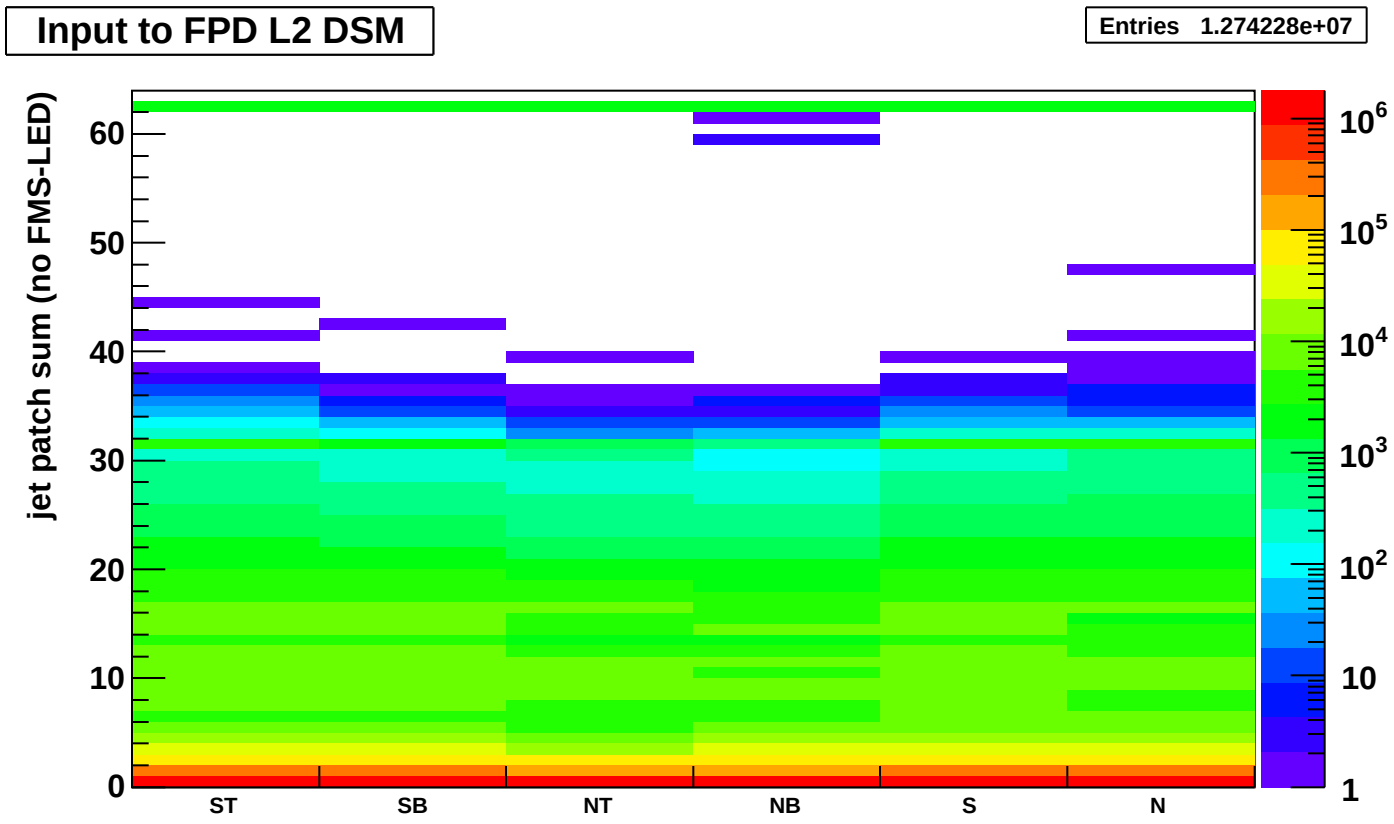

Input to FPD L2 DSM
Entries 3813
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

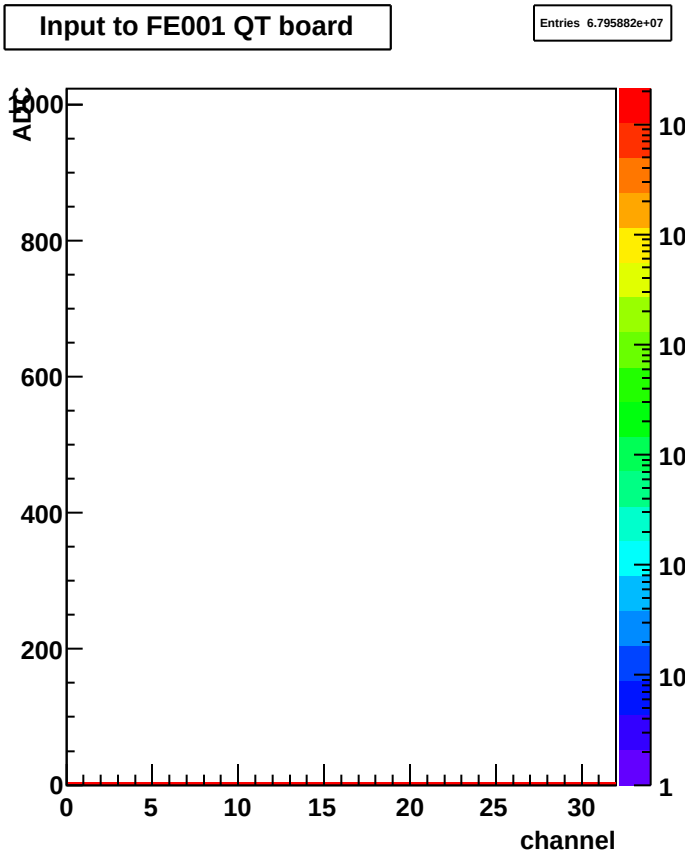

Input to FPD L2 DSM
Entries 6371139
CL bits
7338
4213
91015
24212
16457
106345
38550
24320
1.92635e+06
2.05361e+06
2.07672e+06
SMALL
LARGE-S
LARGE-N

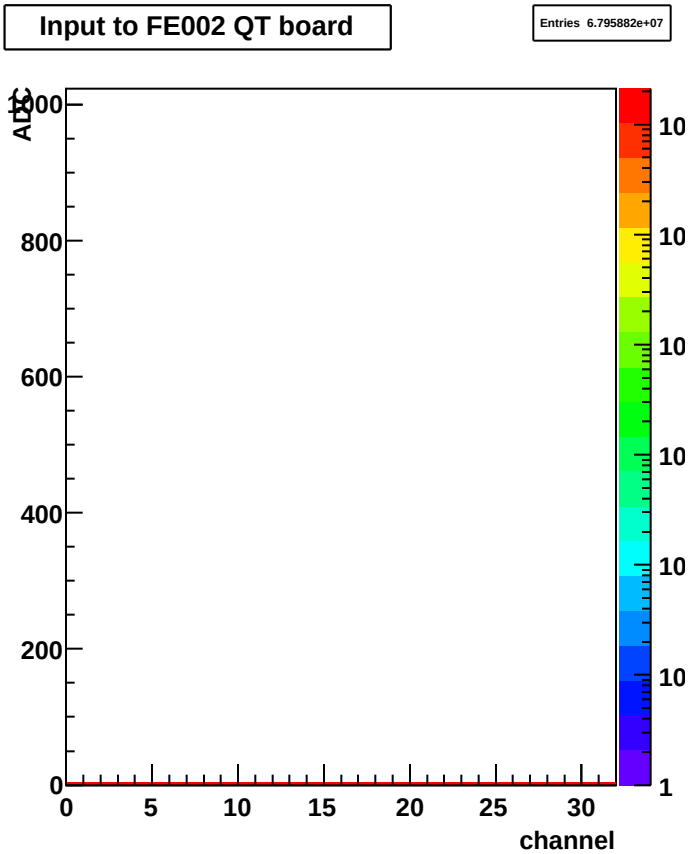

Input to FPD L2 DSM
Entries 84
CL bits - simulated
2
1
21
5
3
12
5
8
1
2
24
SMALL
LARGE-S
LARGE-N

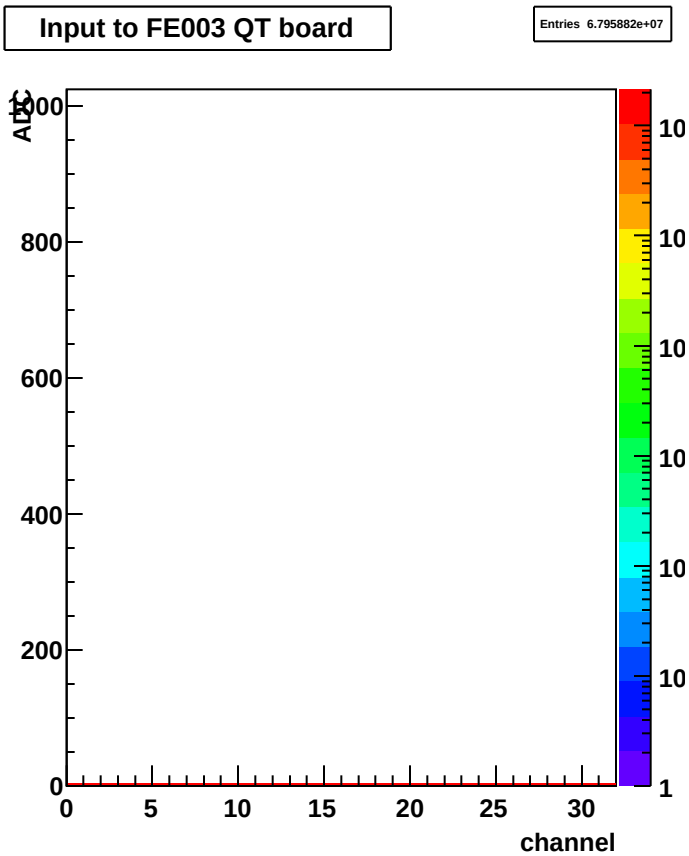

Input to FPD L2 DSM
Entries 6371139
HT bits
20722
48030
26919
45065
114051
32728
18960
1.94388e+06
2.04296e+06
2.07783e+06
SMALL
LARGE-S
LARGE-N

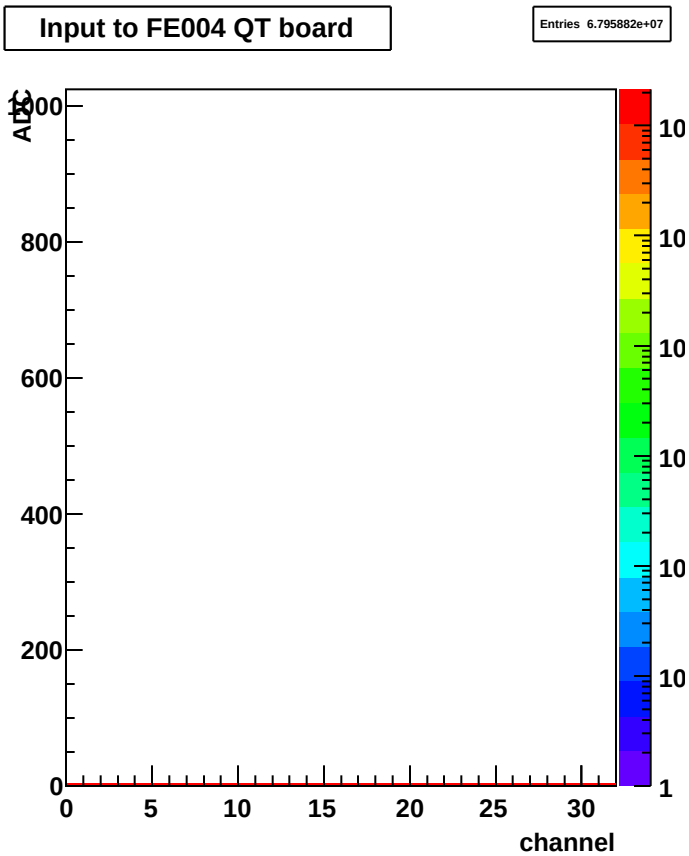

Input to FPD L2 DSM
Entries 1382
HT bits - simulated
479
10
6
598
278
5
1
3
2
SMALL
LARGE-S
LARGE-N

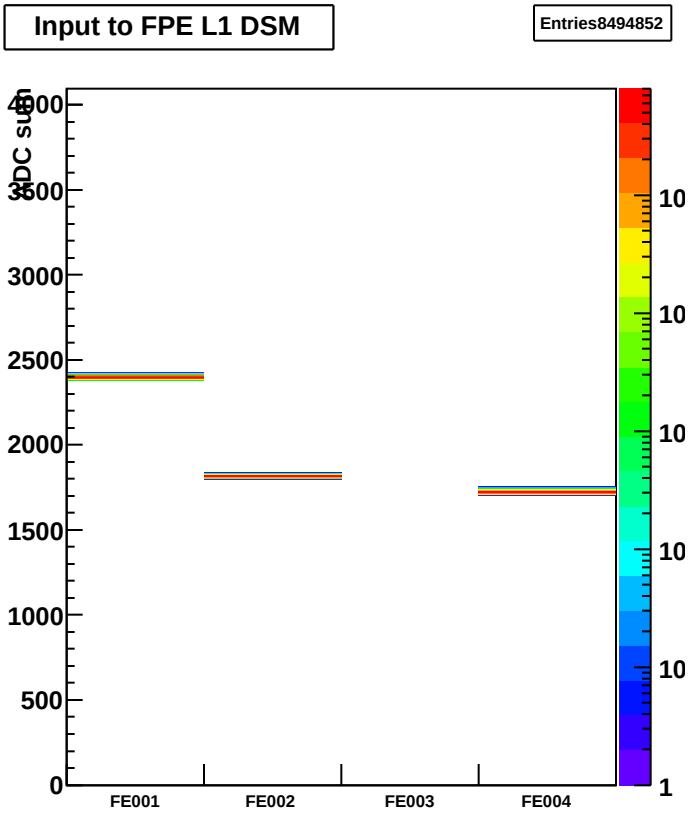

Input to FPD L2 DSM
Entries 1.274228e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.795882e+07
ADC
channel

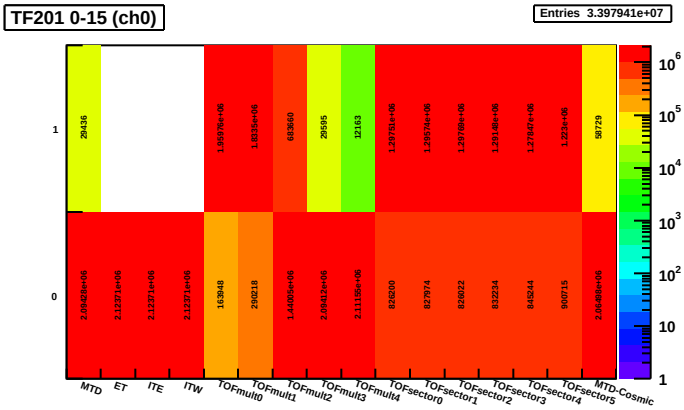

Input to FE002 QT board
Entries 6.795882e+07
ADC
channel


Input to FE003 QT board
Entries 6.795882e+07
ADC
channel

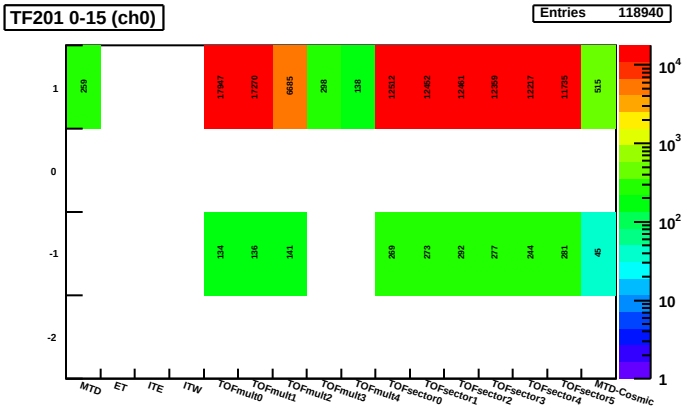

Input to FE004 QT board
Entries 6.795882e+07
ADC
channel


Input to FPE L1 DSM
Entries8494852
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

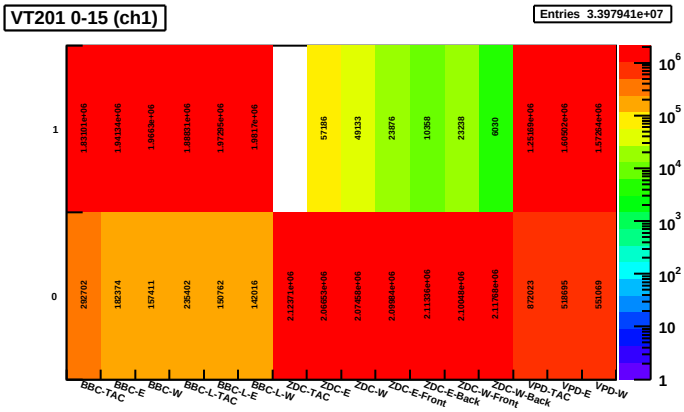
TF201 0-15 (ch0)



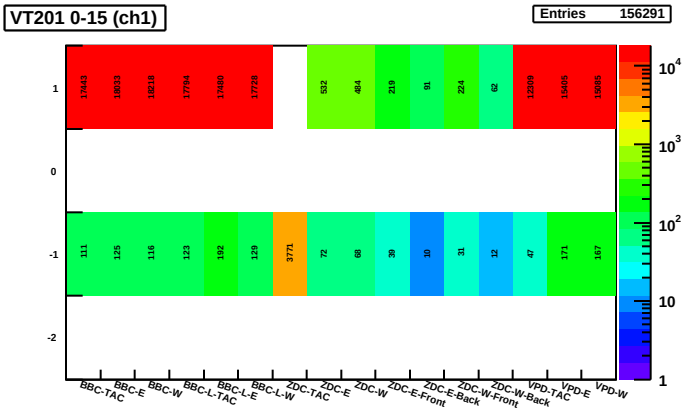
TF201 0-15 (ch0)



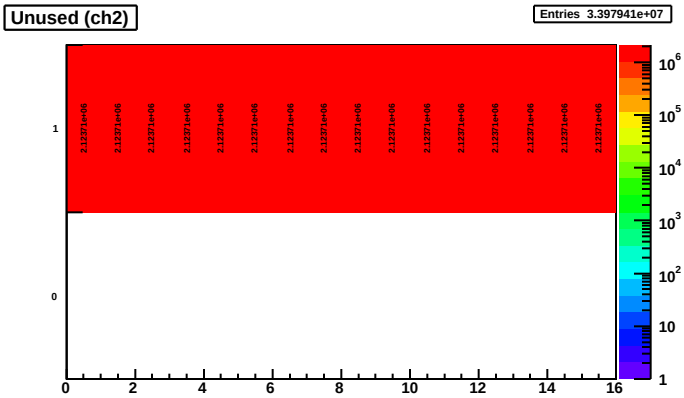
VT201 0-15 (ch1)

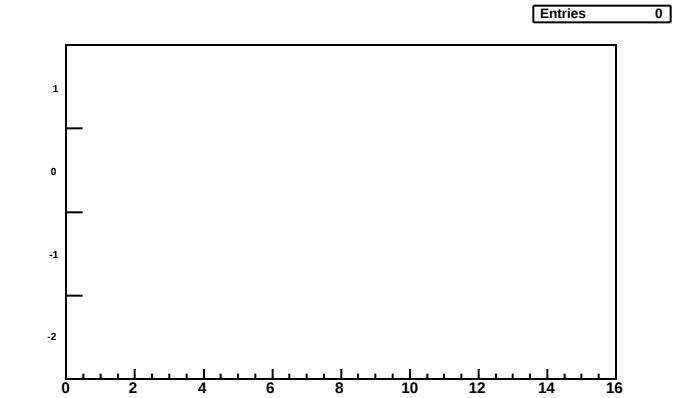


VT201 0-15 (ch1)

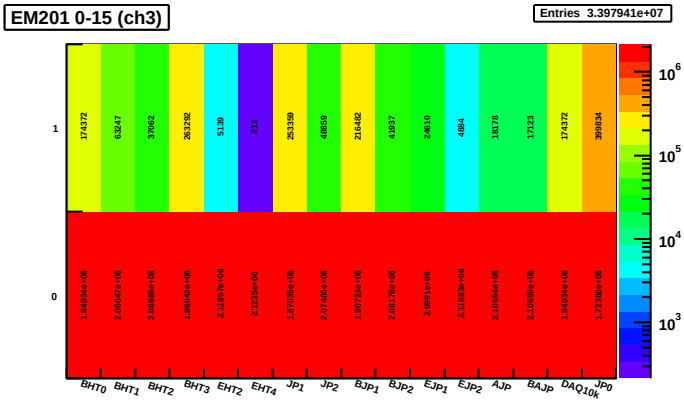


Unused (ch2)





EM201 0-15 (ch3)



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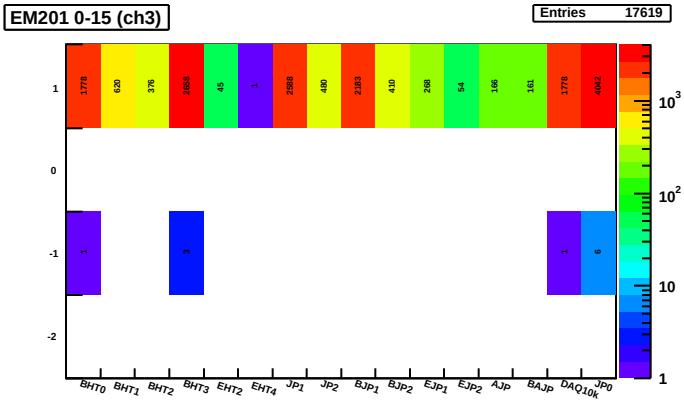
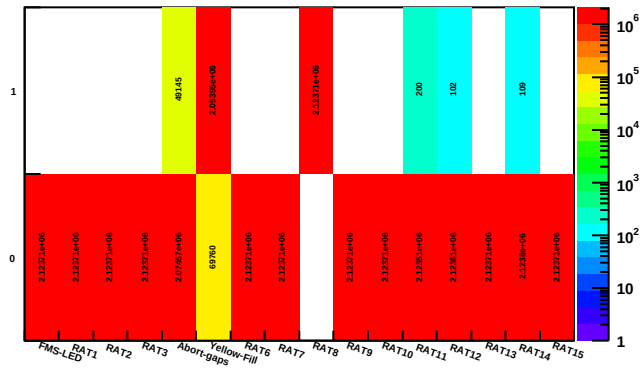
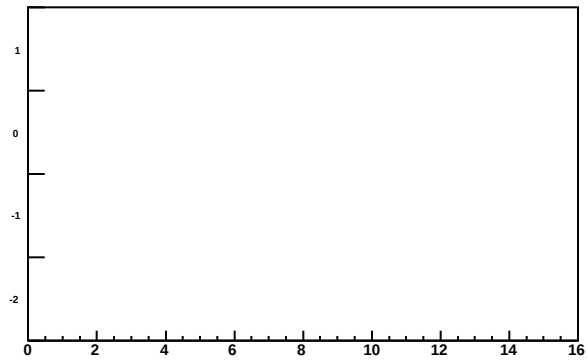


RAT board (ch4)





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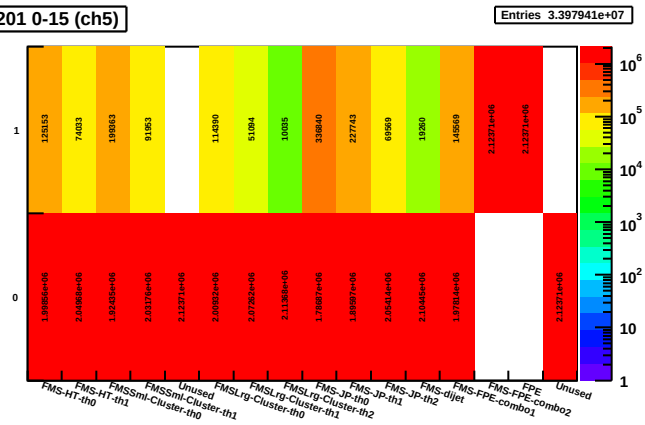

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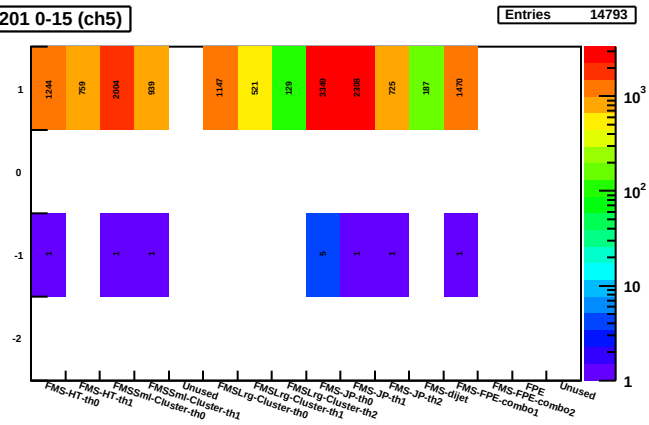

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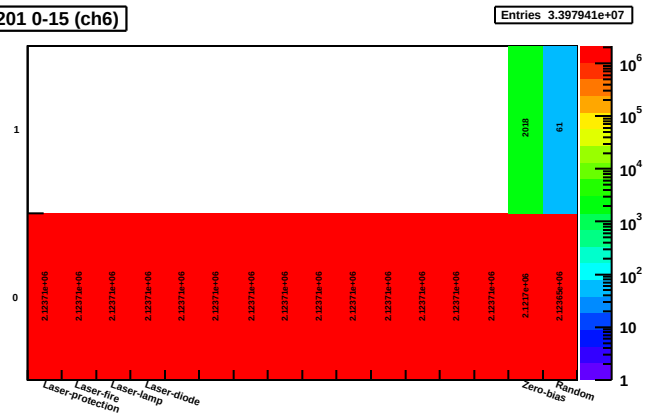

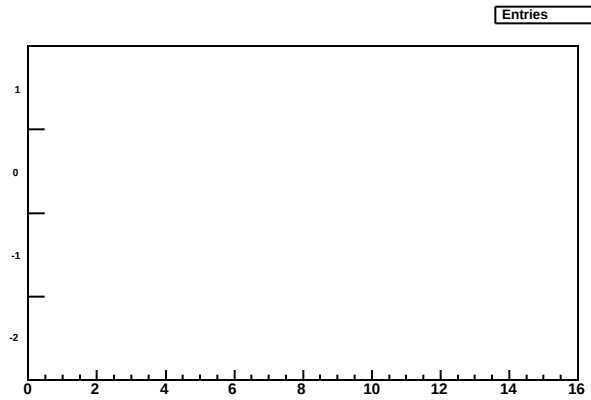
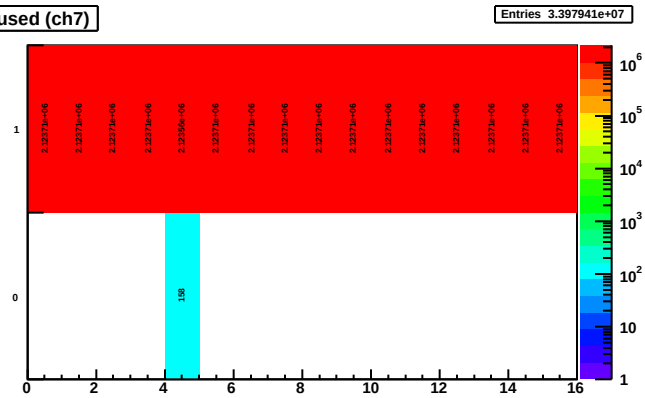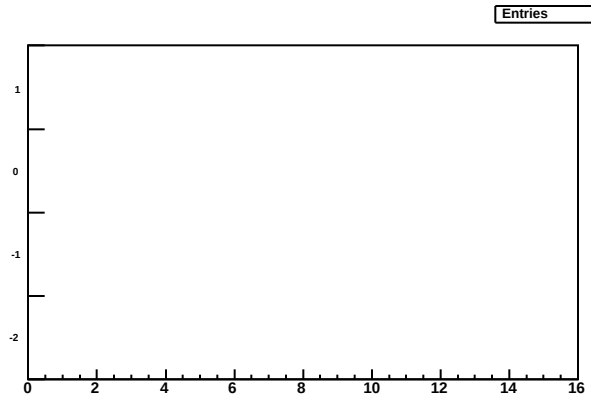
Unused (ch7)