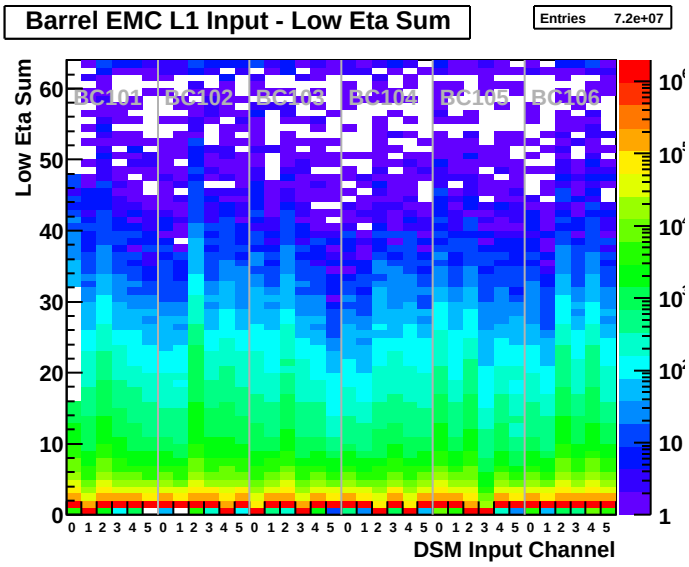
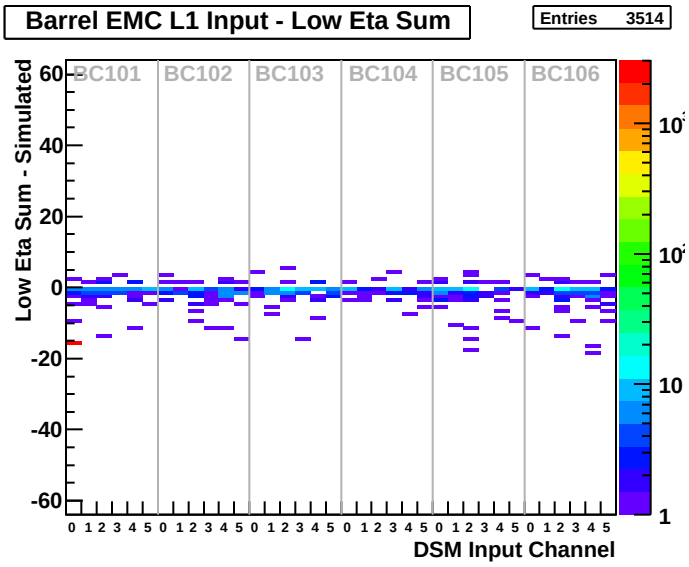
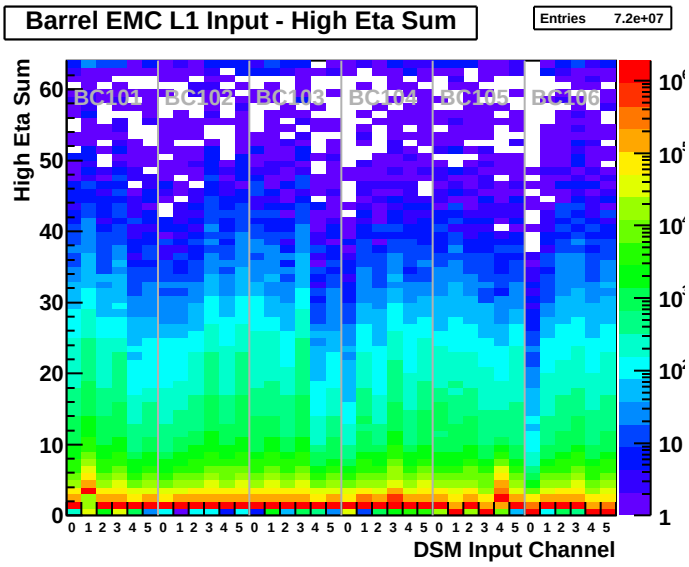
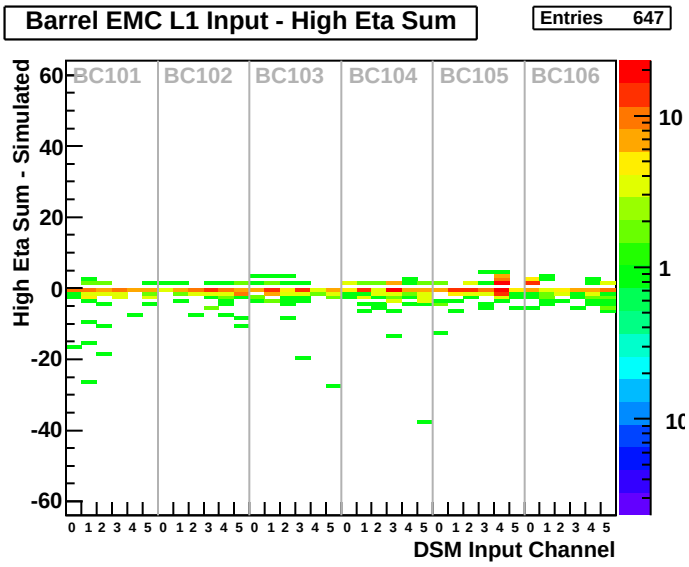
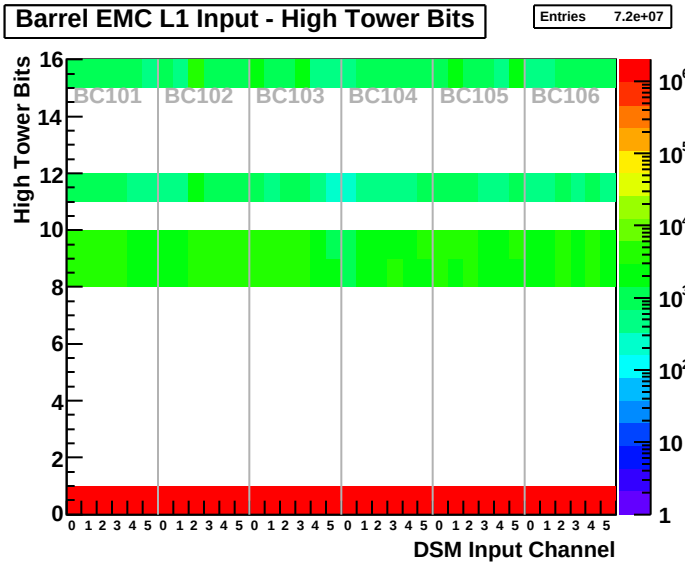
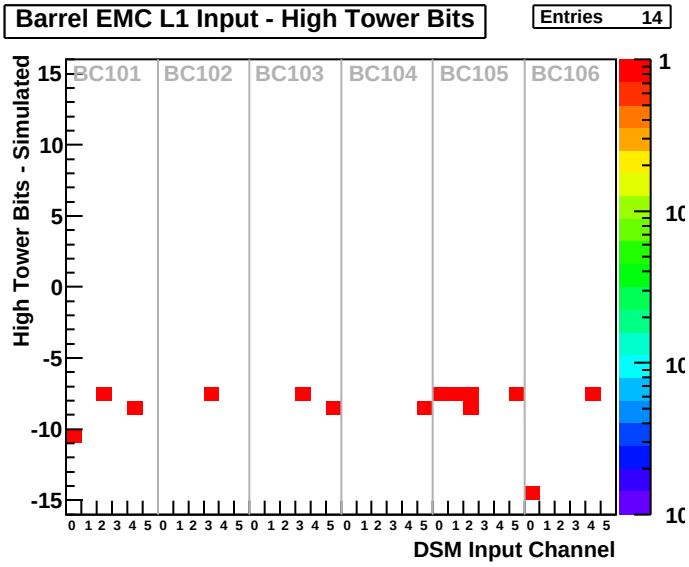

Barrel EMC L1 Input - Low Eta Sum
Entries 7.2e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

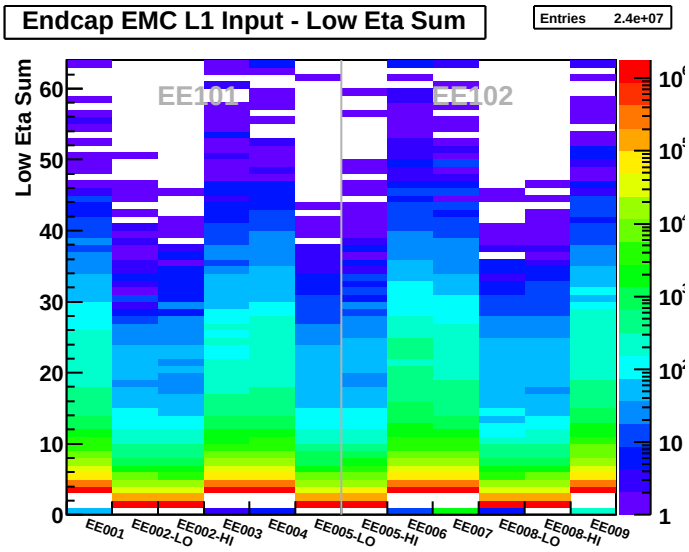

Barrel EMC L1 Input - Low Eta Sum
Entries 3514
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

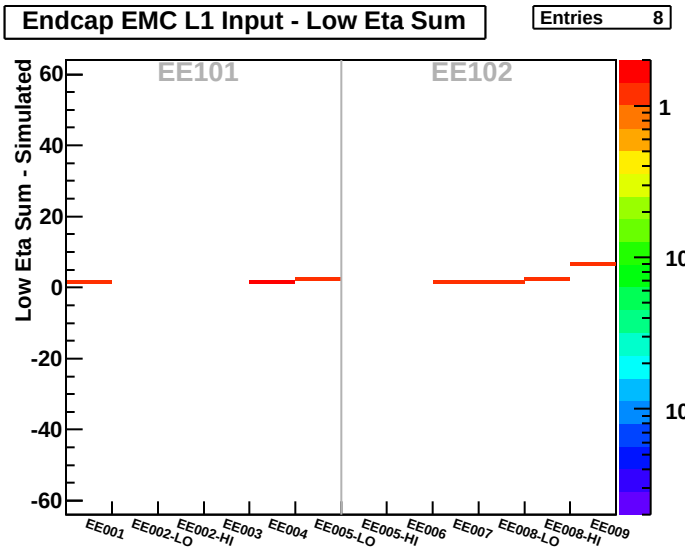

Barrel EMC L1 Input - High Eta Sum
Entries 7.2e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

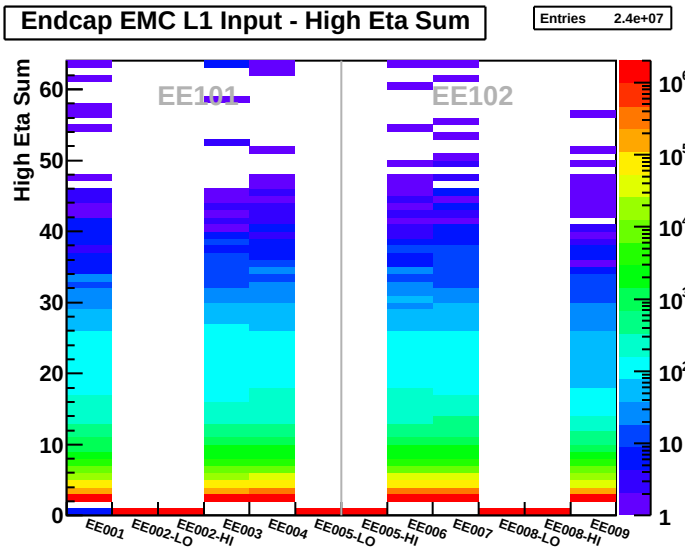

Barrel EMC L1 Input - High Eta Sum
Entries 647
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

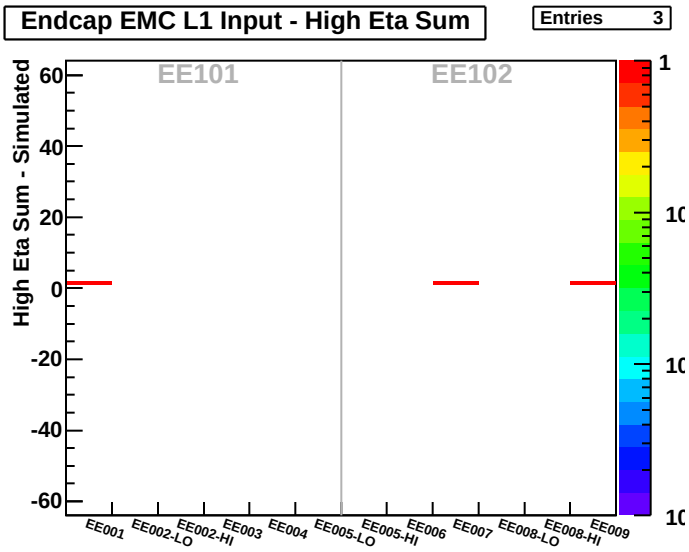

Barrel EMC L1 Input - High Tower Bits
Entries 7.2e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

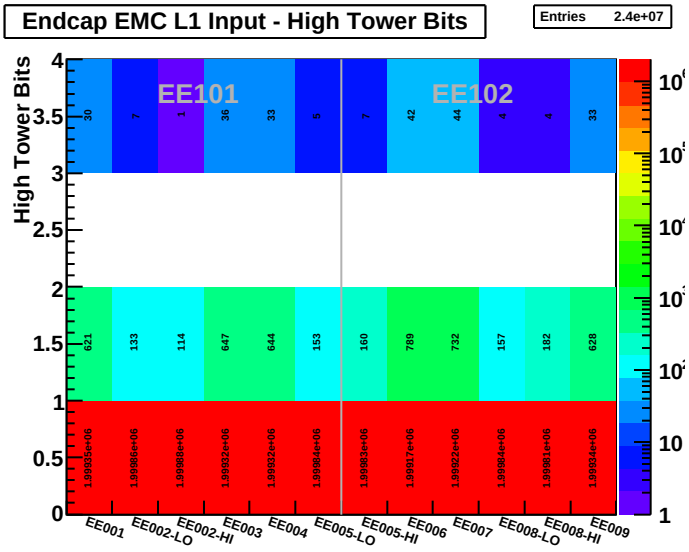

Barrel EMC L1 Input - High Tower Bits
Entries 14
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

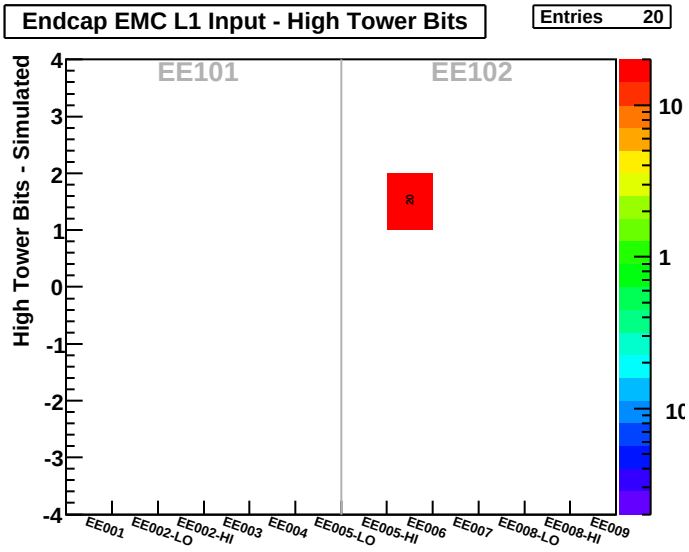

Endcap EMC L1 Input - Low Eta Sum
Entries 2.4e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

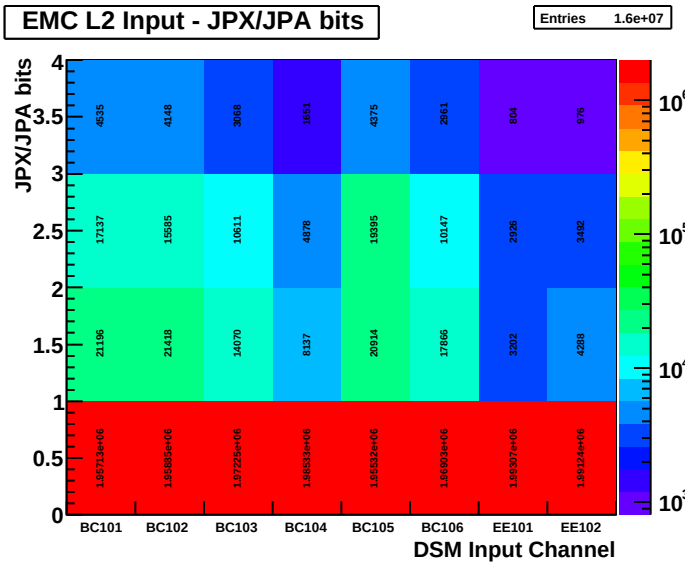

Endcap EMC L1 Input - Low Eta Sum
Entries 8
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

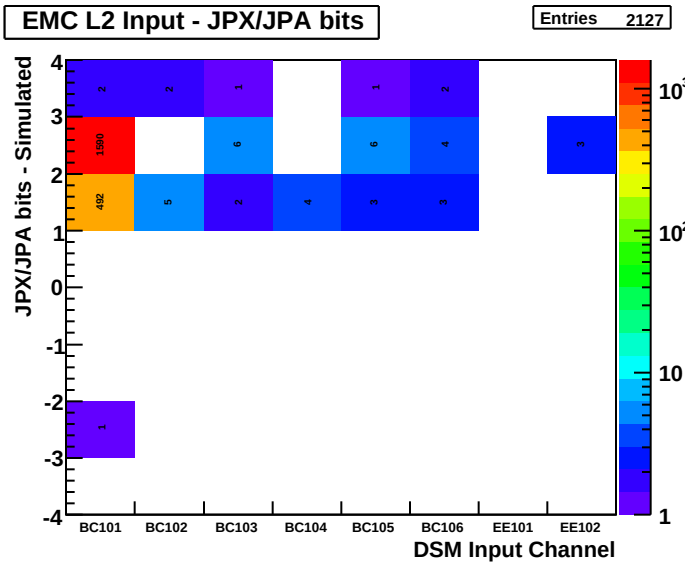

Endcap EMC L1 Input - High Eta Sum
Entries 2.4e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

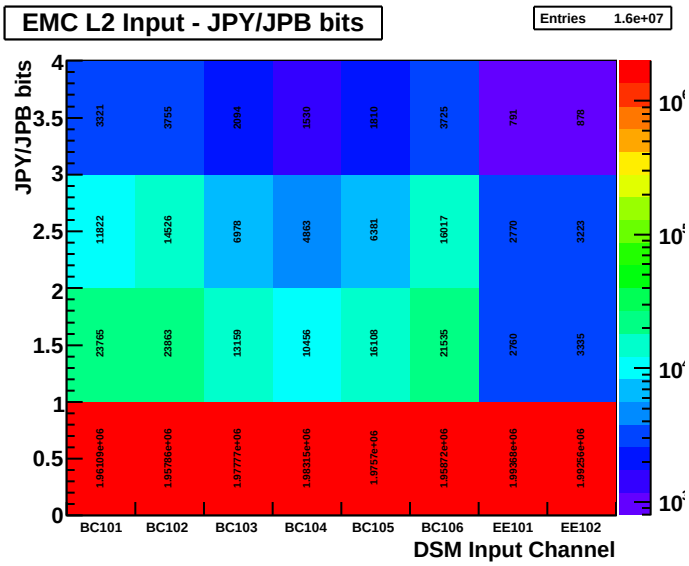

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

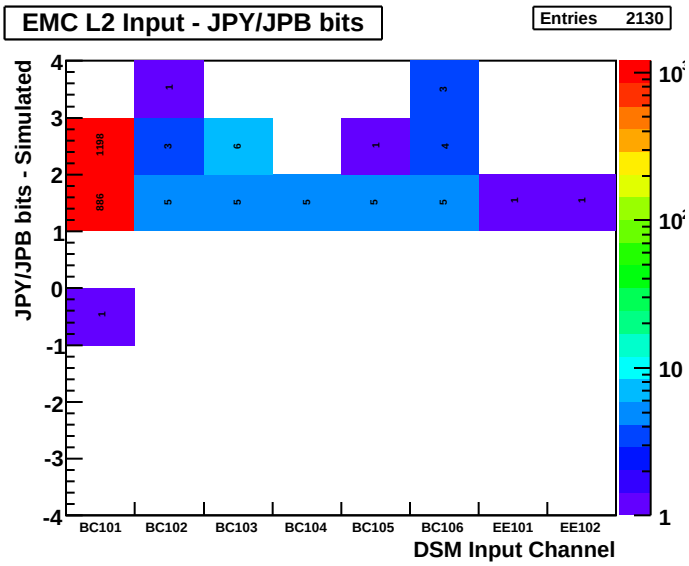

Endcap EMC L1 Input - High Tower Bits
Entries 2.4e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

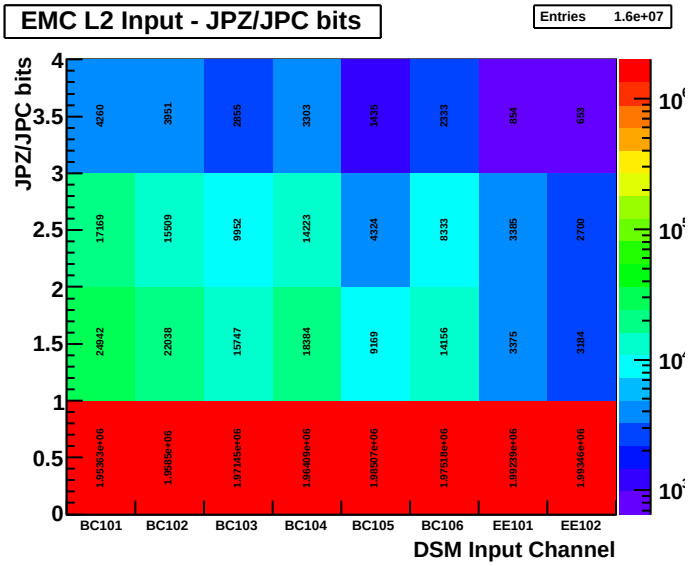

Endcap EMC L1 Input - High Tower Bits
Entries 20
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

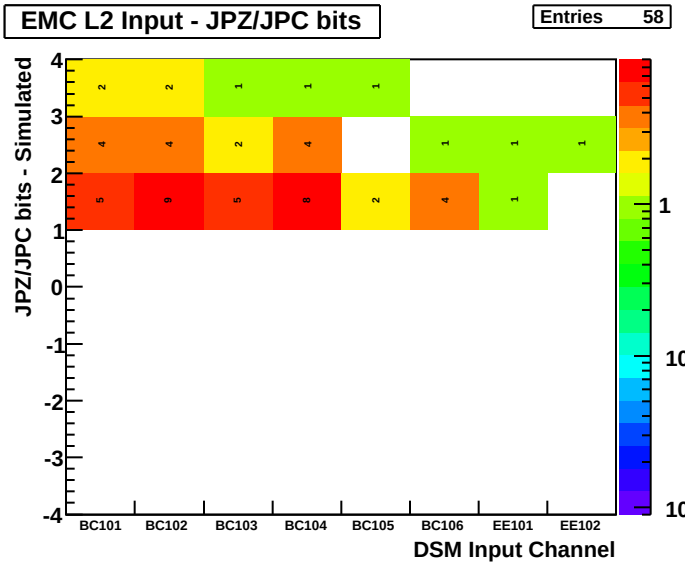

EMC L2 Input - JPX/JPA bits
Entries 1.6e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

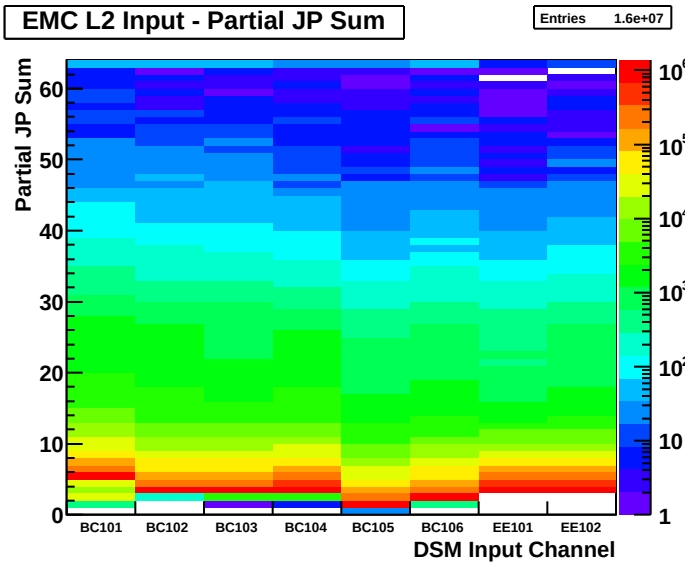

EMC L2 Input - JPX/JPA bits
Entries 2127
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

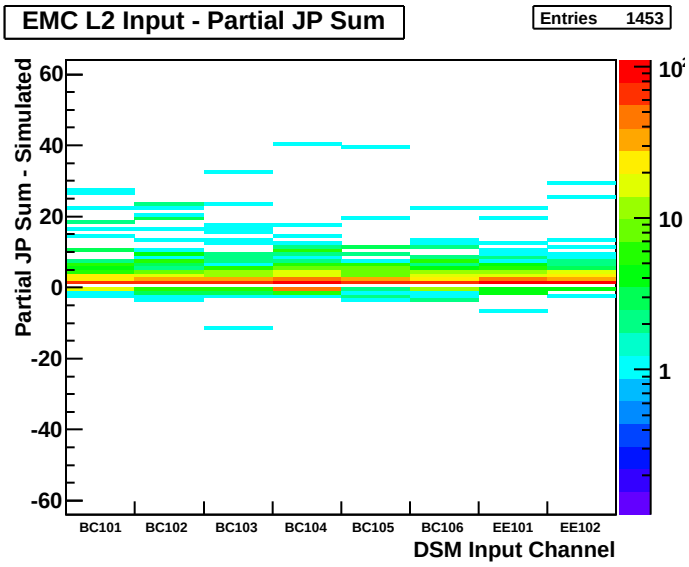

EMC L2 Input - JPY/JPB bits
Entries 1.6e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

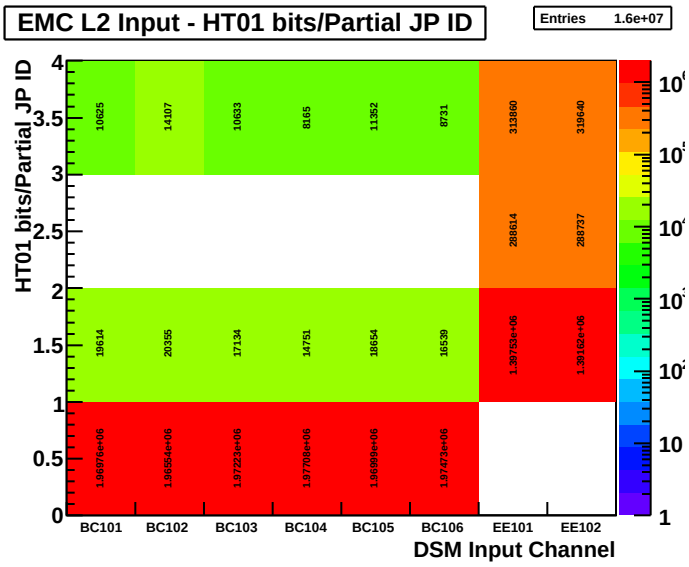

EMC L2 Input - JPY/JPB bits
Entries 2130
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

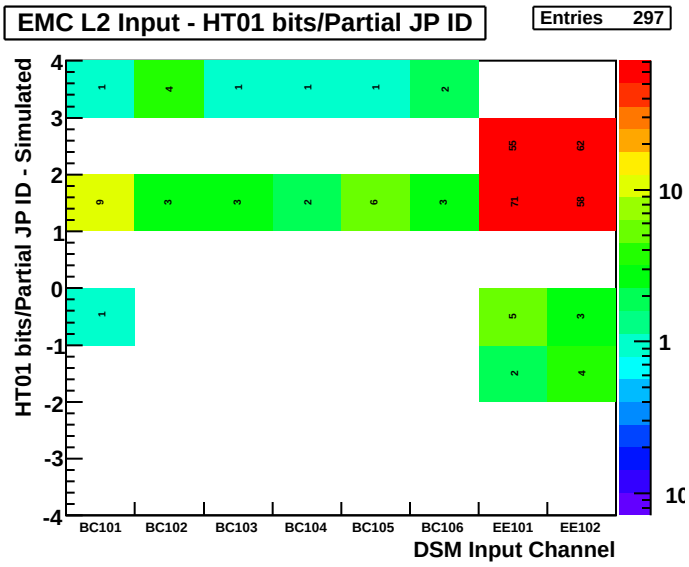

EMC L2 Input - JPZ/JPC bits
Entries 1.6e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

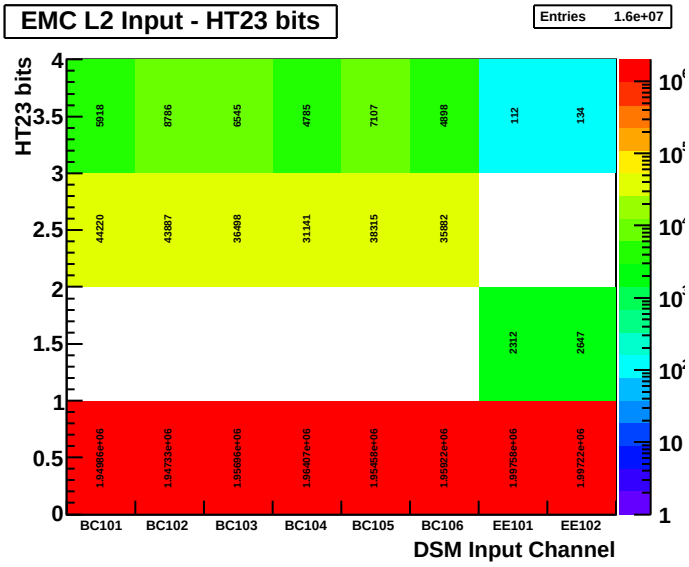

EMC L2 Input - JPZ/JPC bits
Entries 58
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

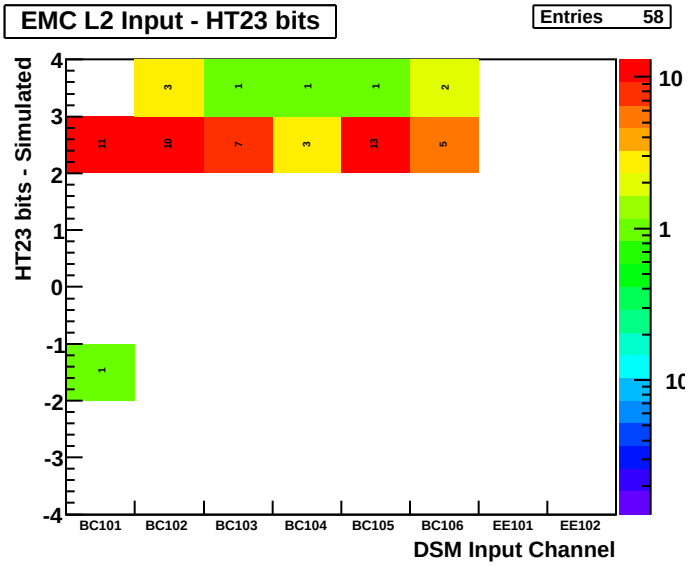

EMC L2 Input - Partial JP Sum
Entries 1.6e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

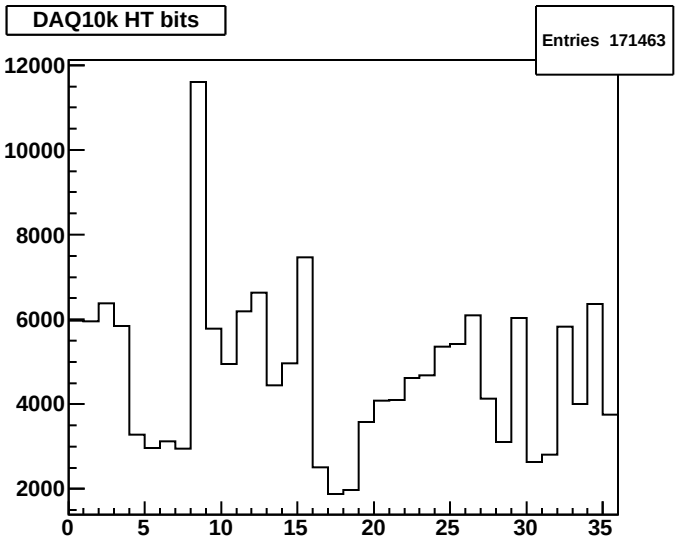

EMC L2 Input - Partial JP Sum
Entries 1453
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

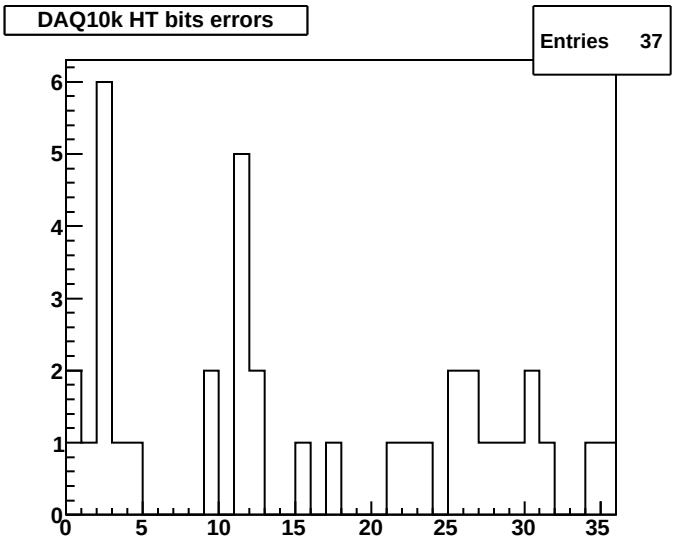

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.6e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

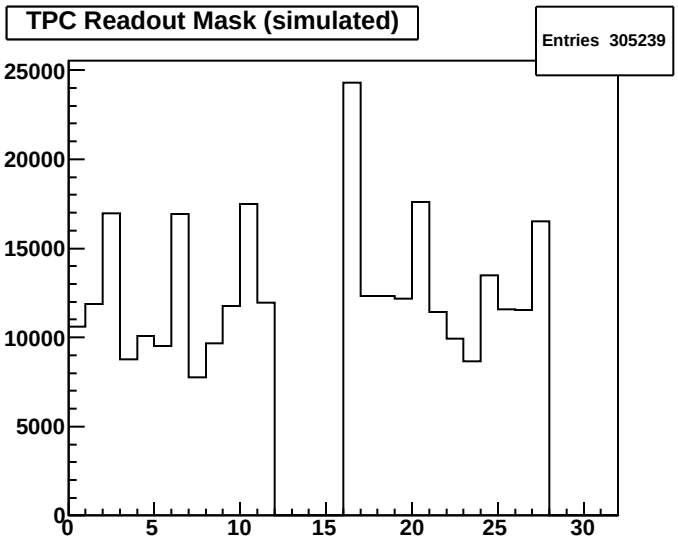

EMC L2 Input - HT01 bits/Partial JP ID
Entries 297
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

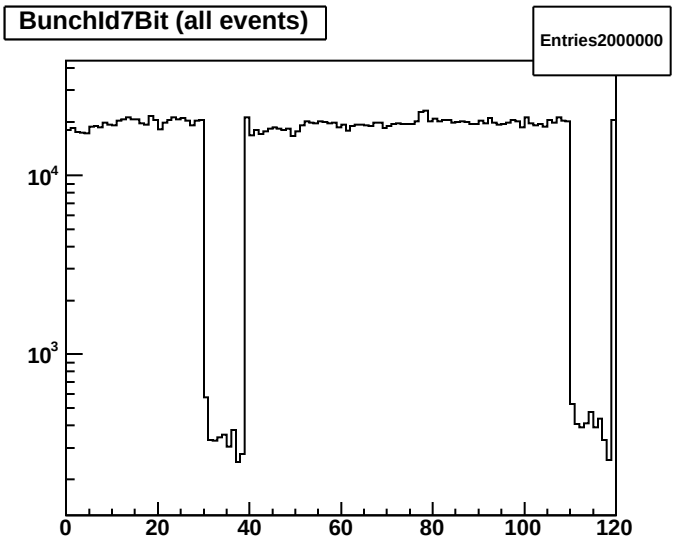

EMC L2 Input - HT23 bits
Entries 1.6e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

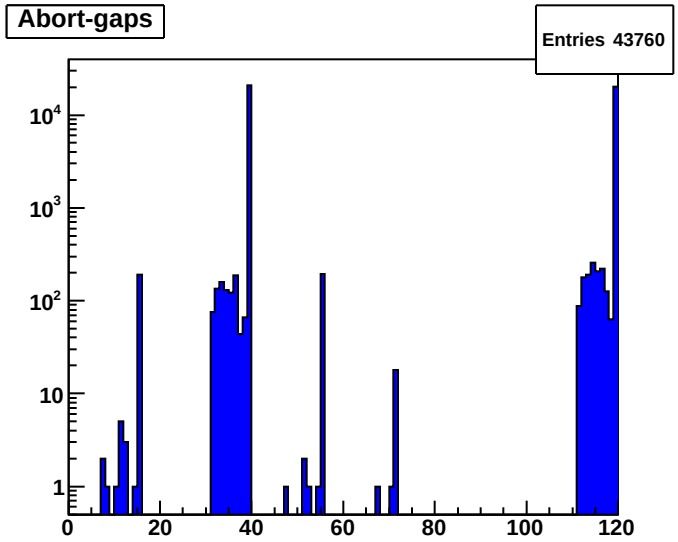

EMC L2 Input - HT23 bits
Entries 58
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

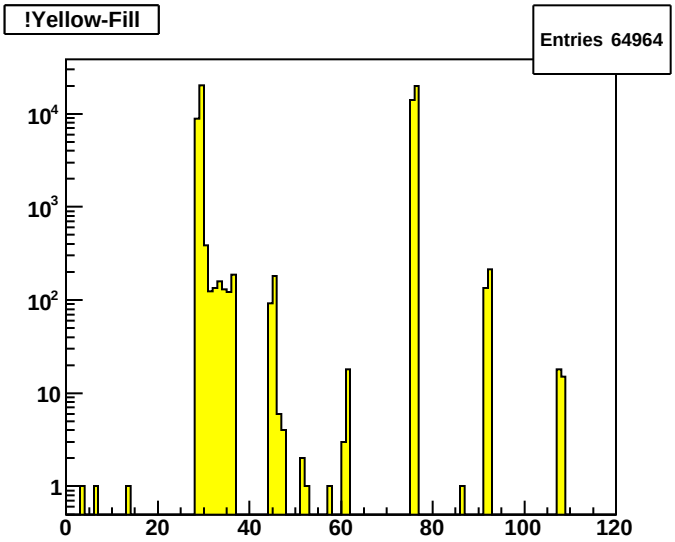

DAQ10k HT bits
Entries 171463

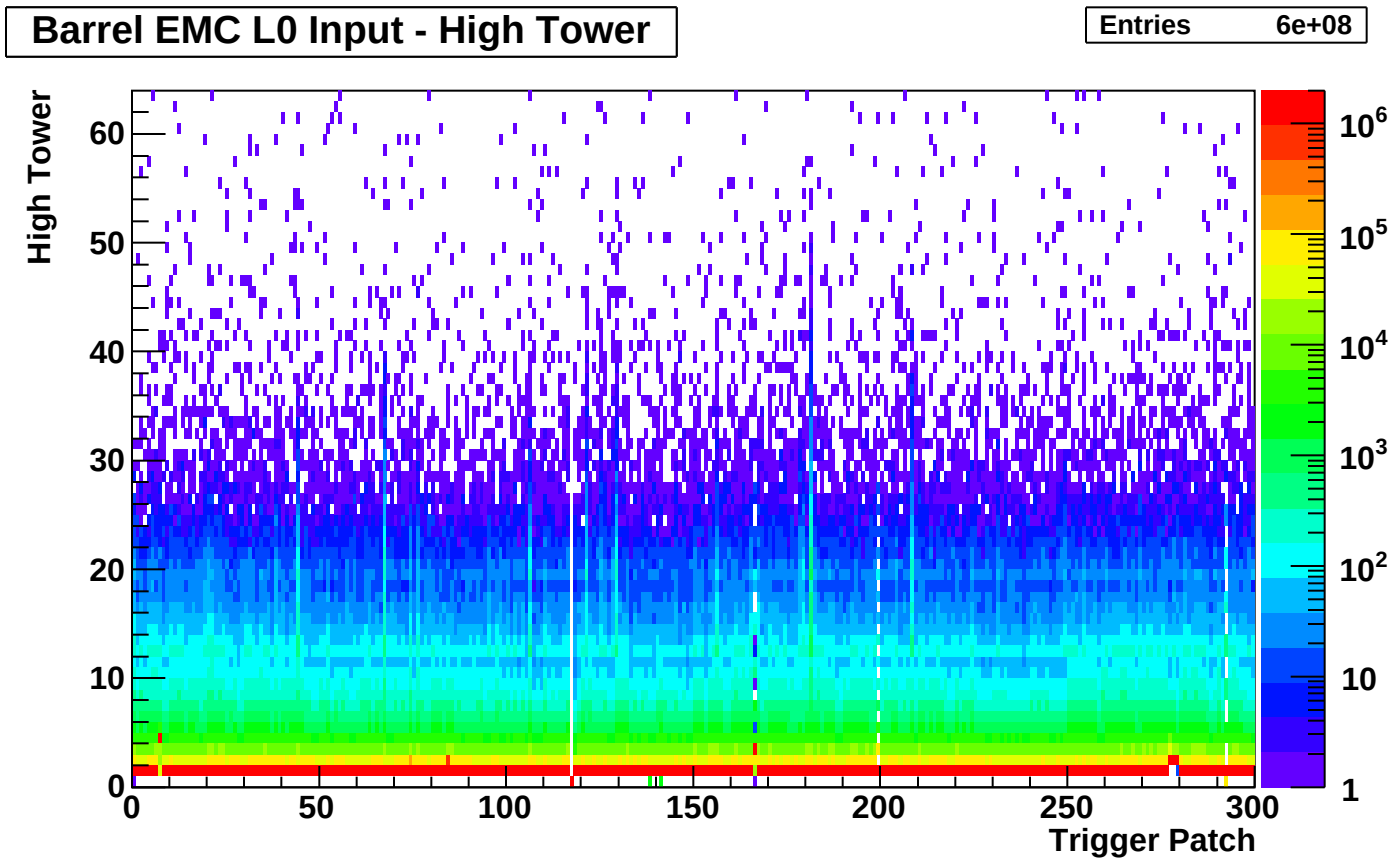

DAQ10k HT bits errors
Entries 37

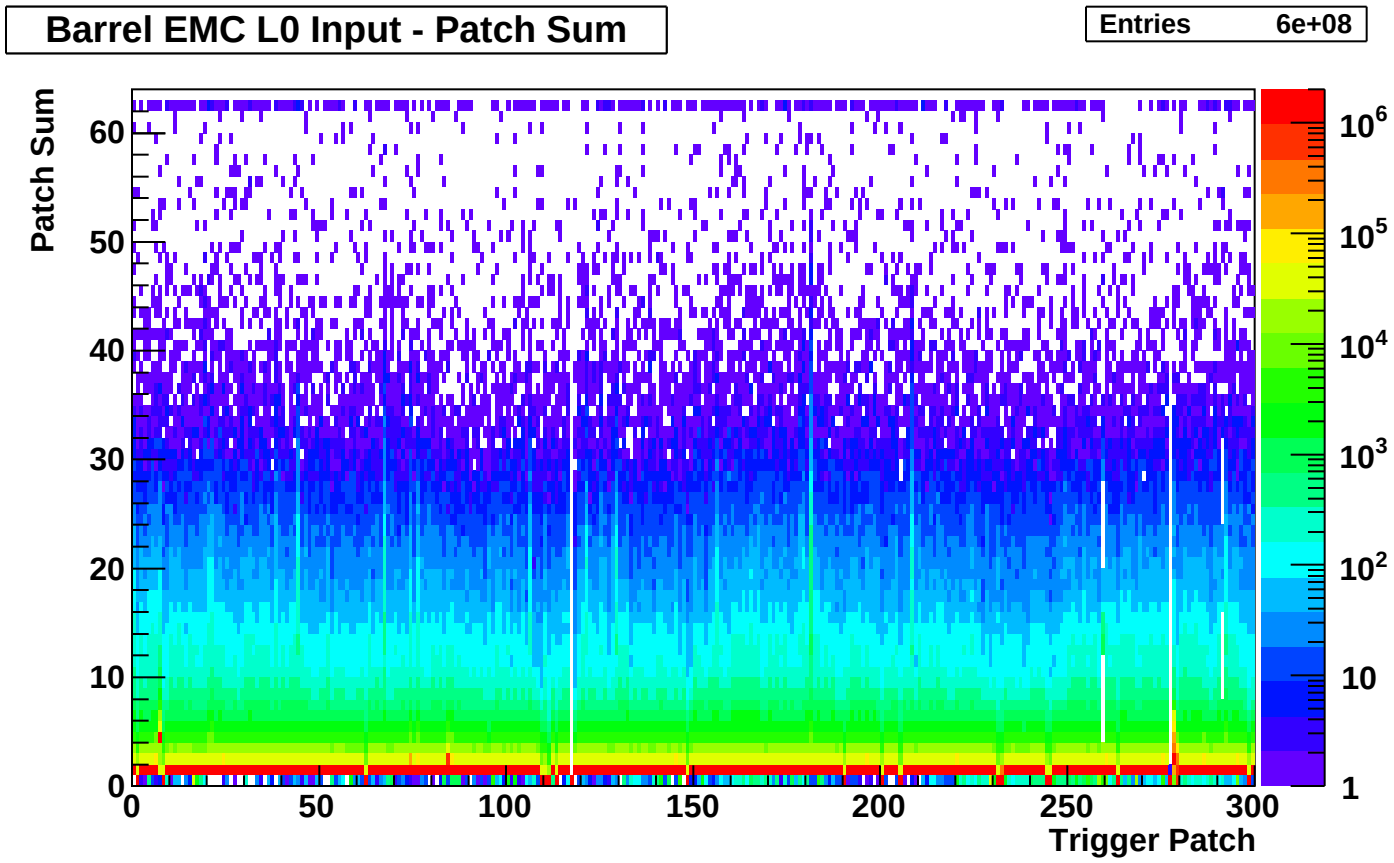

TPC Readout Mask (simulated)
Entries 305239

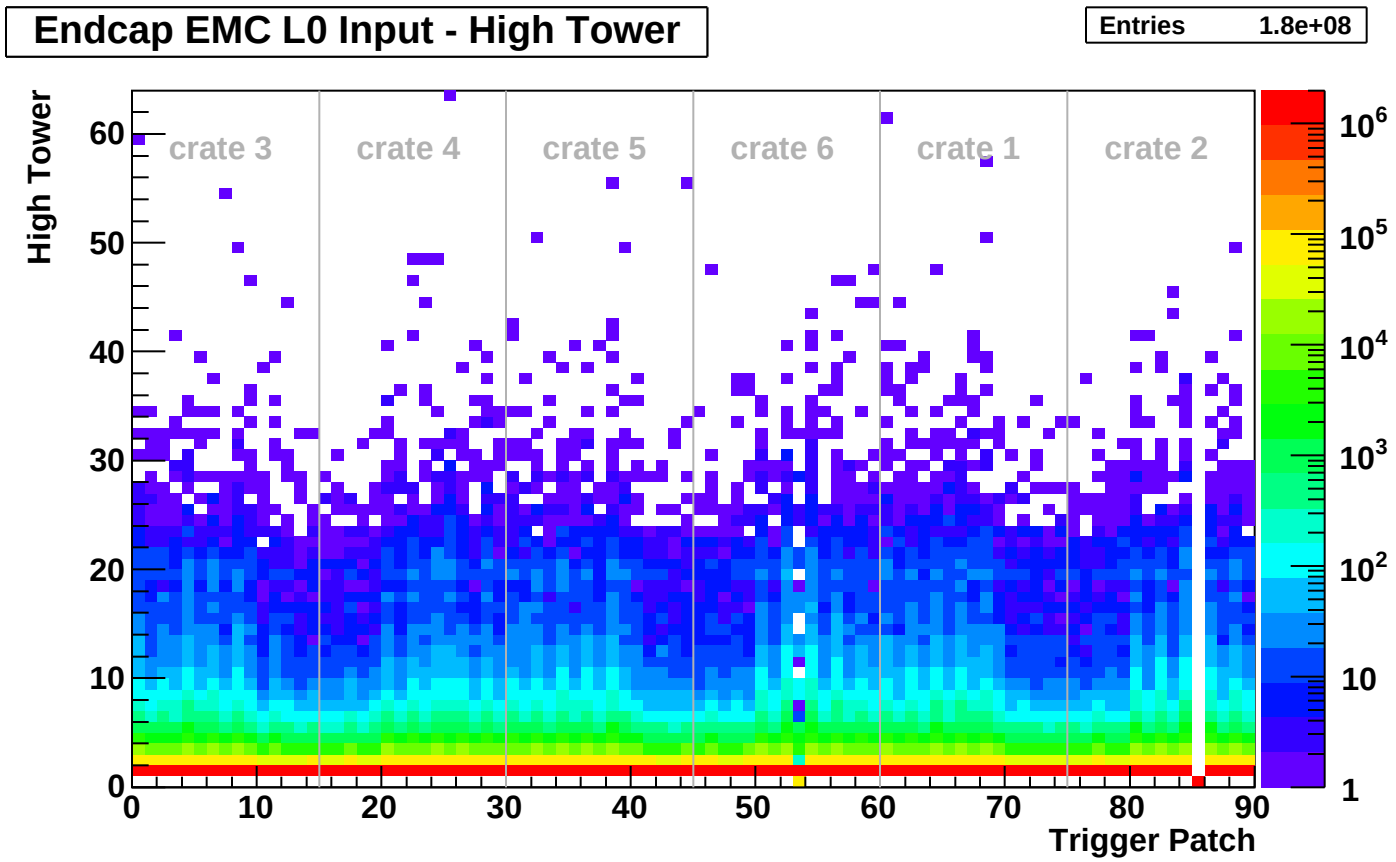

BunchId7Bit (all events)
Entries2000000

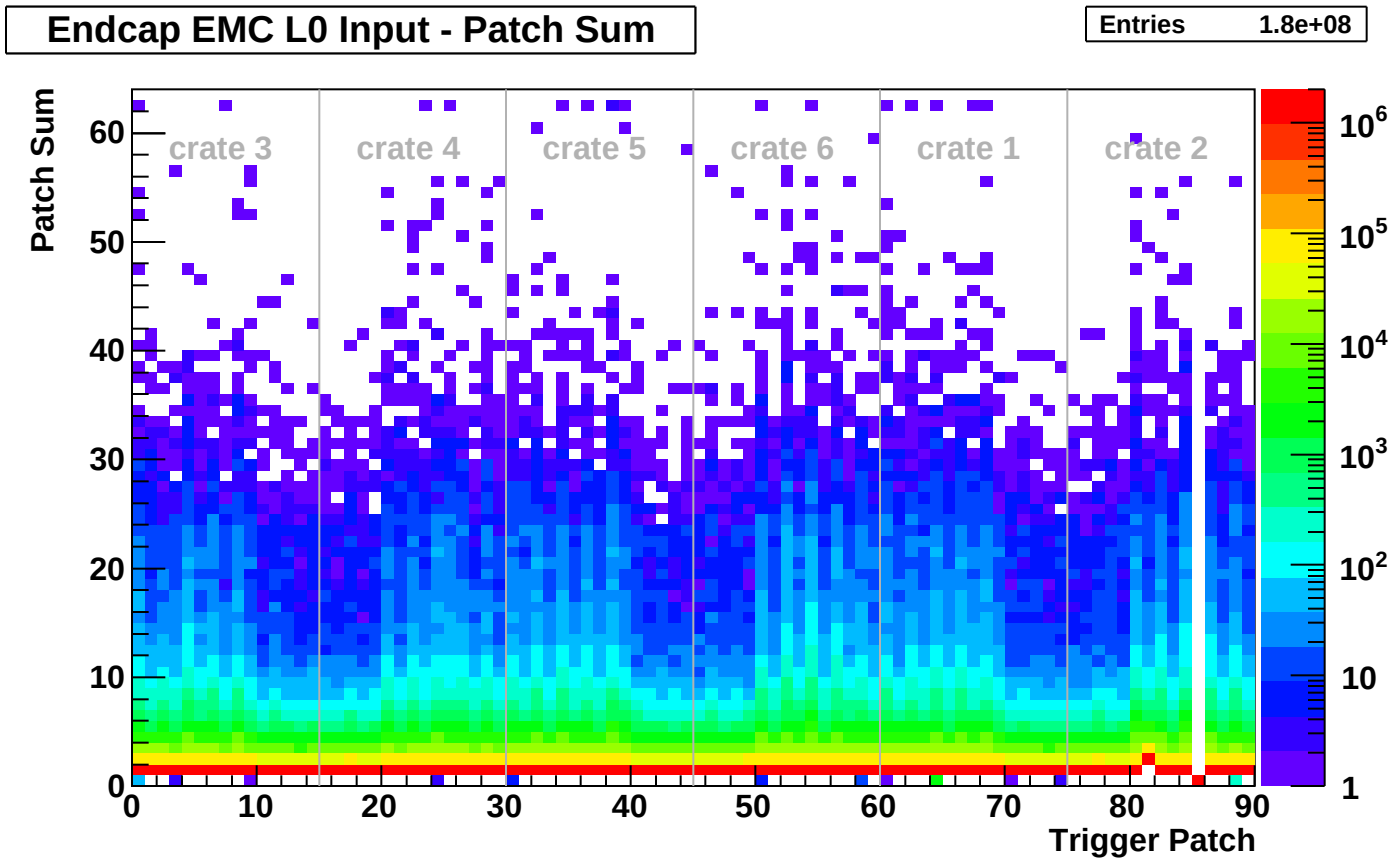

Abort-gaps
Entries 43760

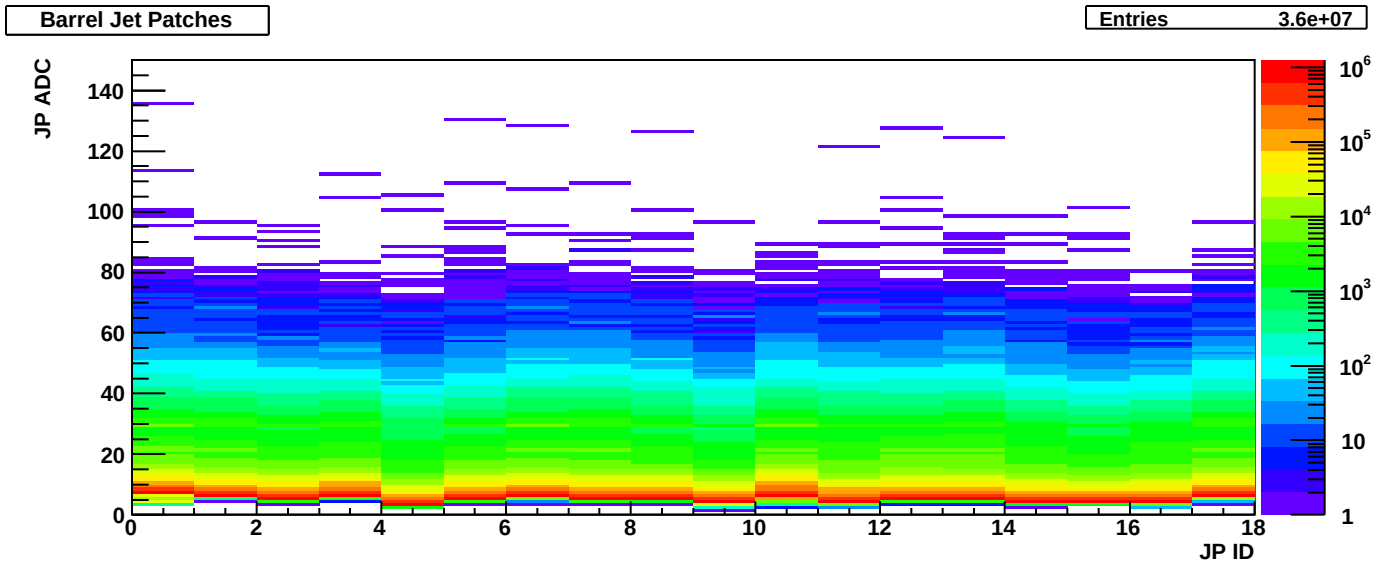

!Yellow-Fill
Entries 64964

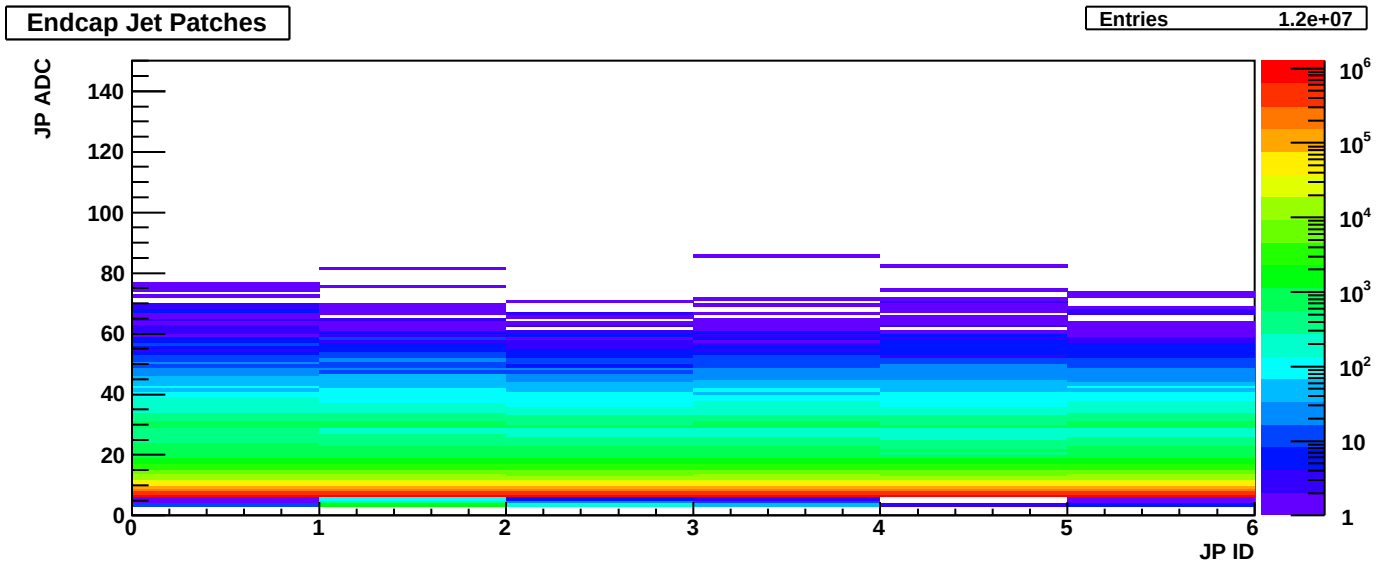

Barrel EMC L0 Input - High Tower
Entries 6e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

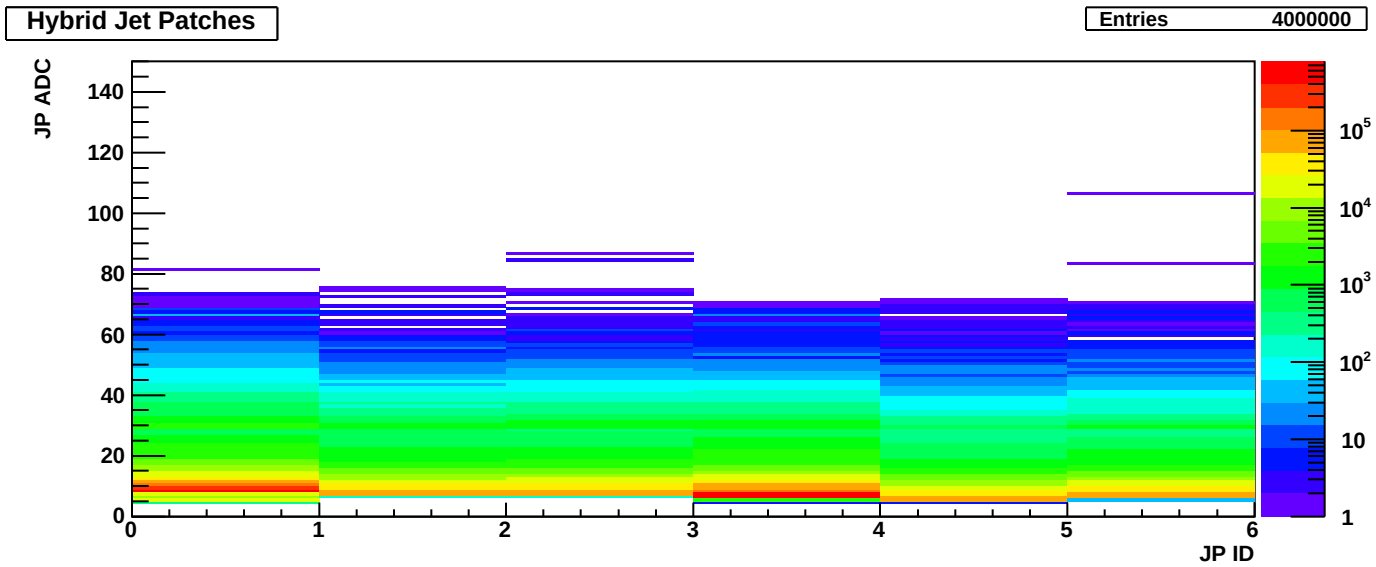

Barrel EMC L0 Input - Patch Sum
Entries 6e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

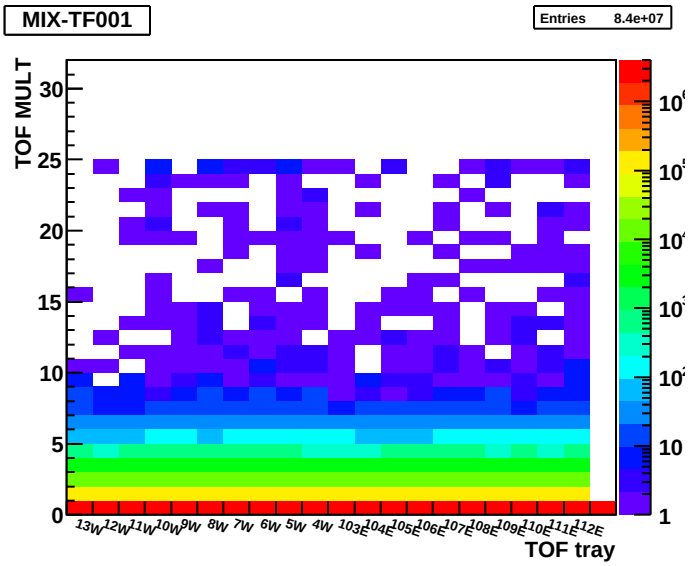

Endcap EMC L0 Input - High Tower
Entries 1.8e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

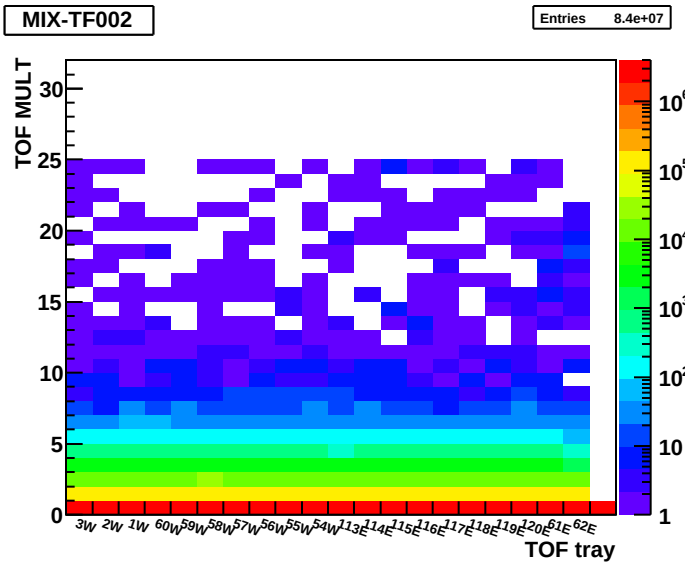

Endcap EMC L0 Input - Patch Sum
Entries 1.8e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

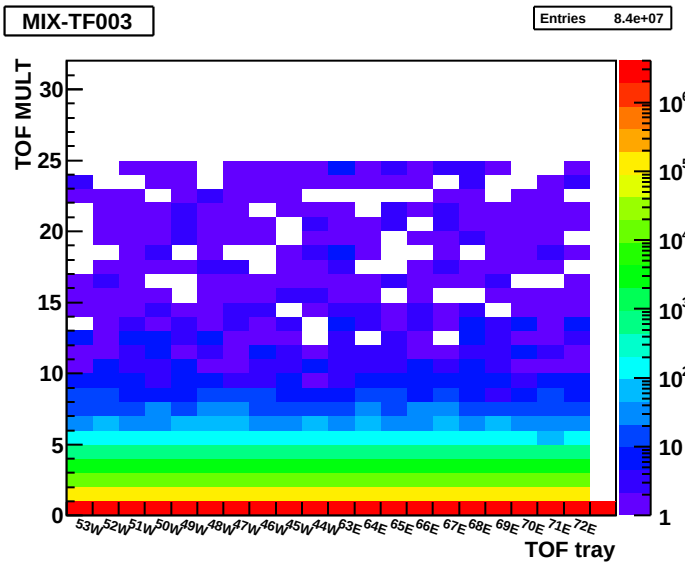

Barrel Jet Patches
Entries 3.6e+07
JP ADC
JP ID

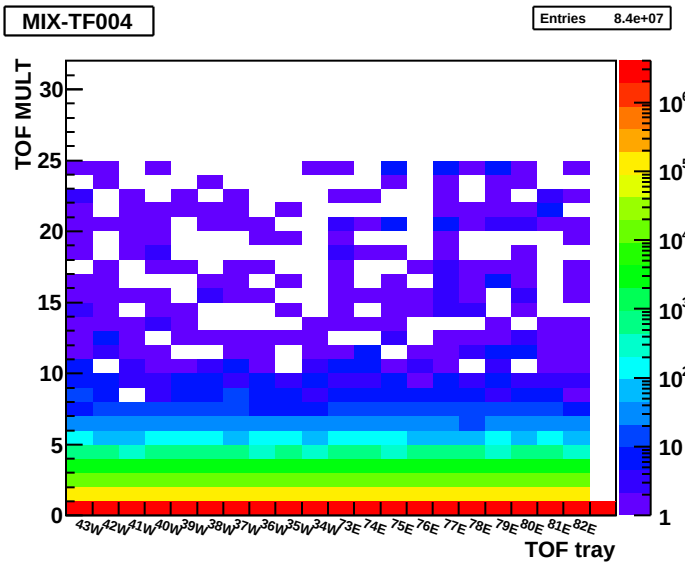

Endcap Jet Patches
Entries 1.2e+07
JP ADC
JP ID

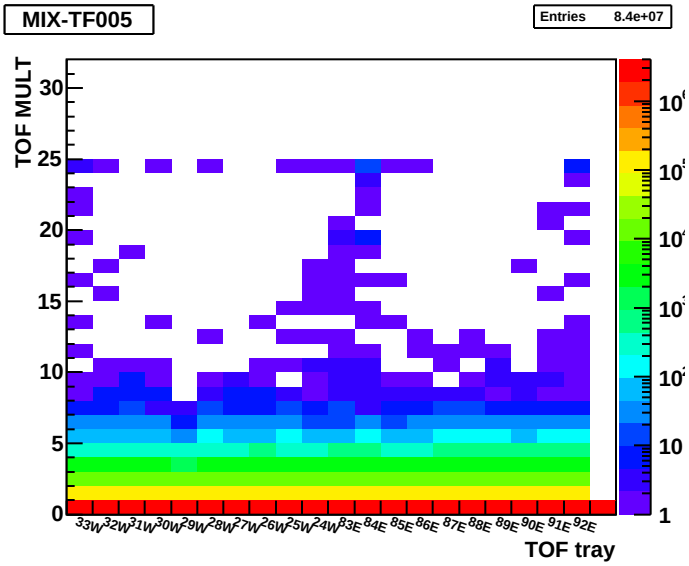

Hybrid Jet Patches
Entries 4000000
JP ADC
JP ID

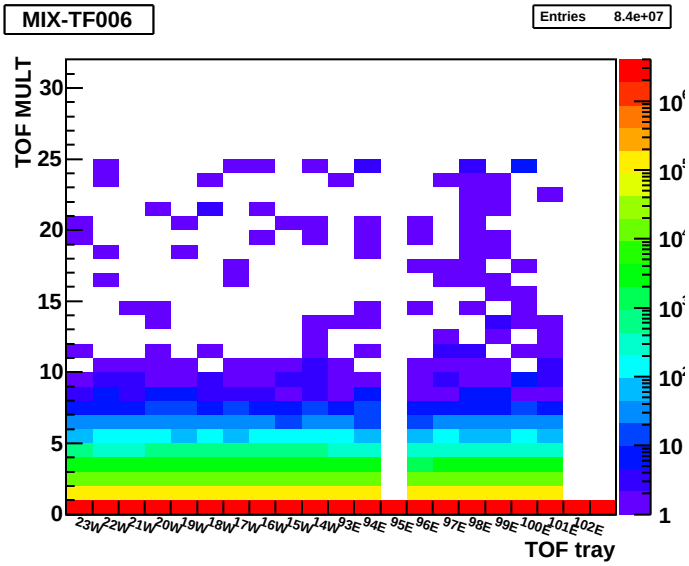

MIX-TF001
Entries 8.4e+07
TOF MULT
TOF tray

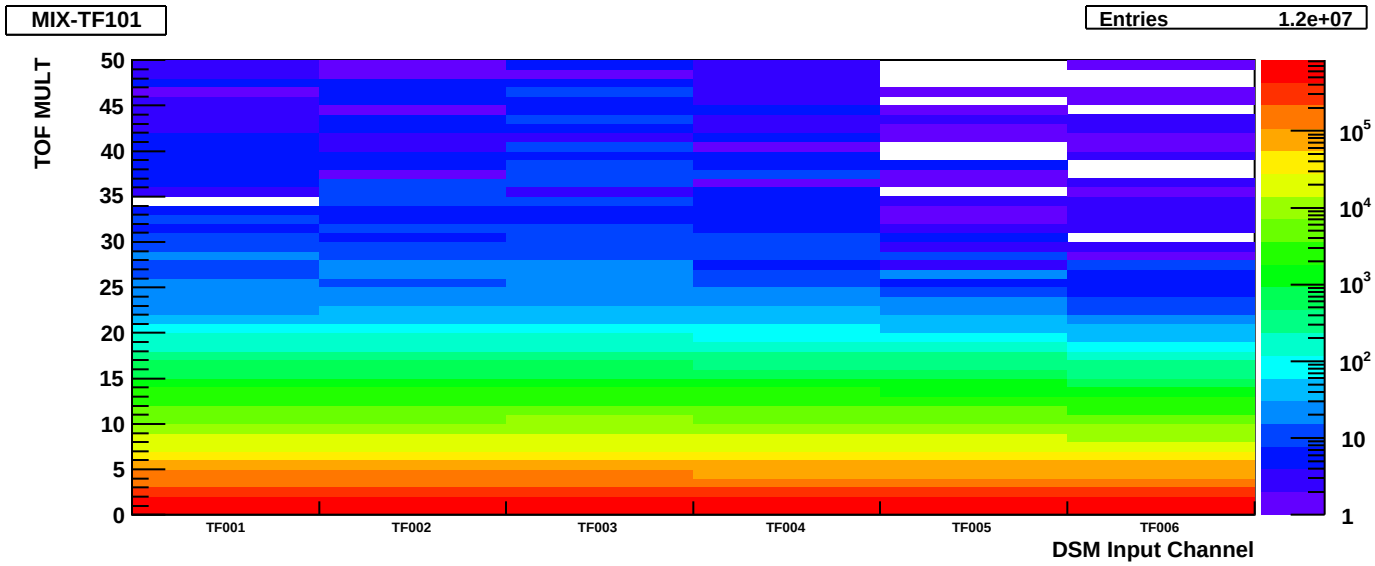

MIX-TF002
Entries 8.4e+07
TOF MULT
TOF tray

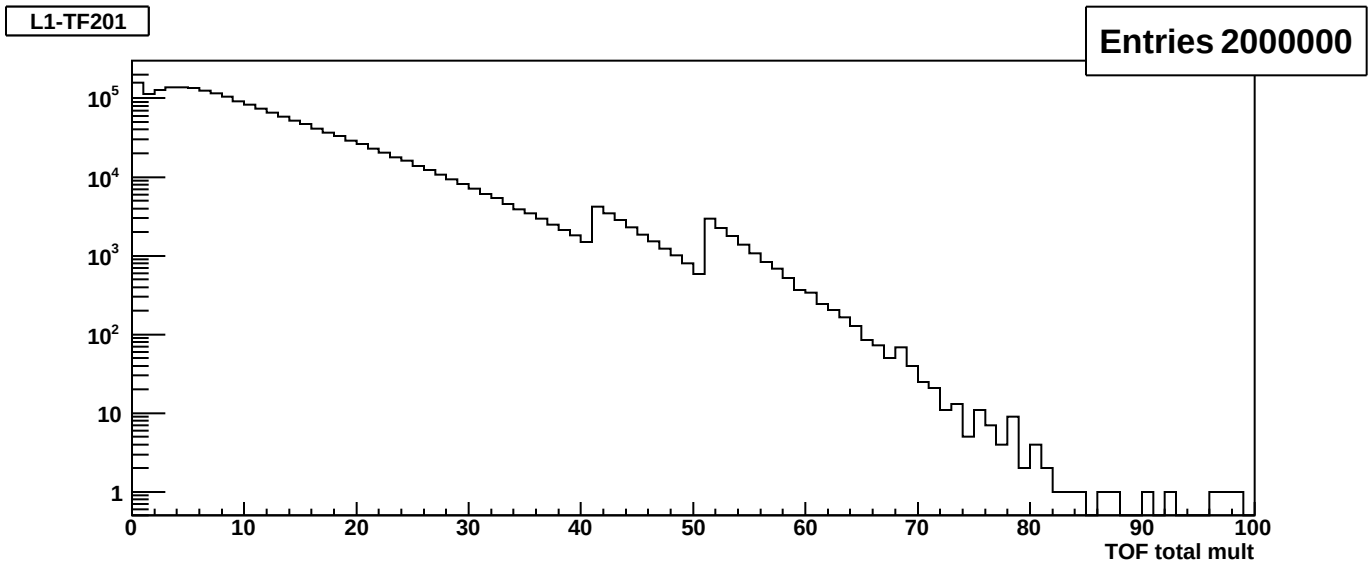

MIX-TF003
Entries 8.4e+07
TOF MULT
TOF tray

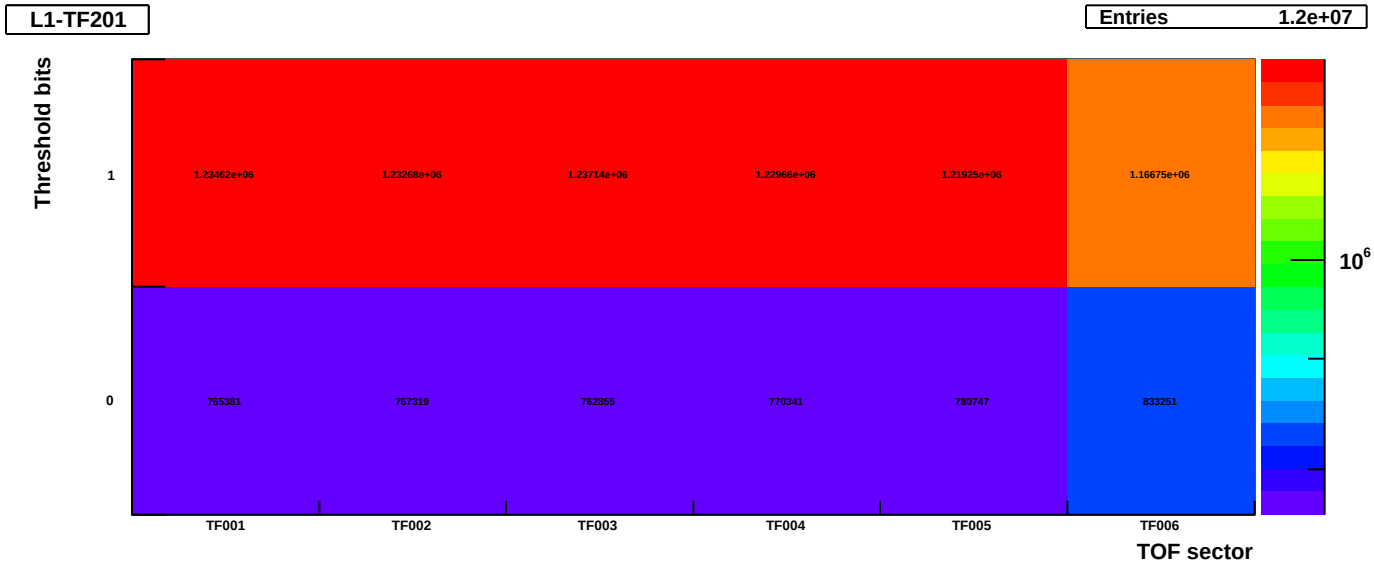

MIX-TF004
Entries 8.4e+07
TOF MULT
TOF tray

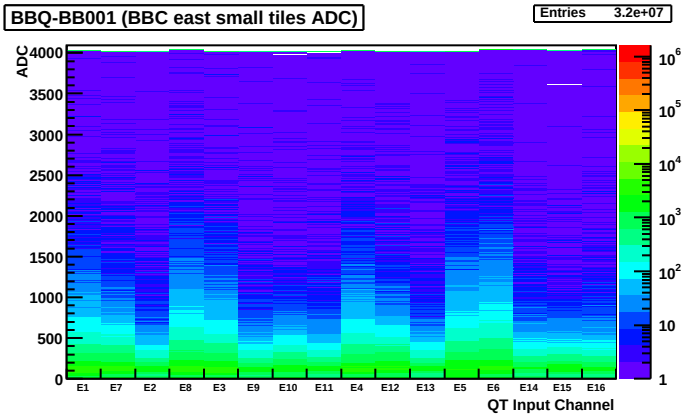

MIX-TF005
Entries 8.4e+07
TOF MULT
TOF tray

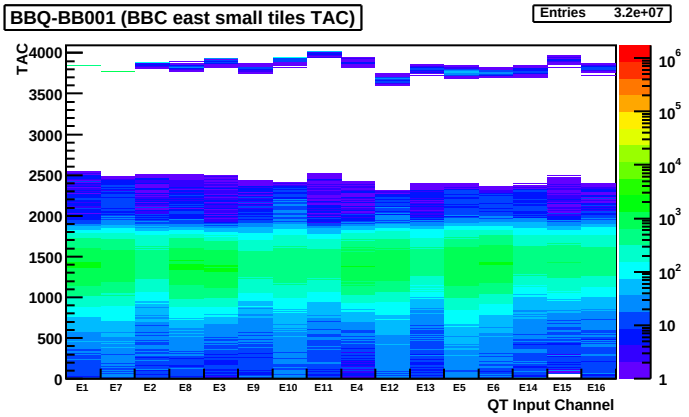

MIX-TF006
Entries 8.4e+07
TOF MULT
TOF tray

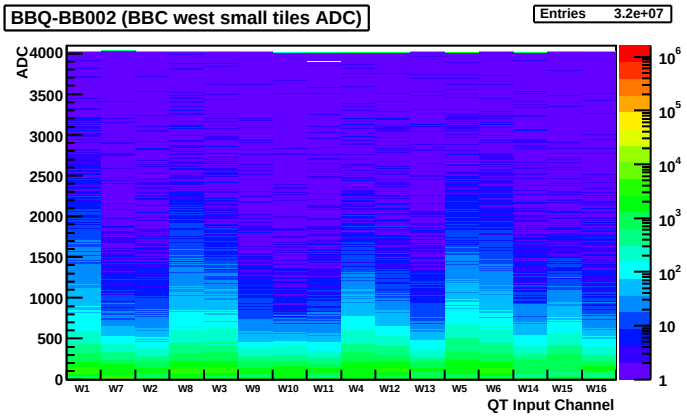

MIX-TF101
Entries 1.2e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

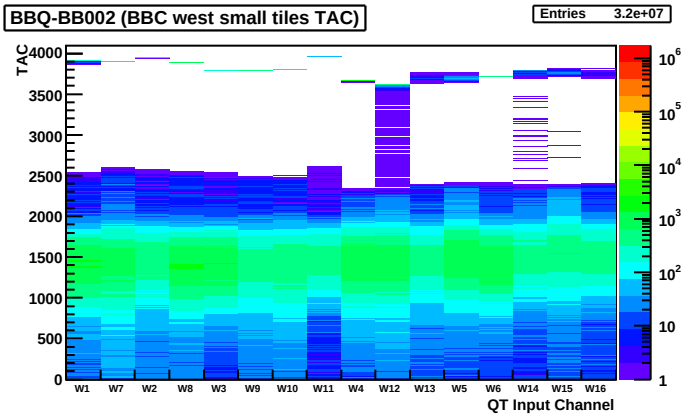

L1-TF201
Entries 2000000
TOF total mult

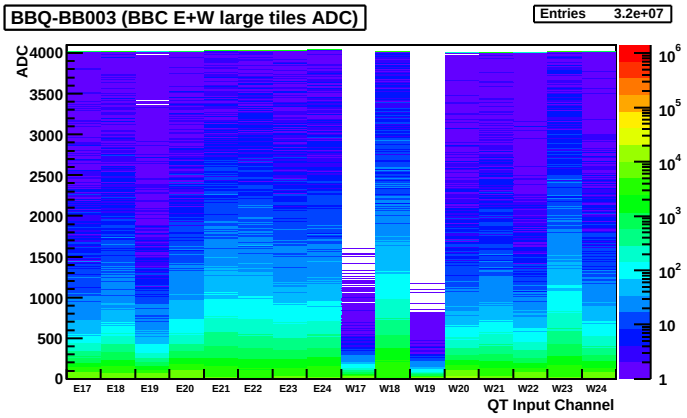

L1-TF201
Entries 1.2e+07
Threshold bits
1.23462e+06
1.23268e+06
1.23714e+06
1.22966e+06
1.21925e+06
1.16675e+06
765381
767319
762855
770341
780747
833251
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

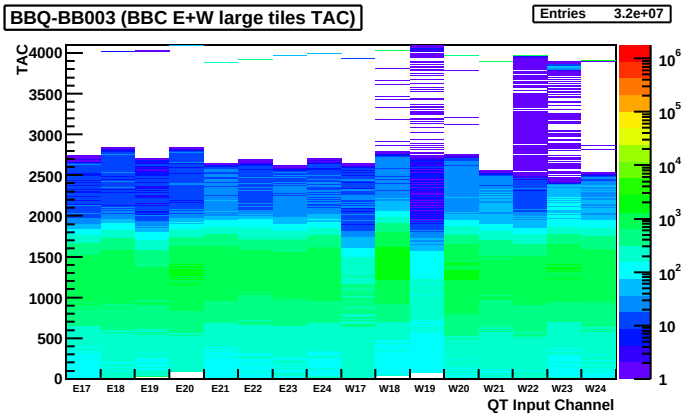

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

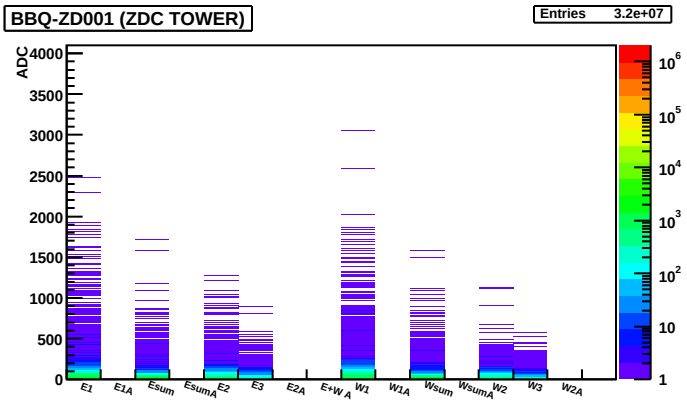

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

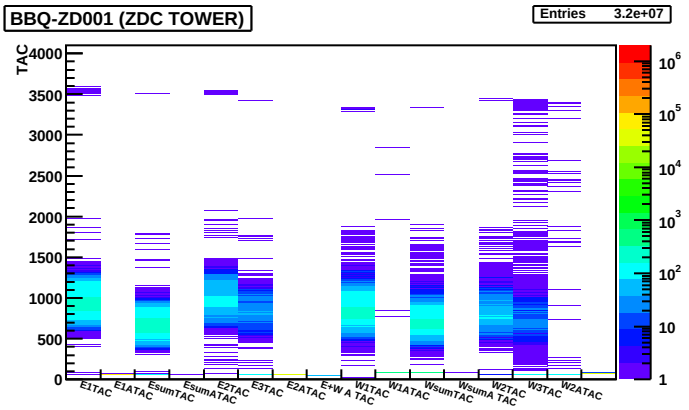

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

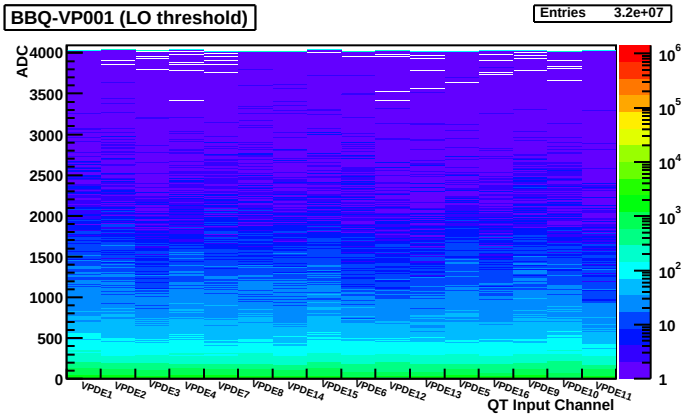

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

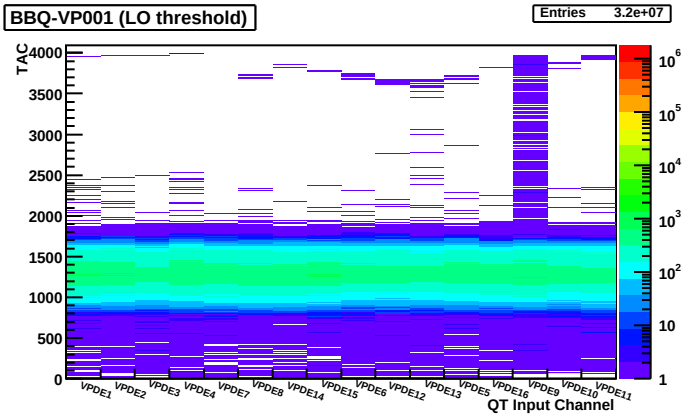

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

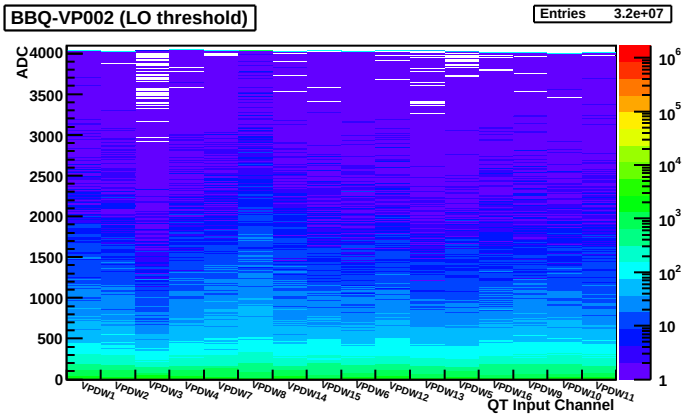

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

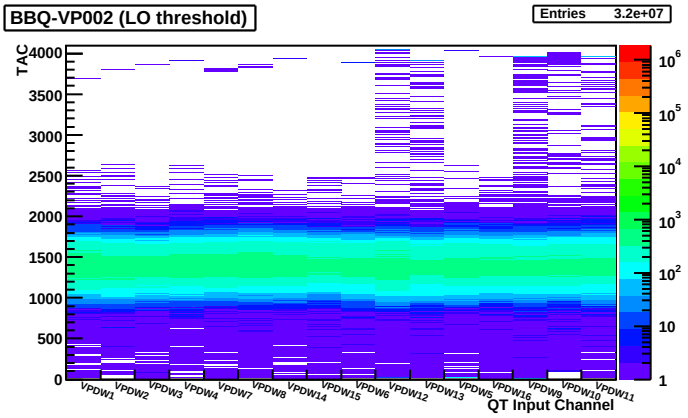

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
ADC

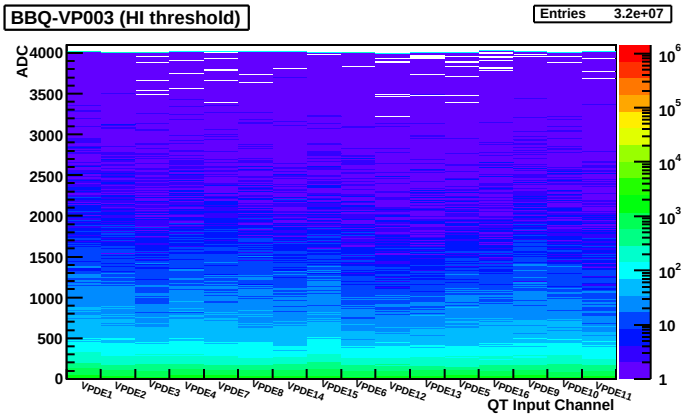

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
TAC

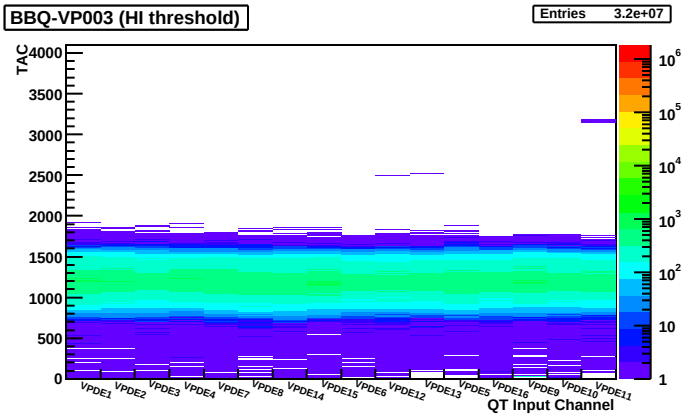

BBQ-VP001 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

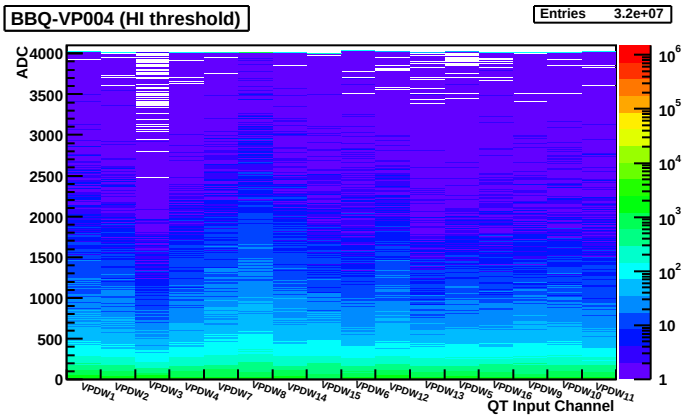

BBQ-VP001 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

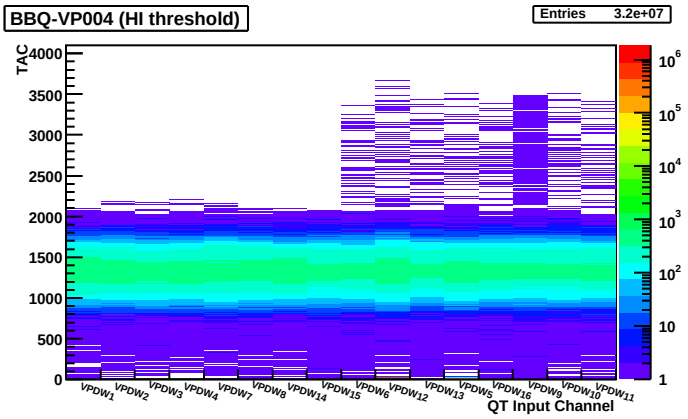

BBQ-VP002 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

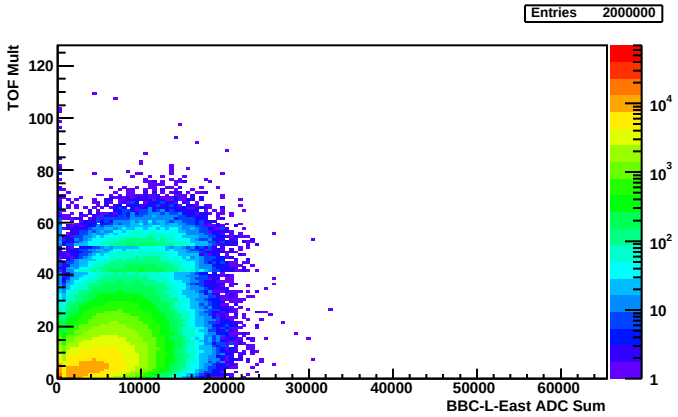

BBQ-VP002 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

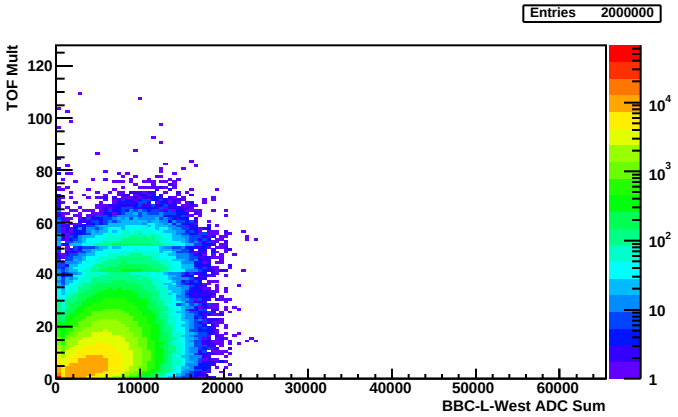

BBQ-VP003 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

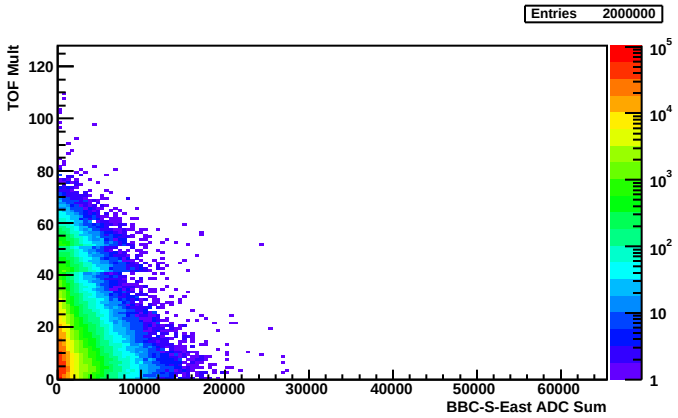

BBQ-VP003 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

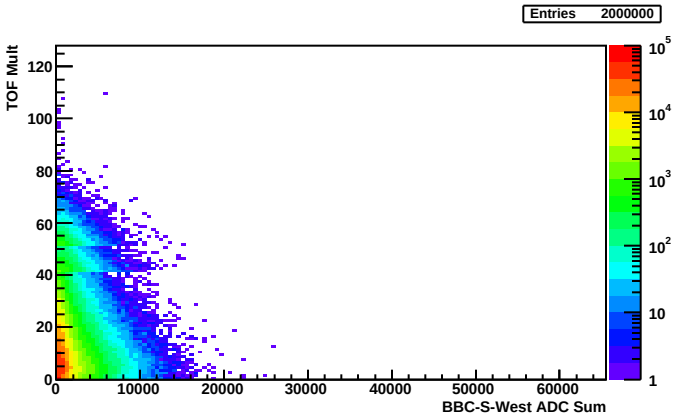

BBQ-VP004 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

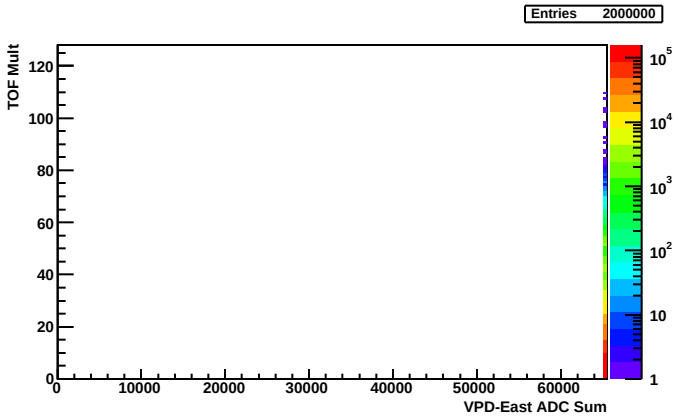

BBQ-VP004 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

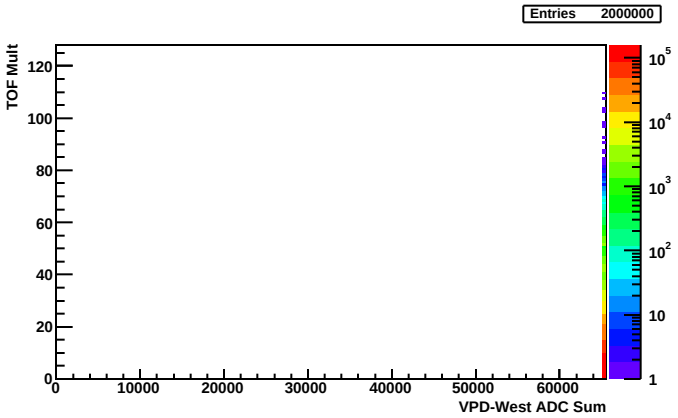

Entries 2000000
TOF Mult
BBC-L-East ADC Sum

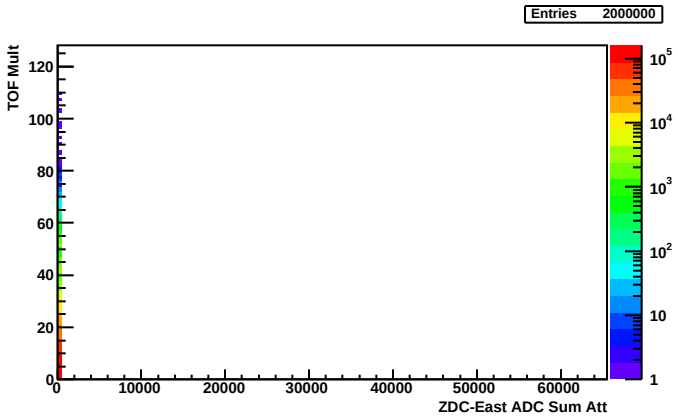

Entries 2000000
TOF Mult
BBC-L-West ADC Sum

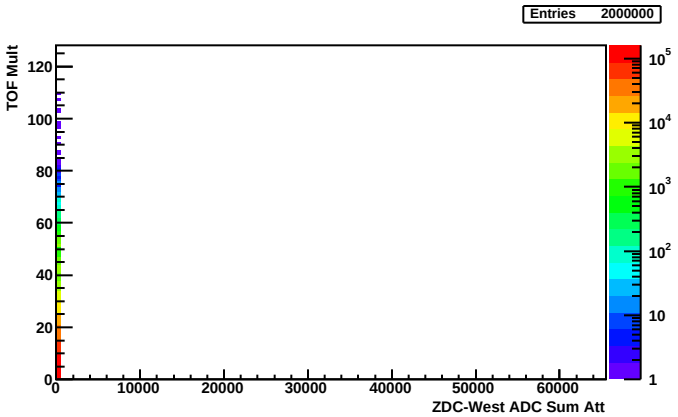

Entries 2000000
TOF Mult
BBC-S-East ADC Sum

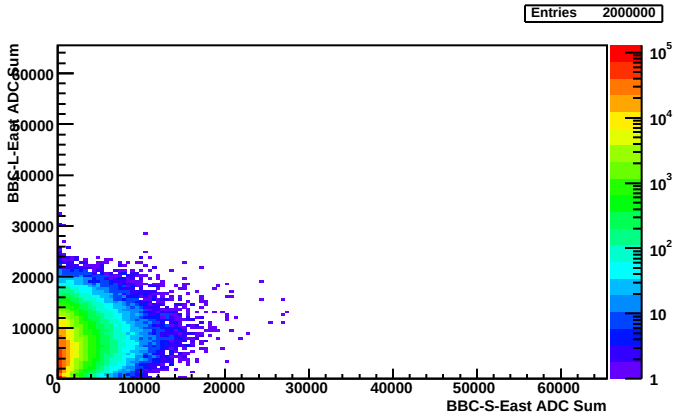

Entries 2000000
TOF Mult
BBC-S-West ADC Sum

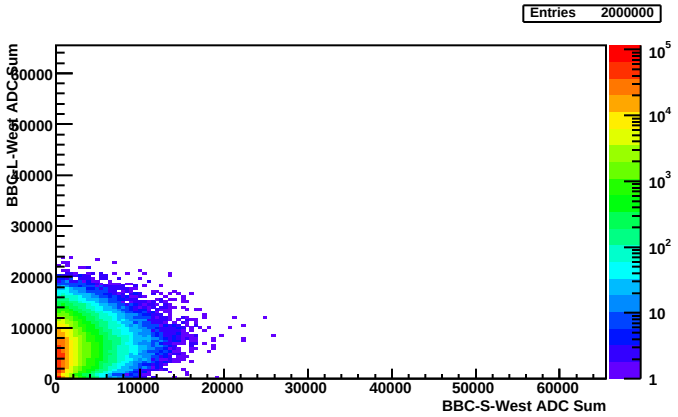

Entries 2000000
TOF Mult
VPD-East ADC Sum

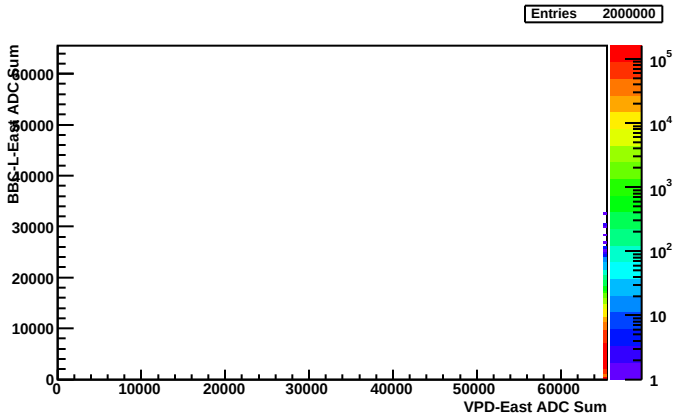

Entries 2000000
TOF Mult
VPD-West ADC Sum

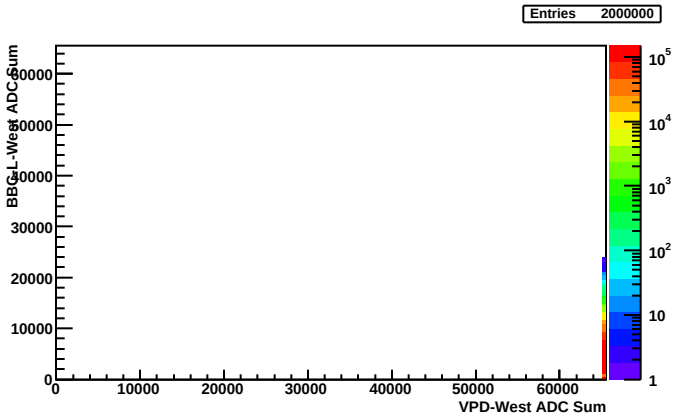

Entries 2000000
TOF Mult
ZDC-East ADC Sum Att

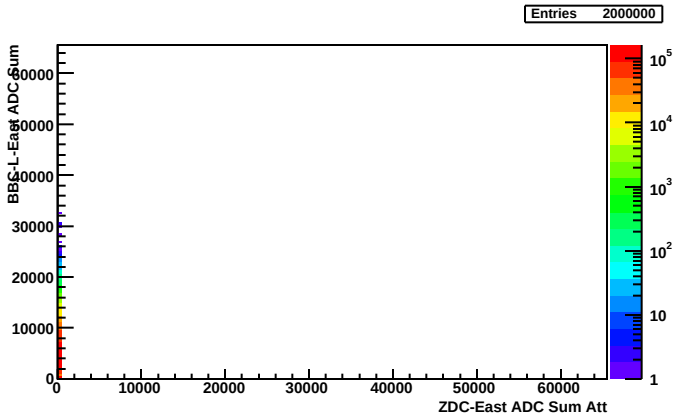

Entries 2000000
TOF Mult
ZDC-West ADC Sum Att

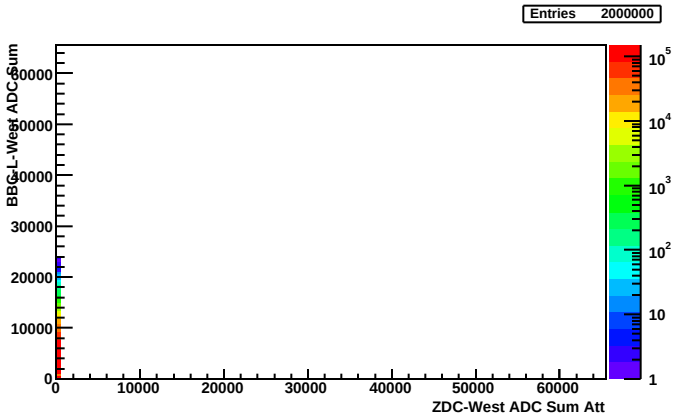

Entries 2000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

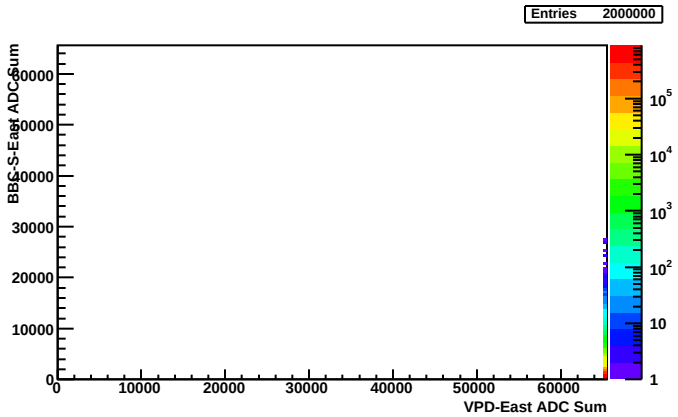

Entries 2000000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

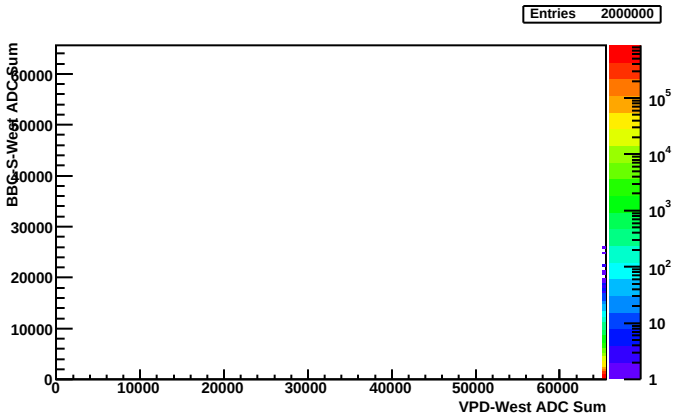

Entries 2000000
BBC-L-East ADC Sum
VPD-East ADC Sum

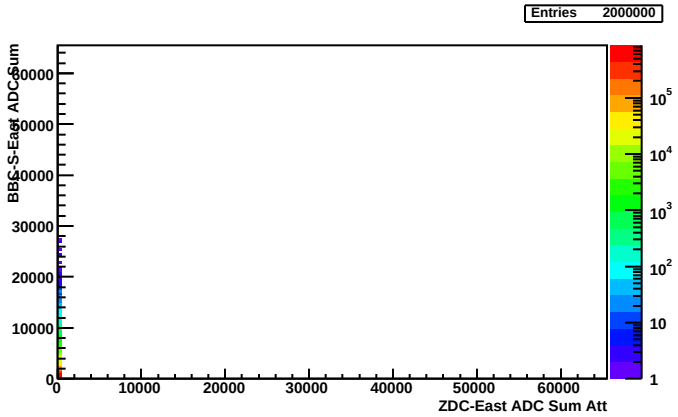

Entries 2000000
BBC-L-West ADC Sum
VPD-West ADC Sum

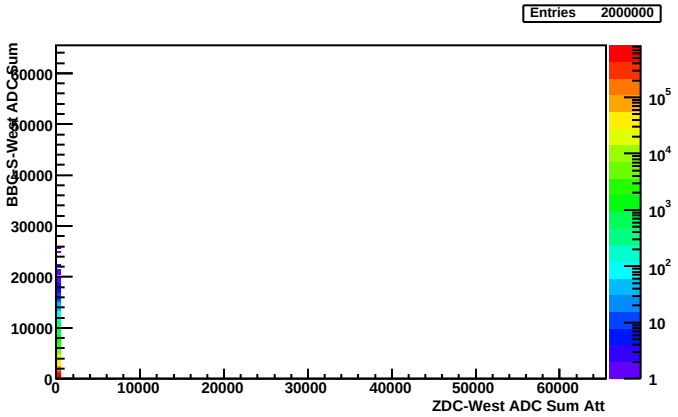

Entries 2000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

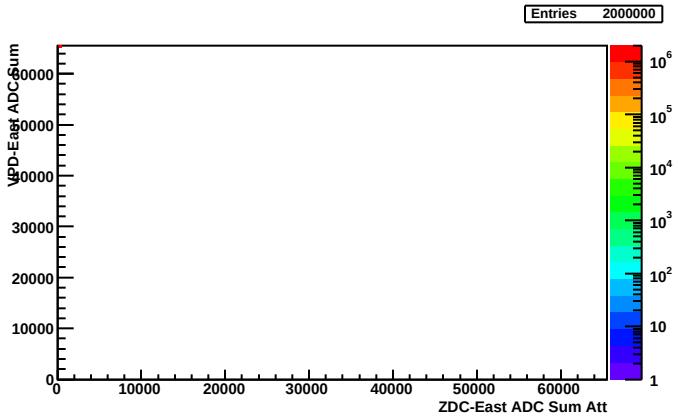

Entries 2000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

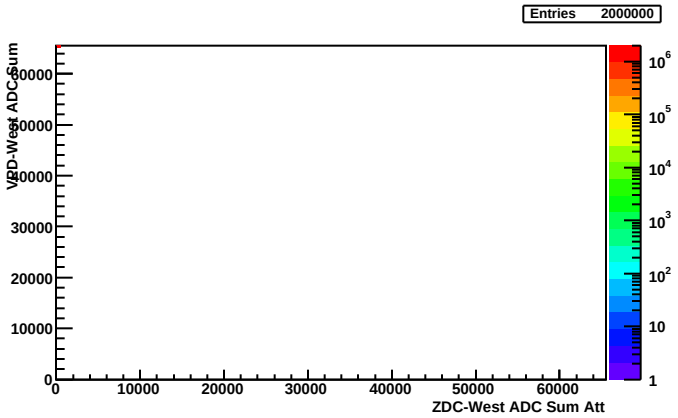

Entries 2000000
BBC-S-East ADC Sum
VPD-East ADC Sum

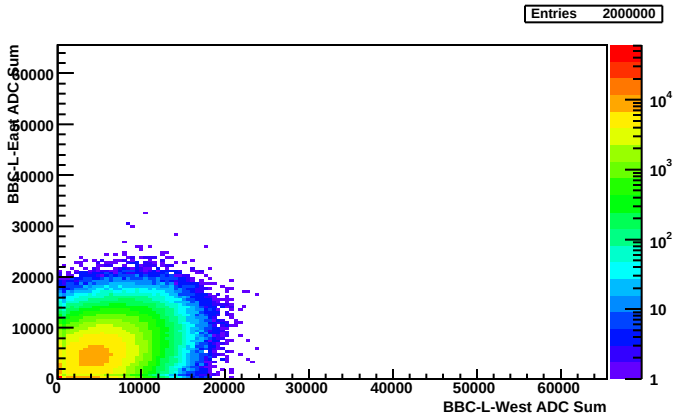

Entries 2000000
BBC-S-West ADC Sum
VPD-West ADC Sum

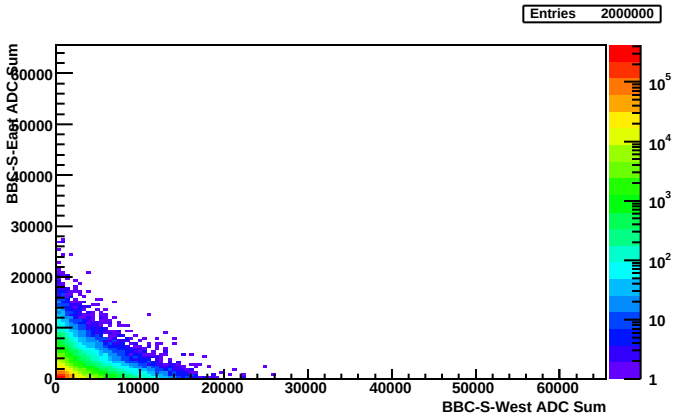

Entries 2000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

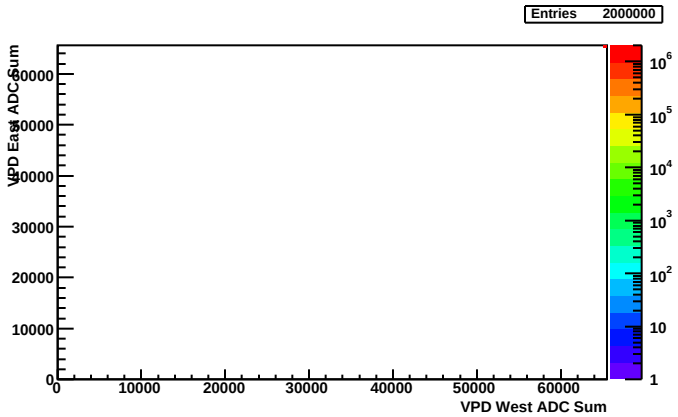

Entries 2000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

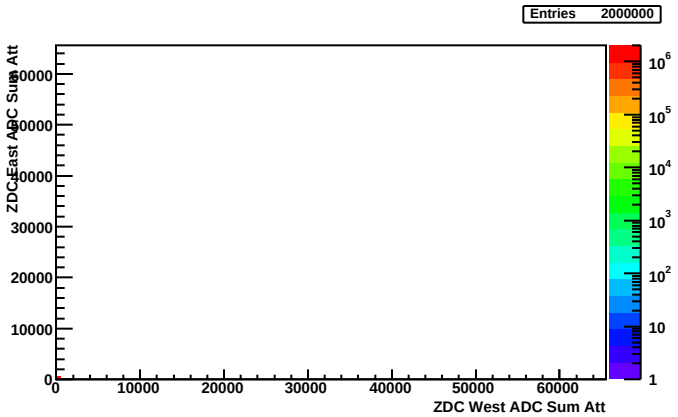

Entries 2000000
VPD-East ADC Sum
ZDC-East ADC Sum Att

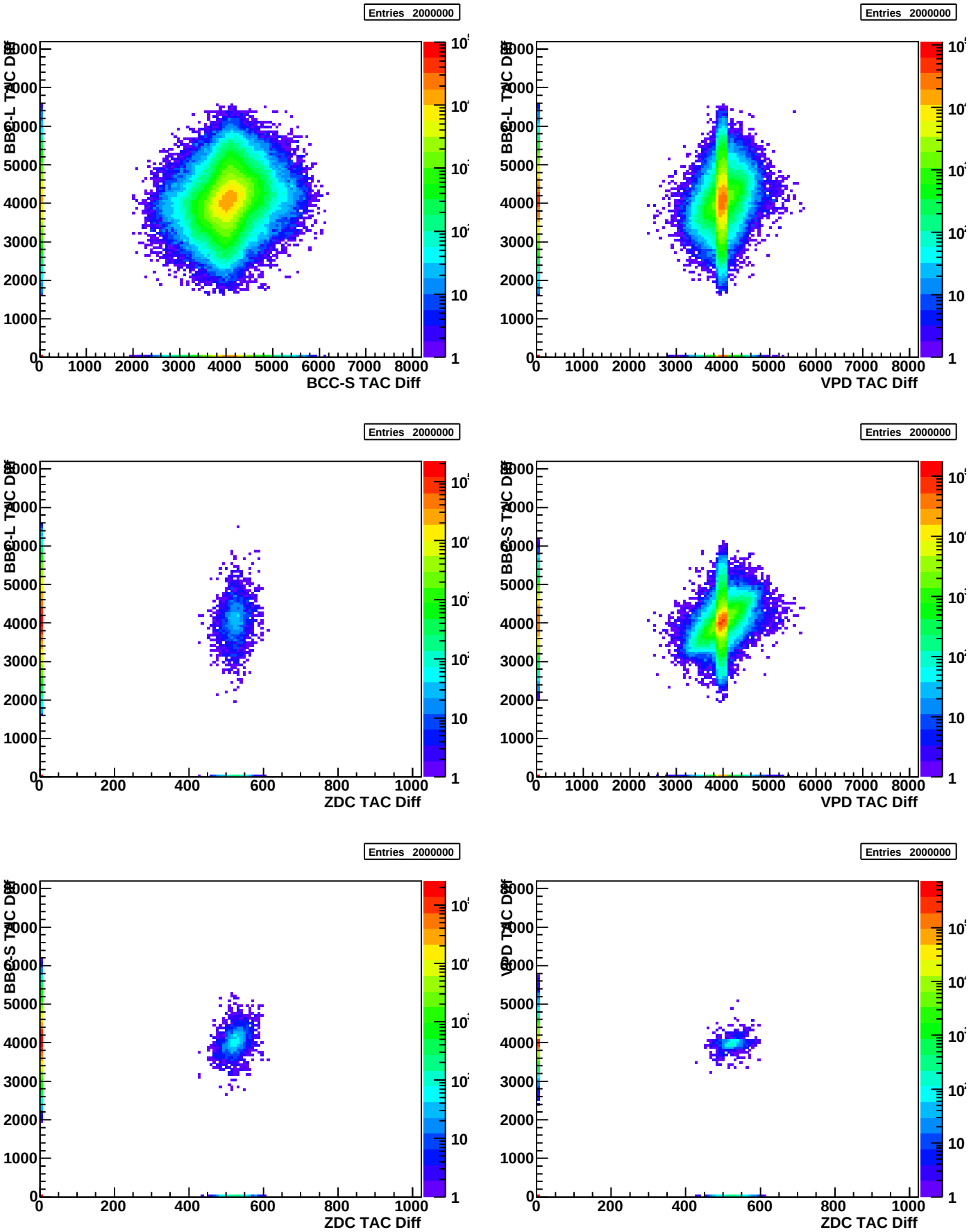

Entries 2000000
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2000000
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 2000000
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 2000000
VPD East ADC Sum
VPD West ADC Sum


Entries 2000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2000000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2000000
BBC-L TAC Diff
VPD TAC Diff
Entries 2000000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2000000
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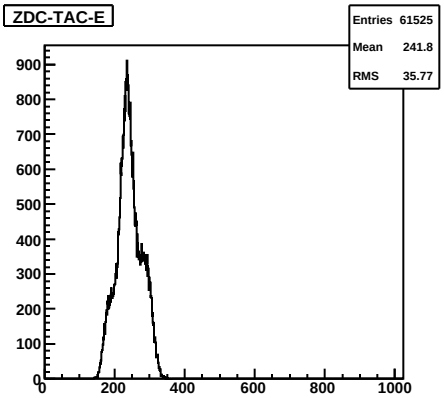
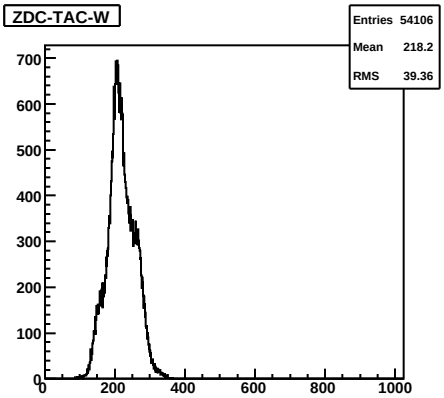
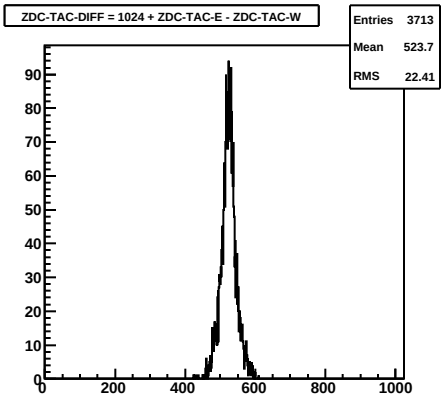
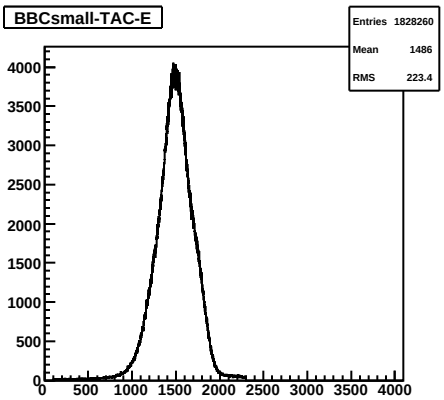
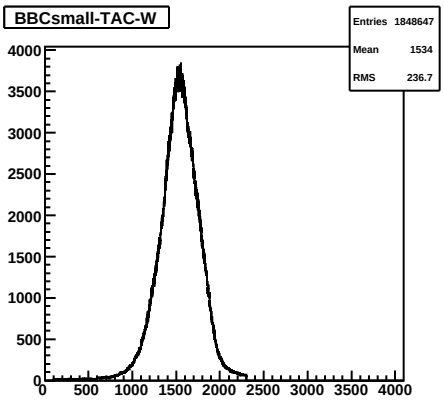
VPD TAC Diff
Entries 2000000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2000000
VPD TAC Diff
ZDC TAC Diff

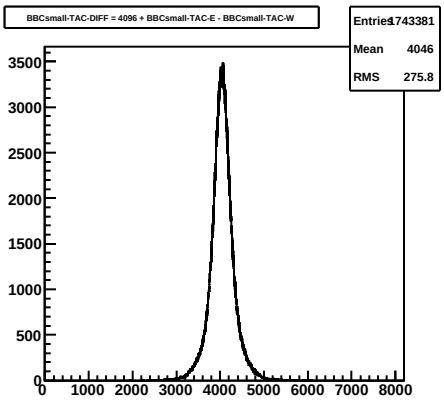

ZDC-TAC-E
Entries 61525
Mean 241.8
RMS 35.77

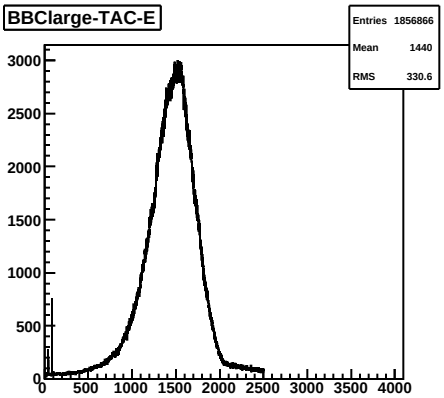

ZDC-TAC-W
Entries 54106
Mean 218.2
RMS 39.36

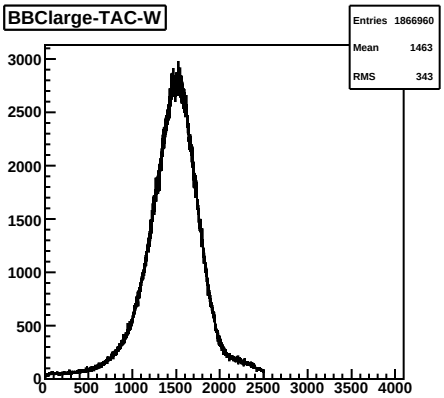

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3713
Mean 523.7
RMS 22.41

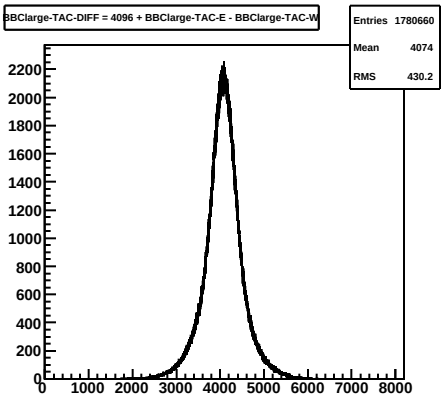

BBCsmall-TAC-E
Entries 1828260
Mean 1486
RMS 223.4

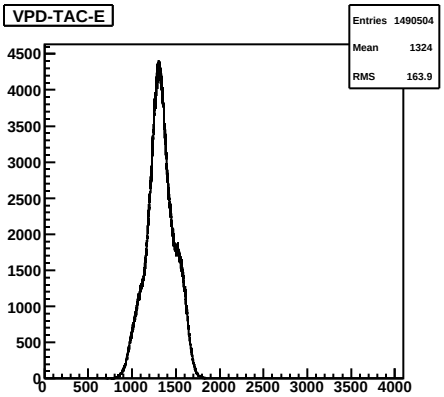

BBCsmall-TAC-W
Entries 1848647
Mean 1534
RMS 236.7

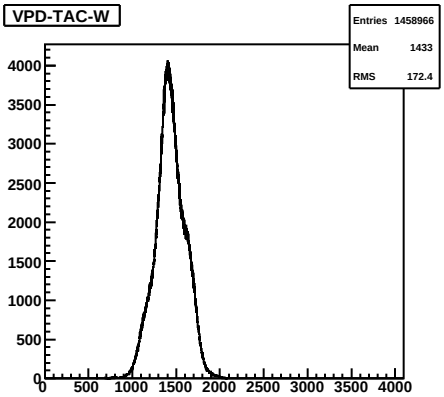

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1743381
Mean 4046
RMS 275.8

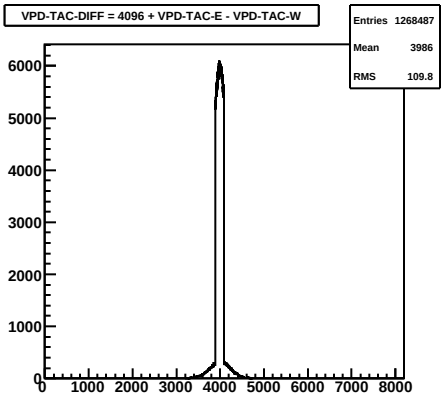

BBClarge-TAC-E
Entries 1856866
Mean 1440
RMS 330.6

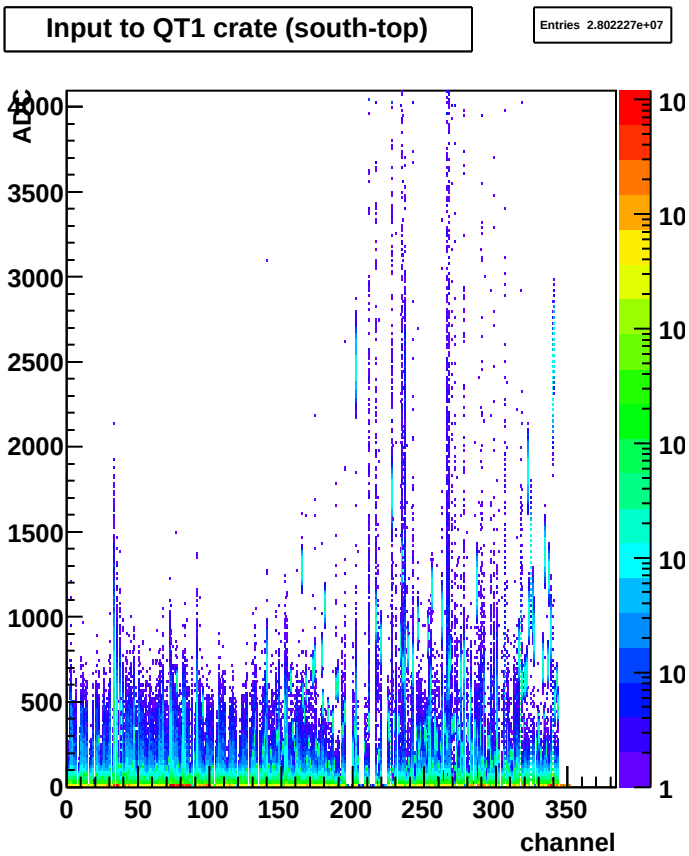

BBClarge-TAC-W
Entries 1866960
Mean 1463
RMS 343

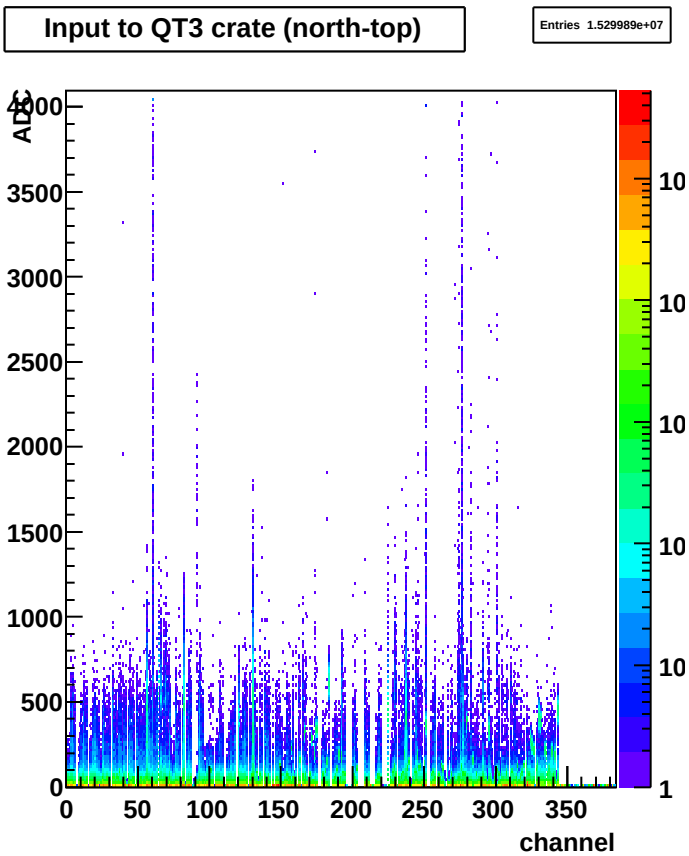

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1780660
Mean 4074
RMS 430.2

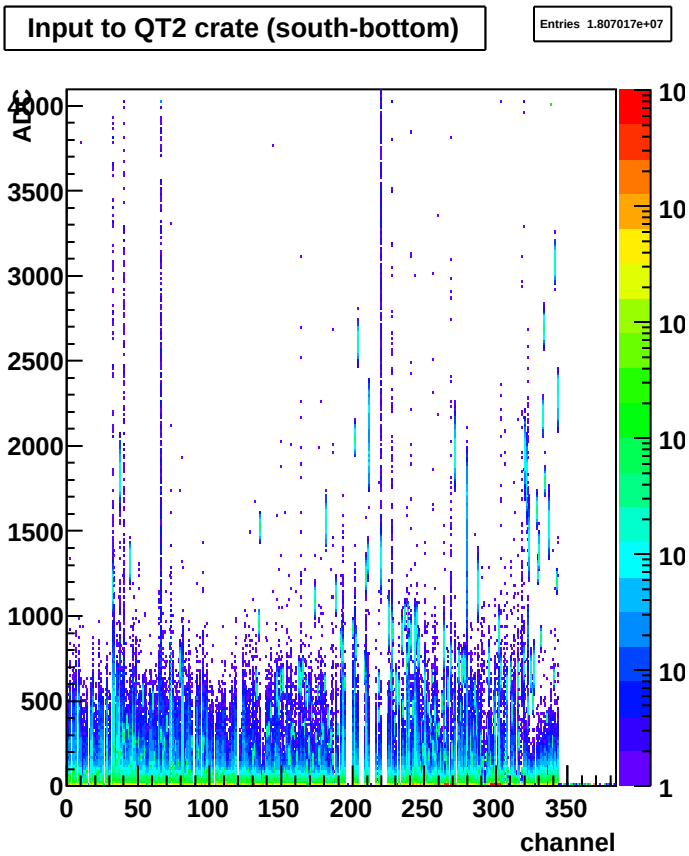

VPD-TAC-E
Entries 1490504
Mean 1324
RMS 163.9

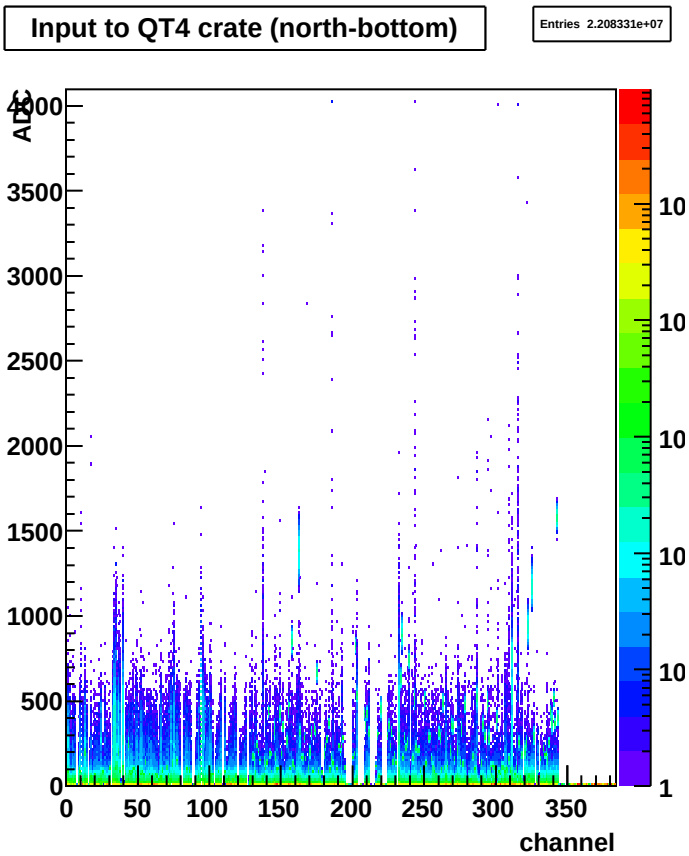

VPD-TAC-W
Entries 1458966
Mean 1433
RMS 172.4

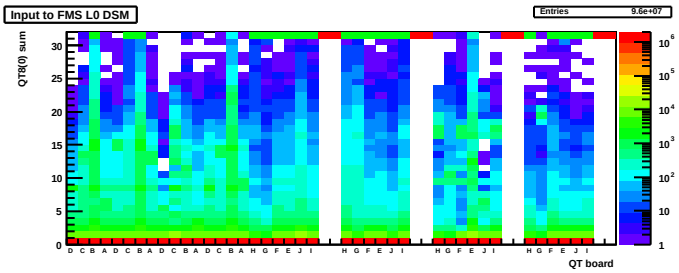

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1268487
Mean 3986
RMS 109.8

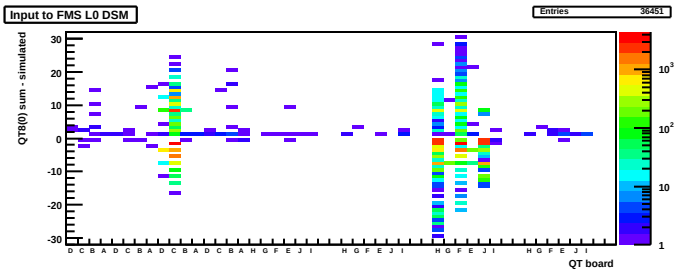

Input to QT1 crate (south-top)
Entries 2.802227e+07
ADC
channel

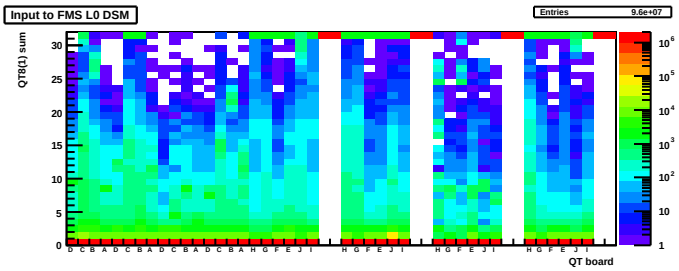

Input to QT3 crate (north-top)
Entries 1.529989e+07
ADC
channel

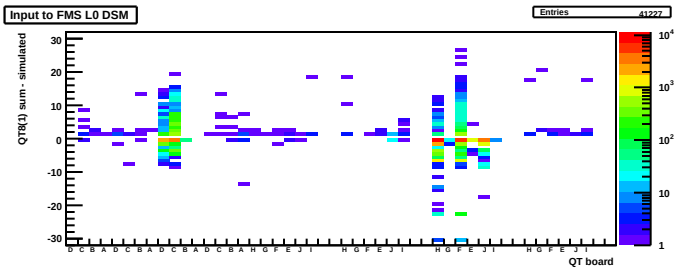

Input to QT2 crate (south-bottom)
Entries 1.807017e+07
ADC
channel

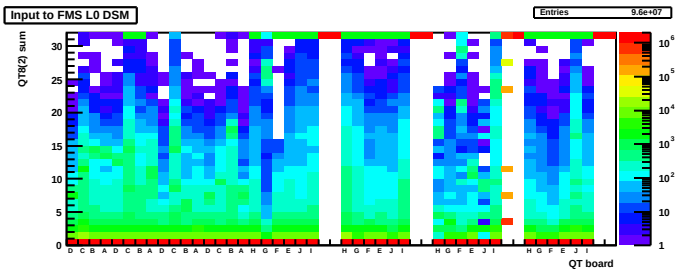

Input to QT4 crate (north-bottom)
Entries 2.208331e+07
ADC
channel

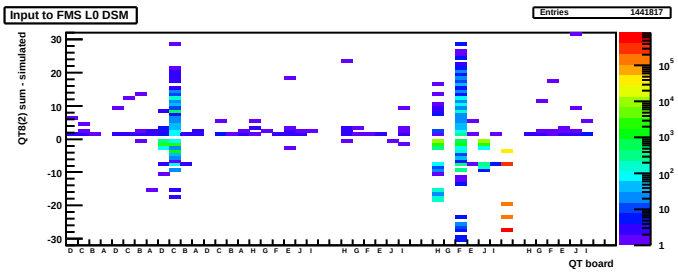

Input to FMS L0 DSM
Entries 9.6e+07
QT8(0) sum
QT board

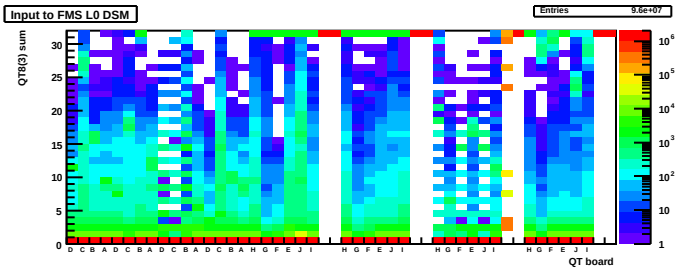

Input to FMS L0 DSM
Entries 36451
QT8(0) sum - simulated
QT board

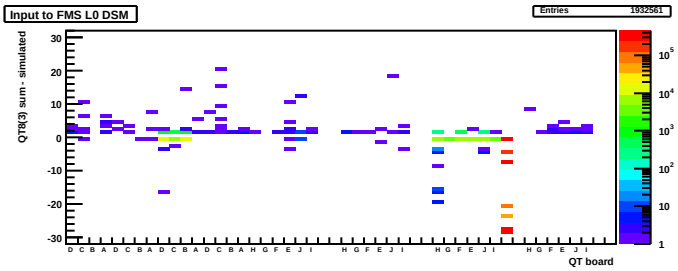

Input to FMS L0 DSM
Entries 9.6e+07
QT8(1) sum
QT board

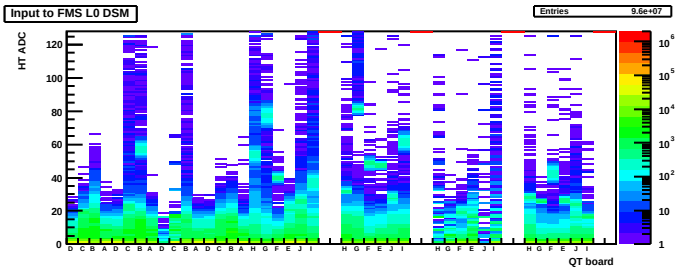

Input to FMS L0 DSM
Entries 41227
QT8(1) sum - simulated
QT board

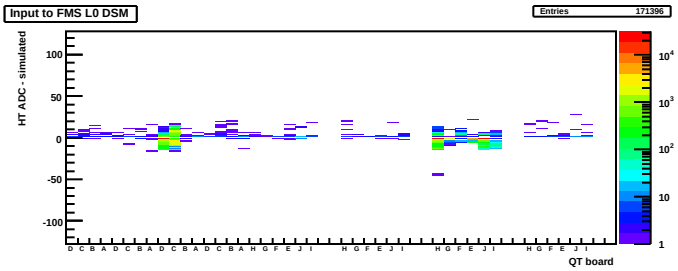

Input to FMS L0 DSM
Entries 9.6e+07
QT8(2) sum
QT board

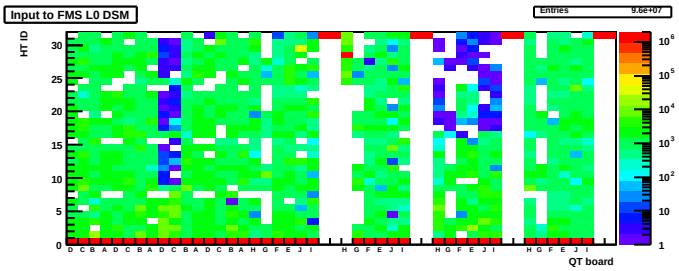

Input to FMS L0 DSM
Entries 1441817
QT8(2) sum - simulated
QT board

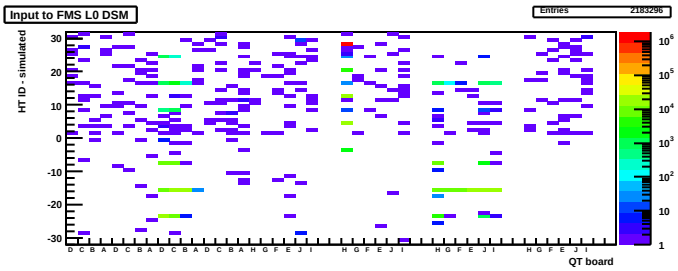

Input to FMS L0 DSM
Entries 9.6e+07
QT8(3) sum
QT board

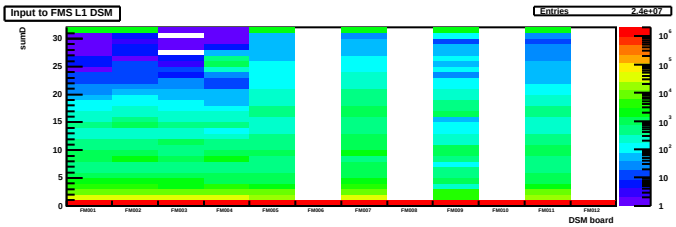

Input to FMS L0 DSM
Entries 1932561
QT8(3) sum - simulated
QT board

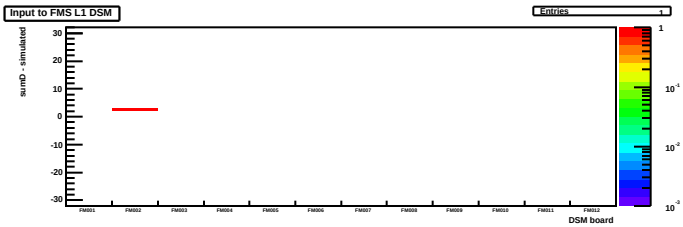

Input to FMS L0 DSM
Entries 9.6e+07
HT ADC
QT board

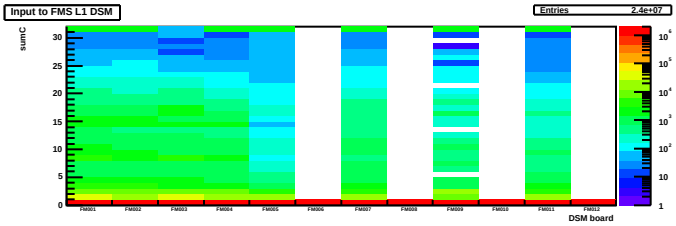

Input to FMS L0 DSM
Entries 171396
HT ADC - simulated
QT board

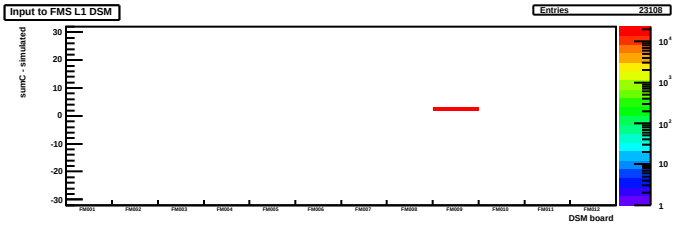

Input to FMS L0 DSM
Entries 9.6e+07
HT ID
QT board

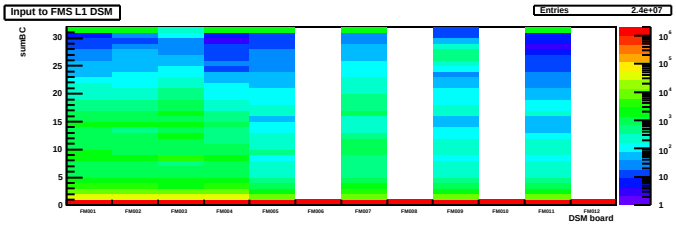

Input to FMS L0 DSM
Entries 2183296
HT ID - simulated
QT board

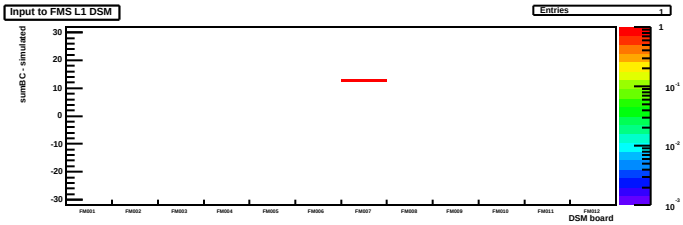

Input to FMS L1 DSM
Entries 2.4e+07
sumD
DSM board

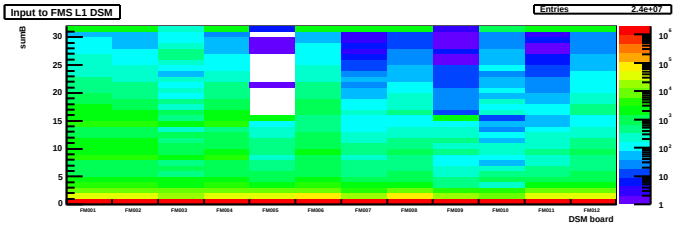

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

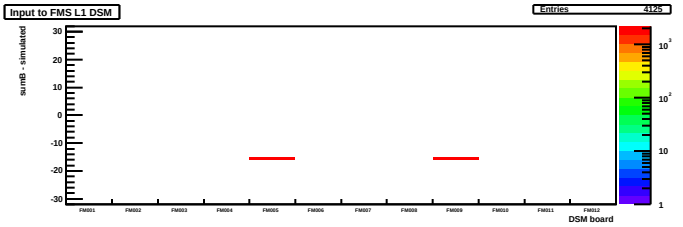

Input to FMS L1 DSM
Entries 2.4e+07
sumC
DSM board

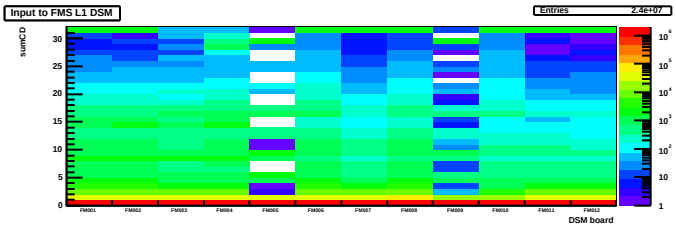

Input to FMS L1 DSM
Entries 23108
sumC - simulated
DSM board

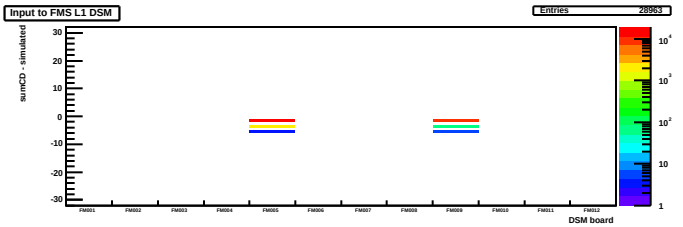

Input to FMS L1 DSM
Entries 2.4e+07
sumBC
DSM board

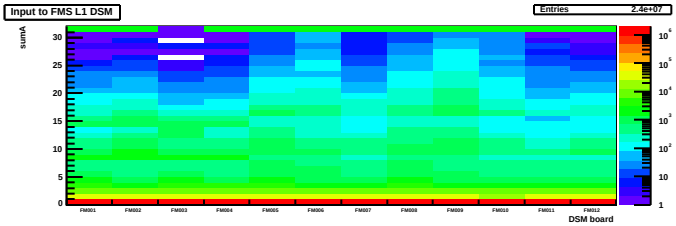

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

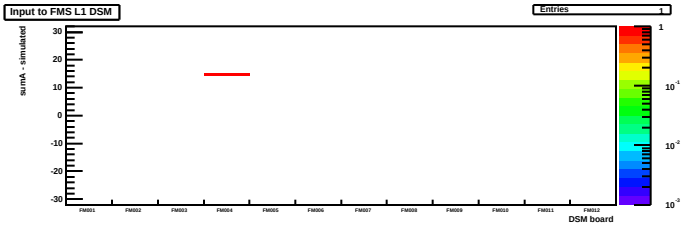

Input to FMS L1 DSM
Entries 2.4e+07
sumB
DSM board

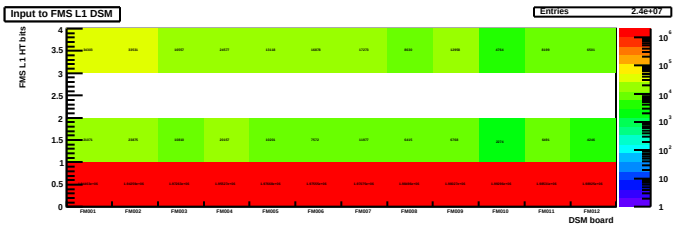

Input to FMS L1 DSM
Entries 4125
sumB - simulated
DSM board

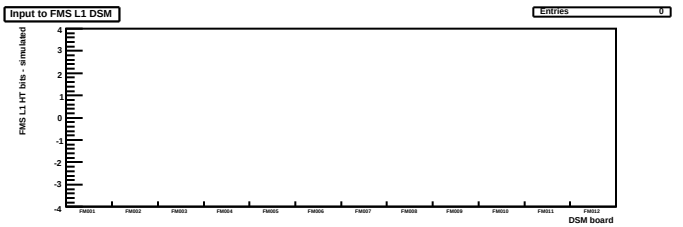

Input to FMS L1 DSM
Entries 2.4e+07
sumCD
DSM board

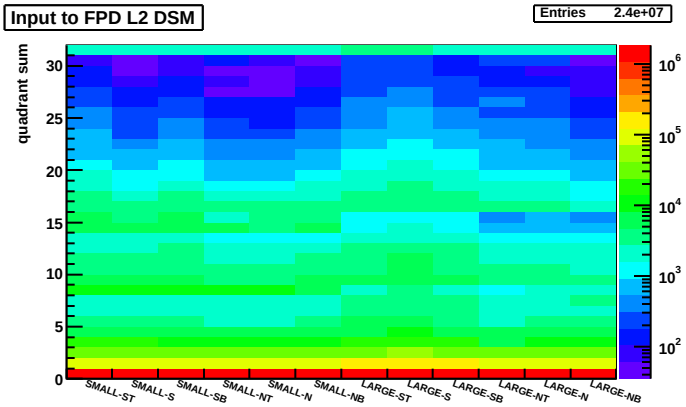

Input to FMS L1 DSM
Entries 28963
sumCD - simulated
DSM board

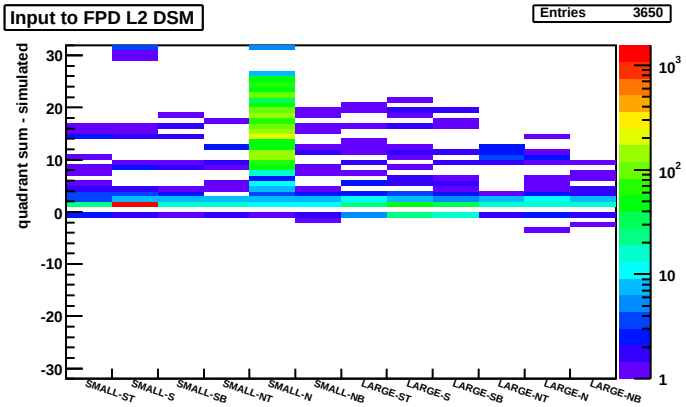

Input to FMS L1 DSM
Entries 2.4e+07
sumA
DSM board

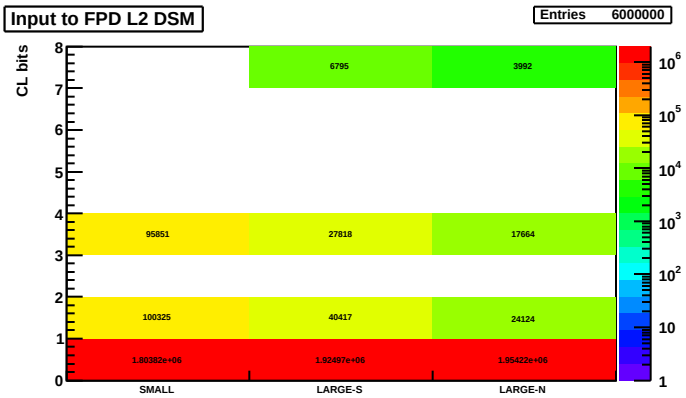

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

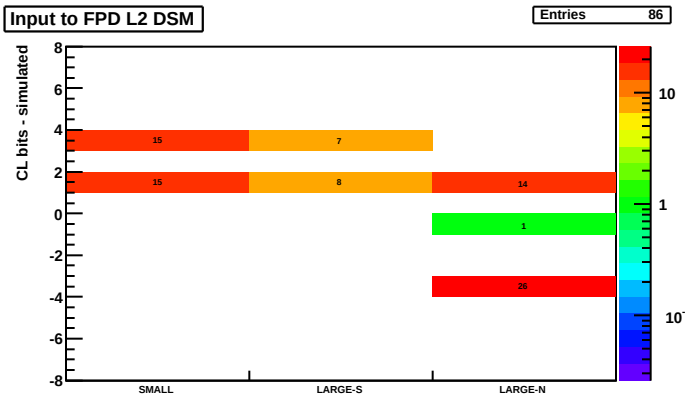

Input to FMS L1 DSM
Entries 2.4e+07
FMS L1 HT bits
DSM board

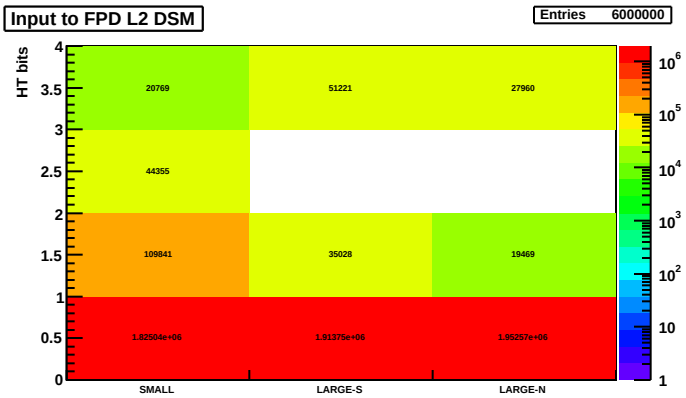

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

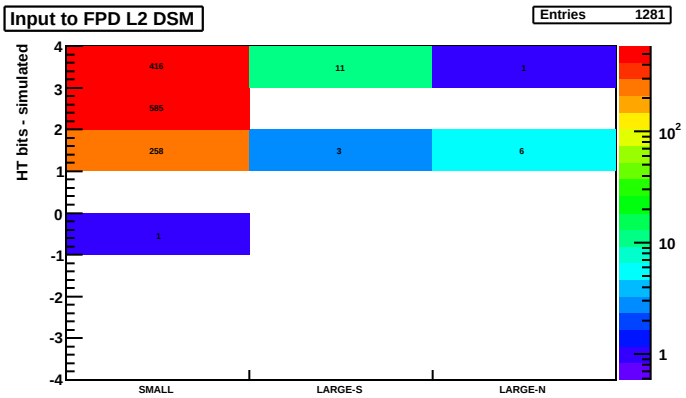

Input to FPD L2 DSM
Entries 2.4e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

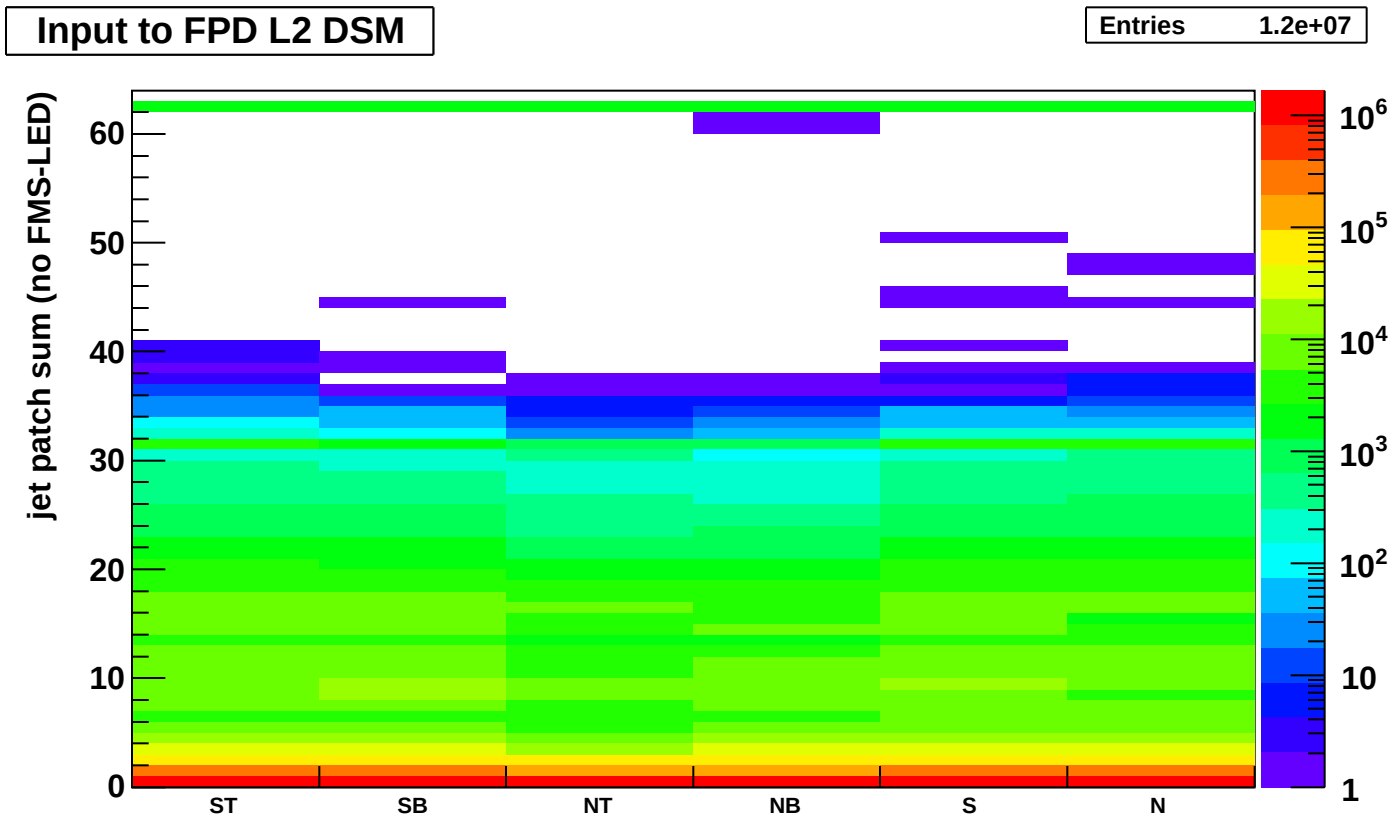

Input to FPD L2 DSM
Entries 3650
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

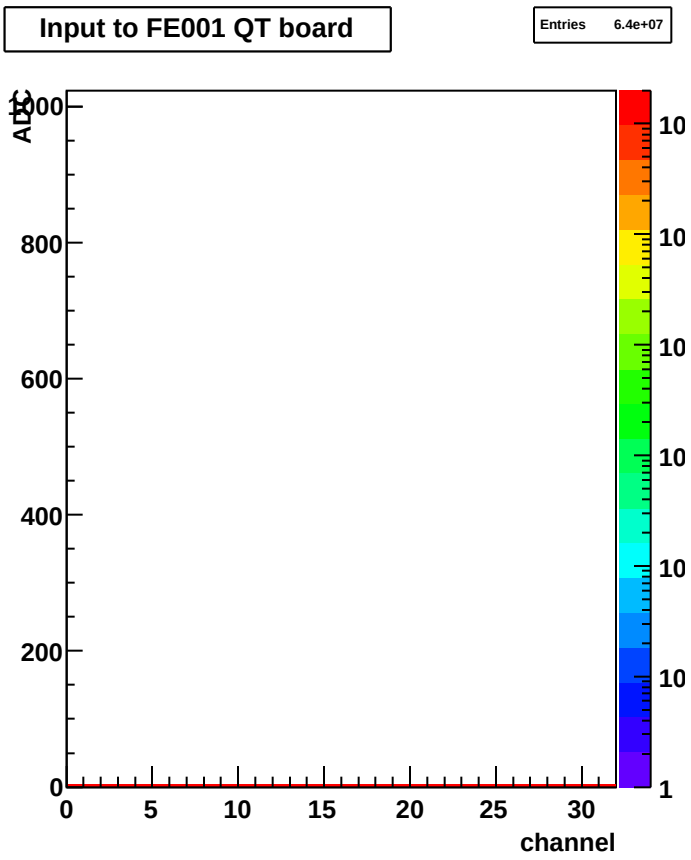

Input to FPD L2 DSM
Entries 6000000
CL bits
6795
3992
95851
27818
17664
100325
40417
24124
1.80382e+06
1.92497e+06
1.95422e+06
SMALL
LARGE-S
LARGE-N

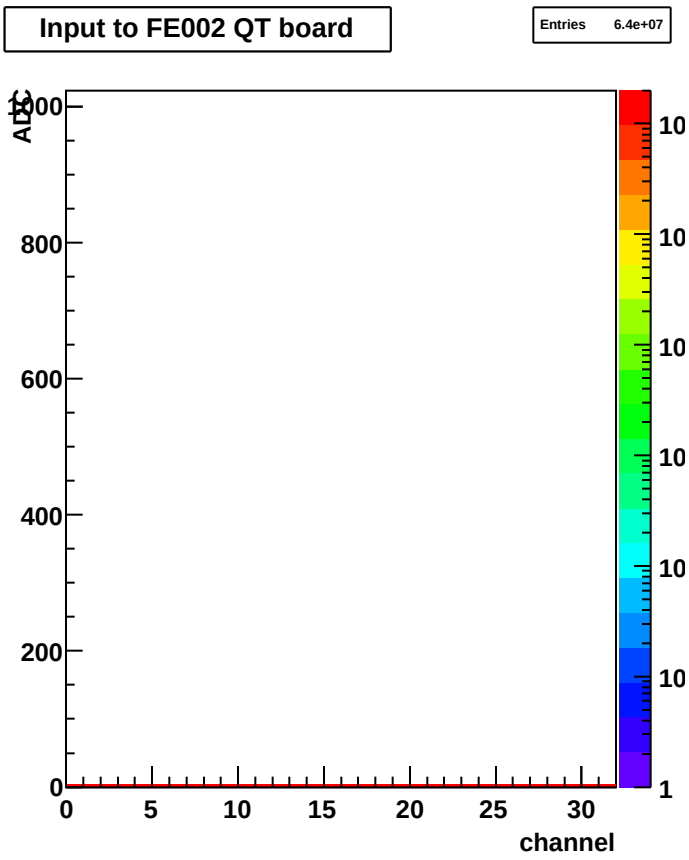

Input to FPD L2 DSM
Entries 86
CL bits - simulated
15
7
15
8
14
1
26
SMALL
LARGE-S
LARGE-N

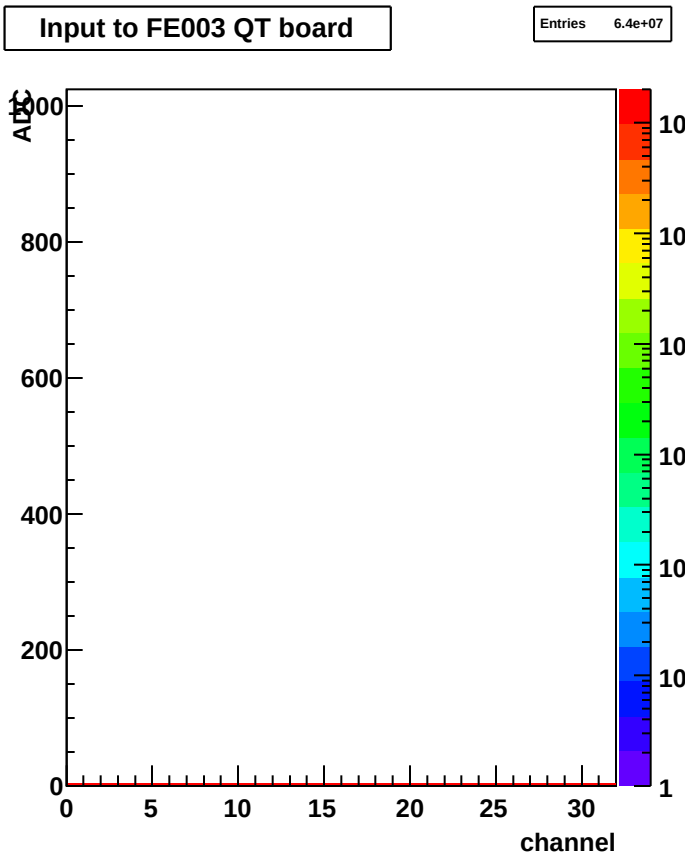

Input to FPD L2 DSM
Entries 6000000
HT bits
20769
51221
27960
44355
109841
35028
19469
1.82504e+06
1.91375e+06
1.95257e+06
SMALL
LARGE-S
LARGE-N

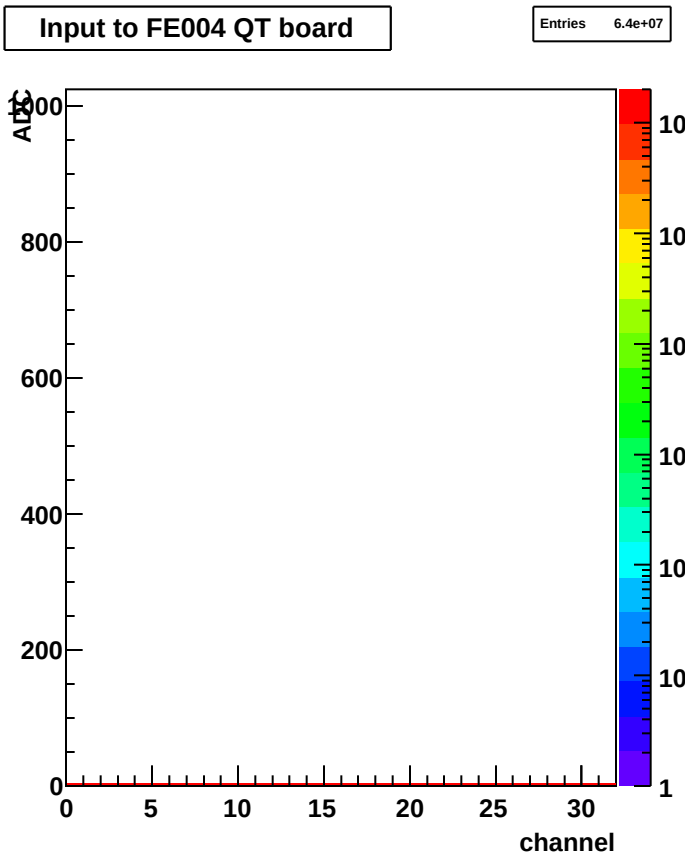

Input to FPD L2 DSM
Entries 1281
HT bits - simulated
416
11
1
585
258
3
6
1
SMALL
LARGE-S
LARGE-N

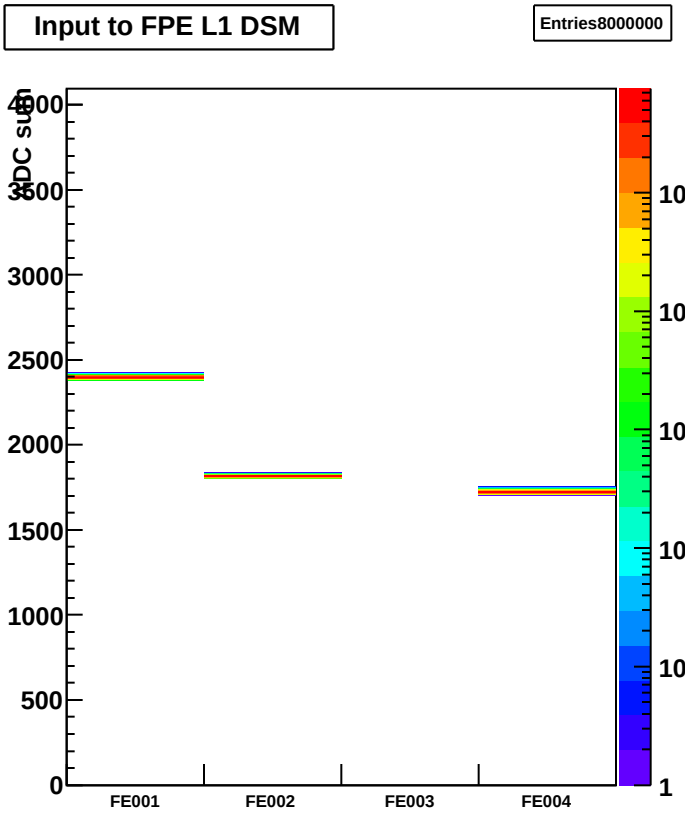

Input to FPD L2 DSM
Entries 1.2e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

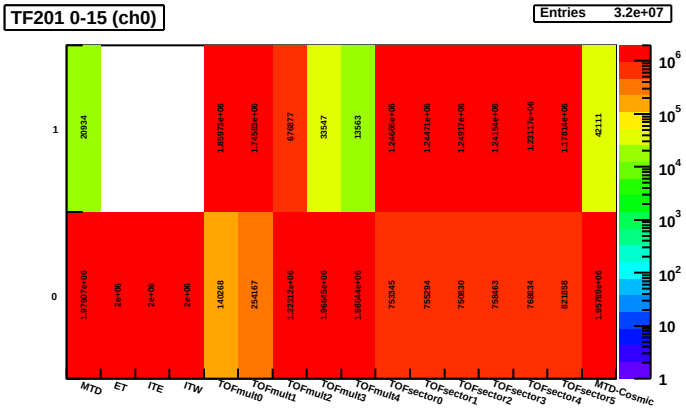

Input to FE001 QT board
Entries 6.4e+07
ADC
channel

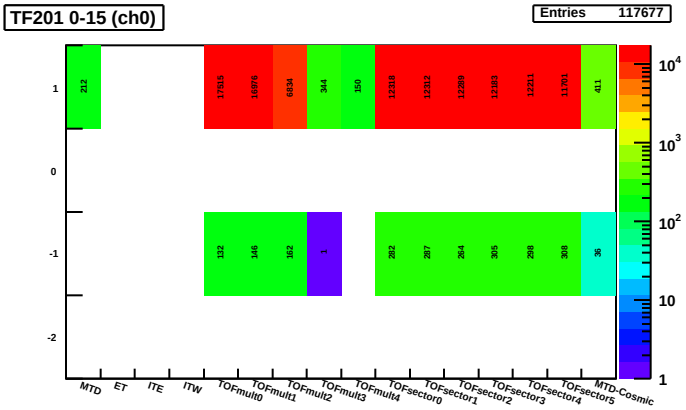

Input to FE002 QT board
Entries 6.4e+07
ADC
channel

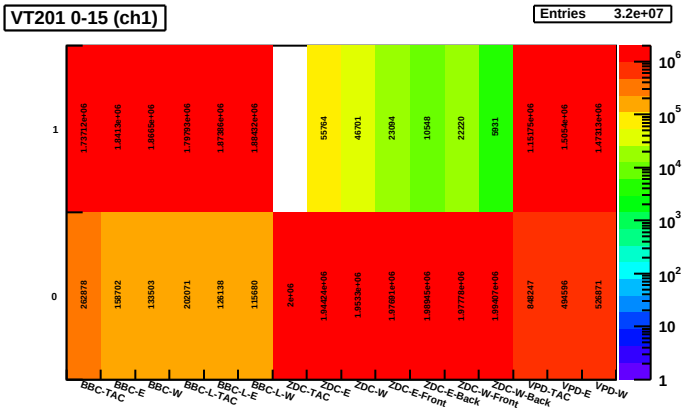

Input to FE003 QT board
Entries 6.4e+07
ADC
channel

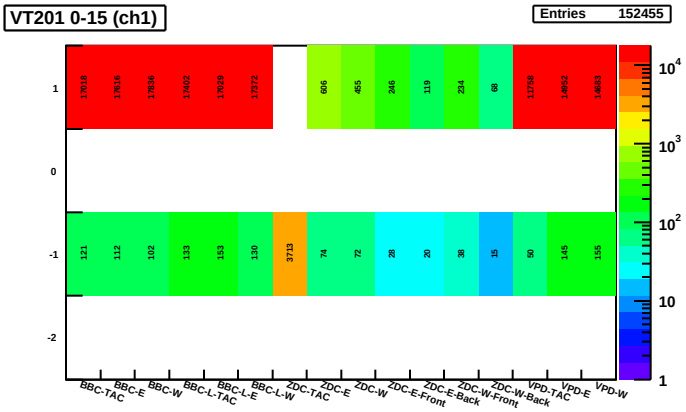

Input to FE004 QT board
Entries 6.4e+07
ADC
channel

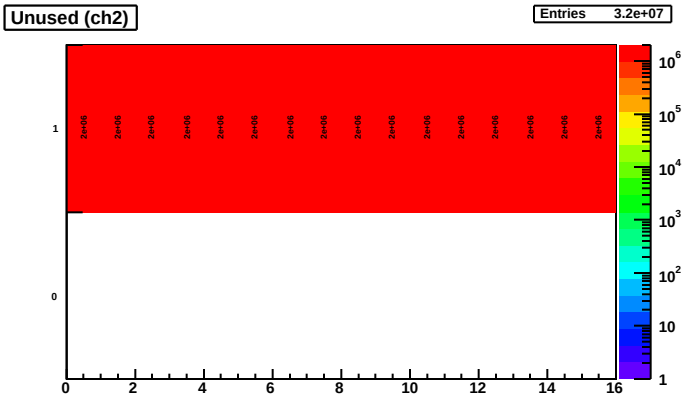

Input to FPE L1 DSM
Entries8000000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

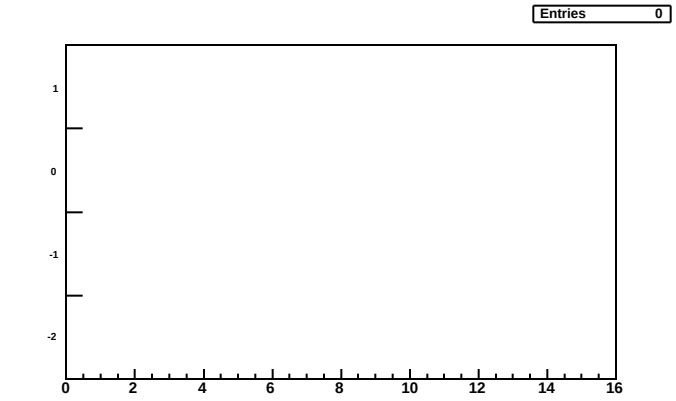

TF201 0-15 (ch0)
Entries 3.2e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 117677
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.2e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 152455
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3.2e+07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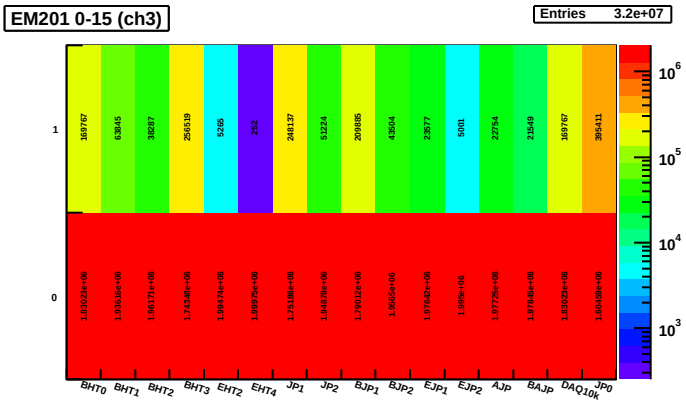
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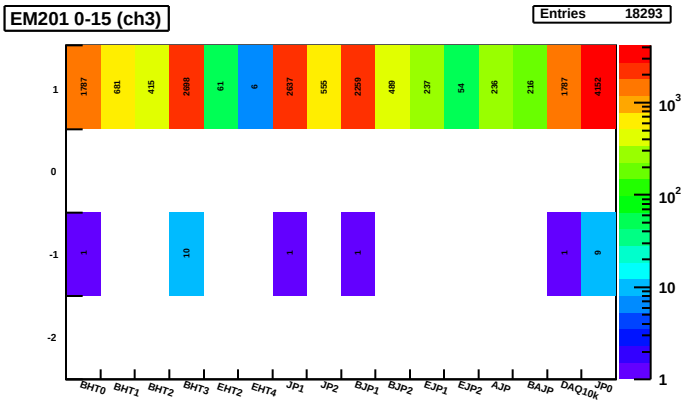
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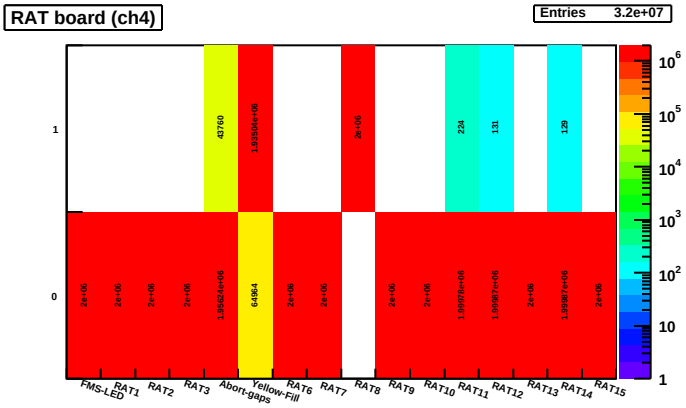
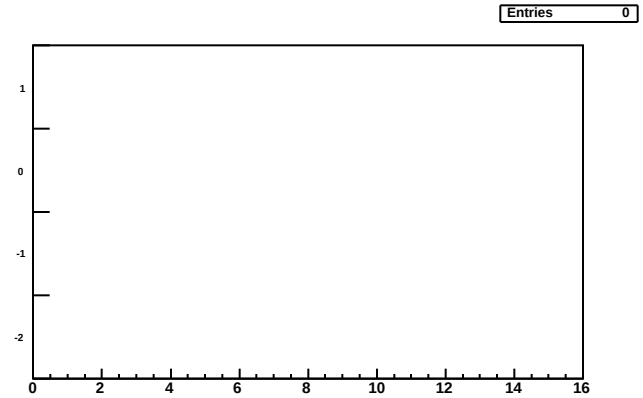
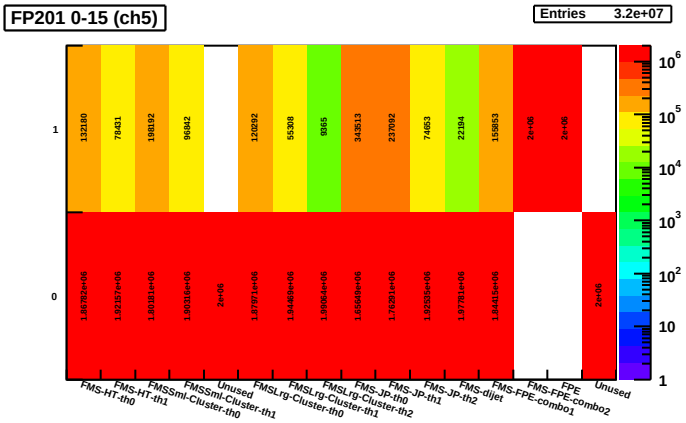
RAT board (ch4)





FP201 0-15 (ch5)



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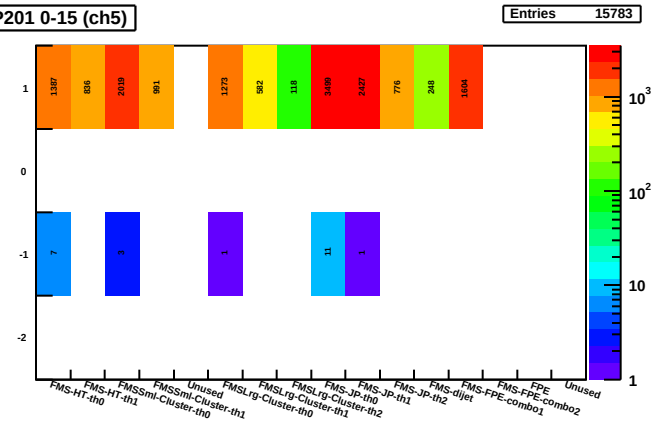
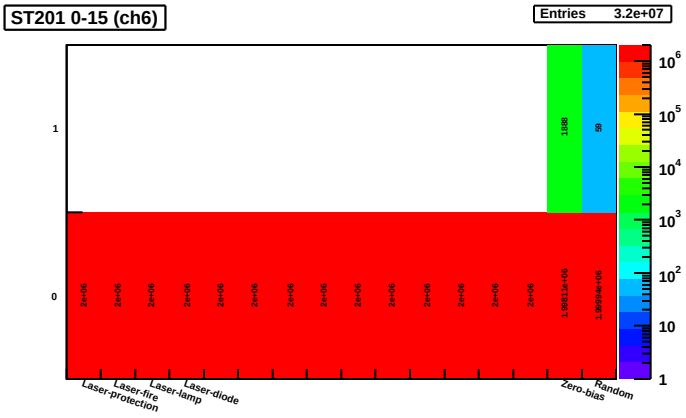
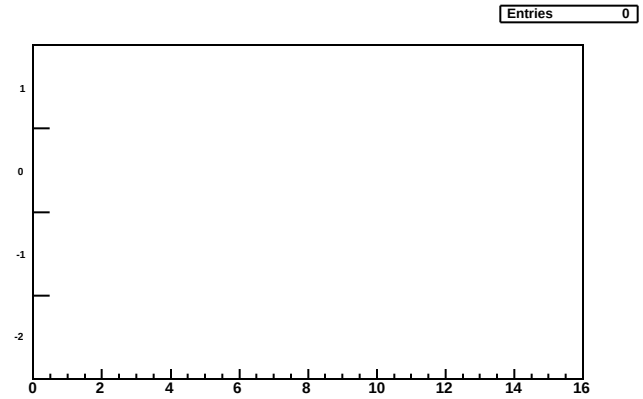
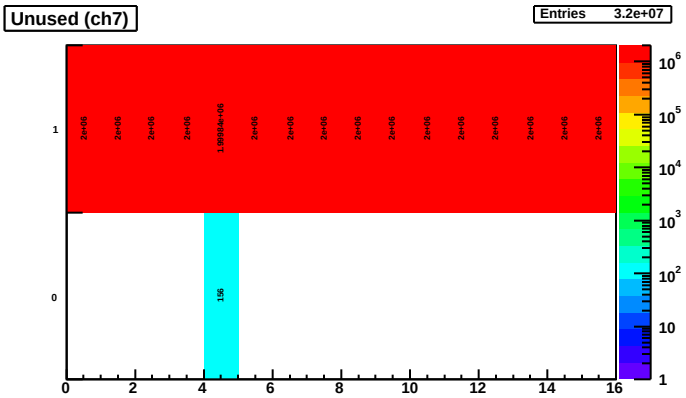
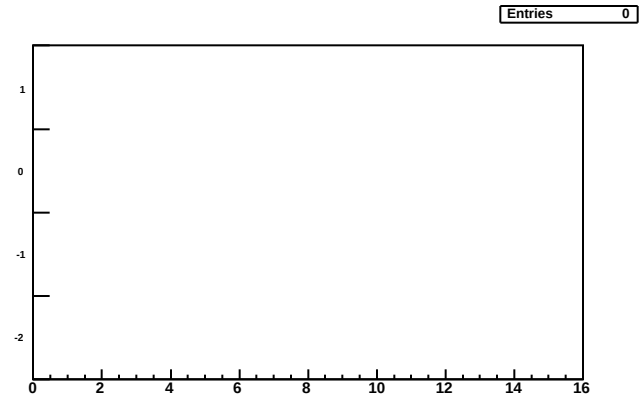


ST201 0-15 (ch6)





Unused (ch7)