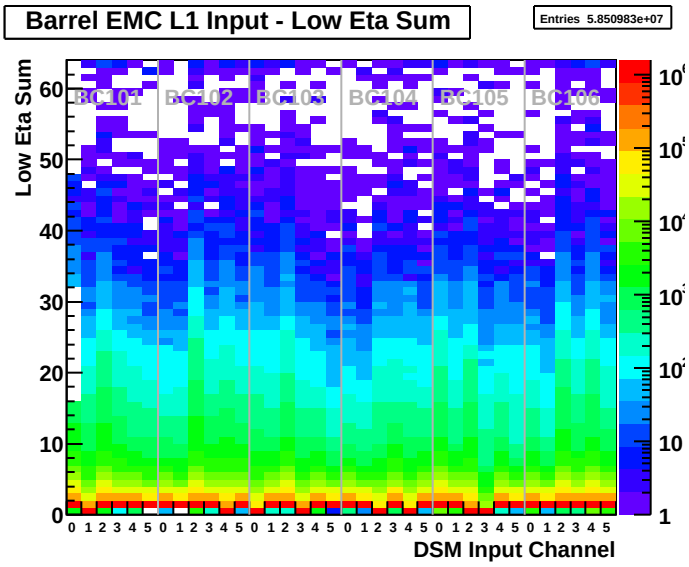
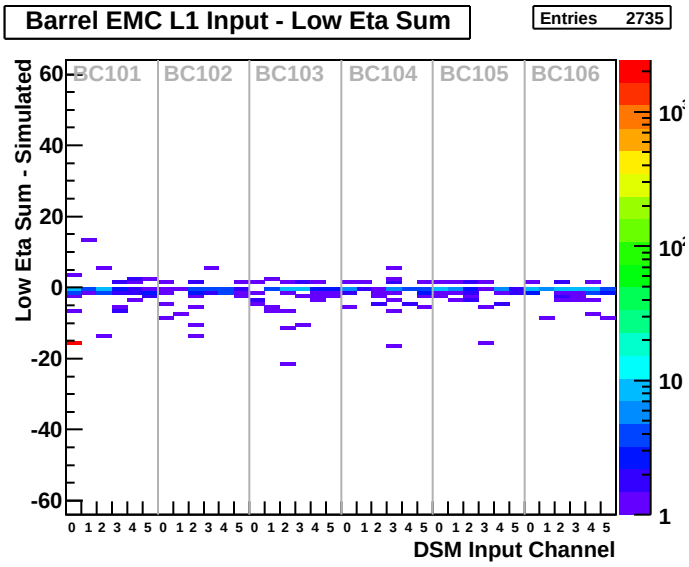
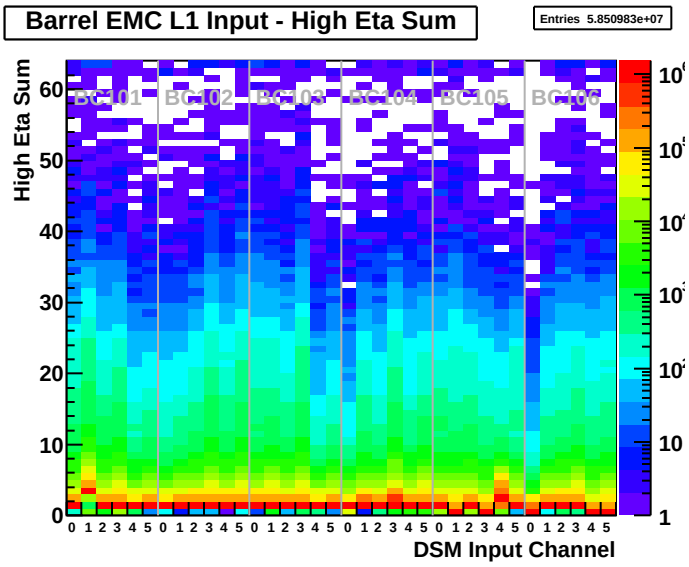
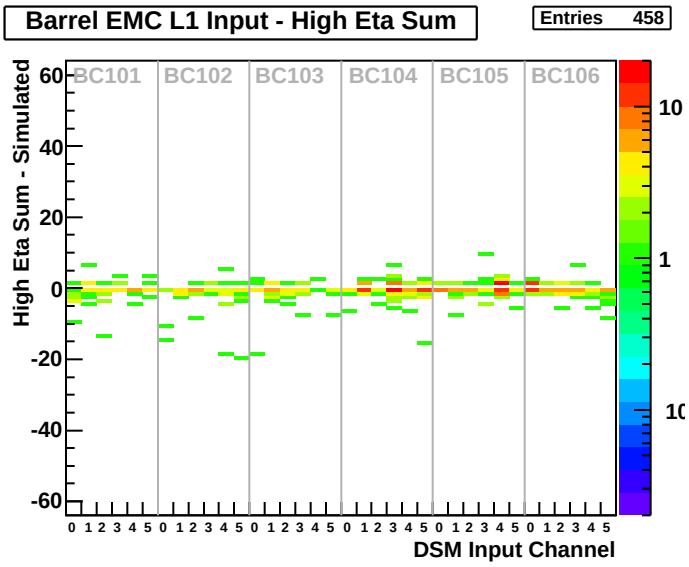
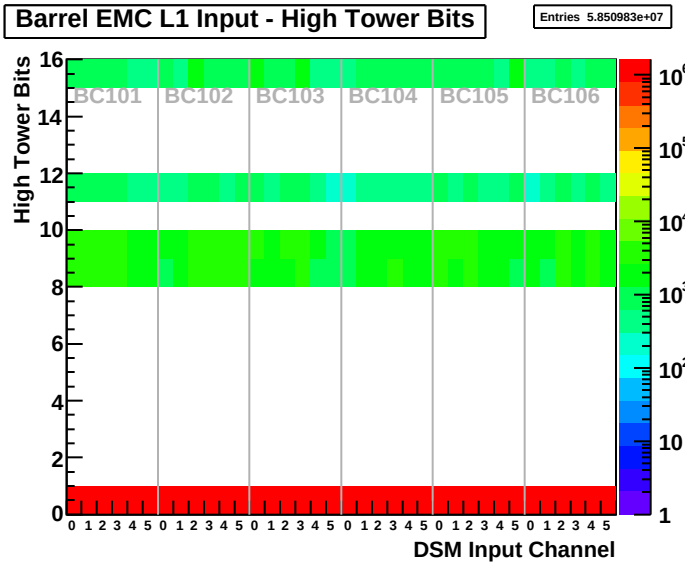
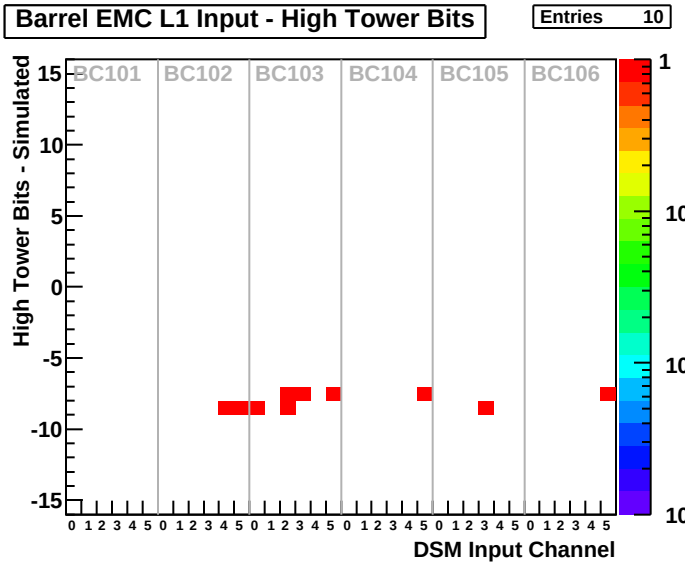

Barrel EMC L1 Input - Low Eta Sum
Entries 5.850983e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

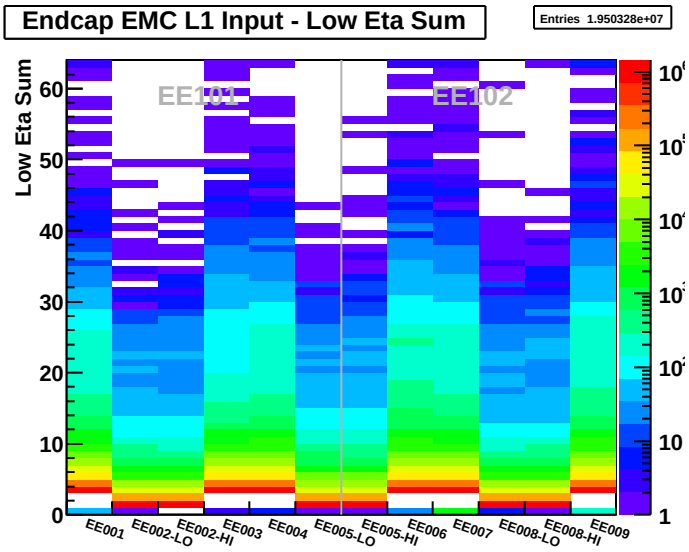

Barrel EMC L1 Input - Low Eta Sum
Entries 2735
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

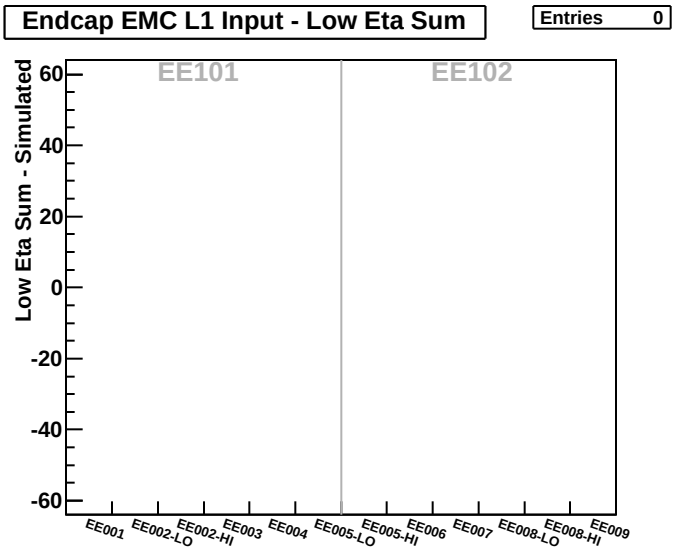

Barrel EMC L1 Input - High Eta Sum
Entries 5.850983e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

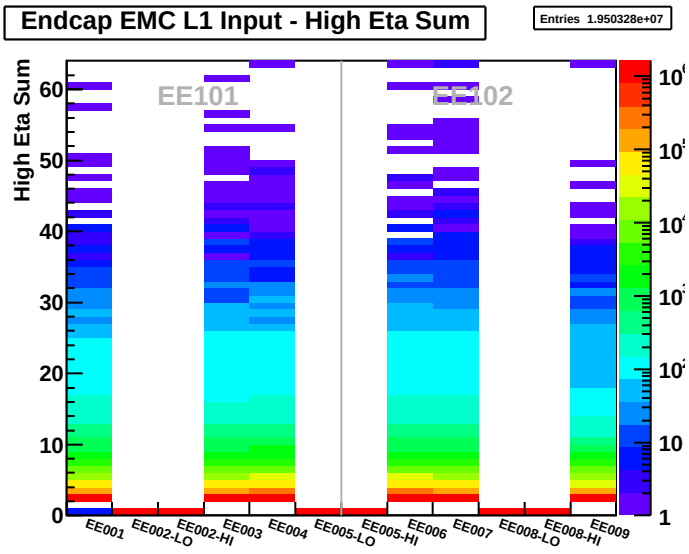

Barrel EMC L1 Input - High Eta Sum
Entries 458
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

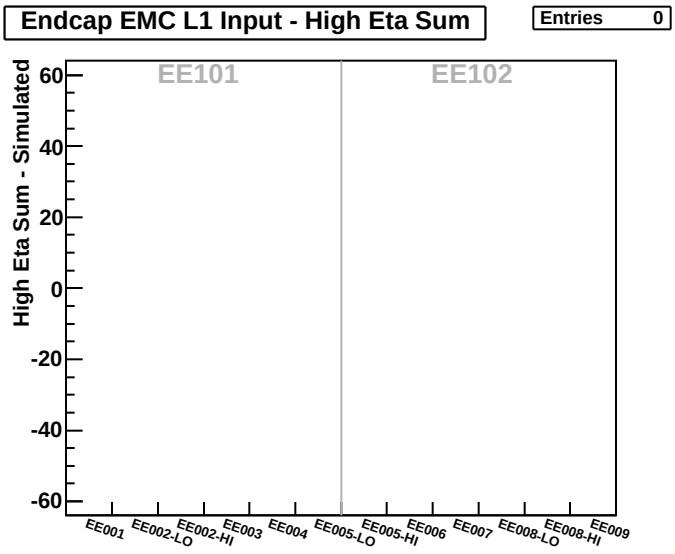

Barrel EMC L1 Input - High Tower Bits
Entries 5.850983e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

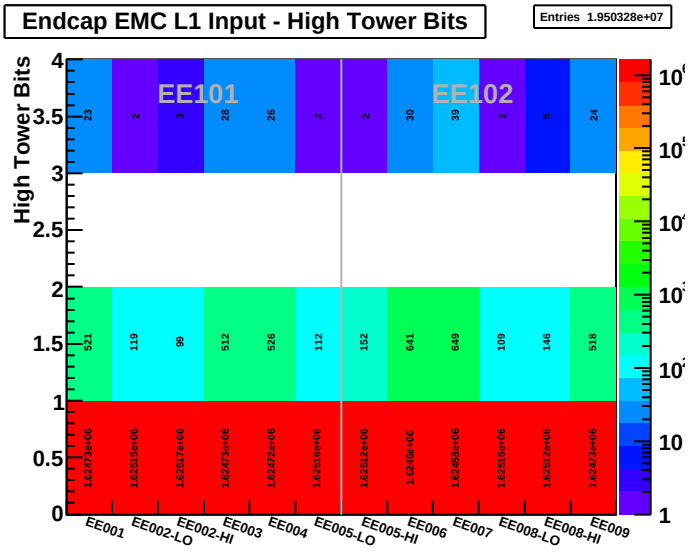

Barrel EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

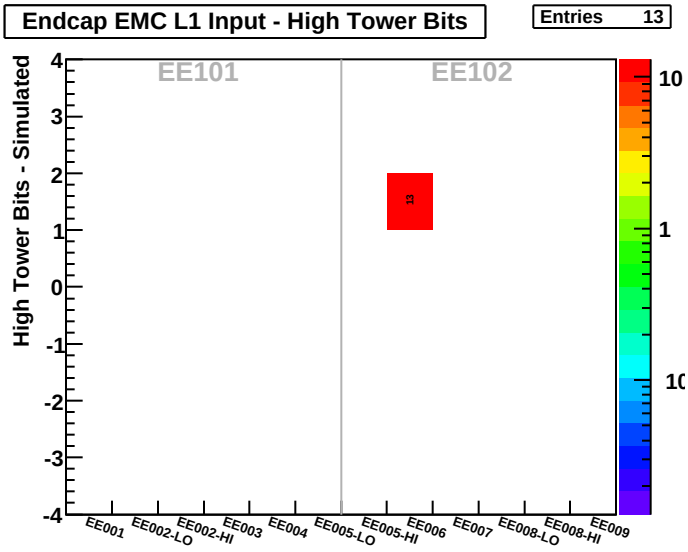

Endcap EMC L1 Input - Low Eta Sum
Entries 1.950328e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

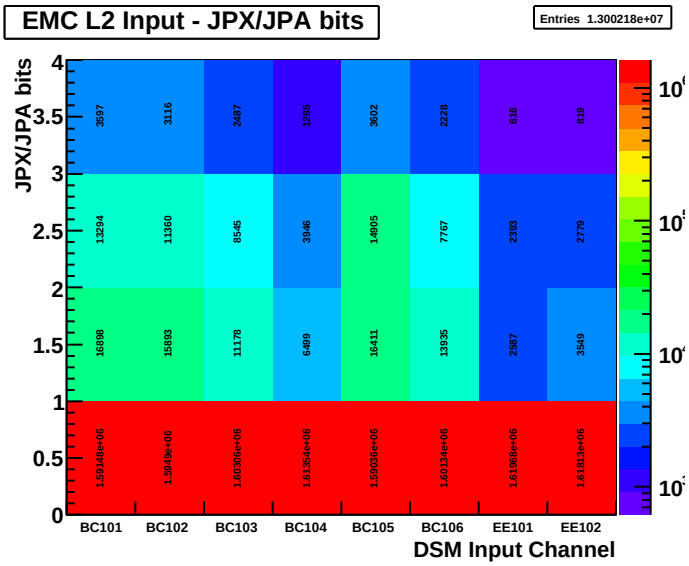

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

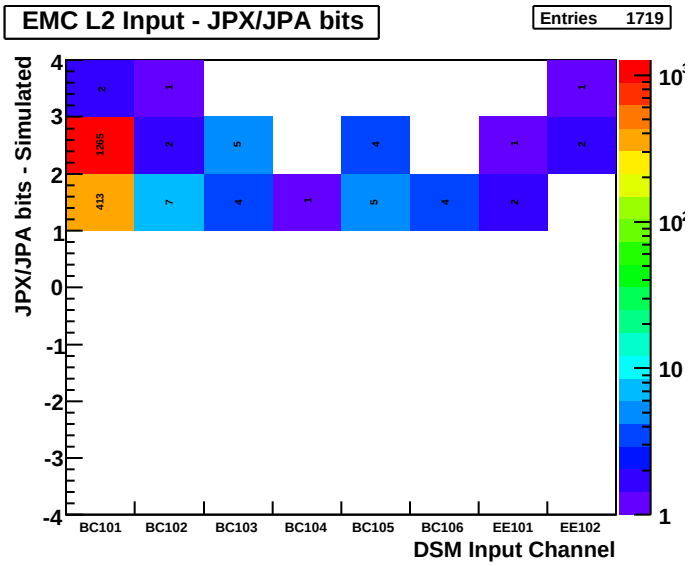

Endcap EMC L1 Input - High Eta Sum
Entries 1.950328e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

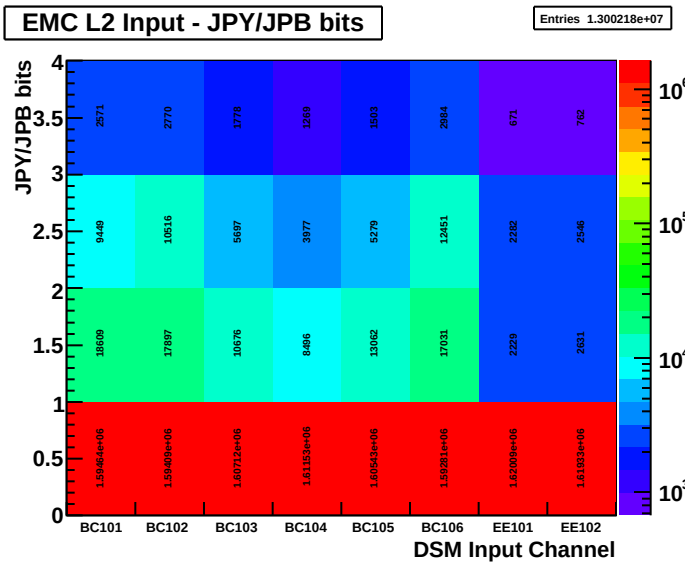

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

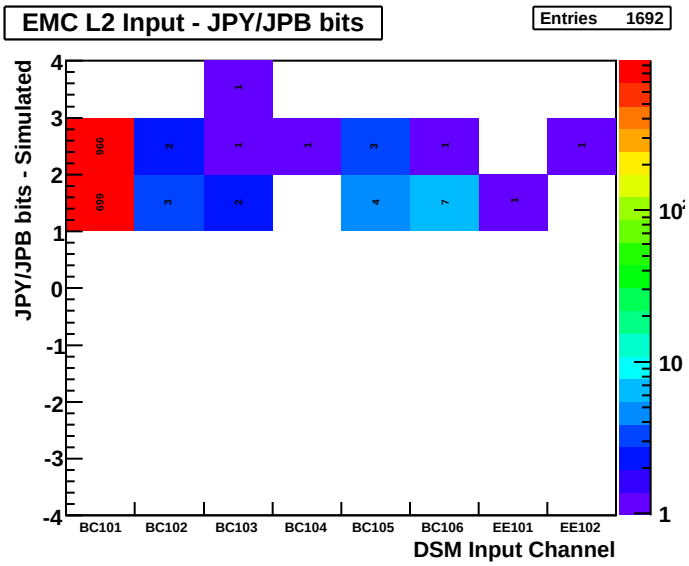

Endcap EMC L1 Input - High Tower Bits
Entries 1.950328e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

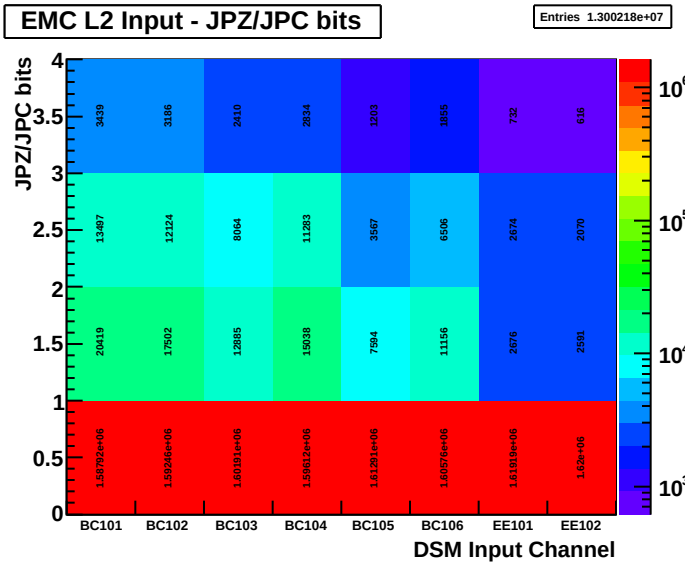

Endcap EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

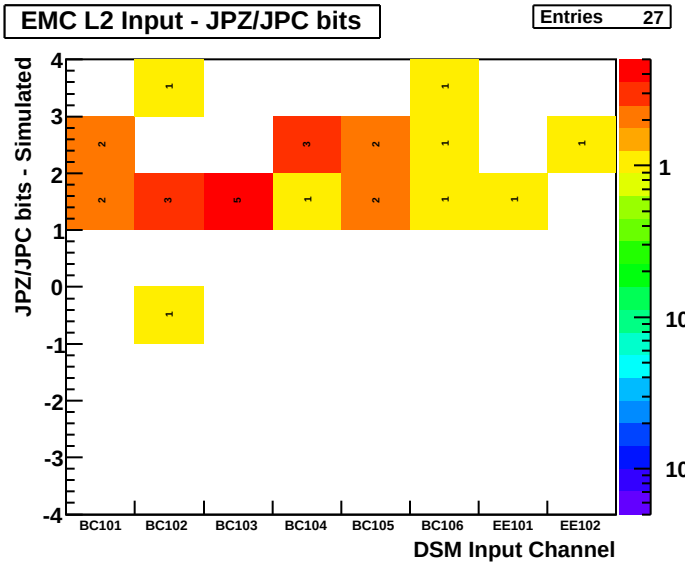

EMC L2 Input - JPX/JPA bits
Entries 1.300218e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

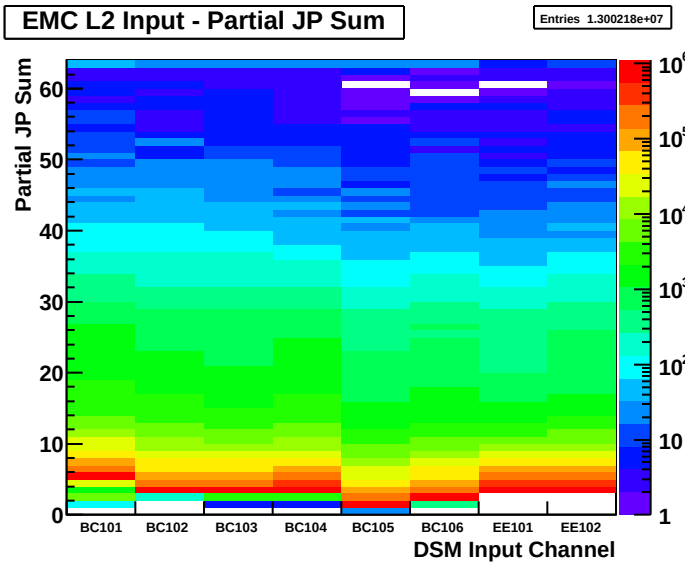

EMC L2 Input - JPX/JPA bits
Entries 1719
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

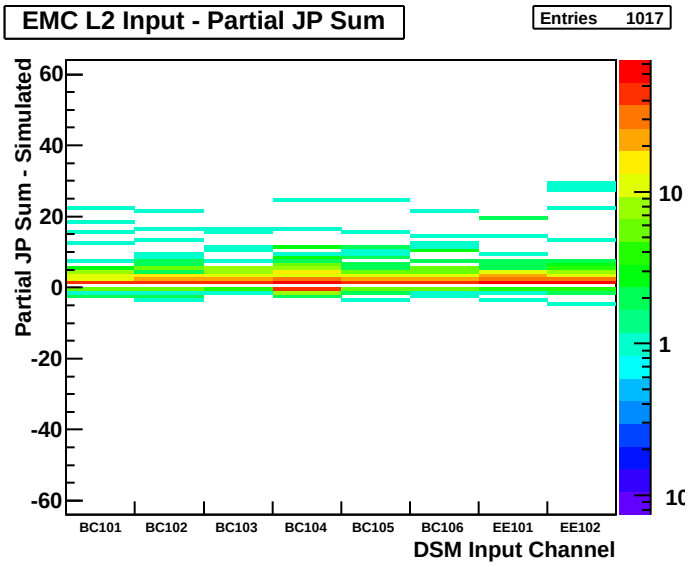

EMC L2 Input - JPY/JPB bits
Entries 1.300218e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

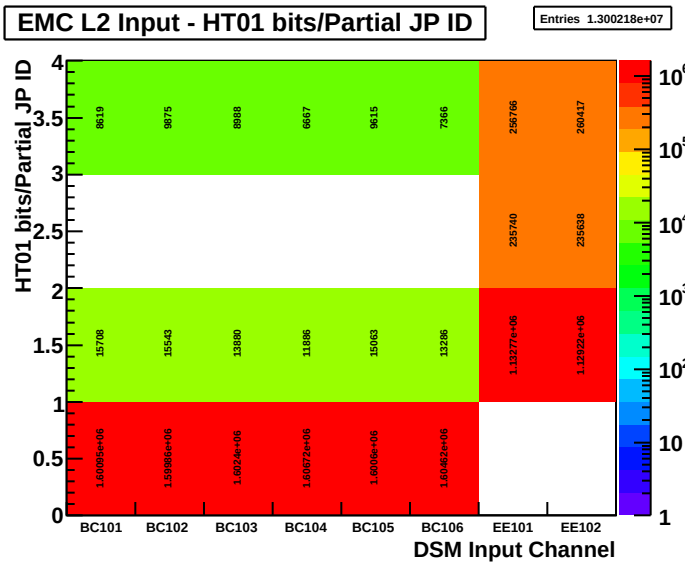

EMC L2 Input - JPY/JPB bits
Entries 1692
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

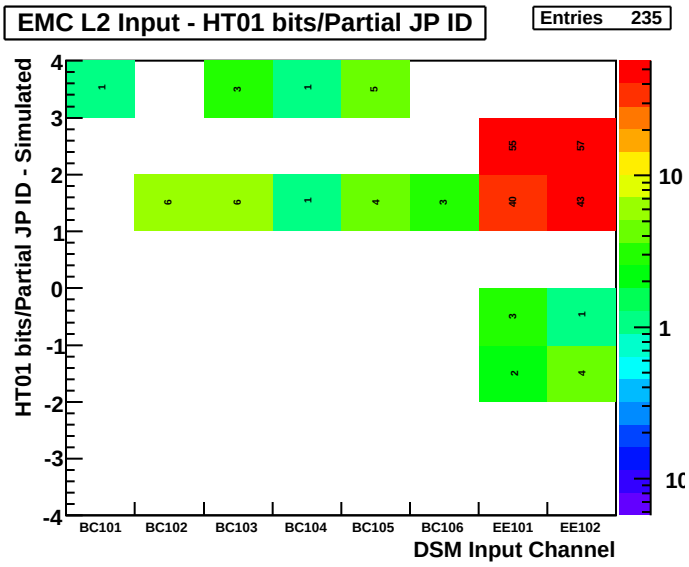

EMC L2 Input - JPZ/JPC bits
Entries 1.300218e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

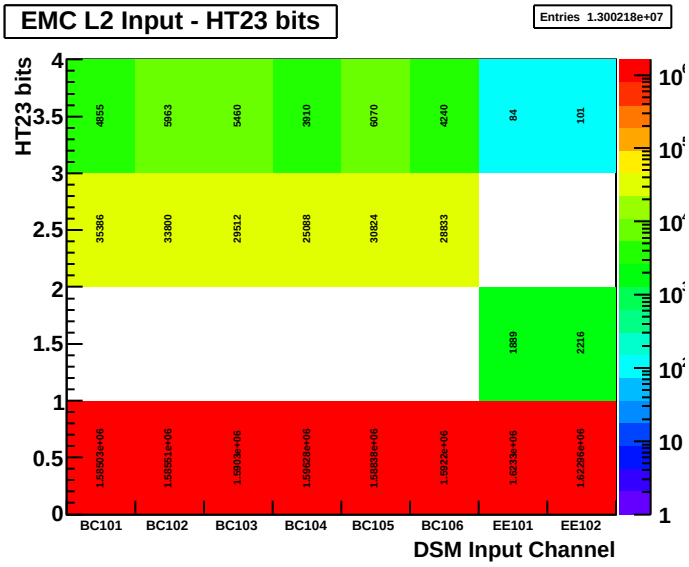

EMC L2 Input - JPZ/JPC bits
Entries 27
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

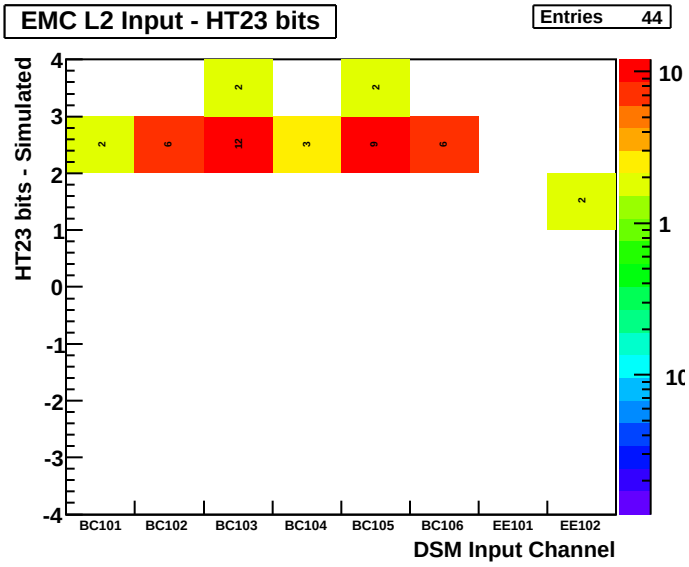

EMC L2 Input - Partial JP Sum
Entries 1.300218e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

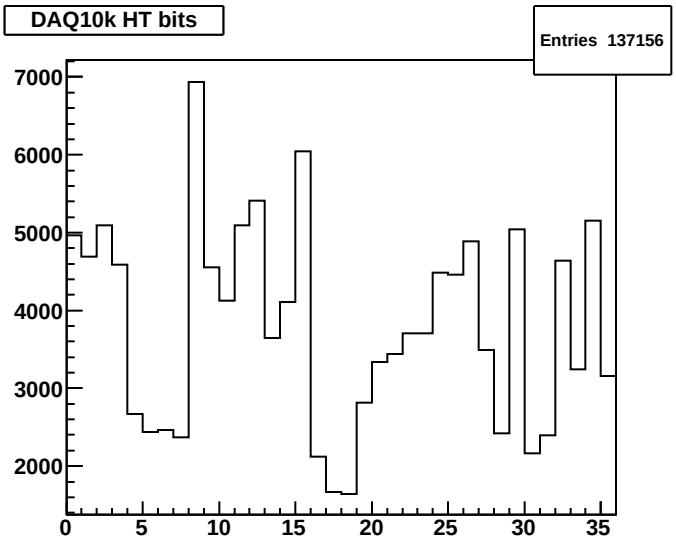

EMC L2 Input - Partial JP Sum
Entries 1017
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

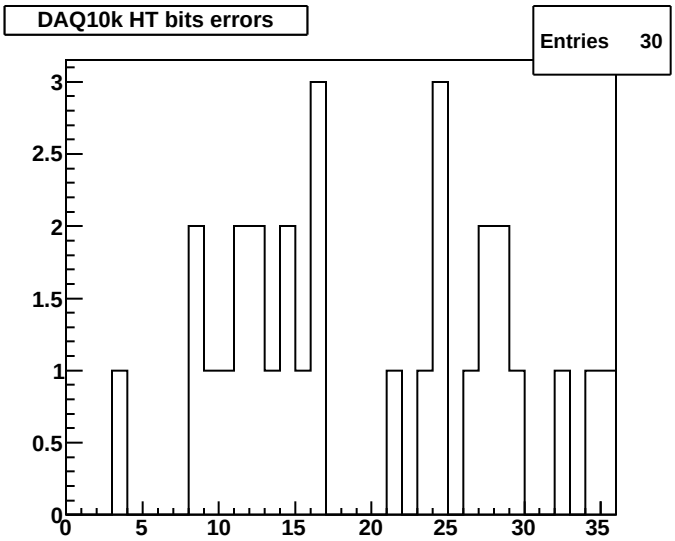

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.300218e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

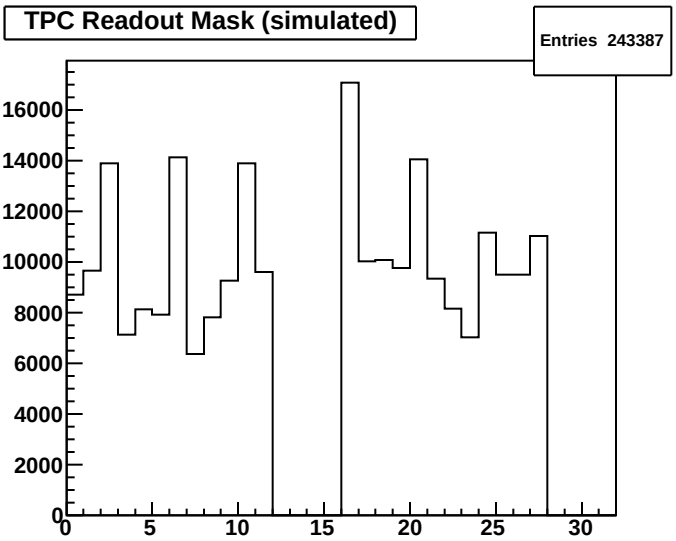

EMC L2 Input - HT01 bits/Partial JP ID
Entries 235
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

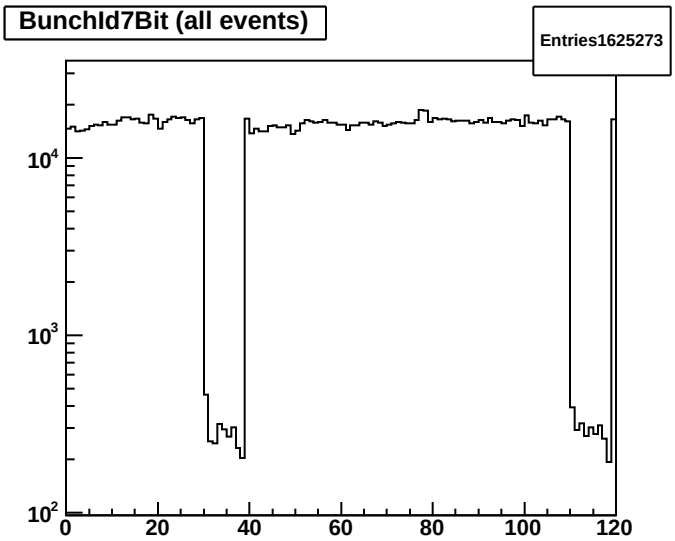

EMC L2 Input - HT23 bits
Entries 1.300218e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

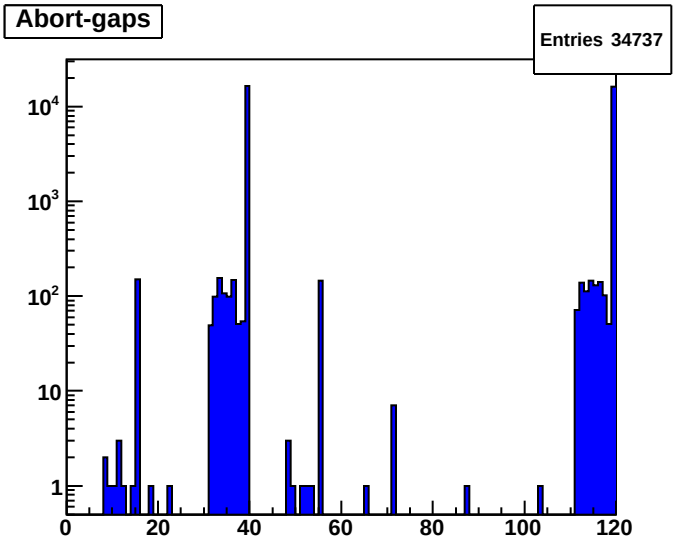

EMC L2 Input - HT23 bits
Entries 44
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

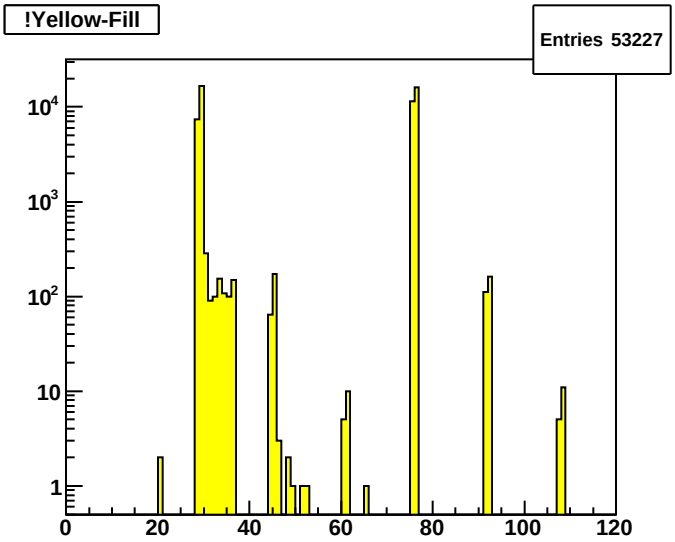

DAQ10k HT bits
Entries 137156

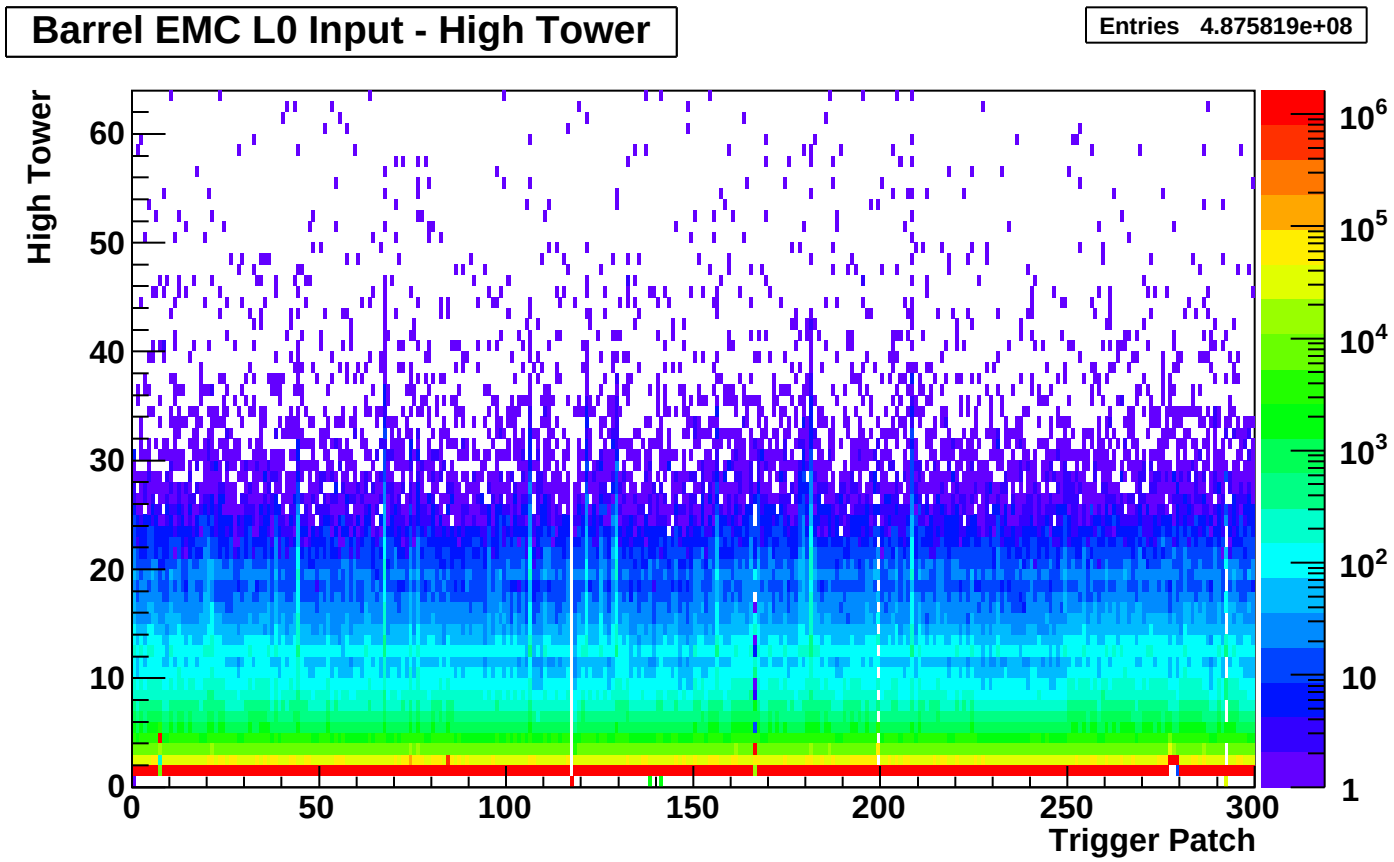

DAQ10k HT bits errors
Entries 30

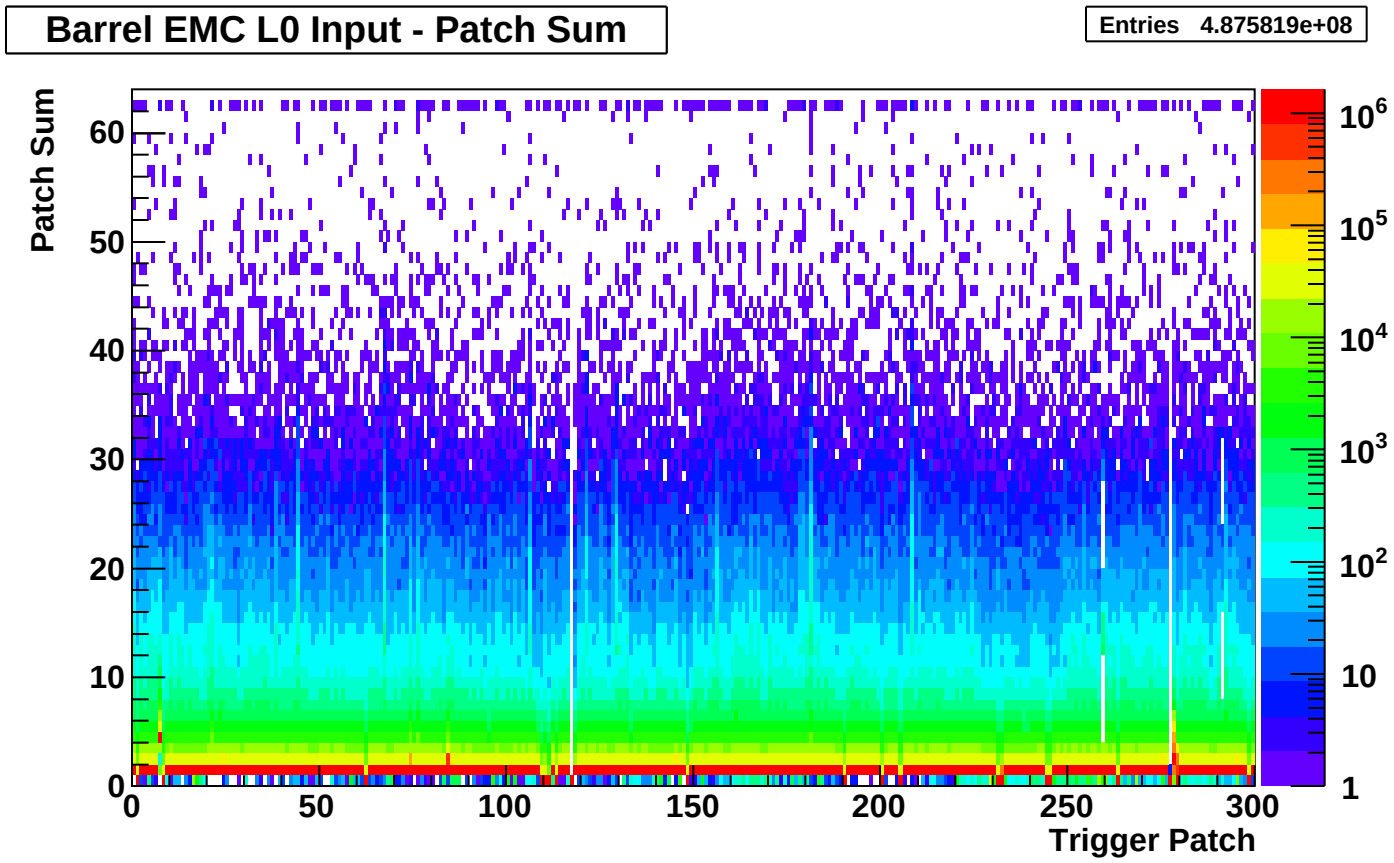

TPC Readout Mask (simulated)
Entries 243387

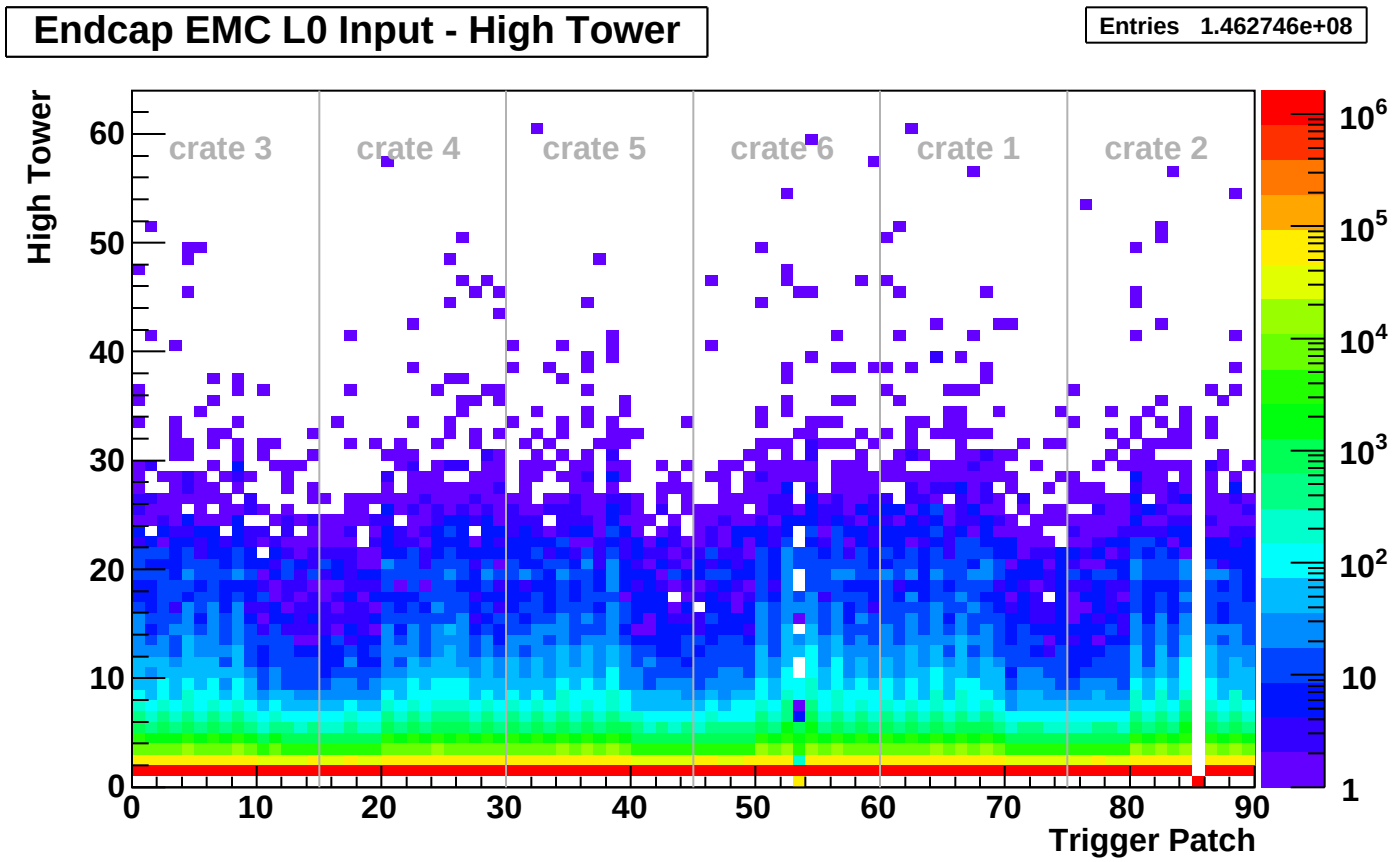

BunchId7Bit (all events)
Entries1625273

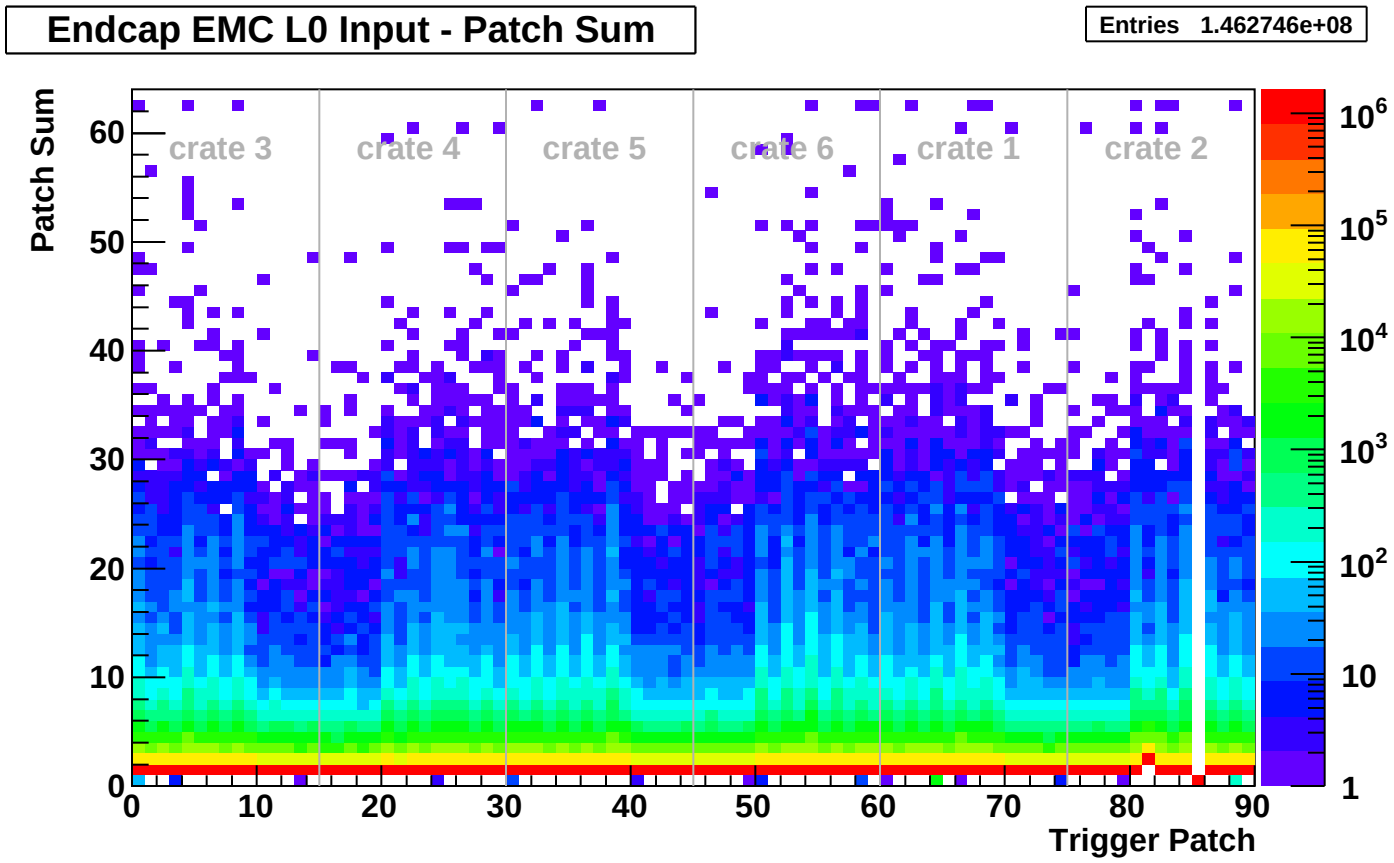

Abort-gaps
Entries 34737

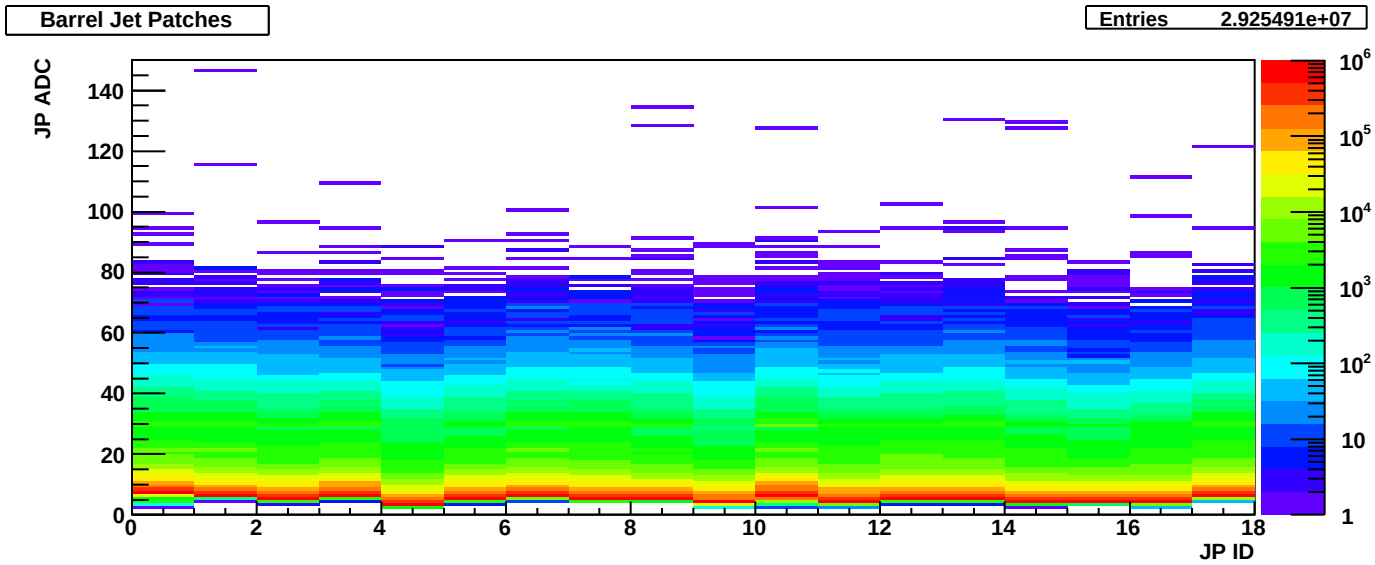

!Yellow-Fill
Entries 53227

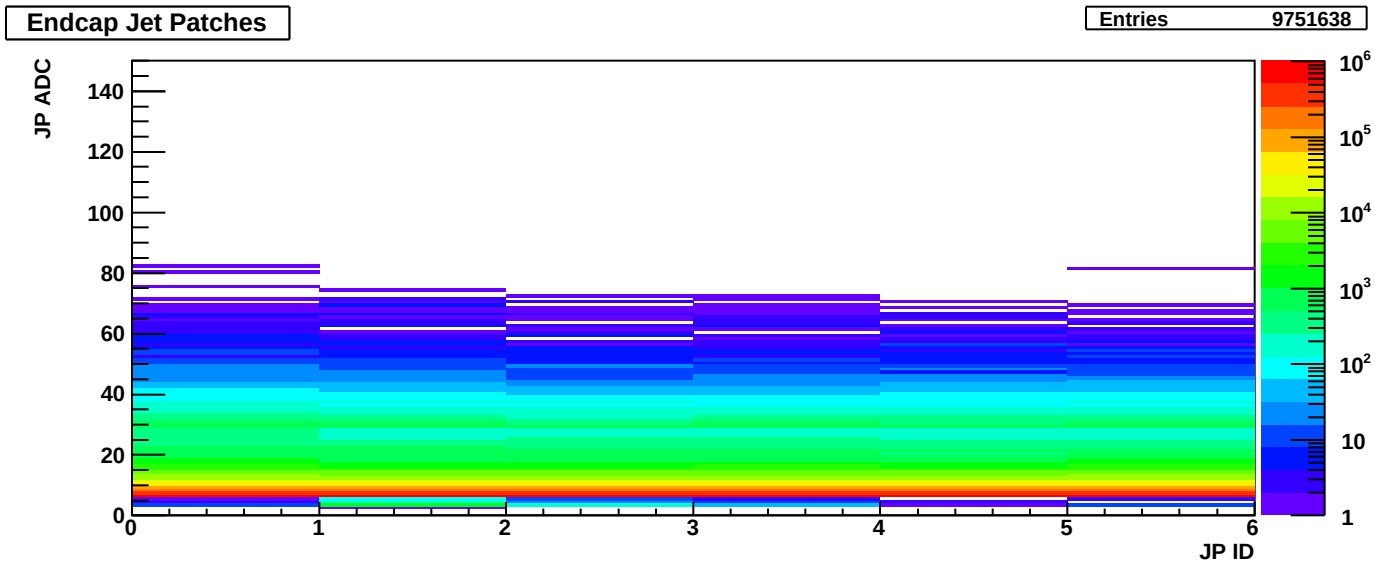

Barrel EMC L0 Input - High Tower
Entries 4.875819e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

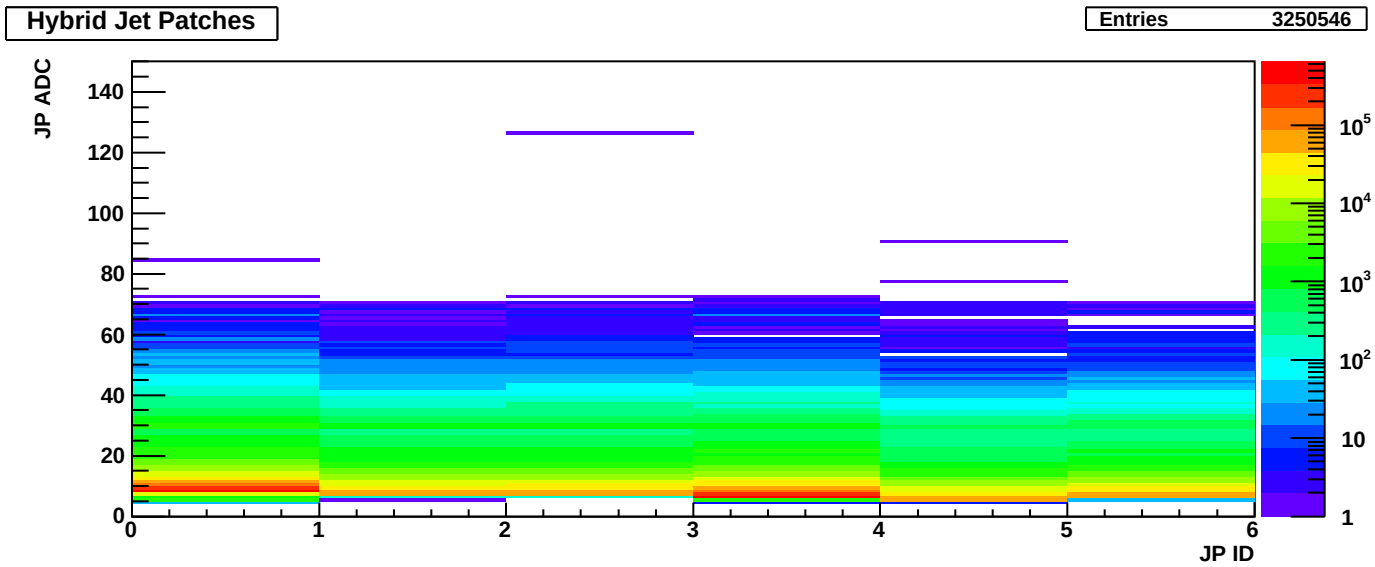

Barrel EMC L0 Input - Patch Sum
Entries 4.875819e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

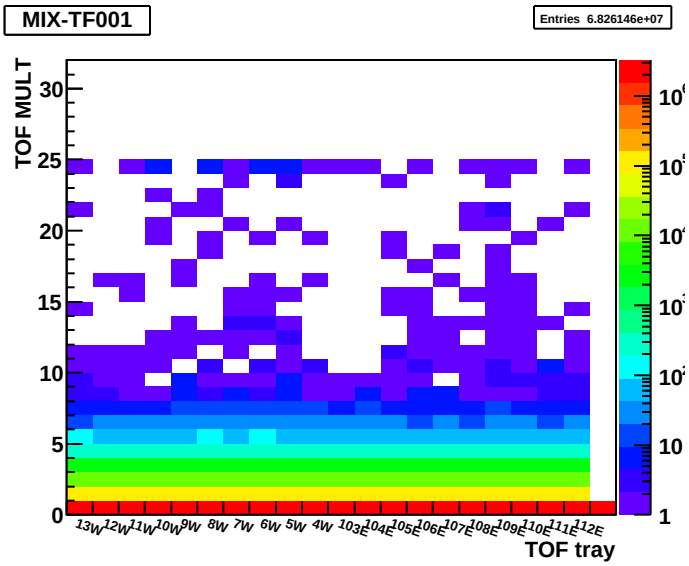

Endcap EMC L0 Input - High Tower
Entries 1.462746e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

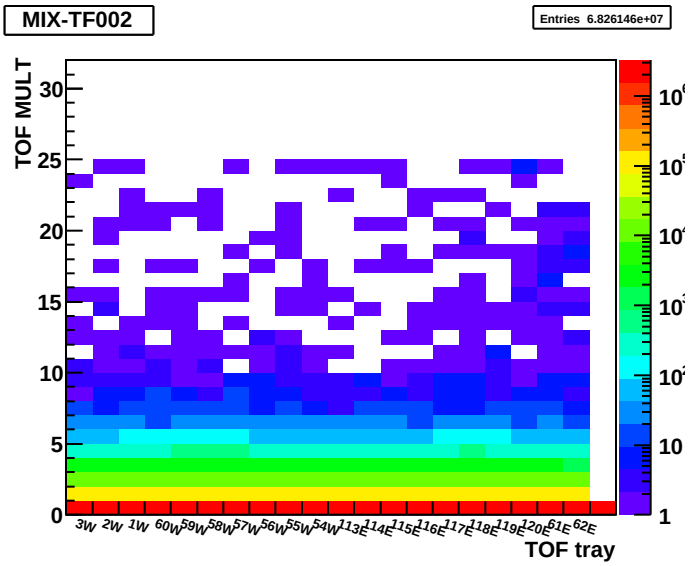

Endcap EMC L0 Input - Patch Sum
Entries 1.462746e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

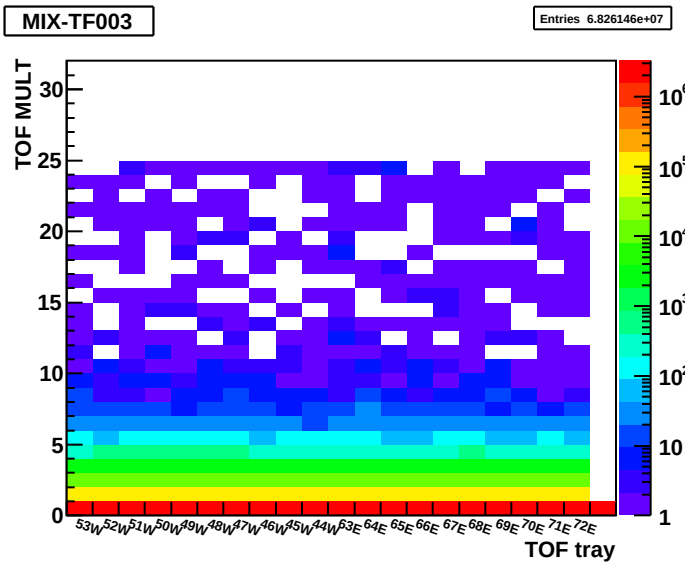

Barrel Jet Patches
Entries 2.925491e+07
JP ADC
JP ID

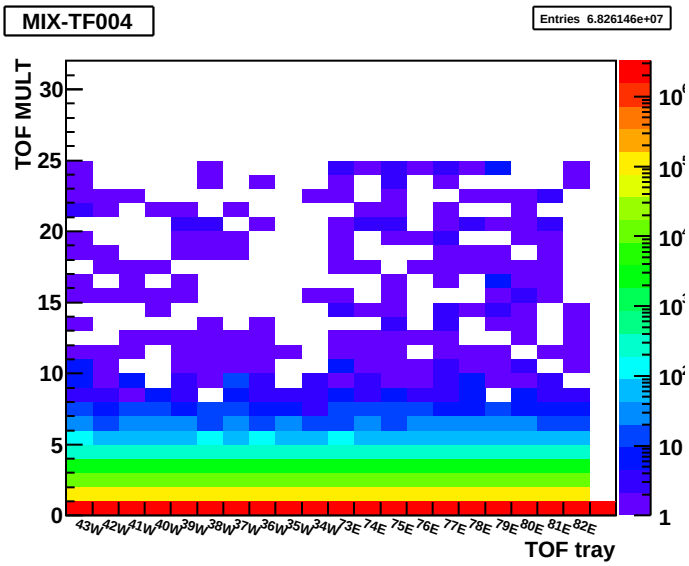

Endcap Jet Patches
Entries 9751638
JP ADC
JP ID

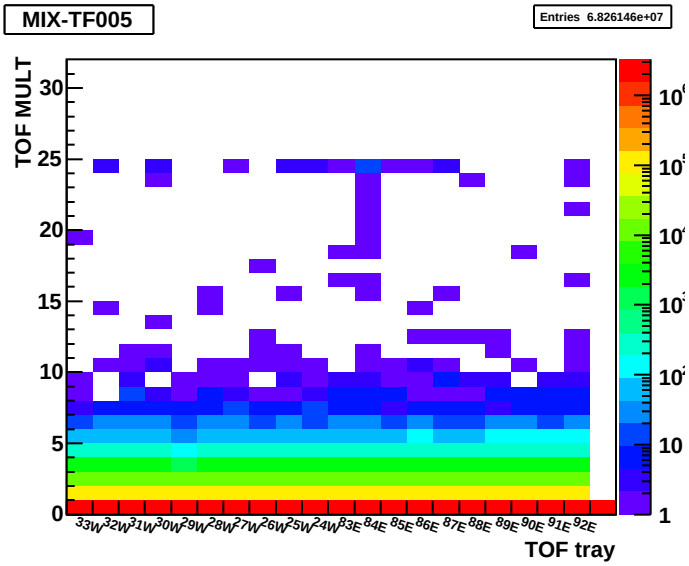

Hybrid Jet Patches
Entries 3250546
JP ADC
JP ID

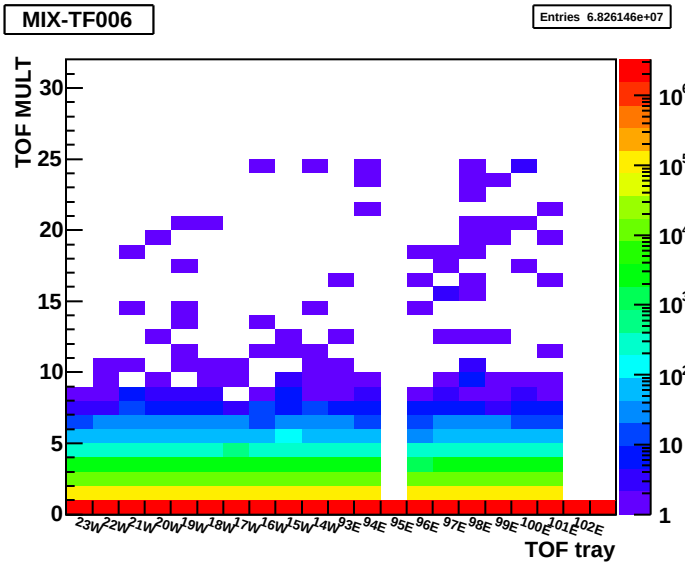

MIX-TF001
Entries 6.826146e+07
TOF MULT
TOF tray

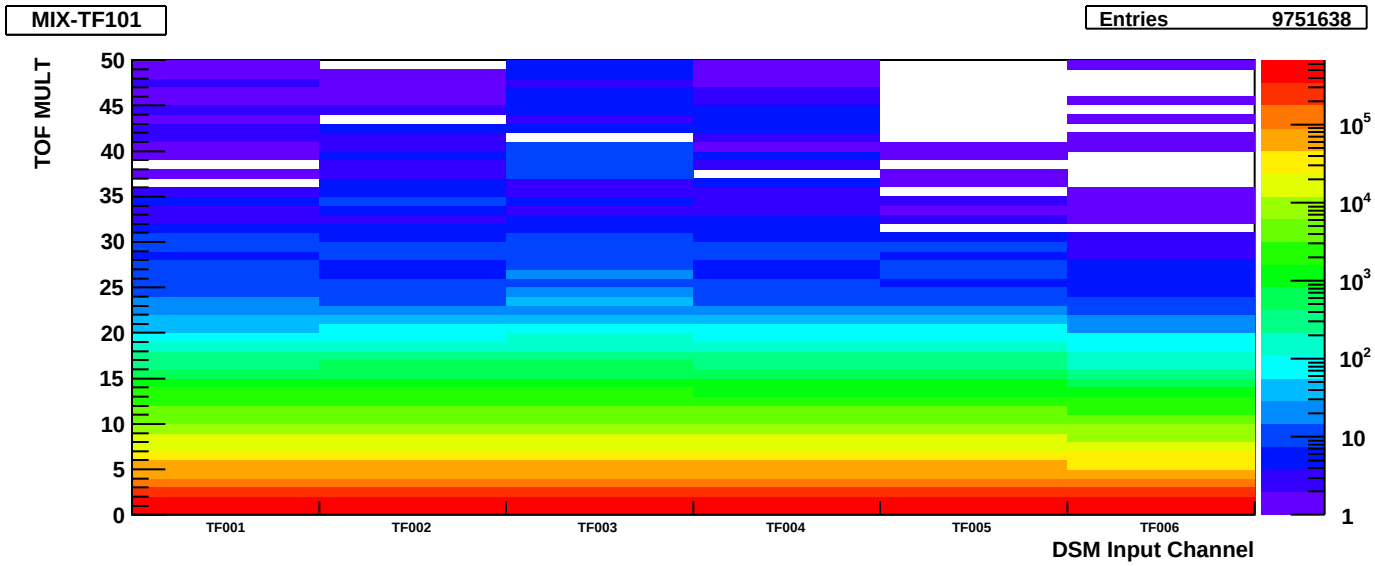

MIX-TF002
Entries 6.826146e+07
TOF MULT
TOF tray

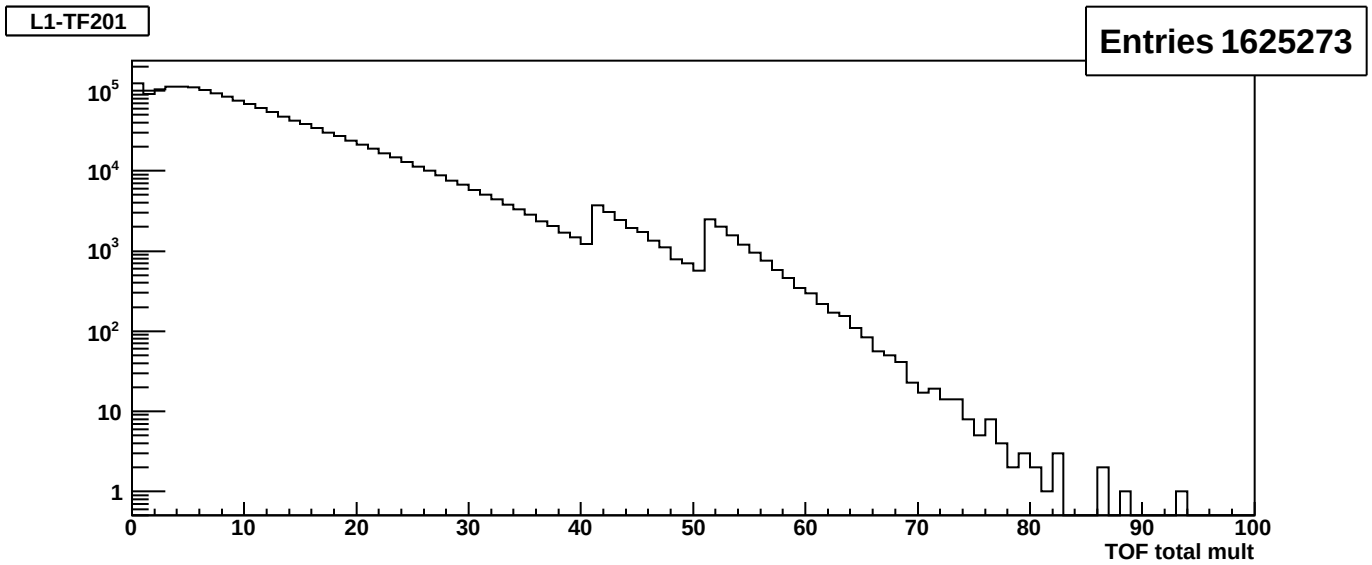

MIX-TF003
Entries 6.826146e+07
TOF MULT
TOF tray

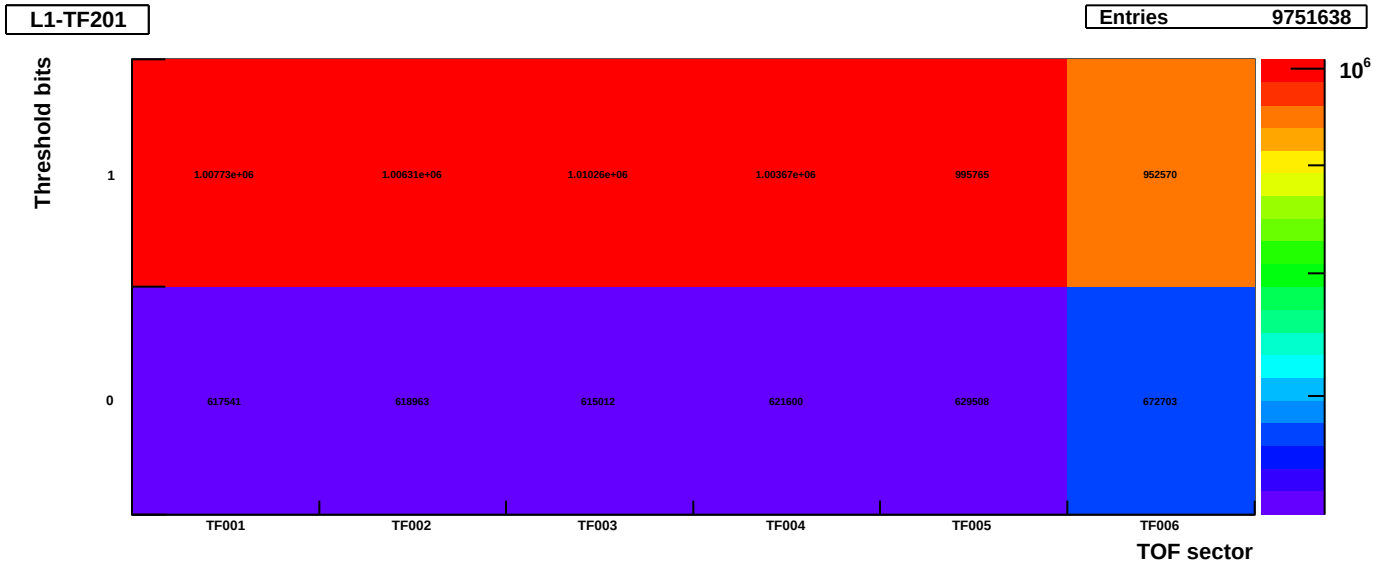

MIX-TF004
Entries 6.826146e+07
TOF MULT
TOF tray

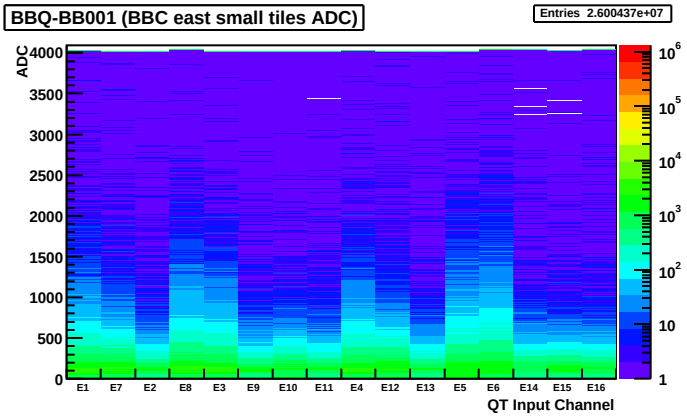

MIX-TF005
Entries 6.826146e+07
TOF MULT
TOF tray

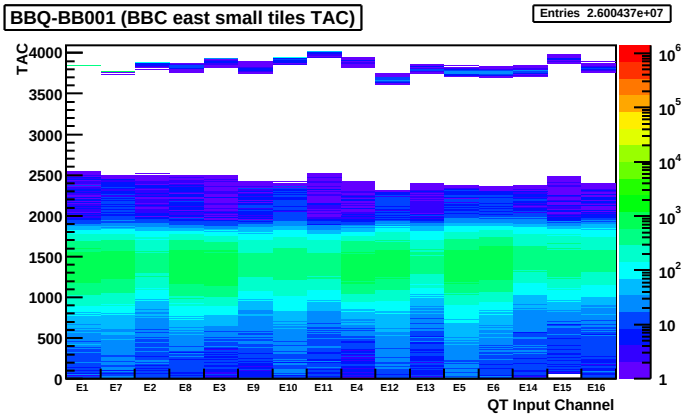

MIX-TF006
Entries 6.826146e+07
TOF MULT
TOF tray

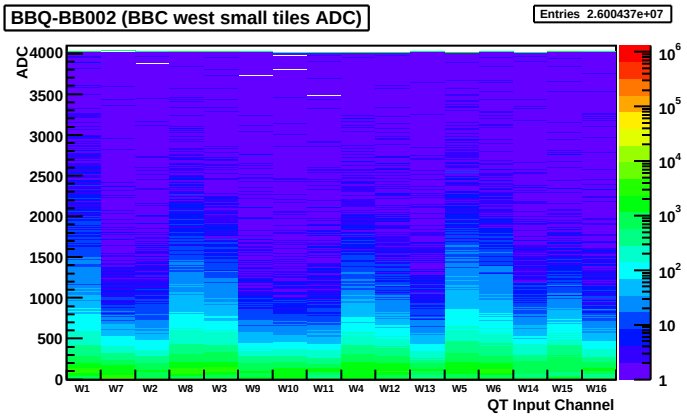

MIX-TF101
Entries 9751638
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

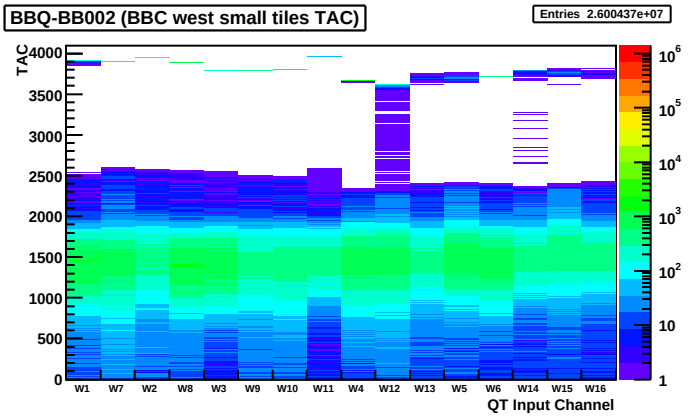

L1-TF201
Entries 1625273
TOF total mult

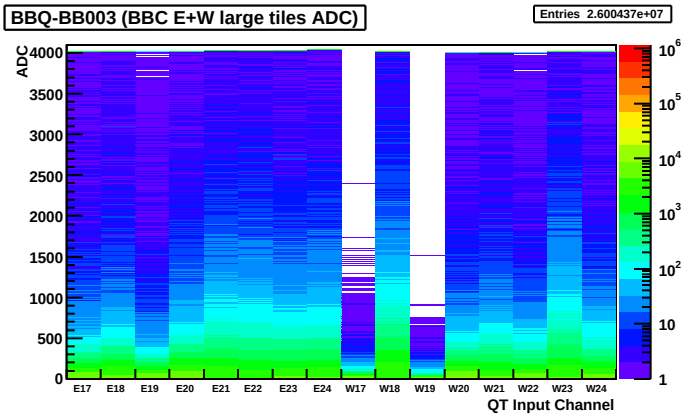

L1-TF201
Entries 9751638
Threshold bits
1.00773e+06
1.00631e+06
1.01026e+06
1.00367e+06
995765
952570
617541
618963
615012
621600
629508
672703
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

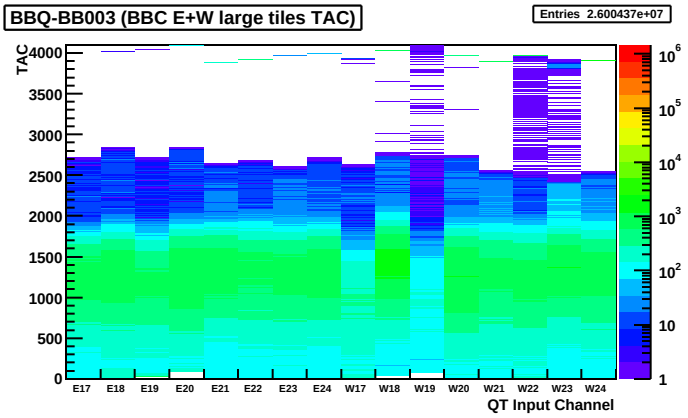

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.600437e+07
ADC
QT Input Channel

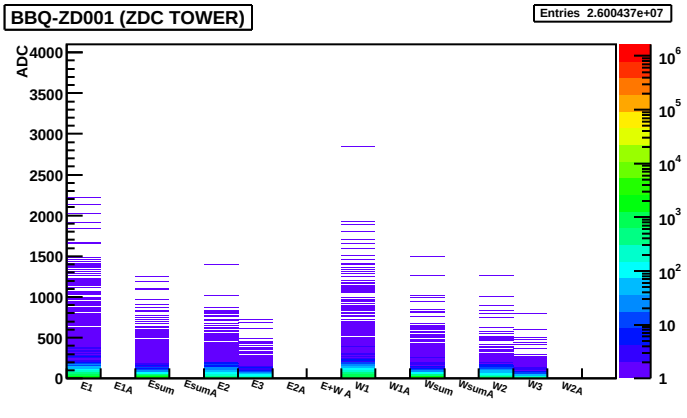

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.600437e+07
TAC
QT Input Channel

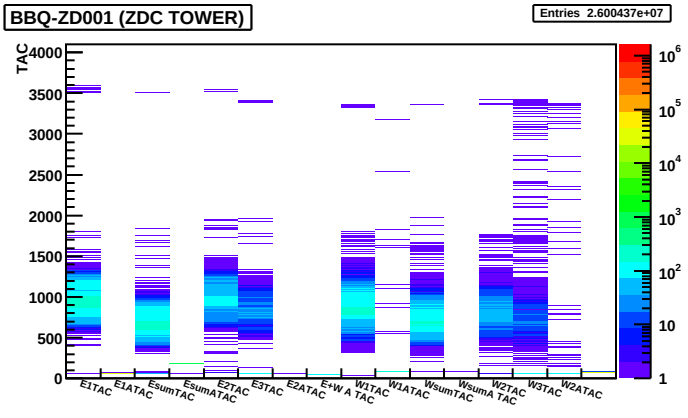

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.600437e+07
ADC
QT Input Channel

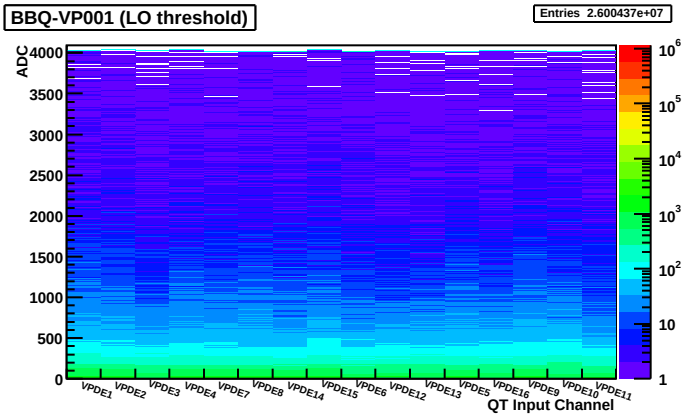

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.600437e+07
TAC
QT Input Channel

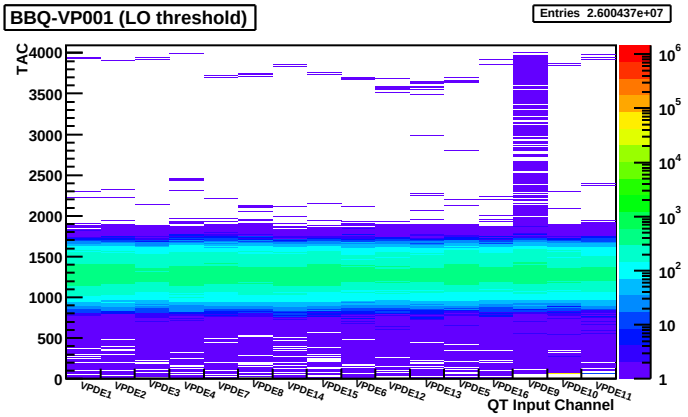

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.600437e+07
ADC
QT Input Channel

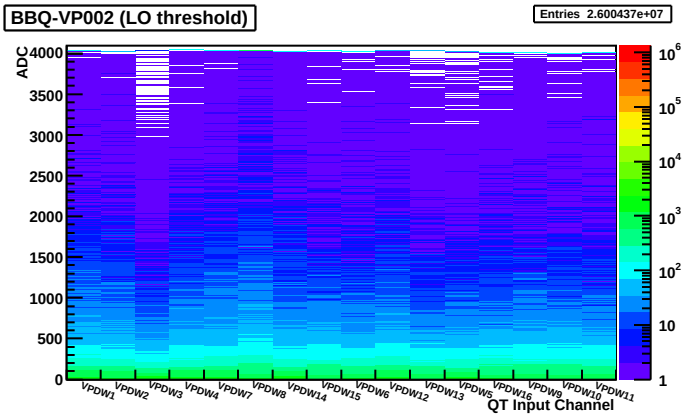

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.600437e+07
TAC
QT Input Channel

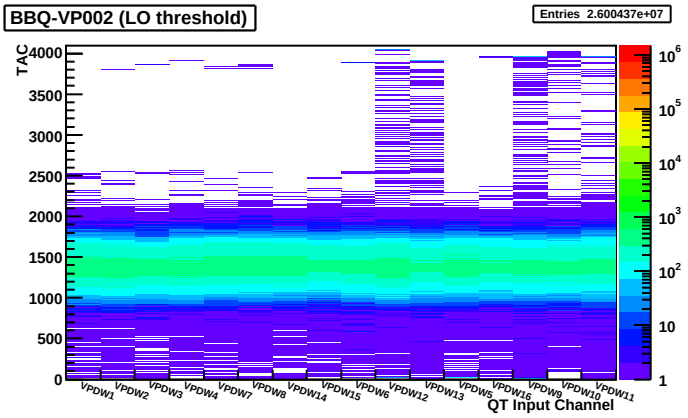

BBQ-ZD001 (ZDC TOWER)
Entries 2.600437e+07
ADC

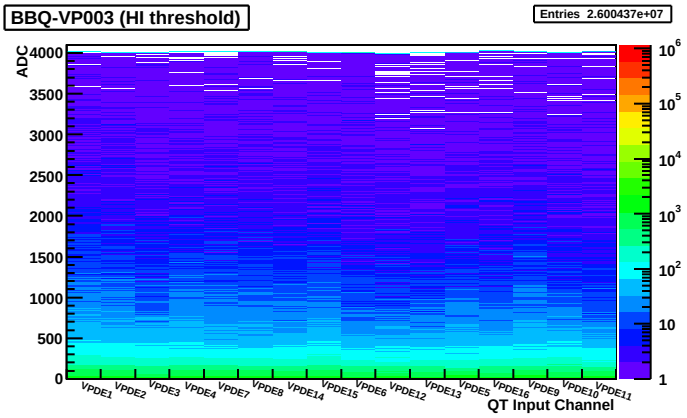

BBQ-ZD001 (ZDC TOWER)
Entries 2.600437e+07
TAC

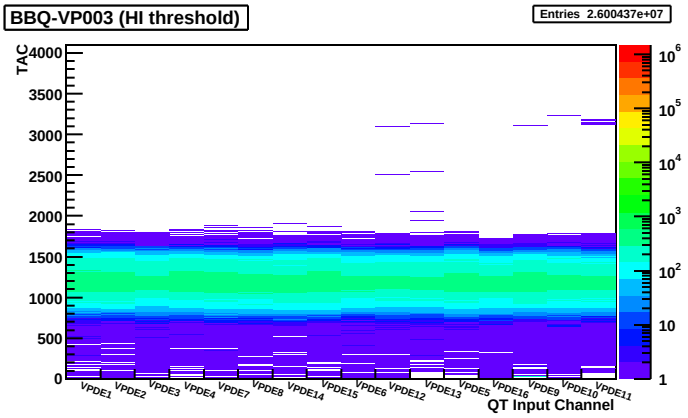

BBQ-VP001 (LO threshold)
Entries 2.600437e+07
ADC
QT Input Channel

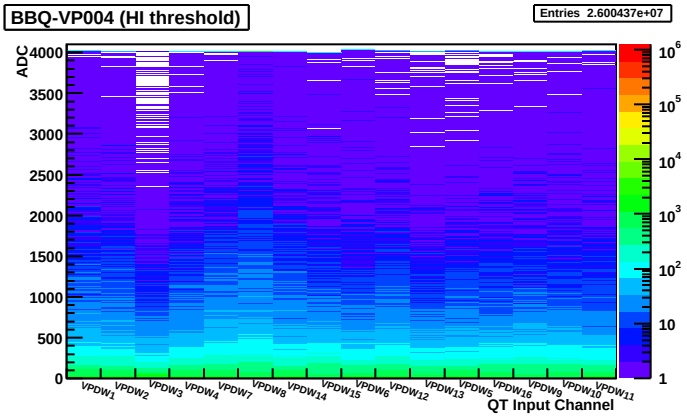

BBQ-VP001 (LO threshold)
Entries 2.600437e+07
TAC
QT Input Channel

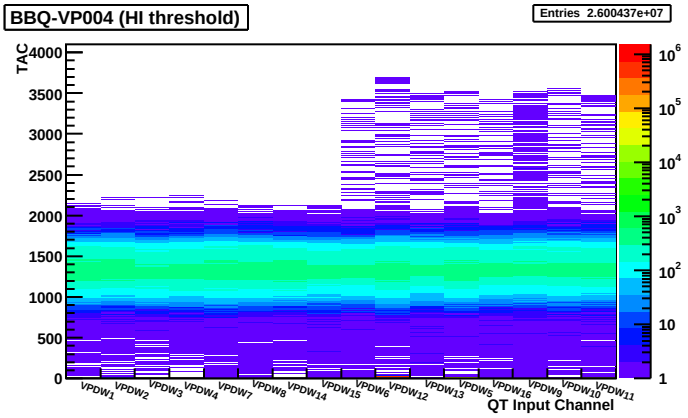

BBQ-VP002 (LO threshold)
Entries 2.600437e+07
ADC
QT Input Channel

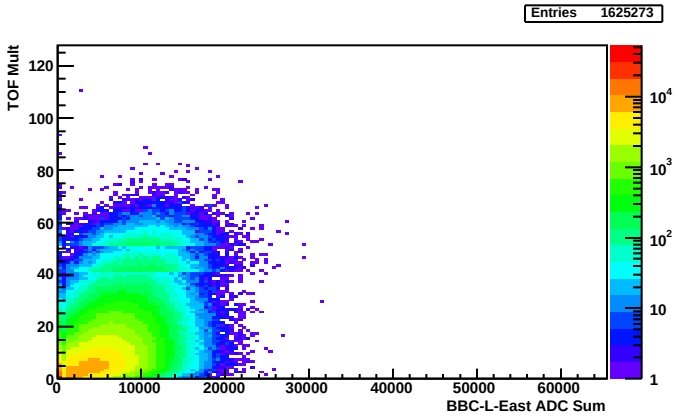

BBQ-VP002 (LO threshold)
Entries 2.600437e+07
TAC
QT Input Channel

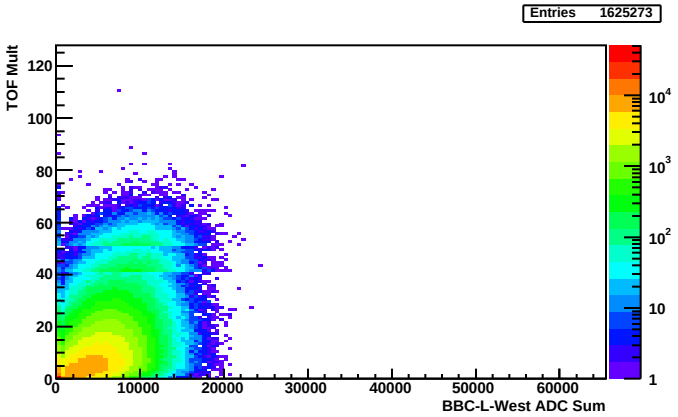

BBQ-VP003 (HI threshold)
Entries 2.600437e+07
ADC
QT Input Channel

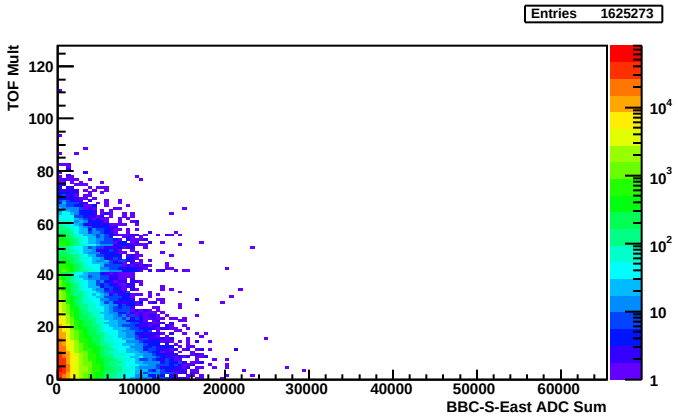

BBQ-VP003 (HI threshold)
Entries 2.600437e+07
TAC
QT Input Channel

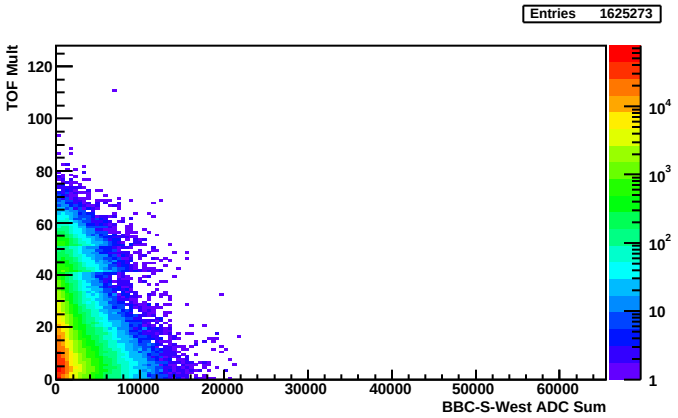

BBQ-VP004 (HI threshold)
Entries 2.600437e+07
ADC
QT Input Channel

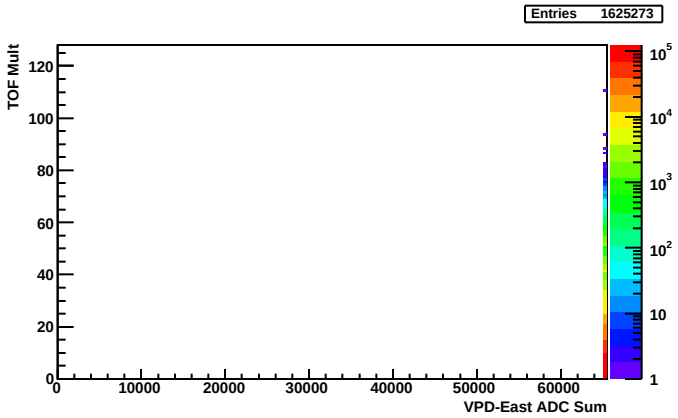

BBQ-VP004 (HI threshold)
Entries 2.600437e+07
TAC
QT Input Channel

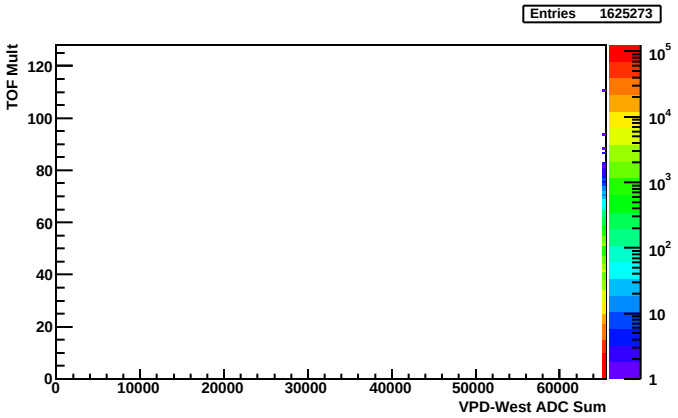

Entries 1625273
TOF Mult
BBC-L-East ADC Sum

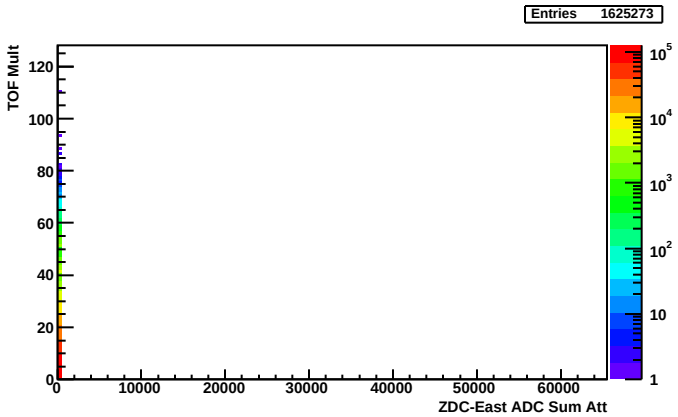

Entries 1625273
TOF Mult
BBC-L-West ADC Sum

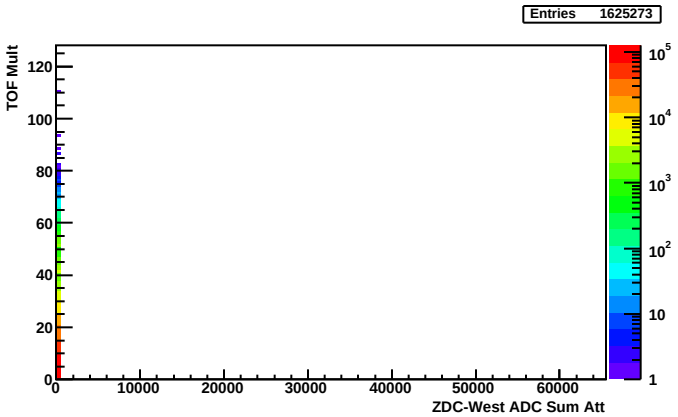

Entries 1625273
TOF Mult
BBC-S-East ADC Sum

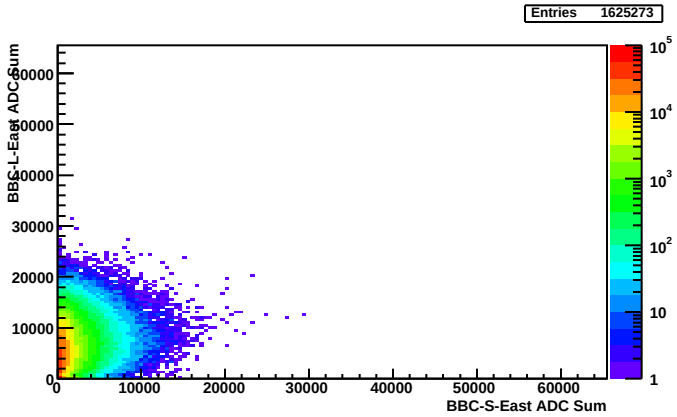

Entries 1625273
TOF Mult
BBC-S-West ADC Sum

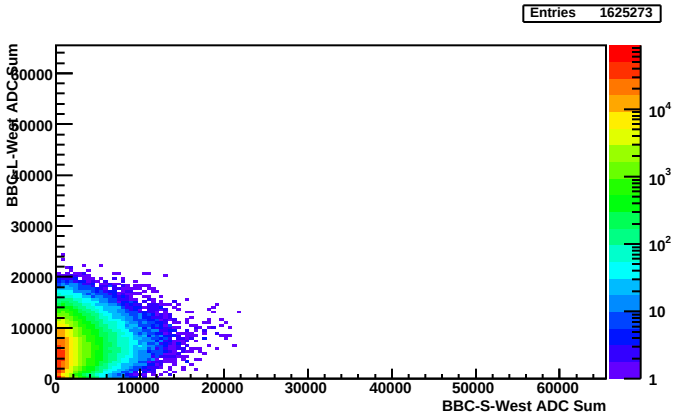

Entries 1625273
TOF Mult
VPD-East ADC Sum

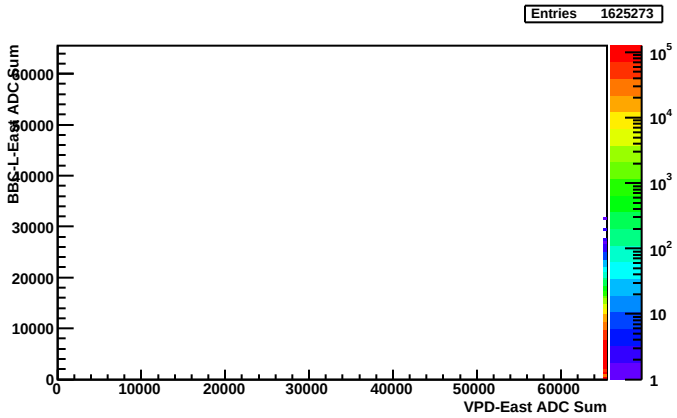

Entries 1625273
TOF Mult
VPD-West ADC Sum


Entries 1625273
TOF Mult
ZDC-East ADC Sum Att


Entries 1625273
TOF Mult
ZDC-West ADC Sum Att


Entries 1625273
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1625273
BBC-L-West ADC Sum
BBC-S-West ADC Sum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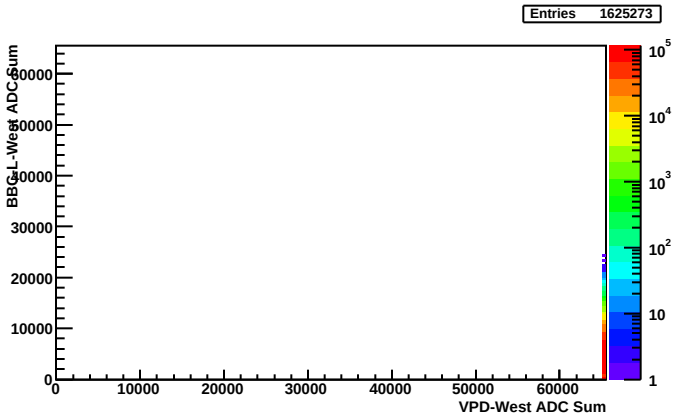
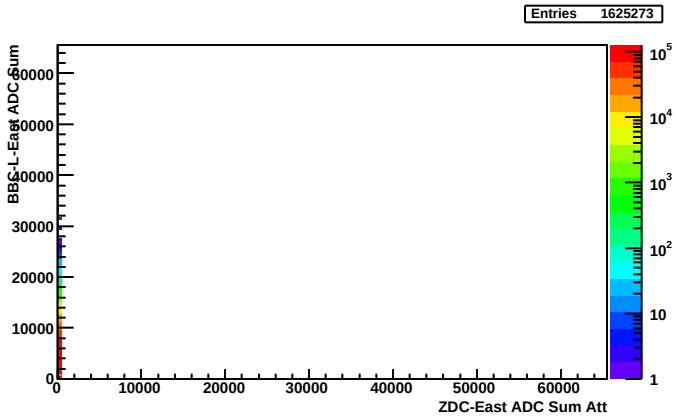
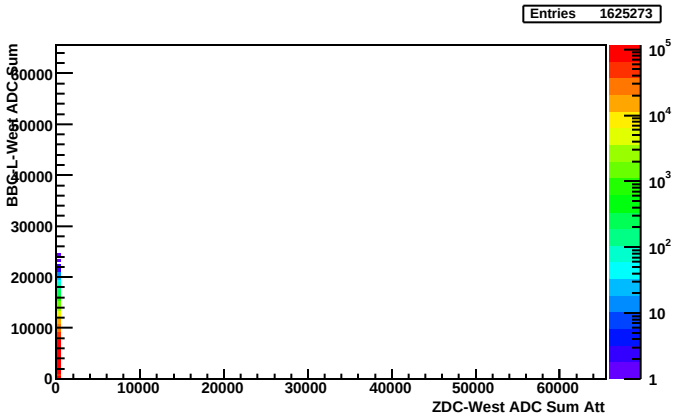
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 1625273
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 1625273
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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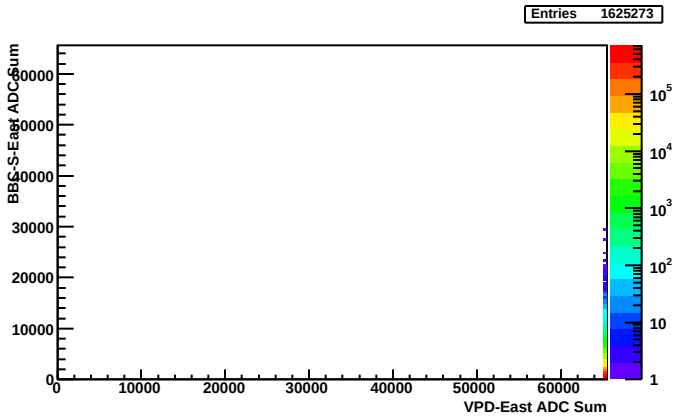
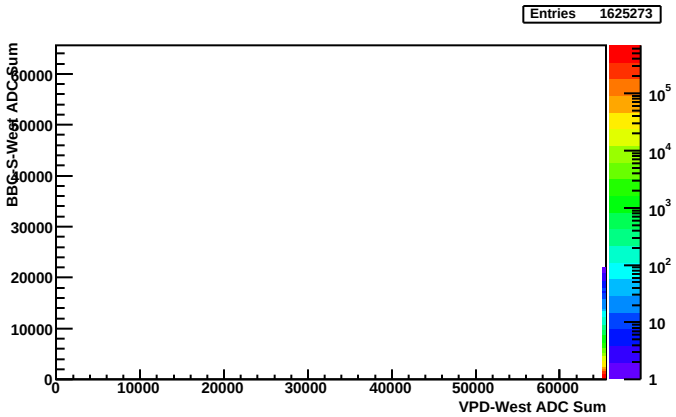
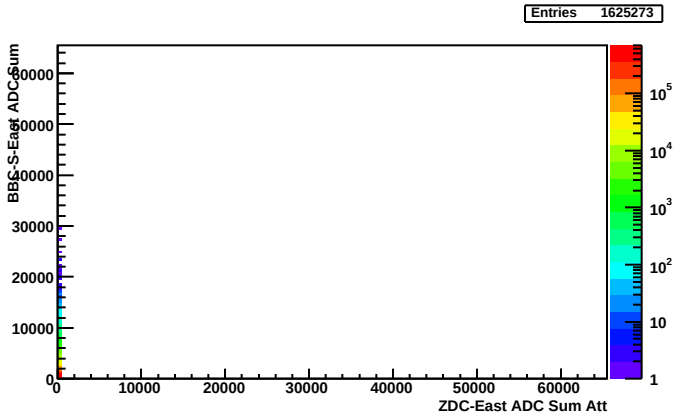
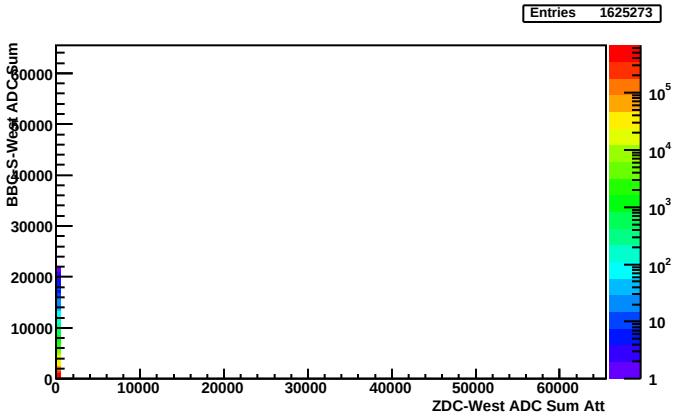
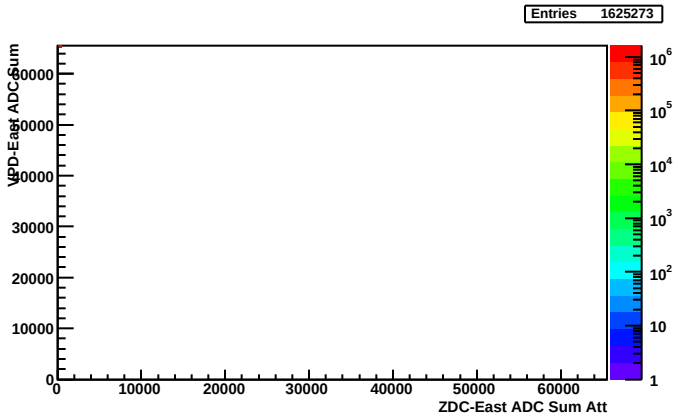
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

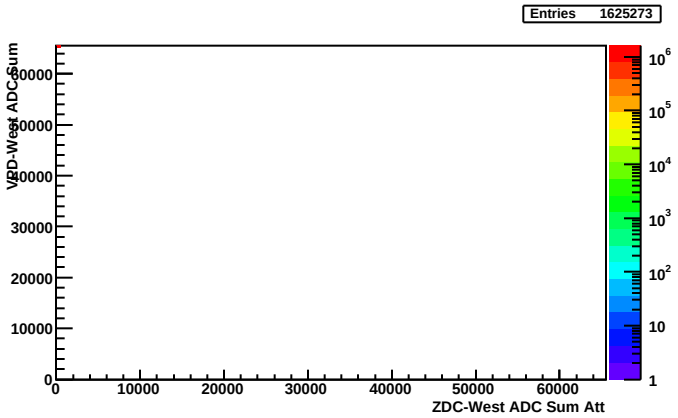

Entries 1625273
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1625273
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1625273
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1625273
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1625273
VPD-East ADC Sum
ZDC-East ADC Sum Att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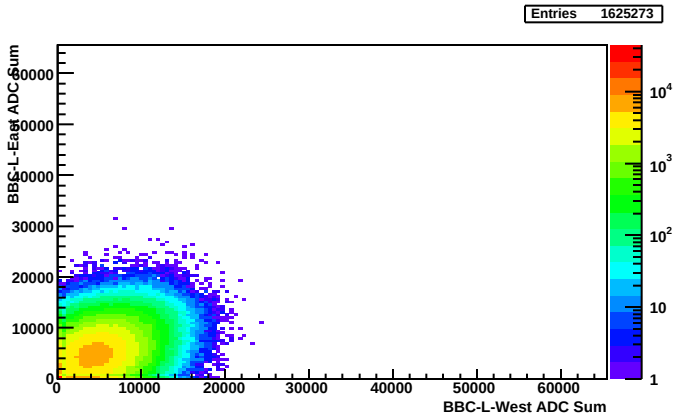
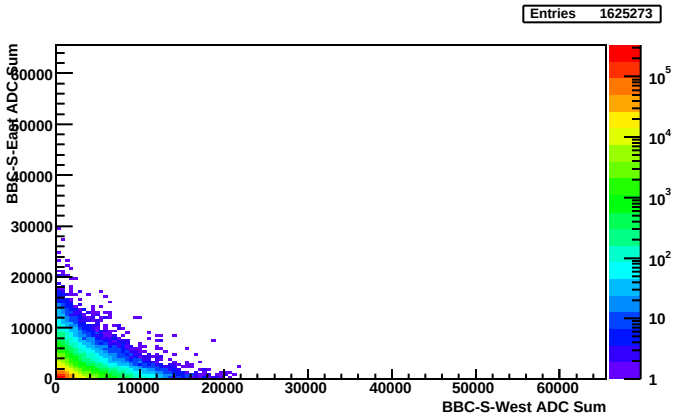
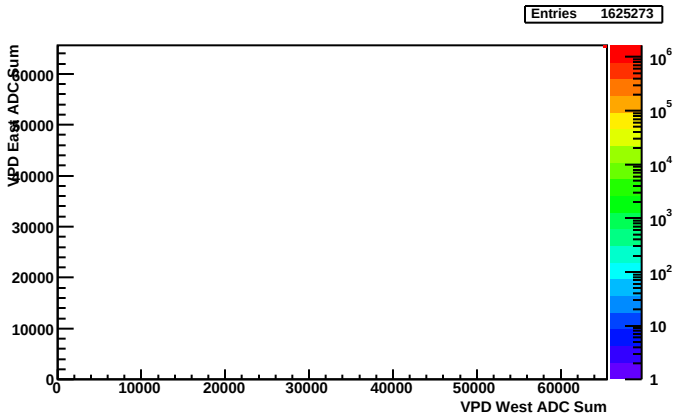
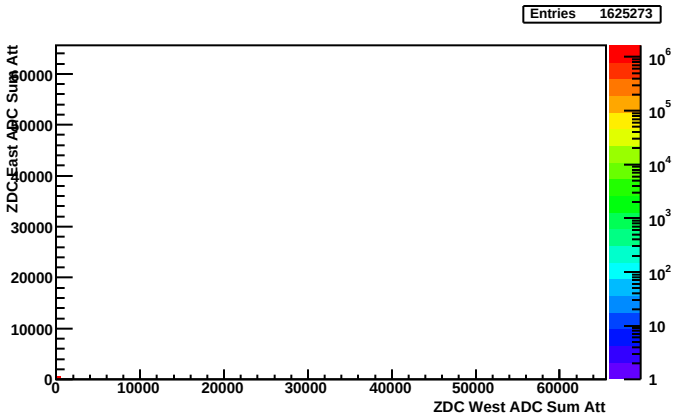
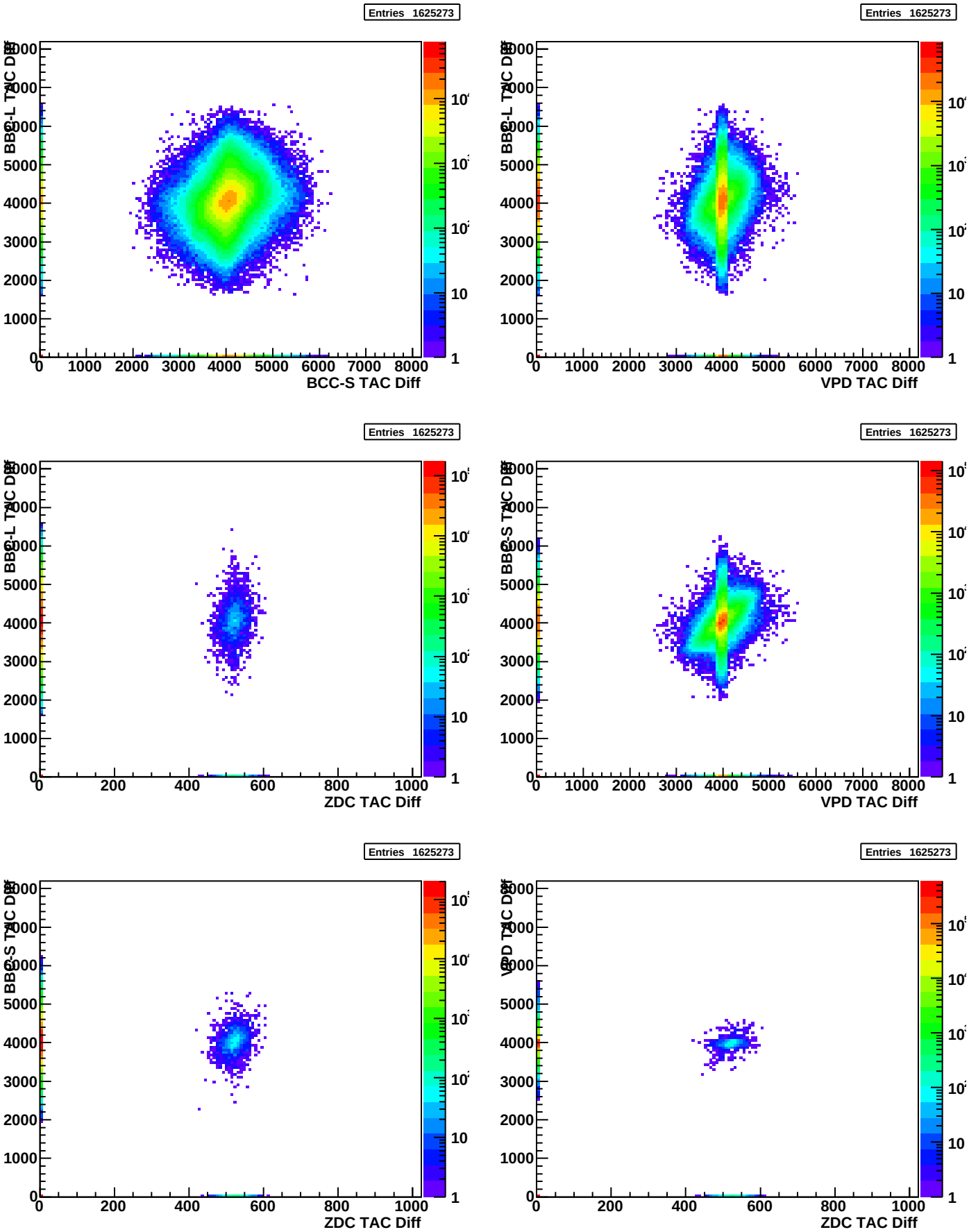
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 1625273
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 1625273
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 1625273
VPD East ADC Sum
VPD West ADC Sum


Entries 1625273
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 1625273
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1625273
BBC-L TAC Diff
VPD TAC Diff
Entries 1625273
BBC-L TAC Diff
ZDC TAC Diff
Entries 1625273
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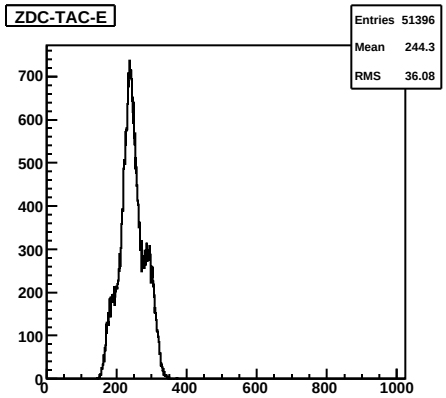
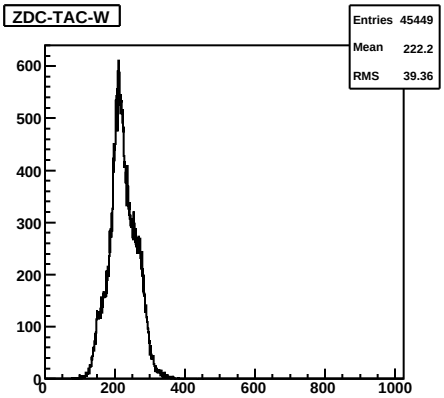
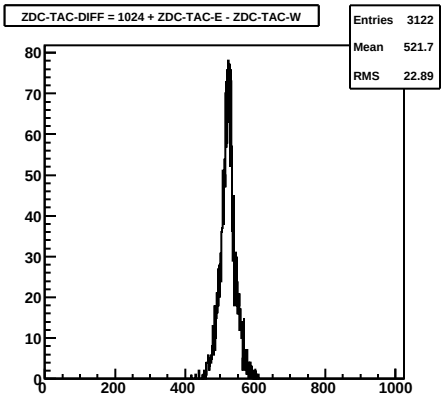
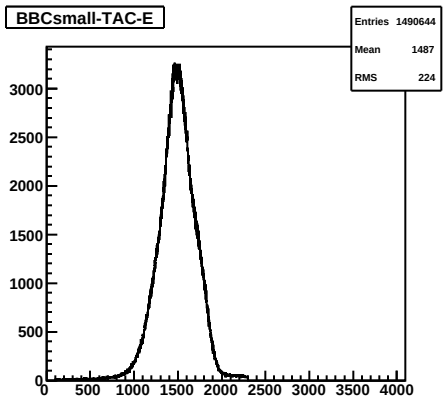
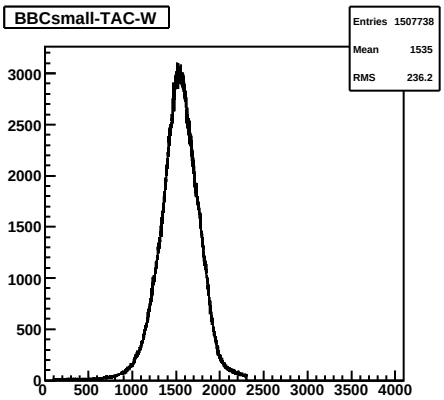
Entries 1625273
VPD TAC Diff
ZDC TAC Diff

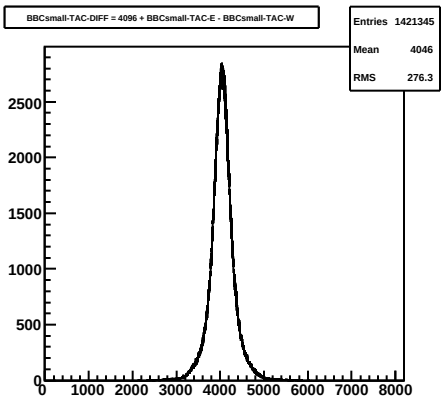

ZDC-TAC-E
Entries 51396
Mean 244.3
RMS 36.08

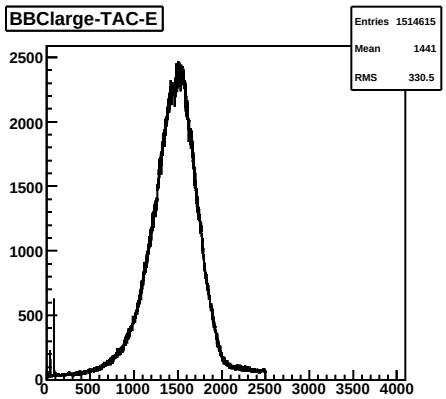

ZDC-TAC-W
Entries 45449
Mean 222.2
RMS 39.36

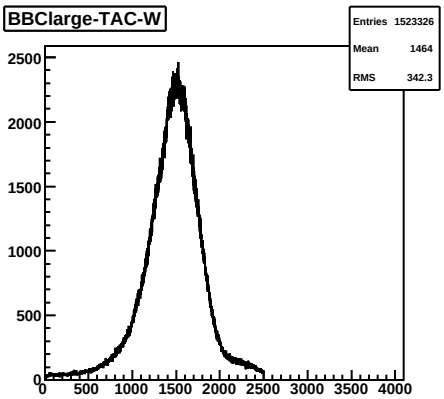

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3122
Mean 521.7
RMS 22.89

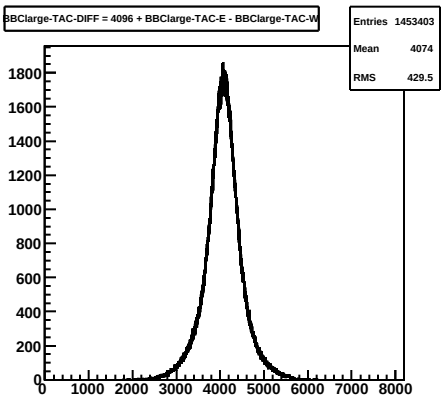

BBCsmall-TAC-E
Entries 1490644
Mean 1487
RMS 224

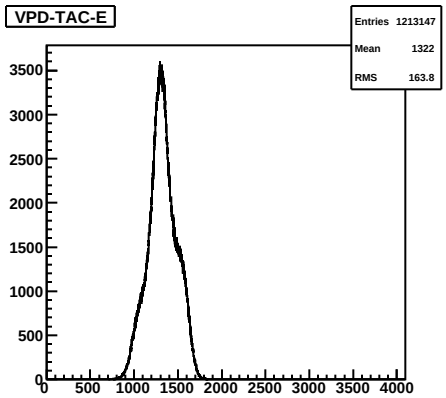

BBCsmall-TAC-W
Entries 1507738
Mean 1535
RMS 236.2

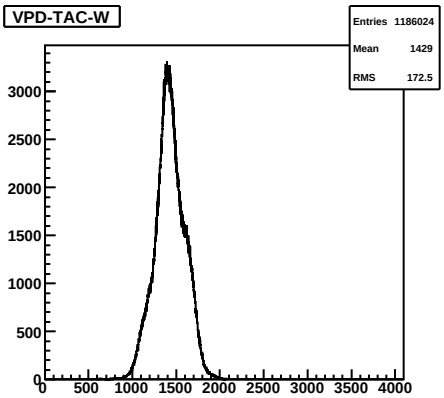

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1421345
Mean 4046
RMS 276.3

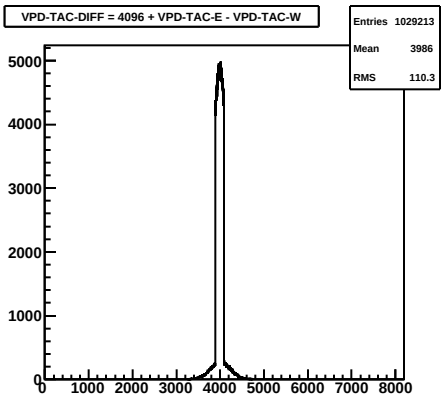

BBClarge-TAC-E
Entries 1514615
Mean 1441
RMS 330.5

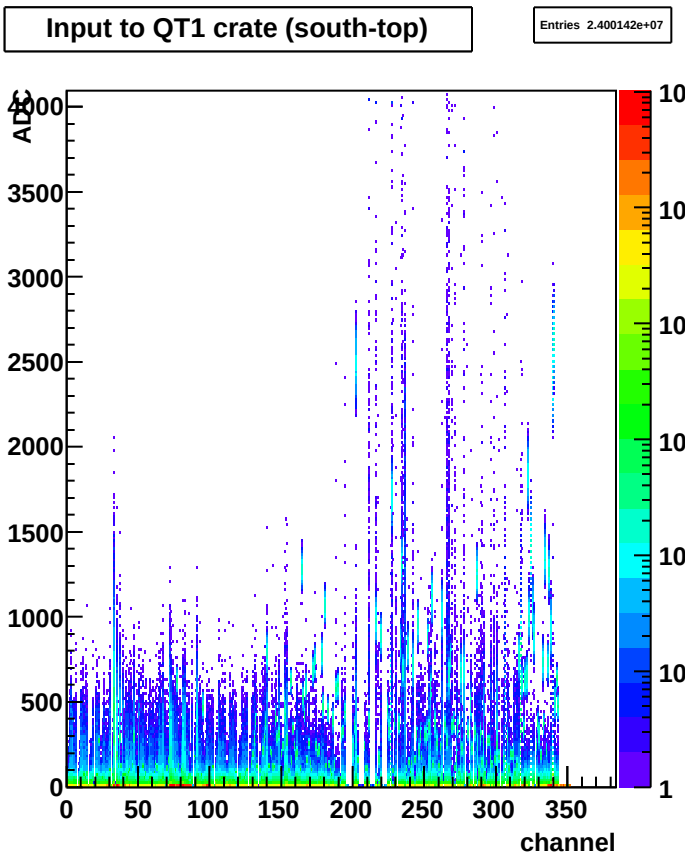

BBClarge-TAC-W
Entries 1523326
Mean 1464
RMS 342.3

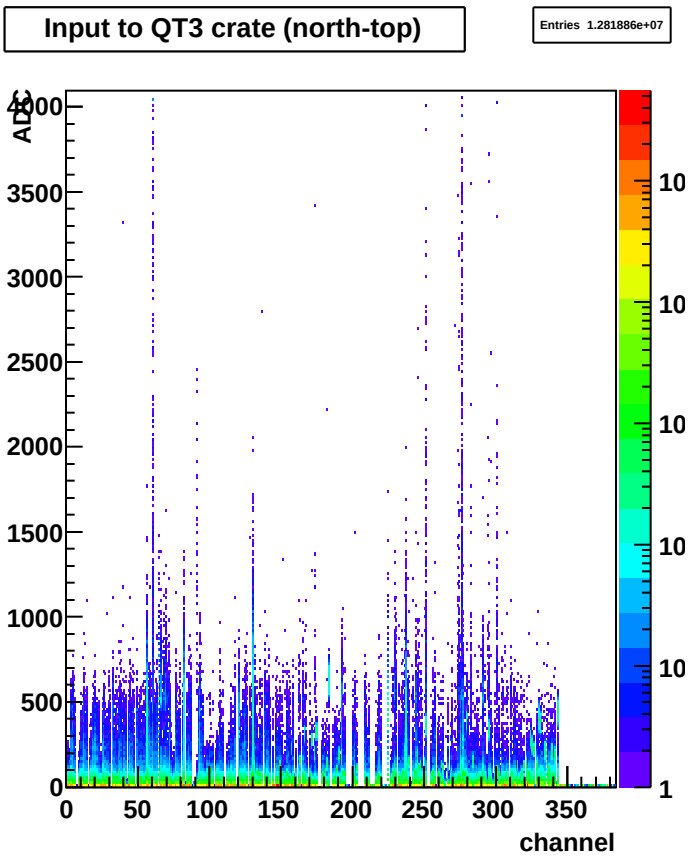

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1453403
Mean 4074
RMS 429.5

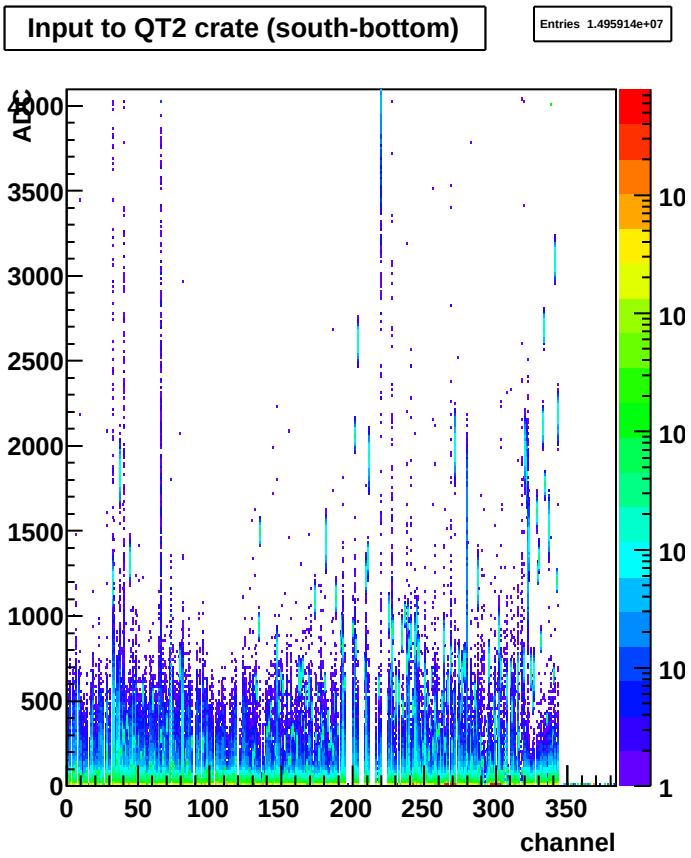

VPD-TAC-E
Entries 1213147
Mean 1322
RMS 163.8

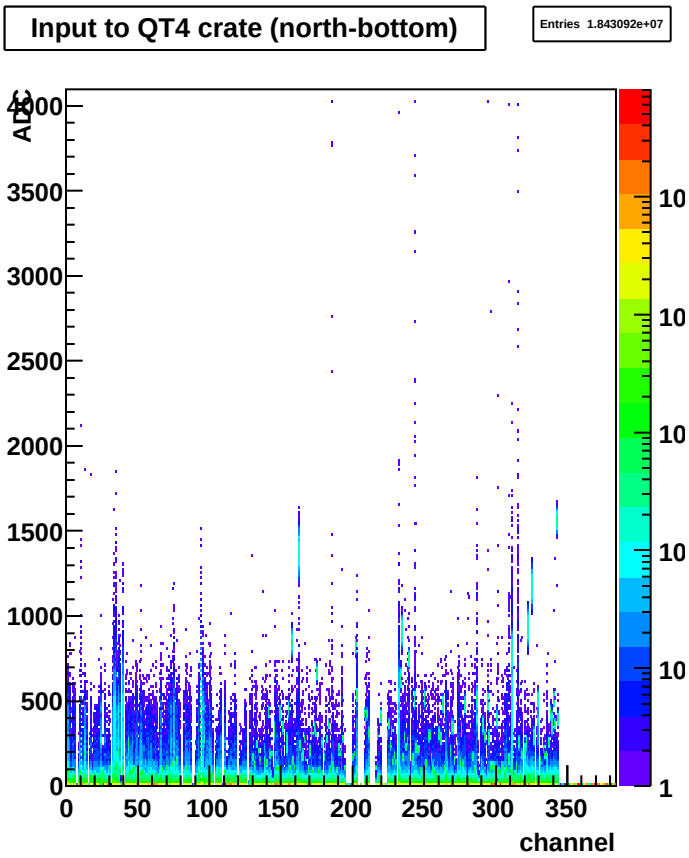

VPD-TAC-W
Entries 1186024
Mean 1429
RMS 172.5

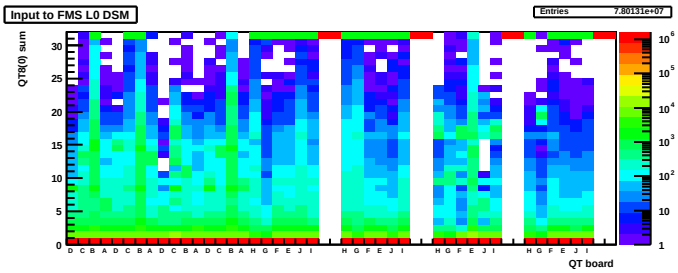

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1029213
Mean 3986
RMS 110.3

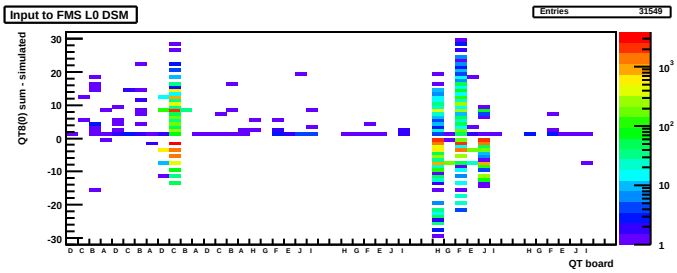

Input to QT1 crate (south-top)
Entries 2.400142e+07
ADC
channel

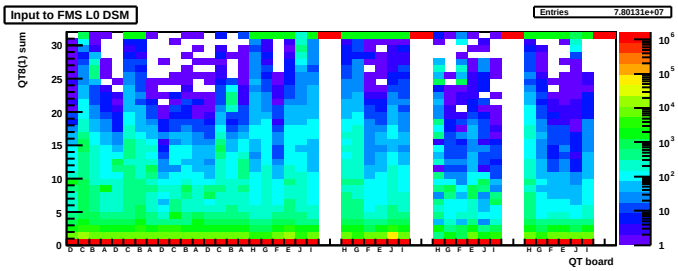

Input to QT3 crate (north-top)
Entries 1.281886e+07
ADC
channel

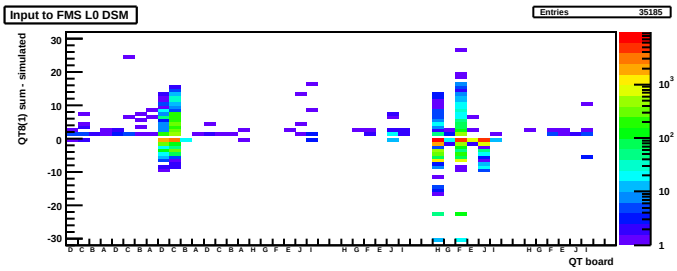

Input to QT2 crate (south-bottom)
Entries 1.495914e+07
ADC
channel

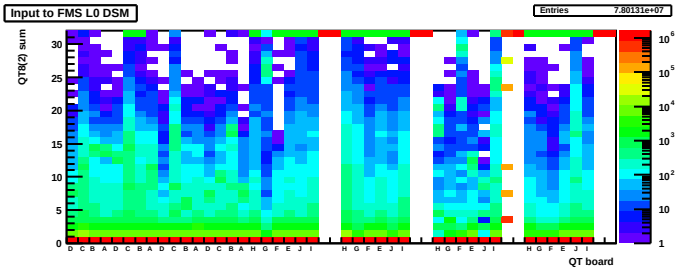

Input to QT4 crate (north-bottom)
Entries 1.843092e+07
ADC
channel

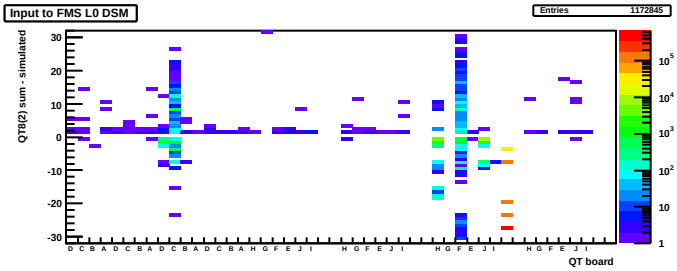

Input to FMS L0 DSM
Entries 7.80131e+07
QT8(0) sum
QT board

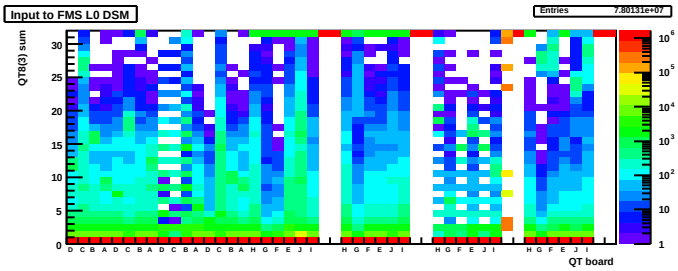

Input to FMS L0 DSM
Entries 31549
QT8(0) sum - simulated
QT board

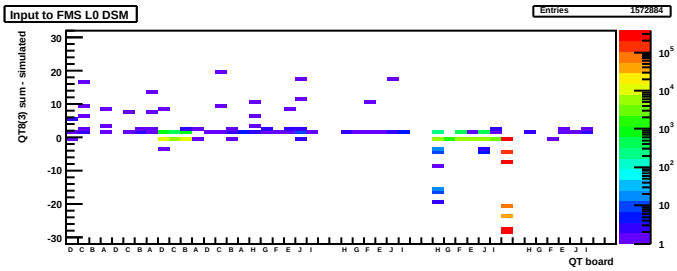

Input to FMS L0 DSM
Entries 7.80131e+07
QT8(1) sum
QT board

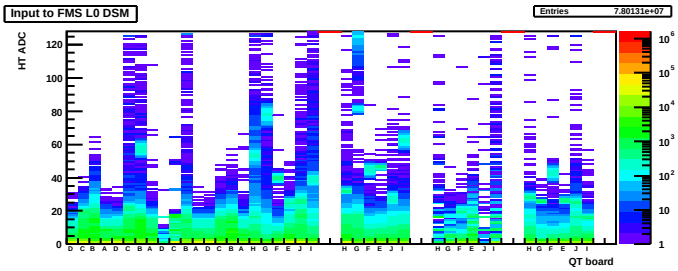

Input to FMS L0 DSM
Entries 35185
QT8(1) sum - simulated
QT board

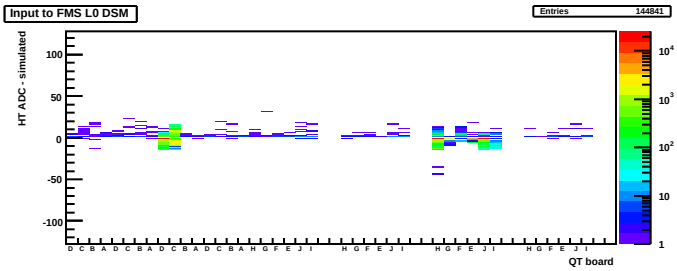

Input to FMS L0 DSM
Entries 7.80131e+07
QT8(2) sum
QT board

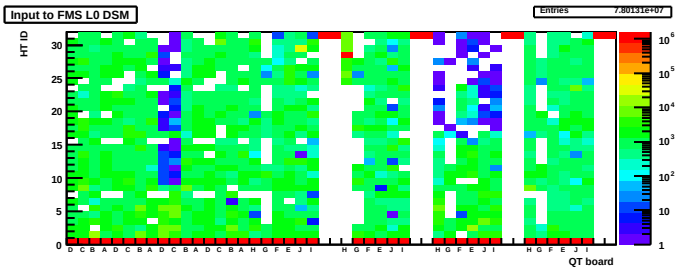

Input to FMS L0 DSM
Entries 1172845
QT8(2) sum - simulated
QT board

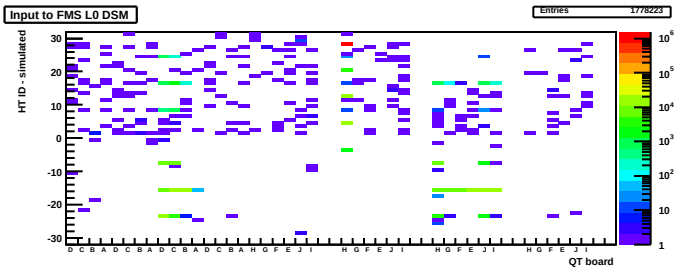

Input to FMS L0 DSM
Entries 7.80131e+07
QT8(3) sum
QT board

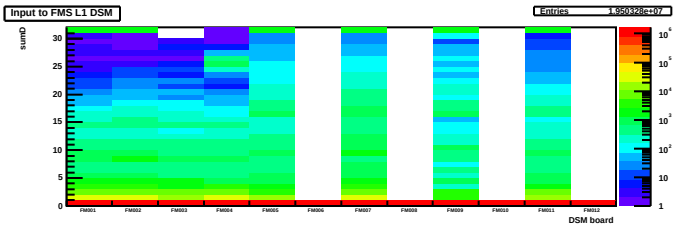

Input to FMS L0 DSM
Entries 1572884
QT8(3) sum - simulated
QT board

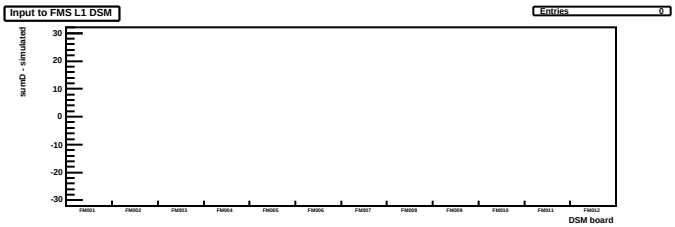

Input to FMS L0 DSM
Entries 7.80131e+07
HT ADC
QT board

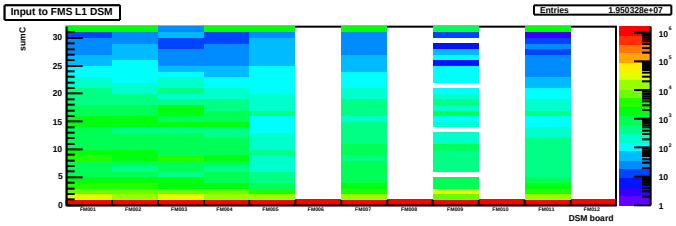

Input to FMS L0 DSM
Entries 144841
HT ADC - simulated
QT board

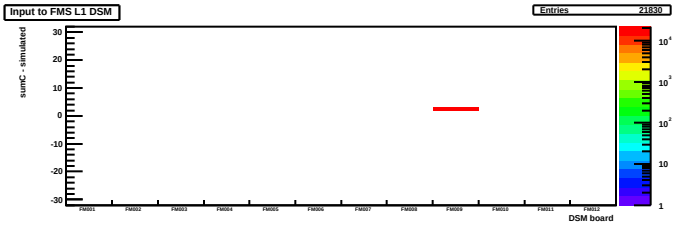

Input to FMS L0 DSM
Entries 7.80131e+07
HT ID
QT board

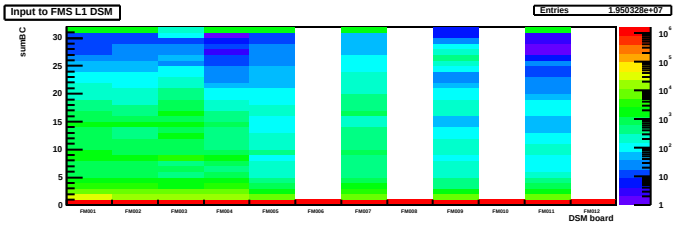

Input to FMS L0 DSM
Entries 1778223
HT ID - simulated
QT board

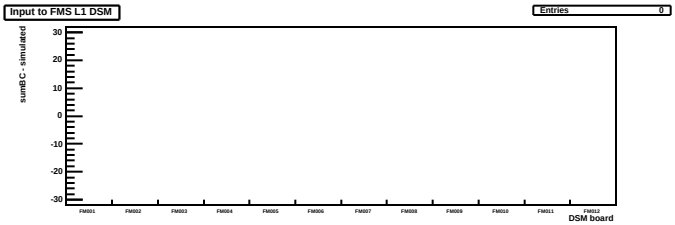

Input to FMS L1 DSM
Entries 1.950328e+07
sumD
DSM board

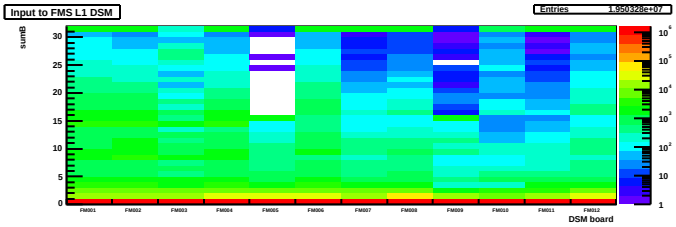

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

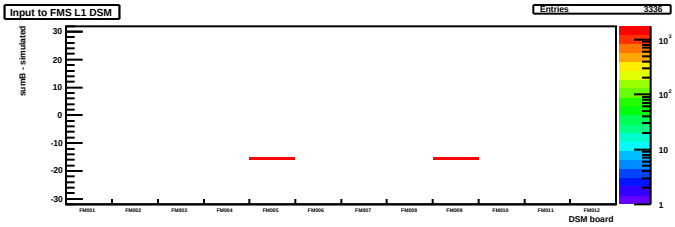

Input to FMS L1 DSM
Entries 1.950328e+07
sumC
DSM board

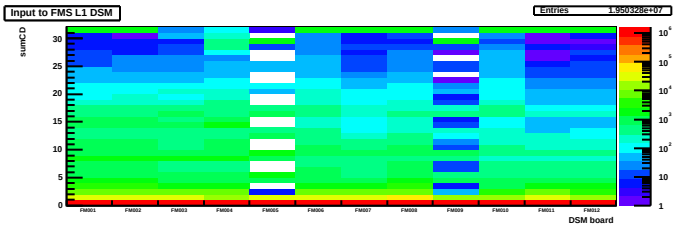

Input to FMS L1 DSM
Entries 21830
sumC - simulated
DSM board

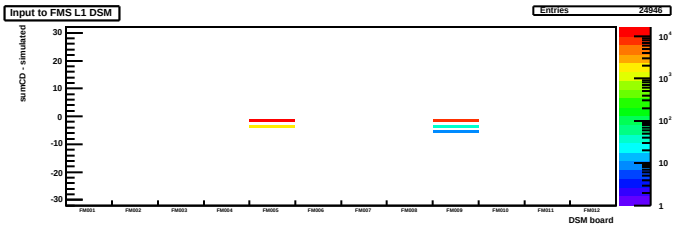

Input to FMS L1 DSM
Entries 1.950328e+07
sumBC
DSM board

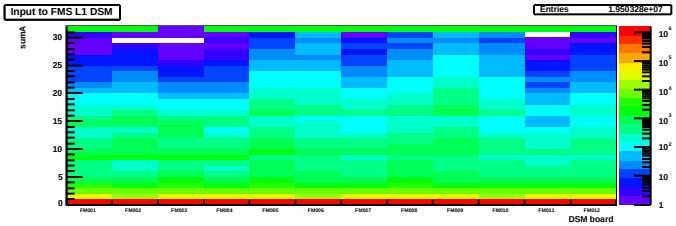

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

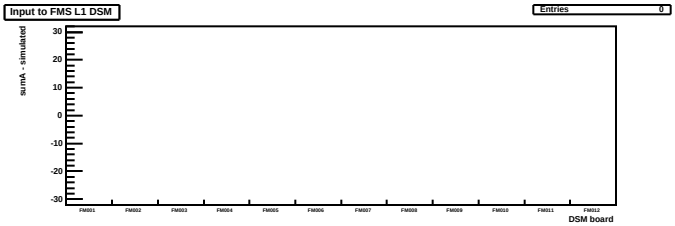

Input to FMS L1 DSM
Entries 1.950328e+07
sumB
DSM board

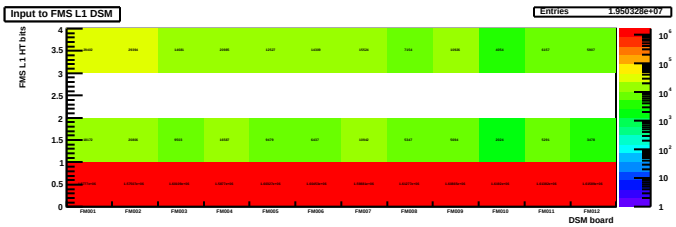

Input to FMS L1 DSM
Entries 3336
sumB - simulated
DSM board

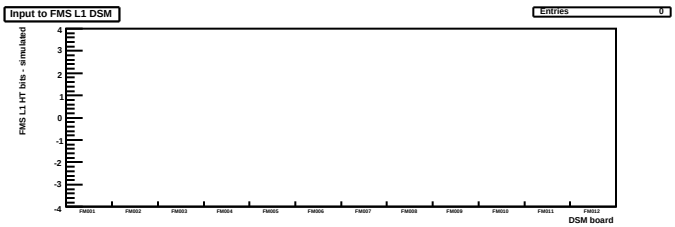

Input to FMS L1 DSM
Entries 1.950328e+07
sumCD
DSM board

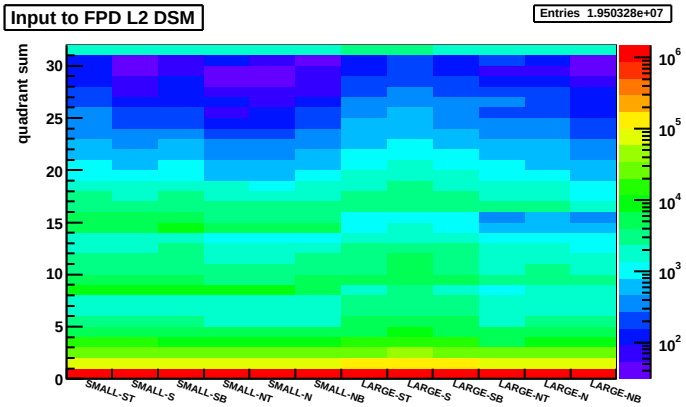

Input to FMS L1 DSM
Entries 24946
sumCD - simulated
DSM board

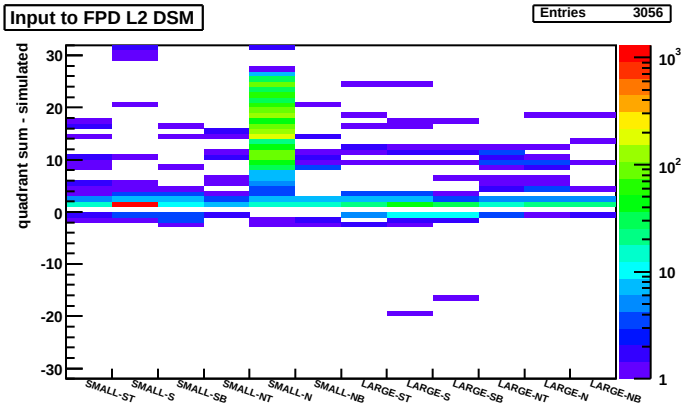

Input to FMS L1 DSM
Entries 1.950328e+07
sumA
DSM board

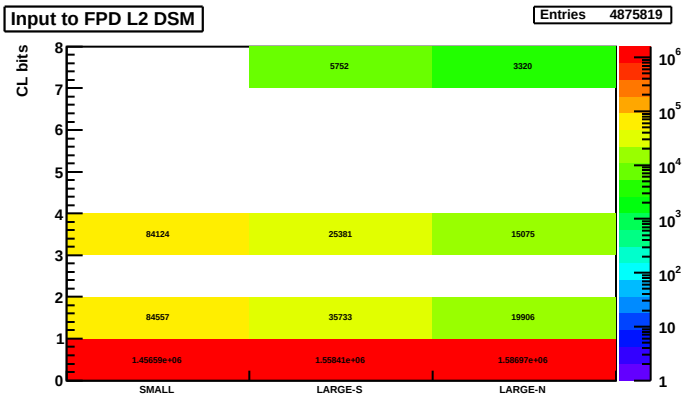

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

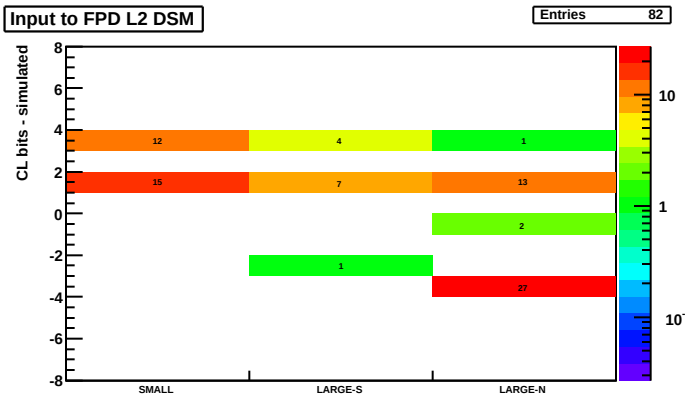

Input to FMS L1 DSM
Entries 1.950328e+07
FMS L1 HT bits
DSM board

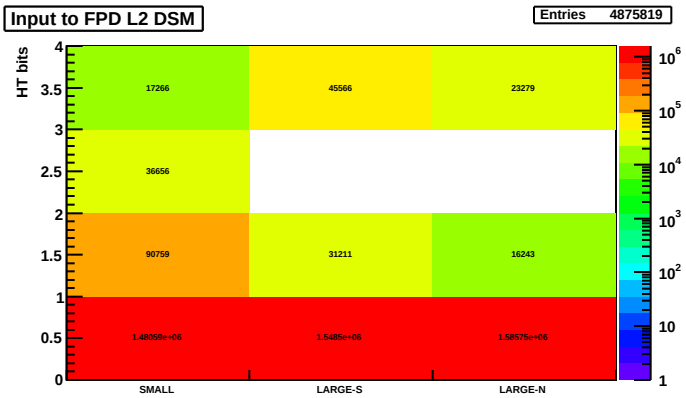

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

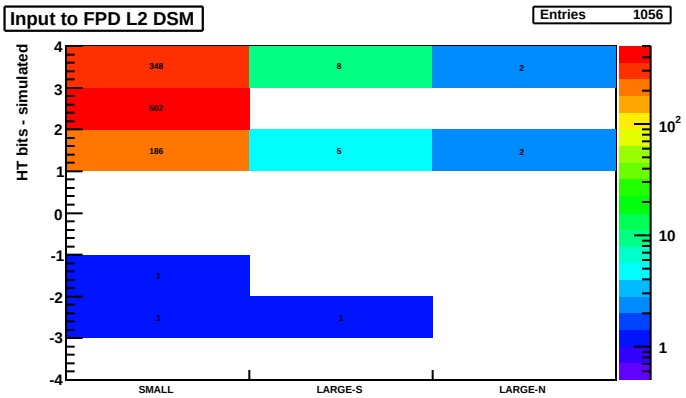

Input to FPD L2 DSM
Entries 1.950328e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

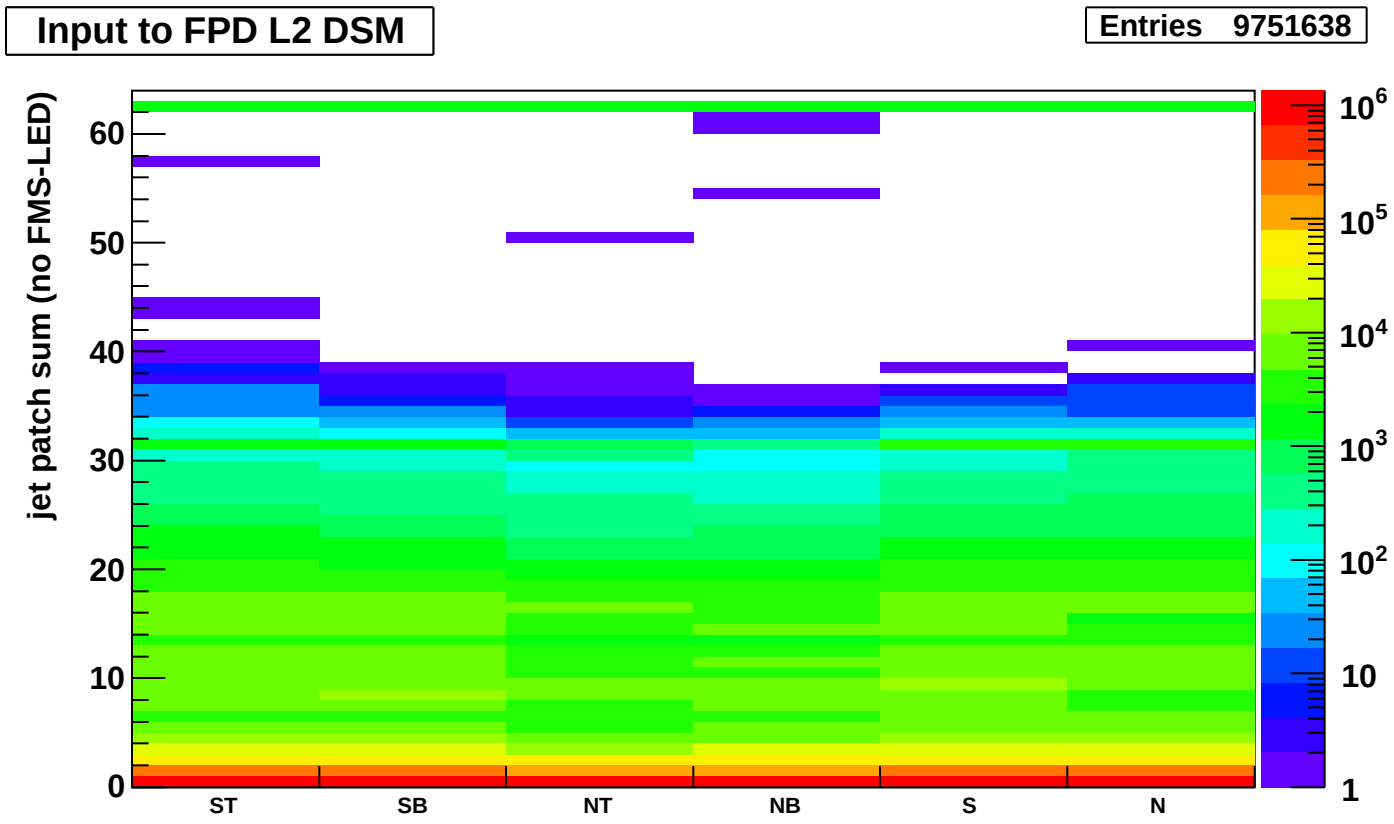

Input to FPD L2 DSM
Entries 3056
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

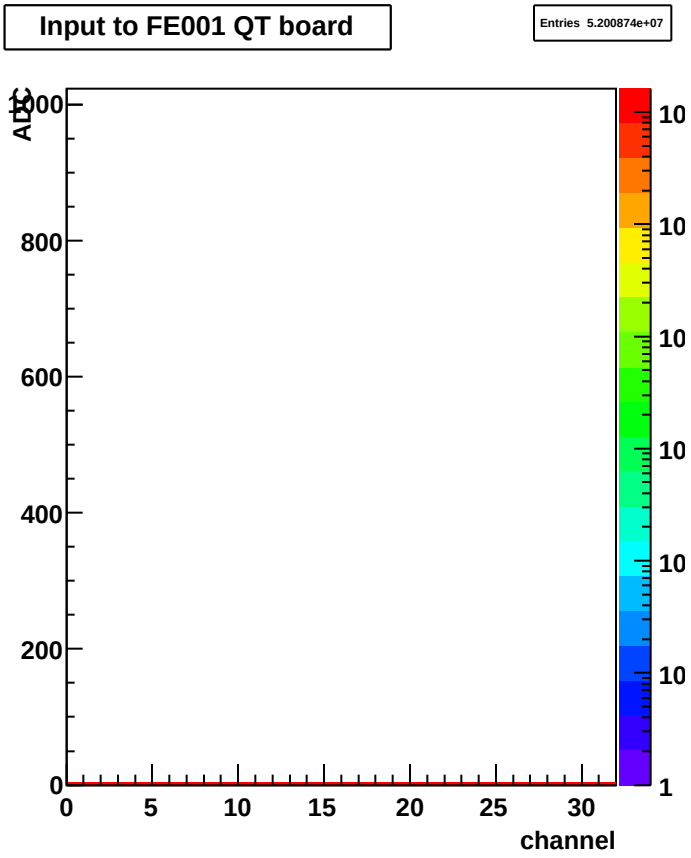

Input to FPD L2 DSM
Entries 4875819
CL bits
5752
3320
84124
25381
15075
84557
35733
19906
1.45659e+06
1.55841e+06
1.58697e+06
SMALL
LARGE-S
LARGE-N

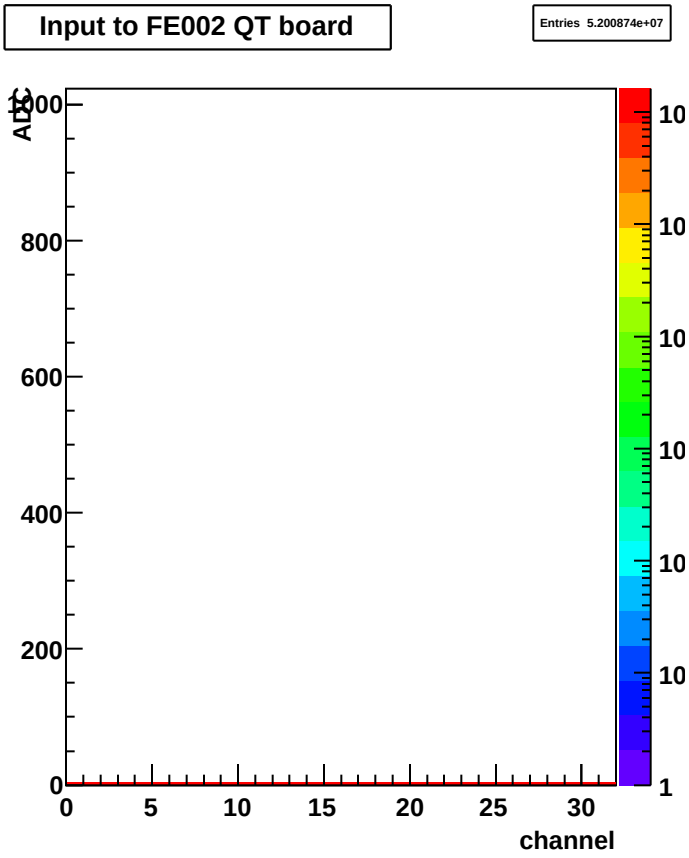

Input to FPD L2 DSM
Entries 82
CL bits - simulated
12
4
1
15
7
13
2
1
27
SMALL
LARGE-S
LARGE-N

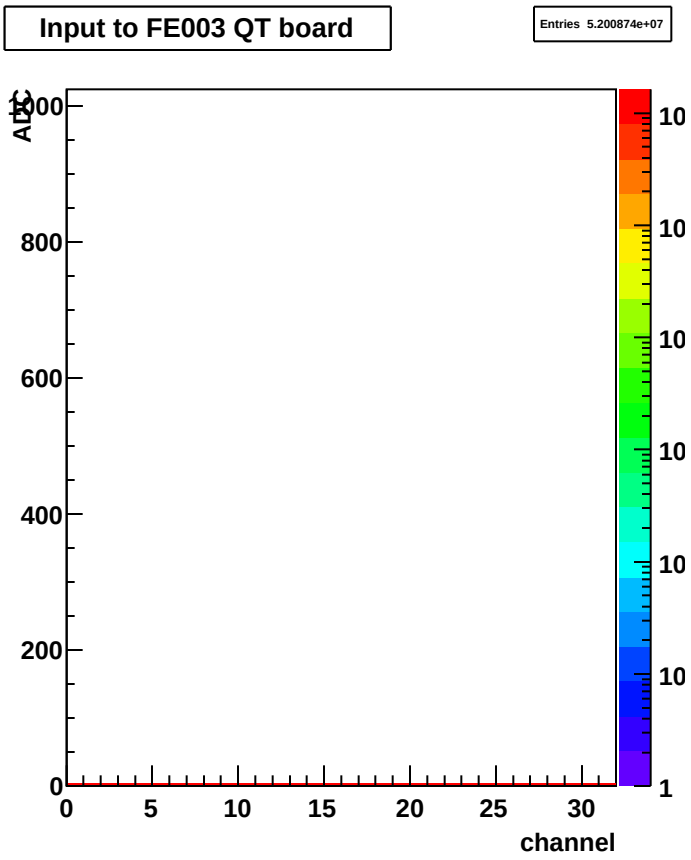

Input to FPD L2 DSM
Entries 4875819
HT bits
17266
45566
23279
36656
90759
31211
16243
1.48059e+06
1.5485e+06
1.58575e+06
SMALL
LARGE-S
LARGE-N

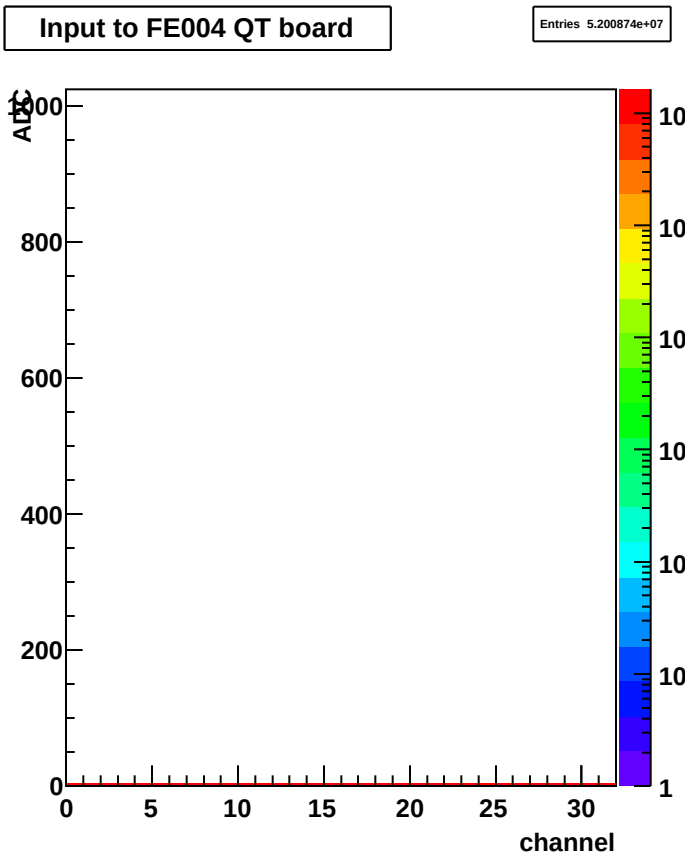

Input to FPD L2 DSM
Entries 1056
HT bits - simulated
348
8
2
502
186
5
2
1
1
1
SMALL
LARGE-S
LARGE-N

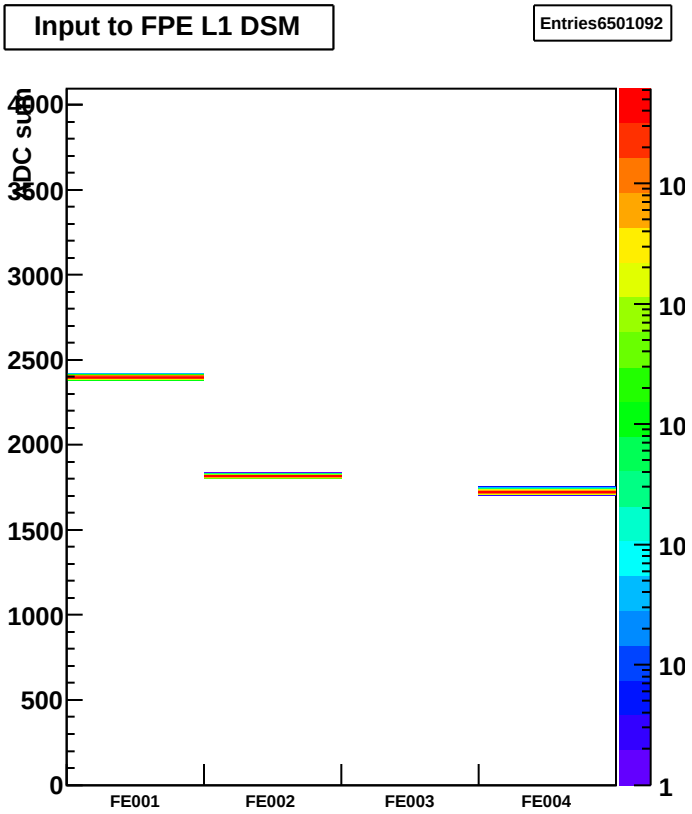

Input to FPD L2 DSM
Entries 9751638
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 5.200874e+07
ADC
channel

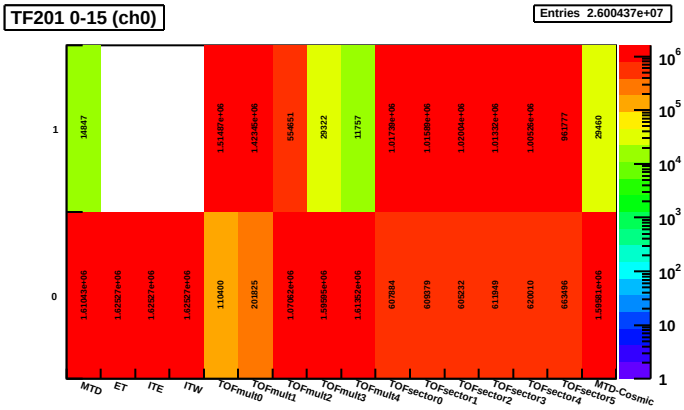

Input to FE002 QT board
Entries 5.200874e+07
ADC
channel


Input to FE003 QT board
Entries 5.200874e+07
ADC
channel

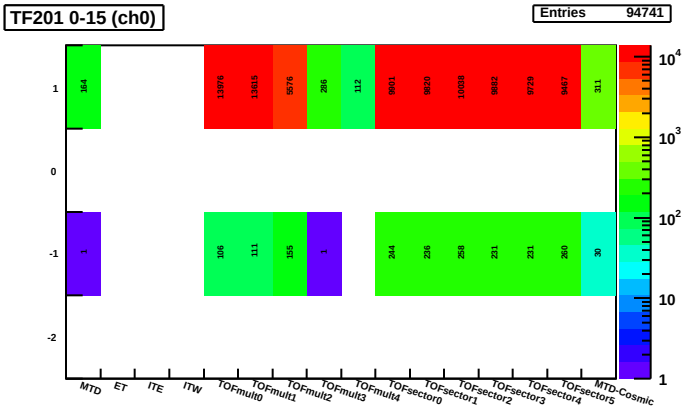

Input to FE004 QT board
Entries 5.200874e+07
ADC
channel


Input to FPE L1 DSM
Entries6501092
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

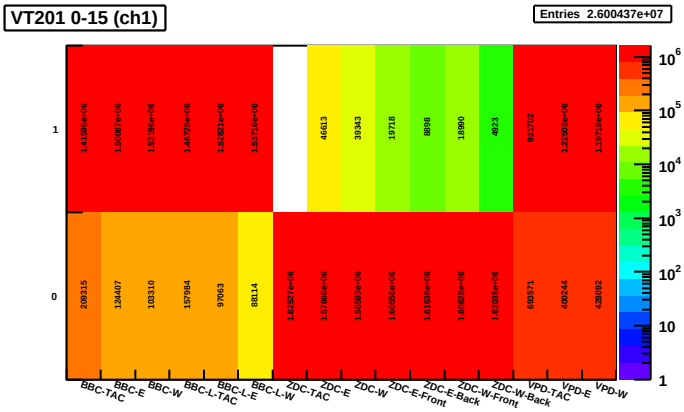
TF201 0-15 (ch0)



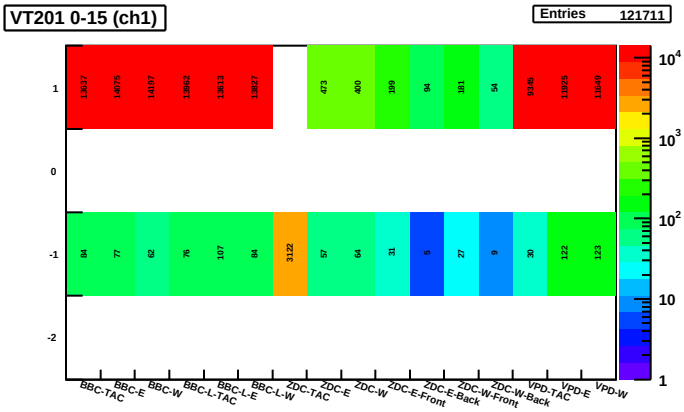
TF201 0-15 (ch0)



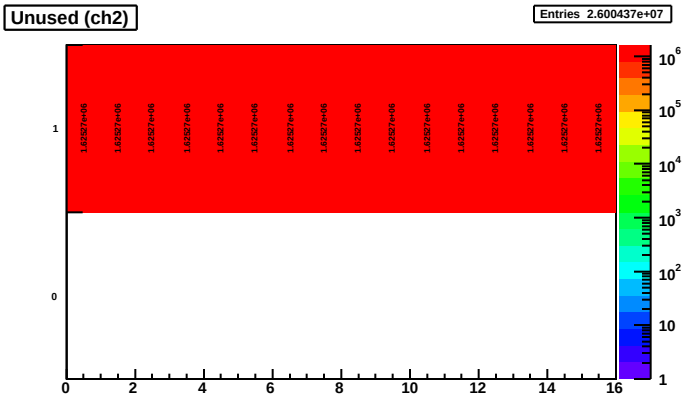
VT201 0-15 (ch1)

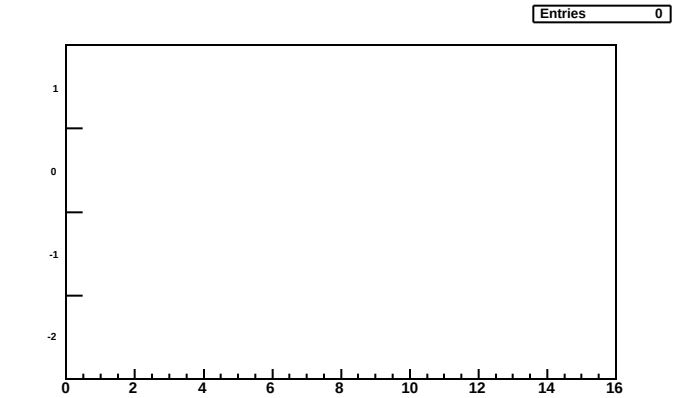


VT201 0-15 (ch1)

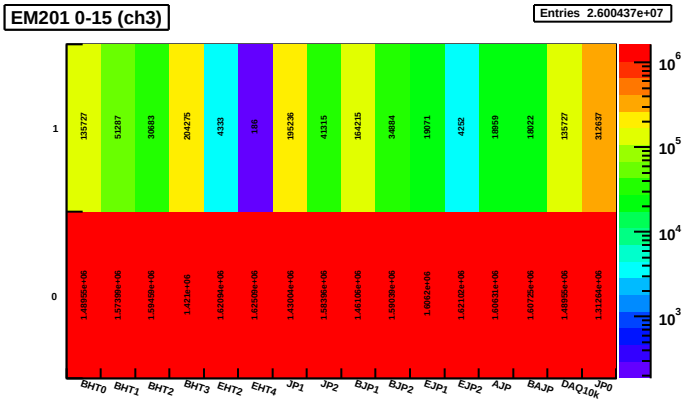


Unused (ch2)





EM201 0-15 (ch3)



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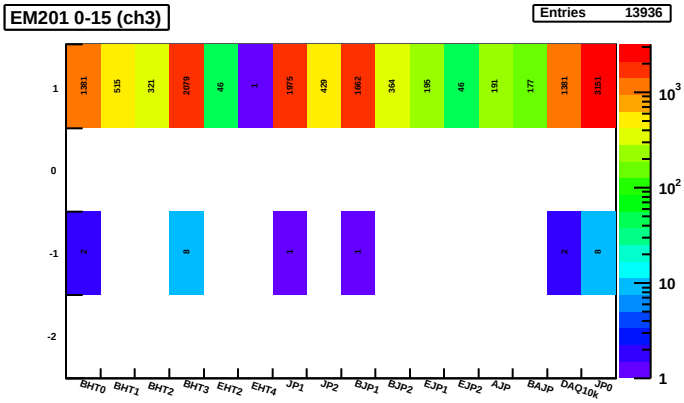
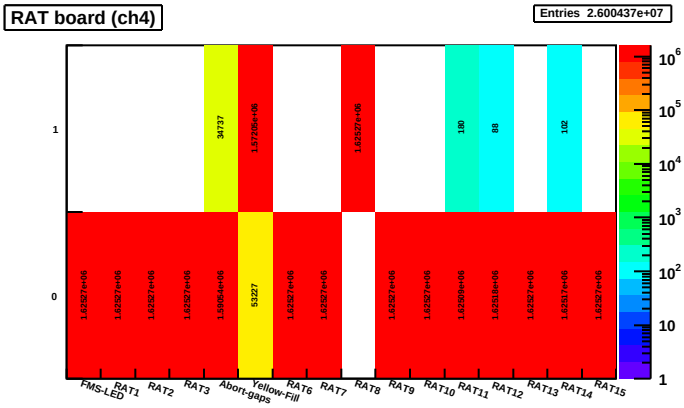
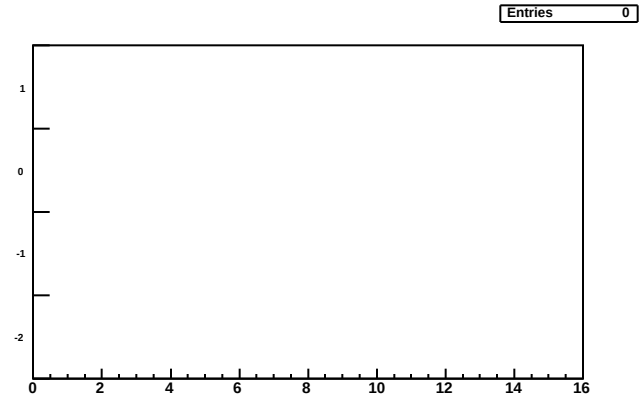
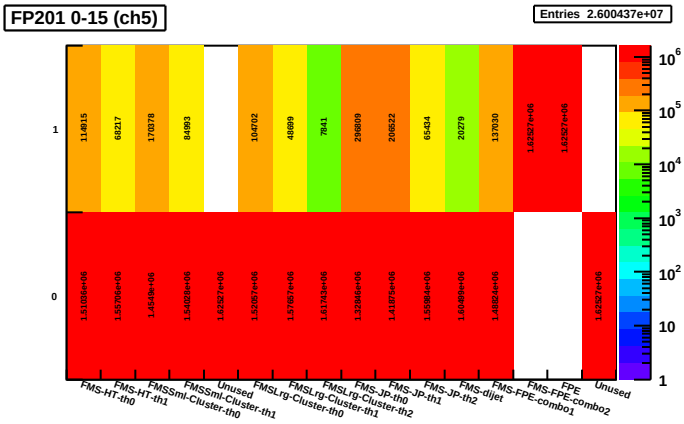


RAT board (ch4)





FP201 0-15 (ch5)



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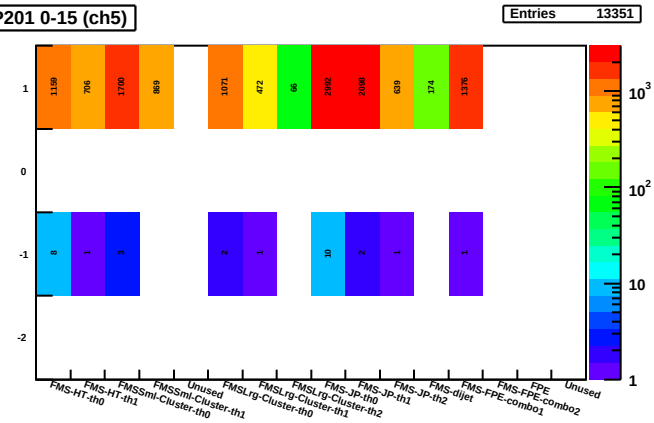
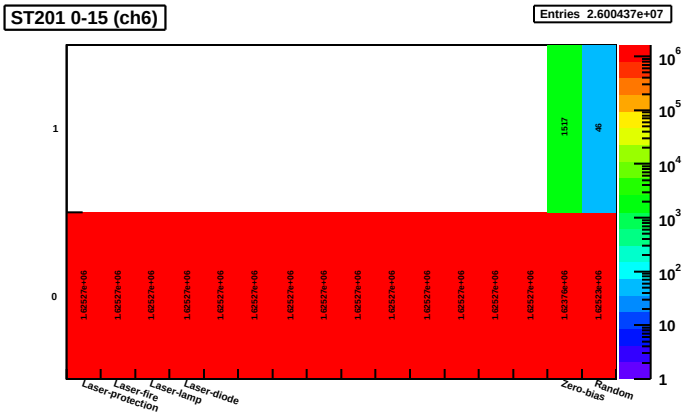
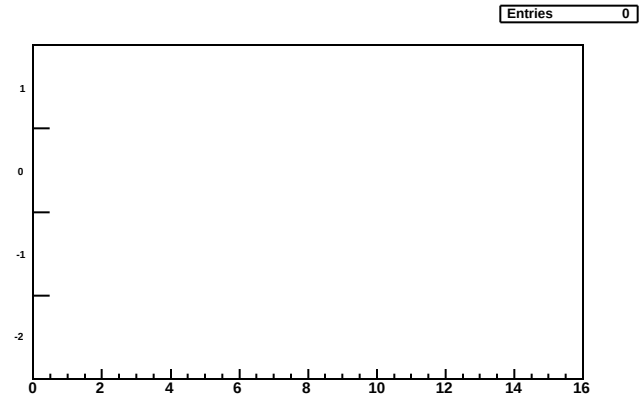
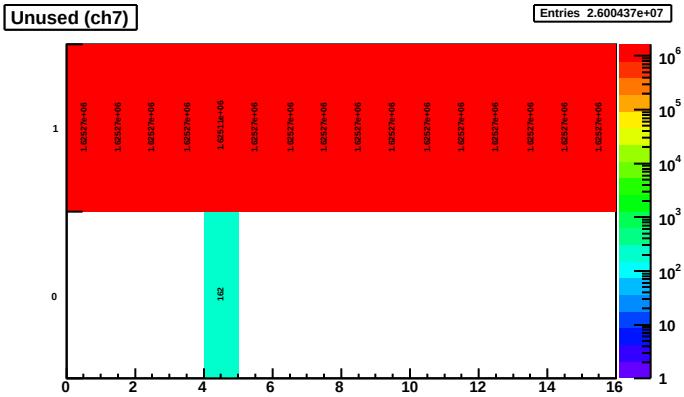
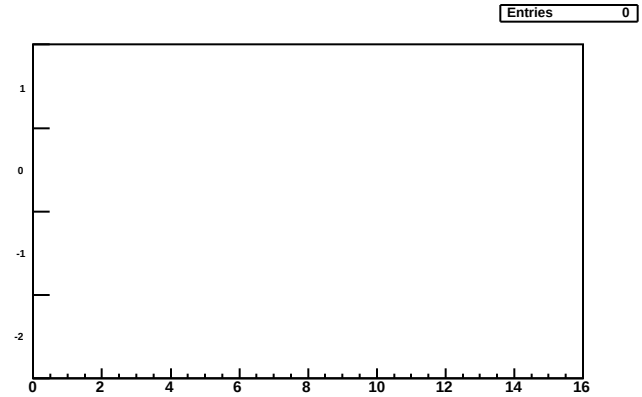


ST201 0-15 (ch6)





Unused (ch7)