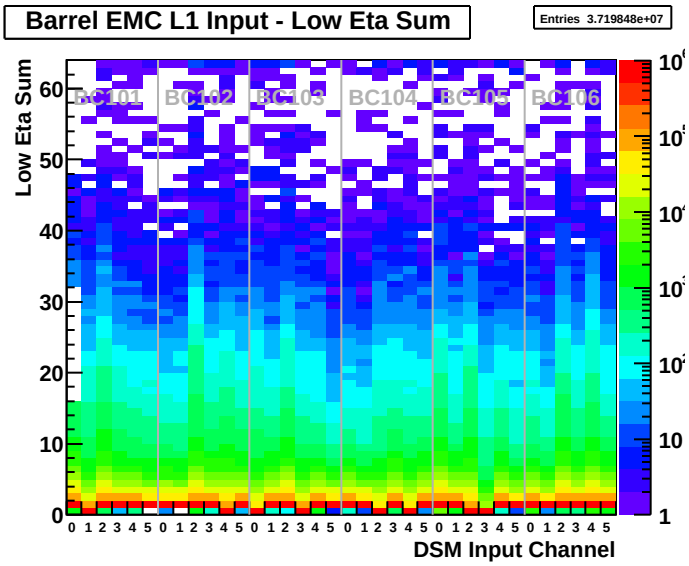
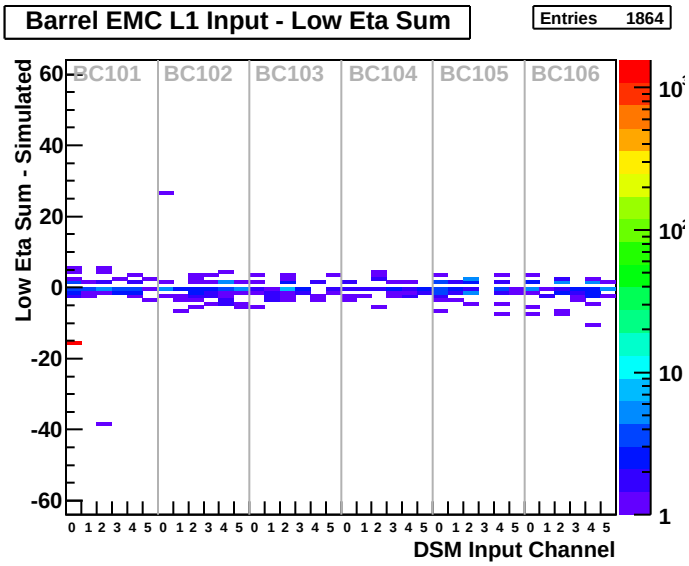
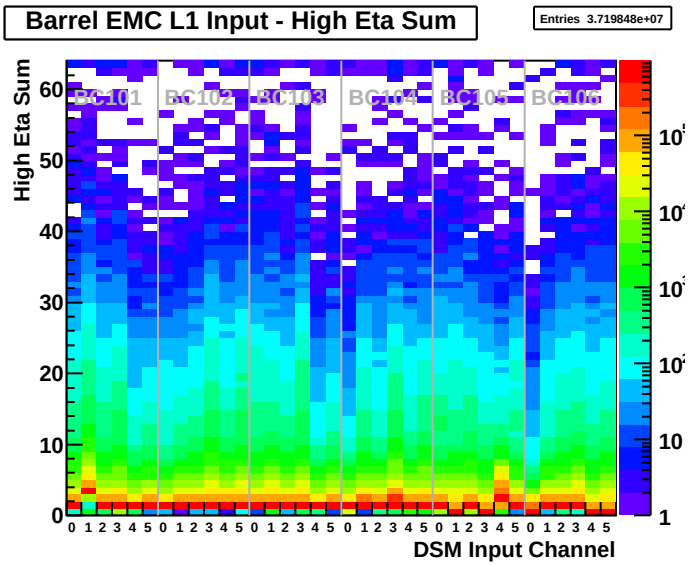
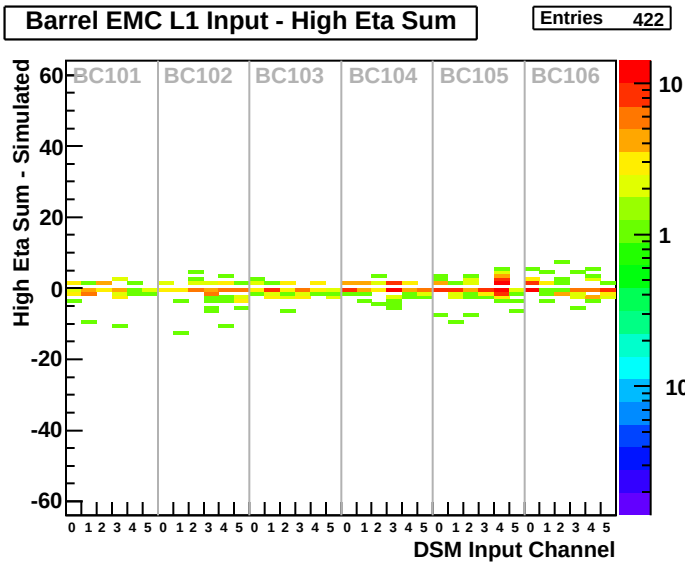
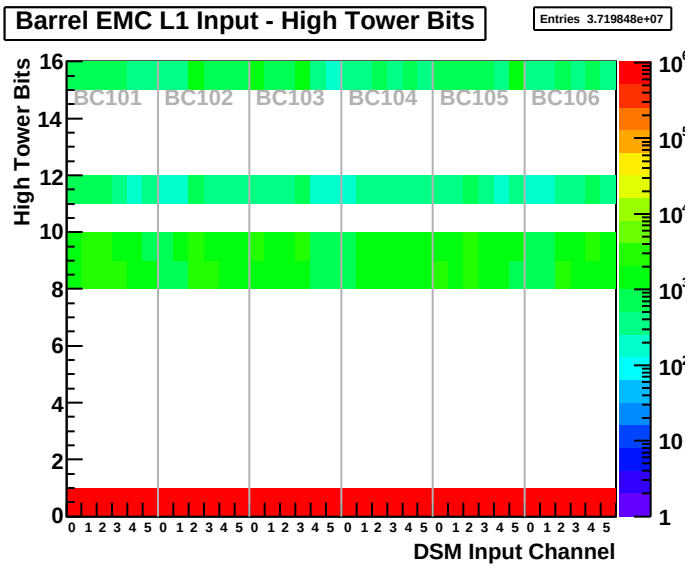
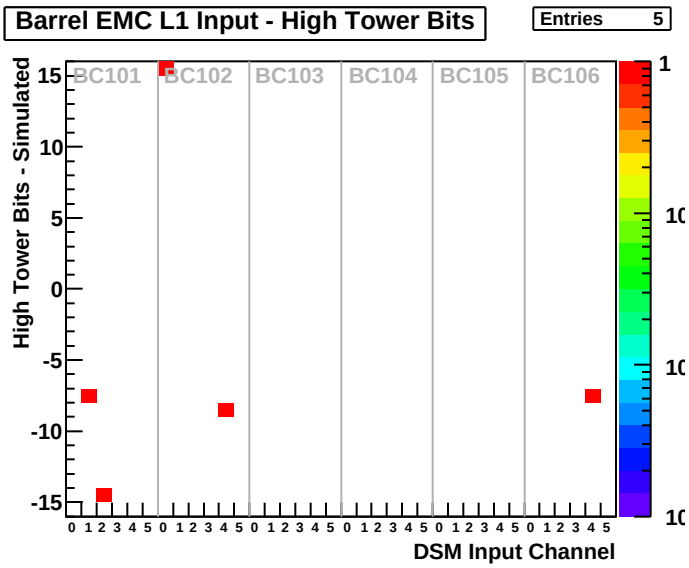

Barrel EMC L1 Input - Low Eta Sum
Entries 3.719848e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

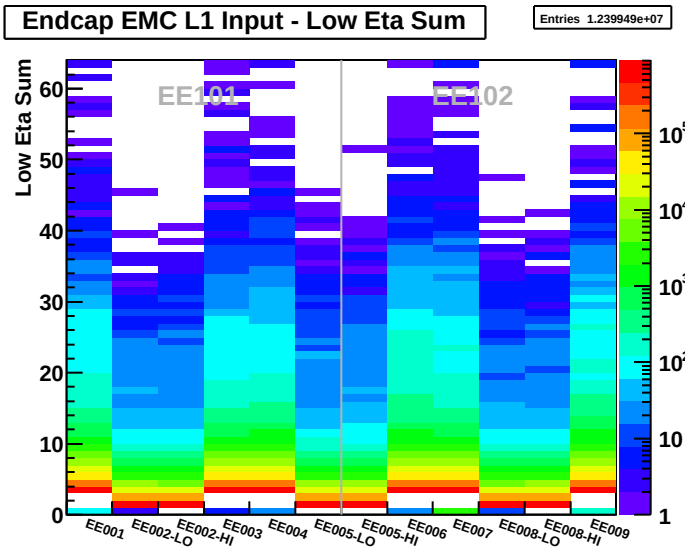

Barrel EMC L1 Input - Low Eta Sum
Entries 1864
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

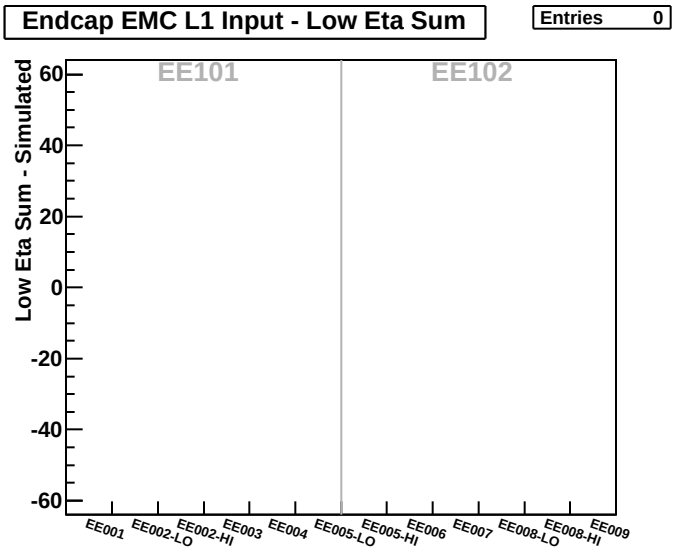

Barrel EMC L1 Input - High Eta Sum
Entries 3.719848e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

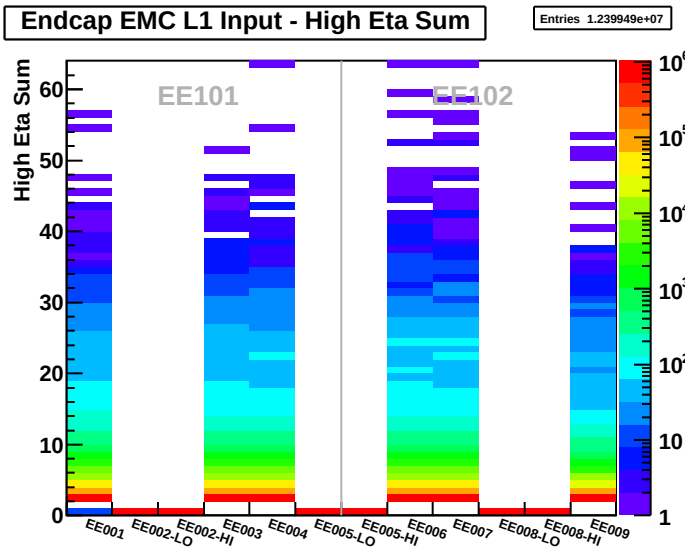

Barrel EMC L1 Input - High Eta Sum
Entries 422
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

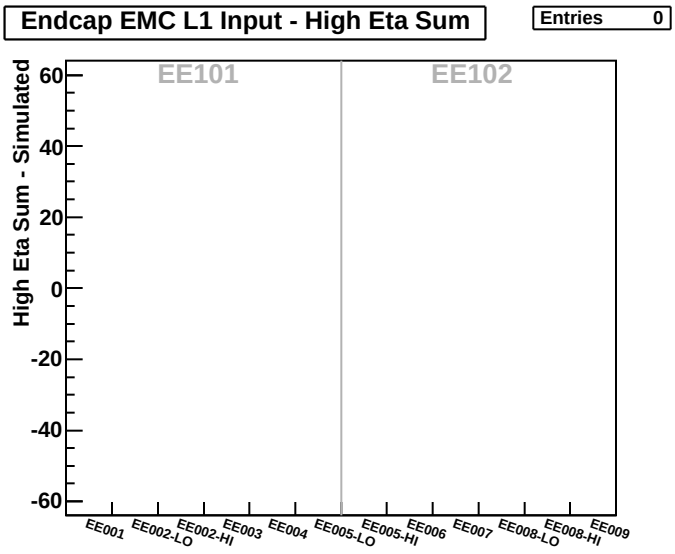

Barrel EMC L1 Input - High Tower Bits
Entries 3.719848e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

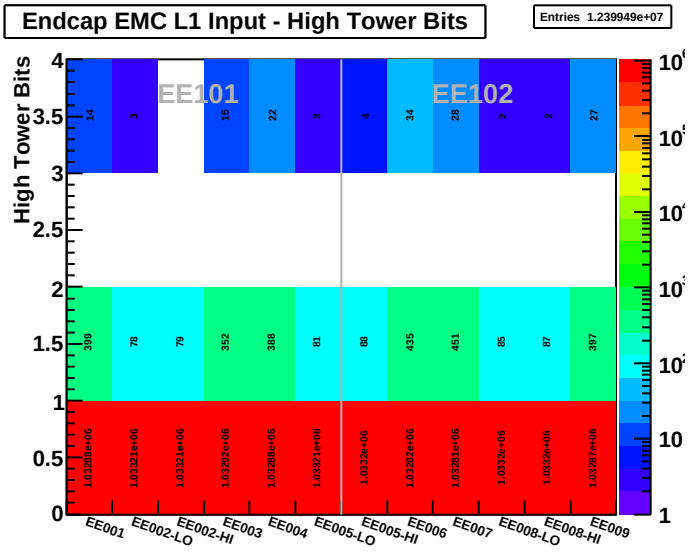

Barrel EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

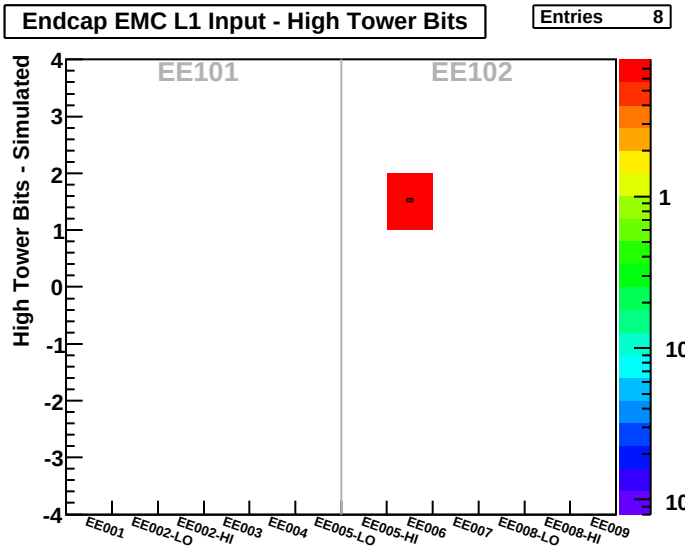

Endcap EMC L1 Input - Low Eta Sum
Entries 1.239949e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

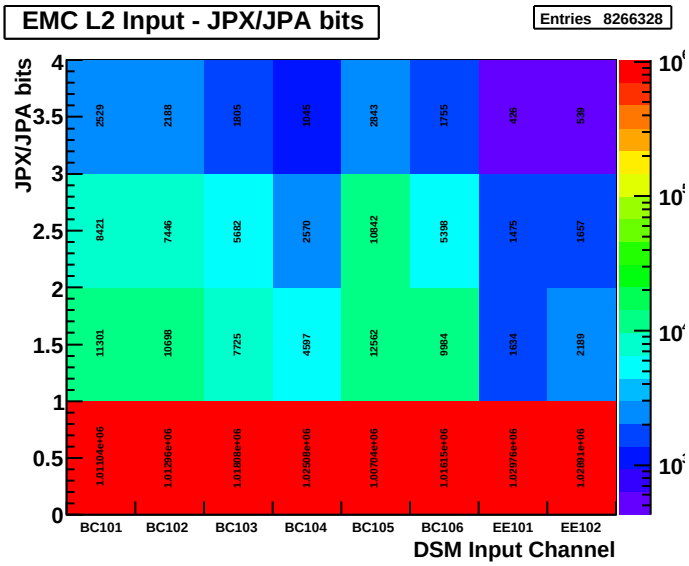

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

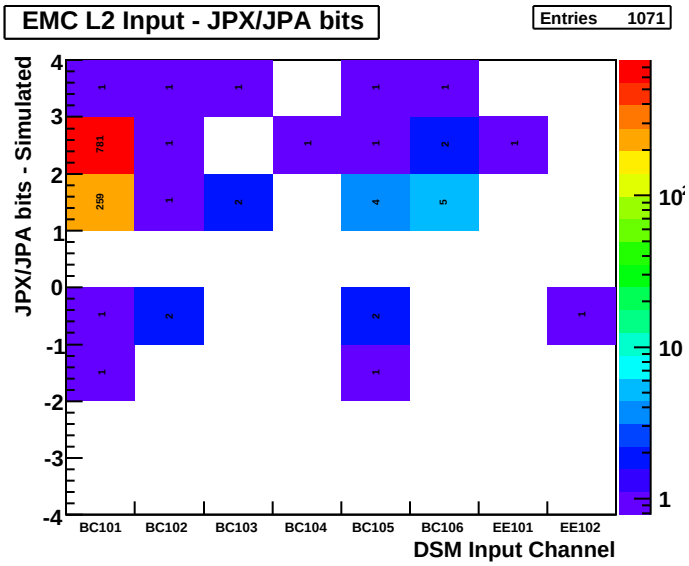

Endcap EMC L1 Input - High Eta Sum
Entries 1.239949e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

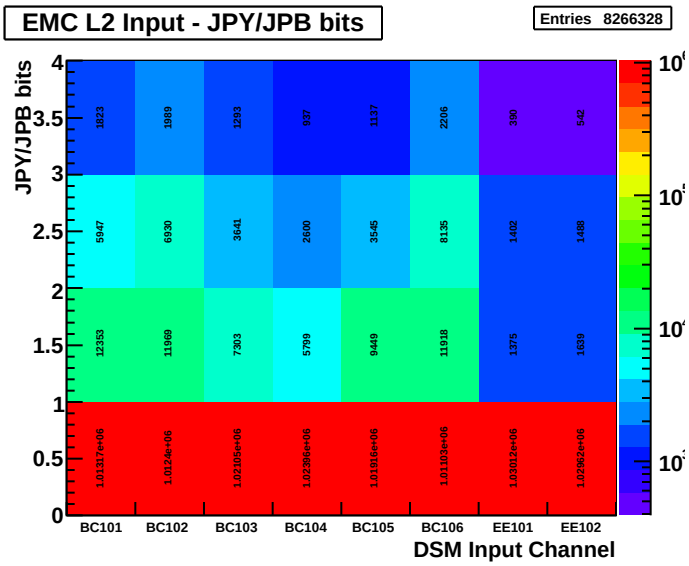

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

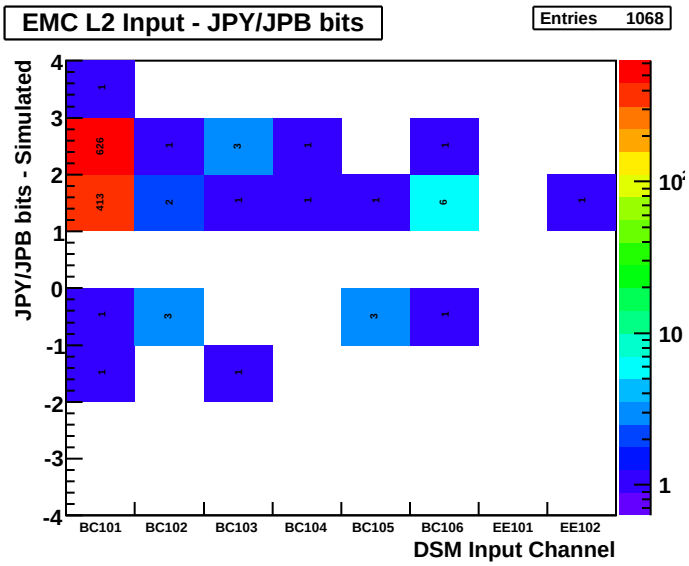

Endcap EMC L1 Input - High Tower Bits
Entries 1.239949e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

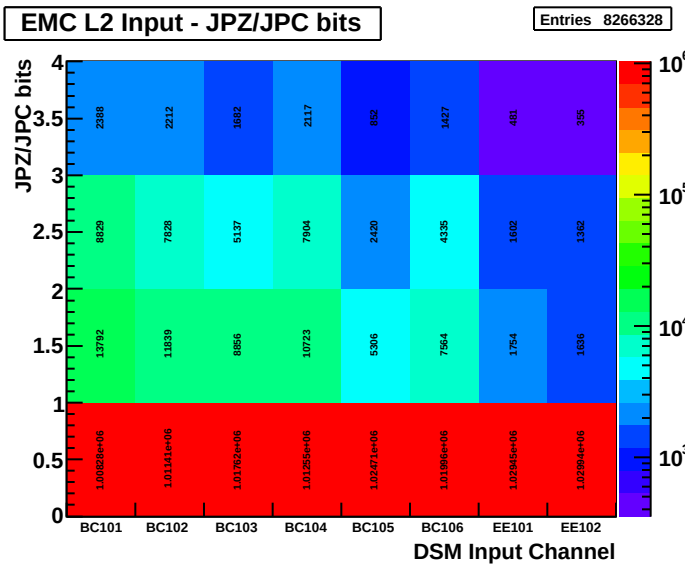

Endcap EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

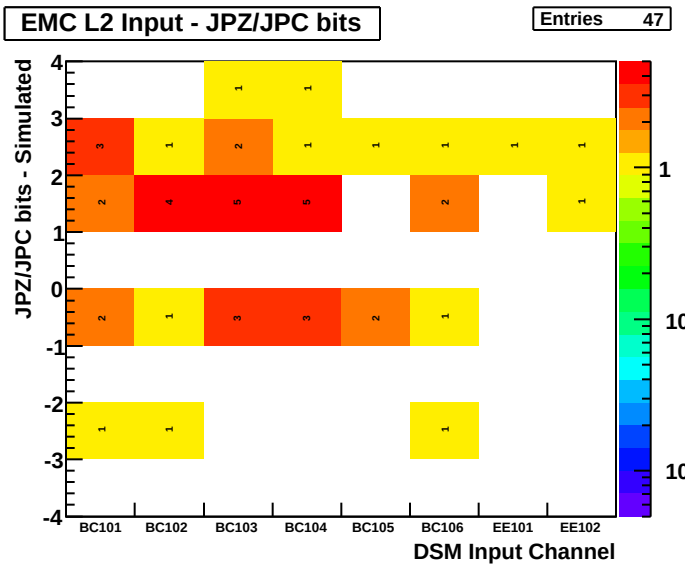

EMC L2 Input - JPX/JPA bits
Entries 8266328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2529 2188 1805 1045 2843 1755 426 539
8421 7446 5682 2570 10842 5398 1475 1657
11301 10698 7725 4597 12562 9984 1634 2189
1.01104e+06 1.01236e+06 1.01808e+06 1.02508e+06 1.00704e+06 1.01615e+06 1.02976e+06 1.02891e+06

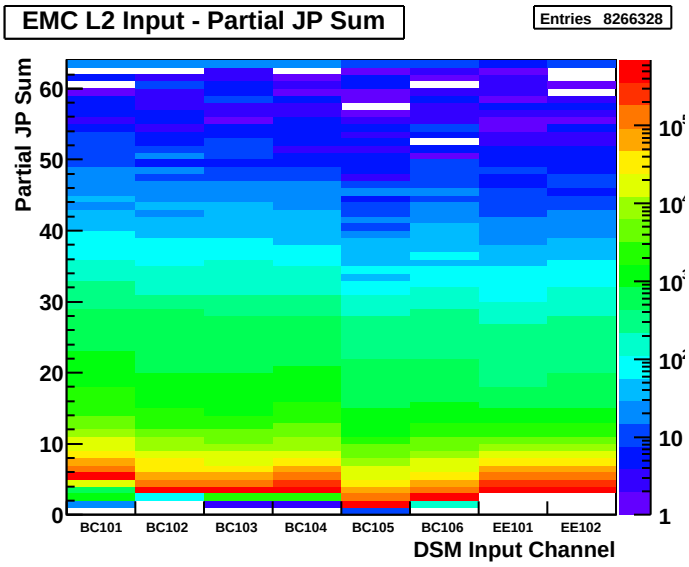

EMC L2 Input - JPX/JPA bits
Entries 1071
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
781
259

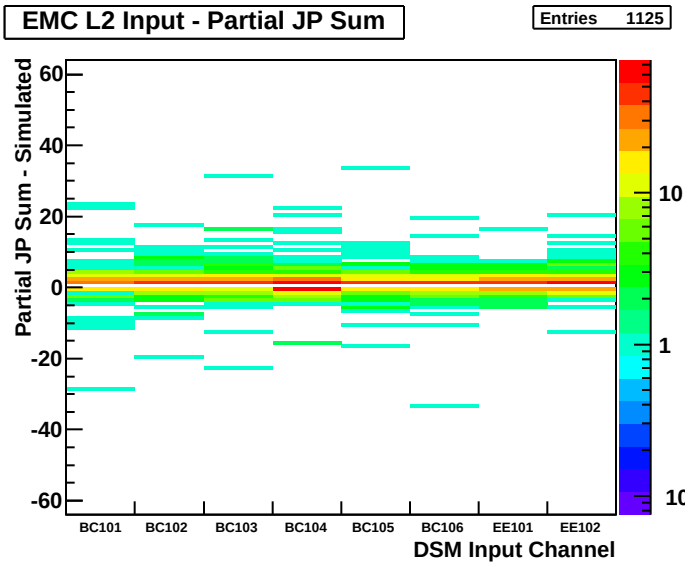

EMC L2 Input - JPY/JPB bits
Entries 8266328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1823 1989 1293 937 1137 2206 390 542
5947 6930 3641 2600 3545 8135 1402 1488
12353 11969 7303 5799 9449 11918 1375 1639
1.01317e+06 1.0124e+06 1.02105e+06 1.02396e+06 1.01916e+06 1.01103e+06 1.03012e+06 1.02962e+06

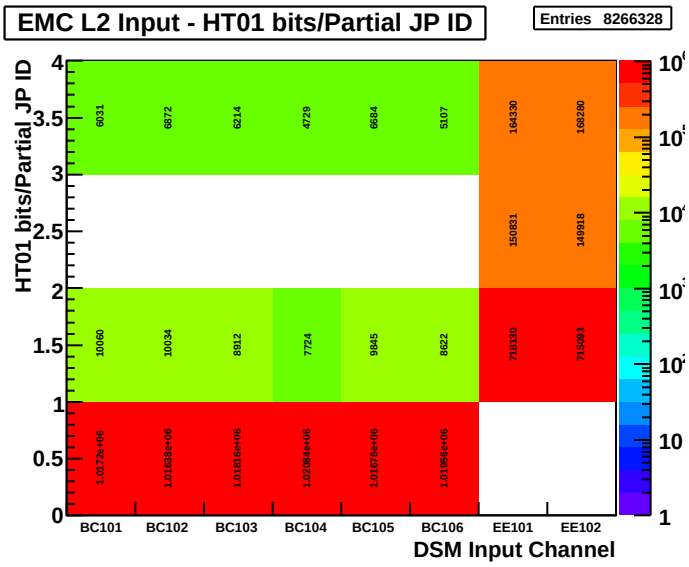

EMC L2 Input - JPY/JPB bits
Entries 1068
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
626
413

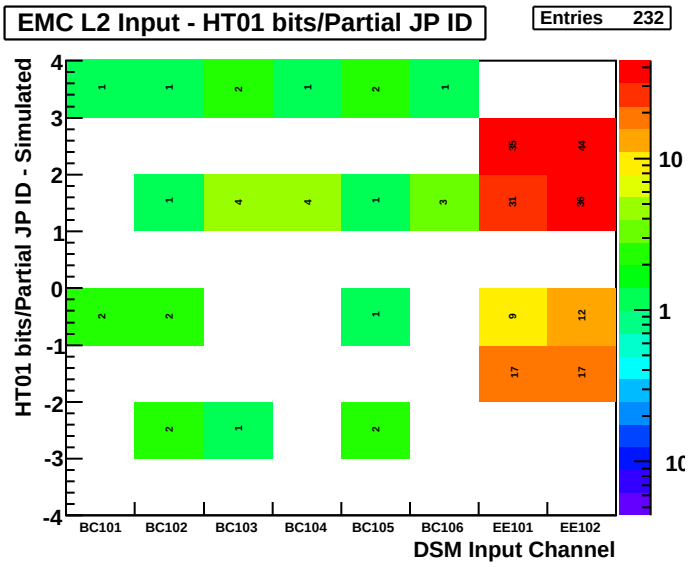

EMC L2 Input - JPZ/JPC bits
Entries 8266328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2388 2212 1682 2117 852 1427 431 355
8829 7828 5137 7904 2420 4335 1602 1382
13792 11839 8856 10723 5306 7564 1754 1636
1.00828e+06 1.01141e+06 1.01762e+06 1.01255e+06 1.02471e+06 1.01996e+06 1.02945e+06 1.02994e+06

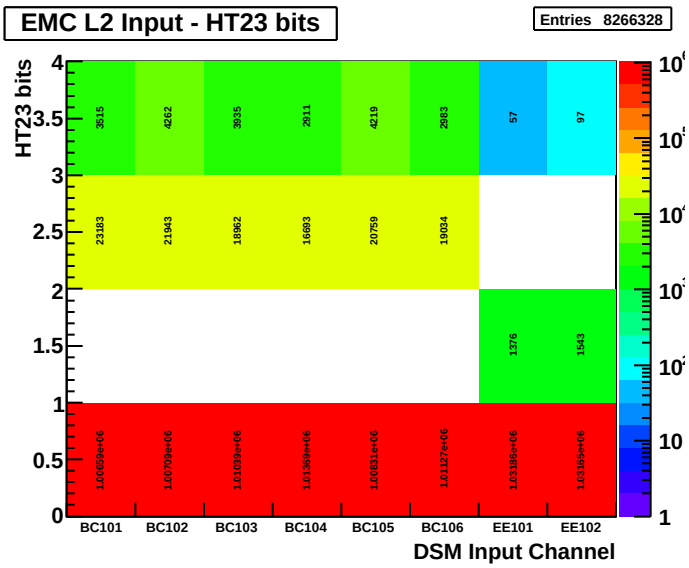

EMC L2 Input - JPZ/JPC bits
Entries 47
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

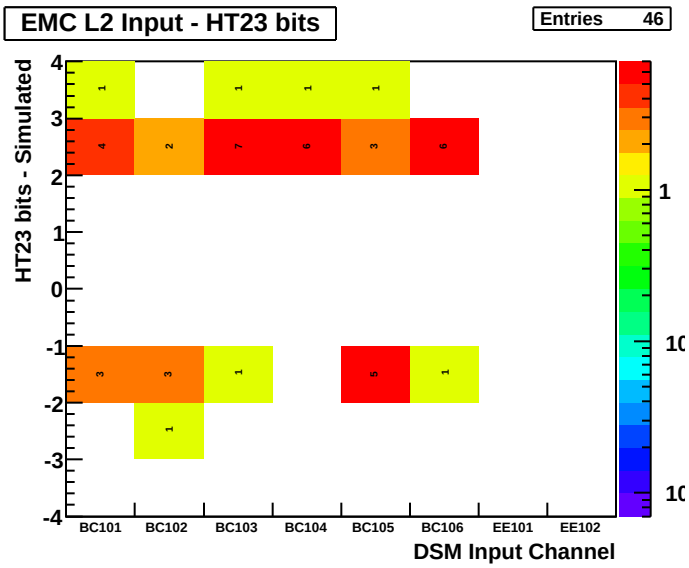

EMC L2 Input - Partial JP Sum
Entries 8266328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

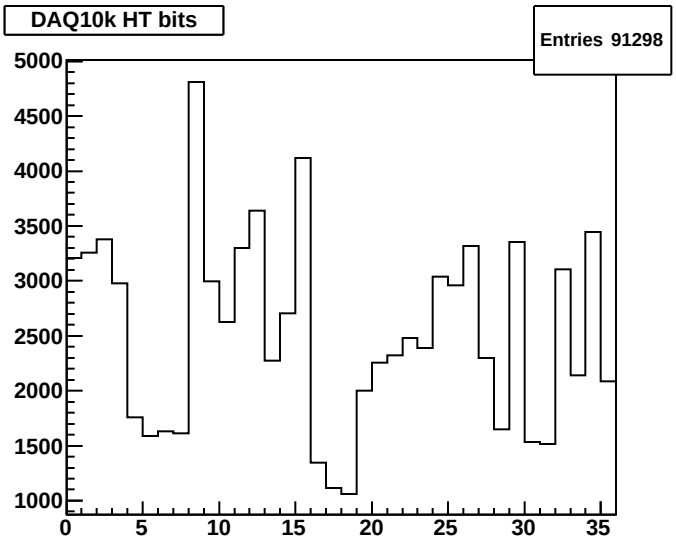

EMC L2 Input - Partial JP Sum
Entries 1125
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

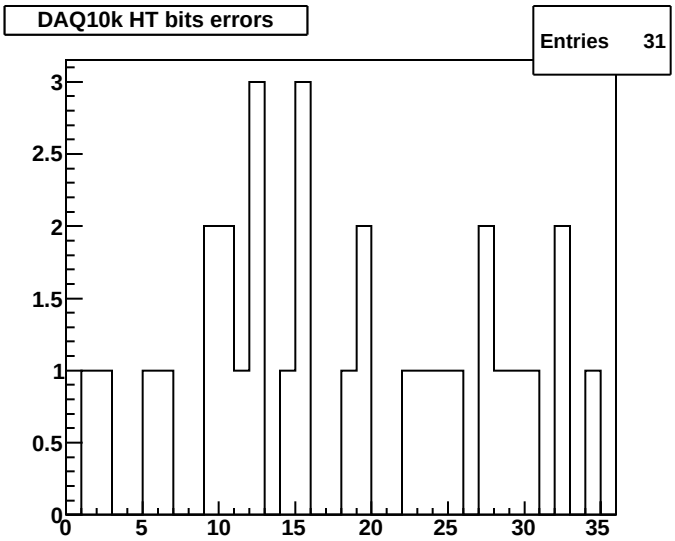

EMC L2 Input - HT01 bits/Partial JP ID
Entries 8266328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

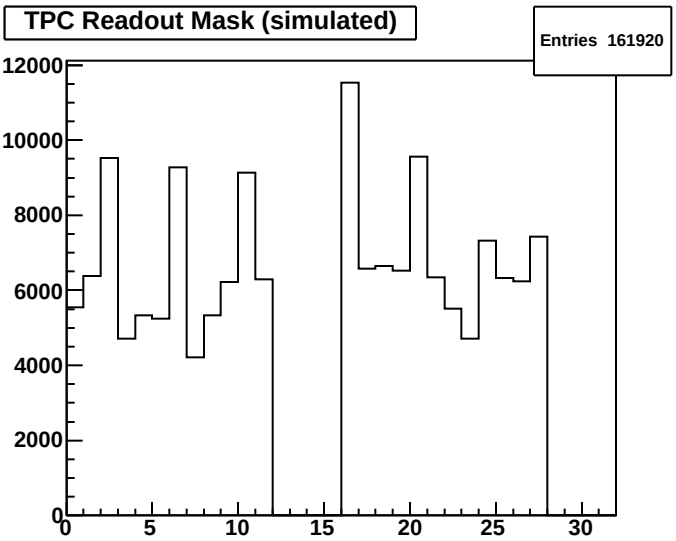

EMC L2 Input - HT01 bits/Partial JP ID
Entries 232
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

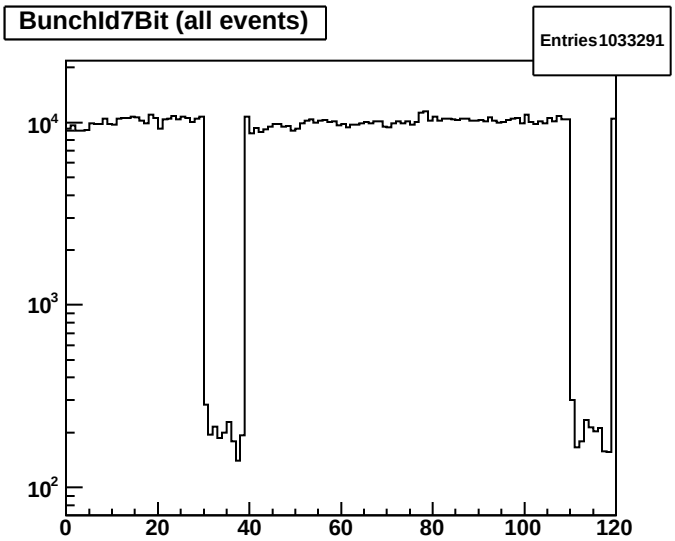

EMC L2 Input - HT23 bits
Entries 8266328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

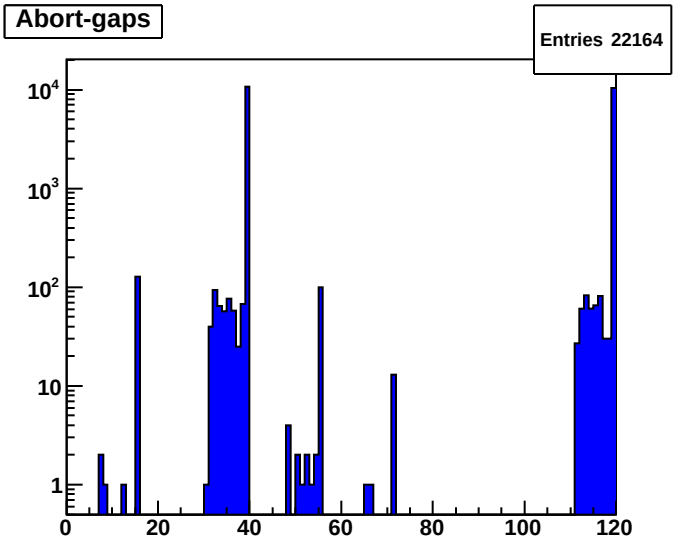

EMC L2 Input - HT23 bits
Entries 46
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

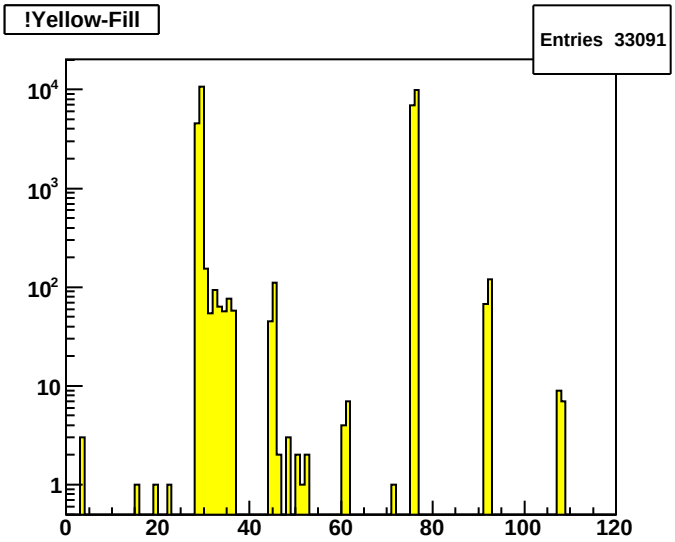

DAQ10k HT bits
Entries 91298

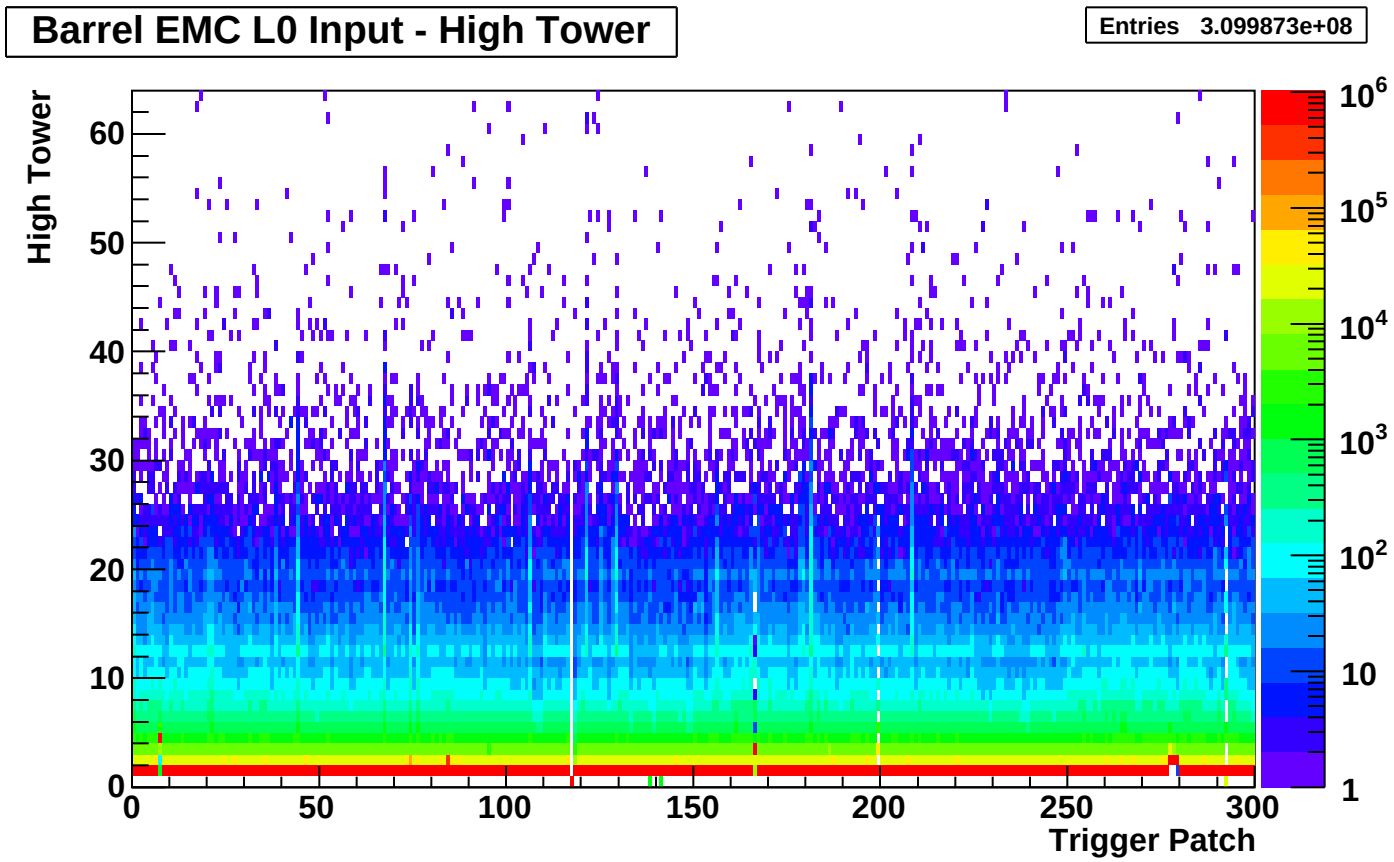

DAQ10k HT bits errors
Entries 31

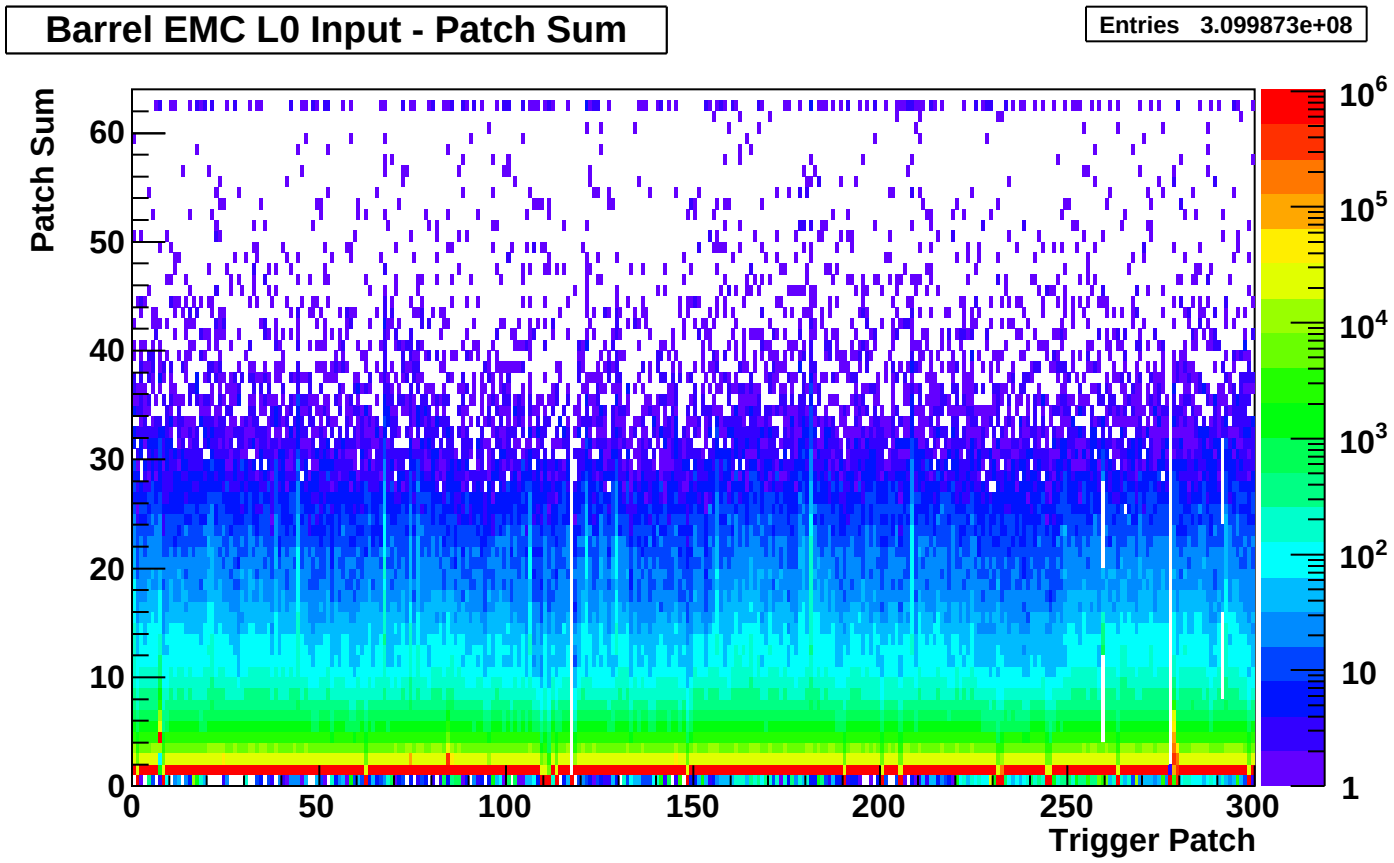

TPC Readout Mask (simulated)
Entries 161920

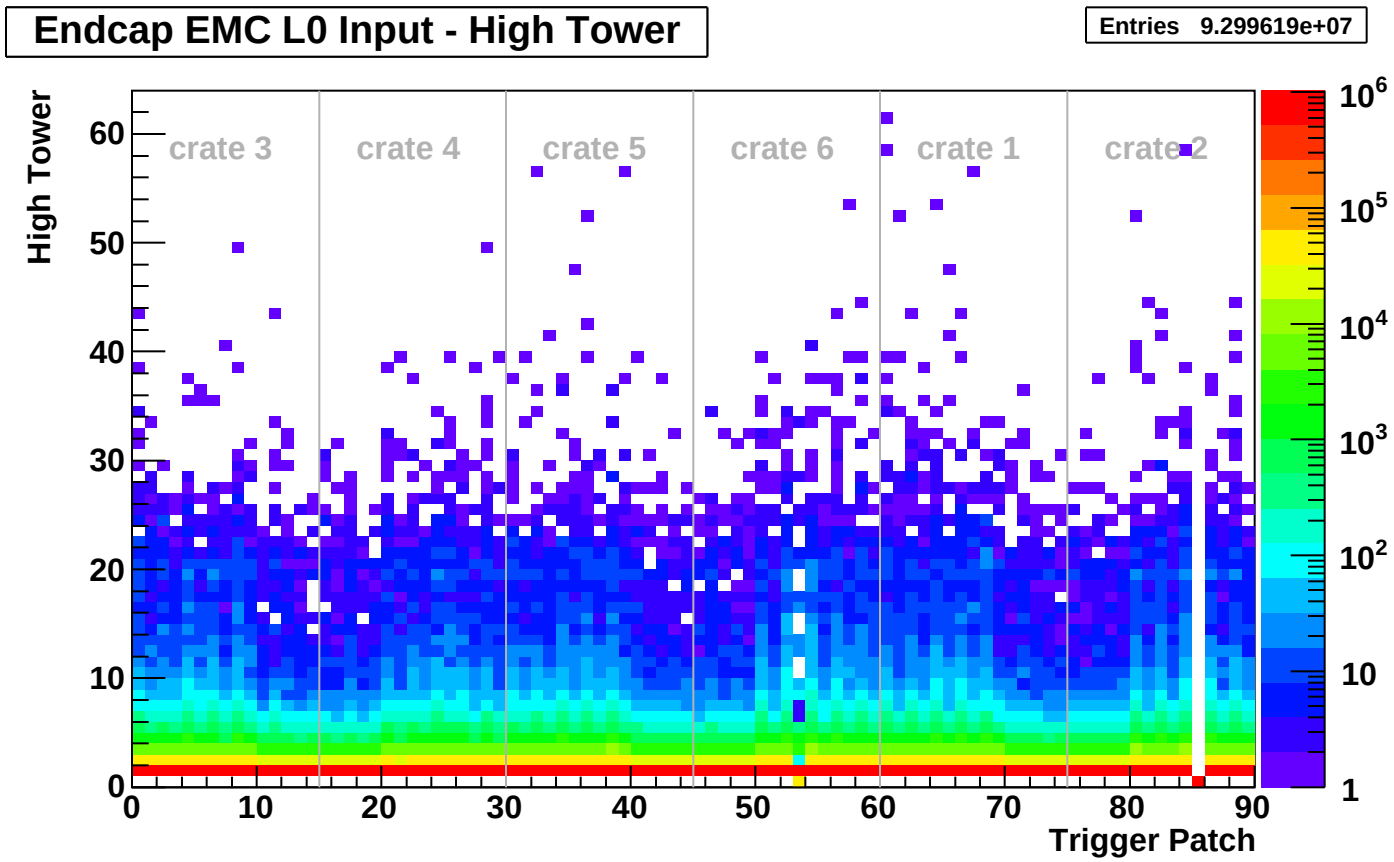

BunchId7Bit (all events)
Entries 1033291

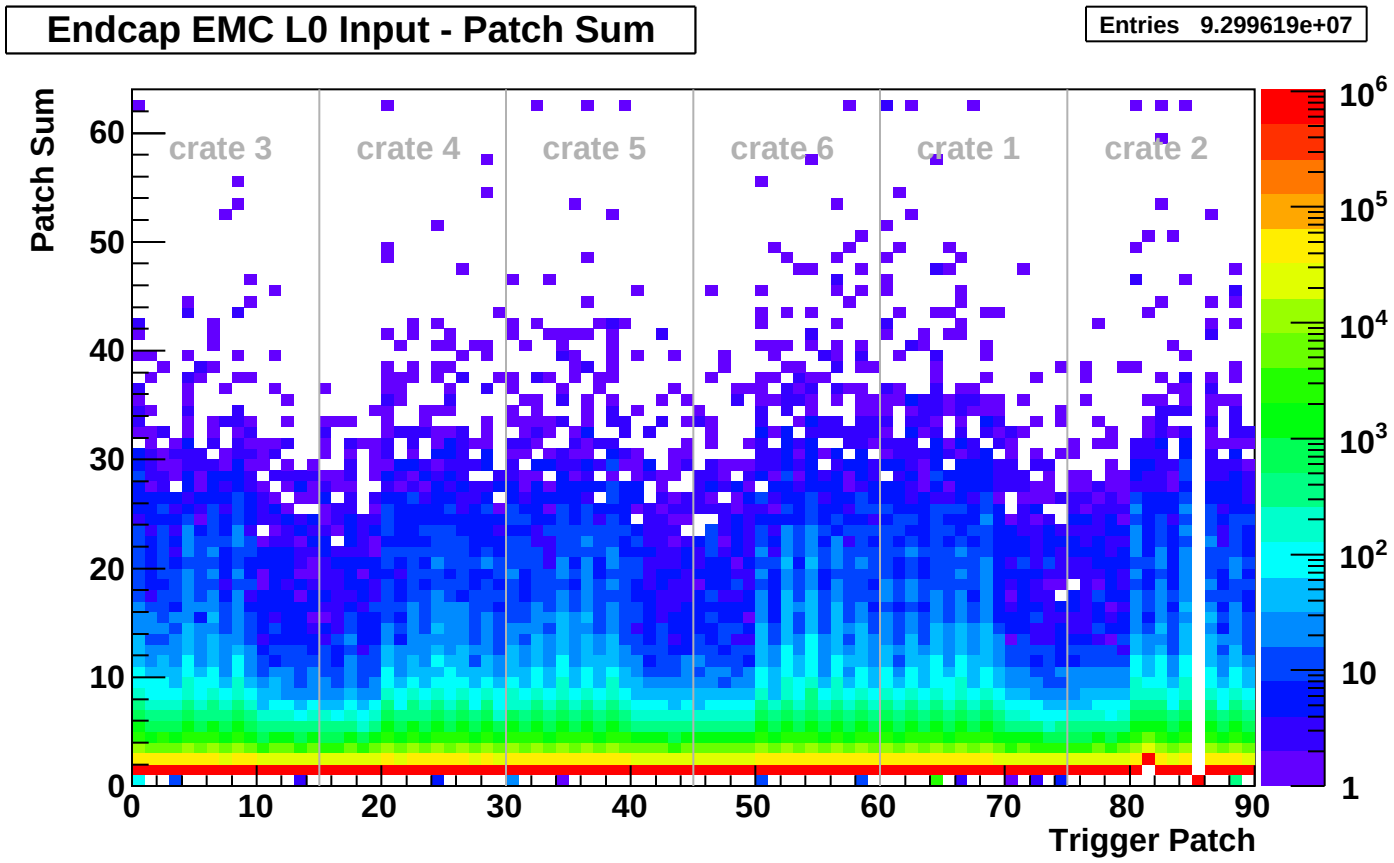

Abort-gaps
Entries 22164

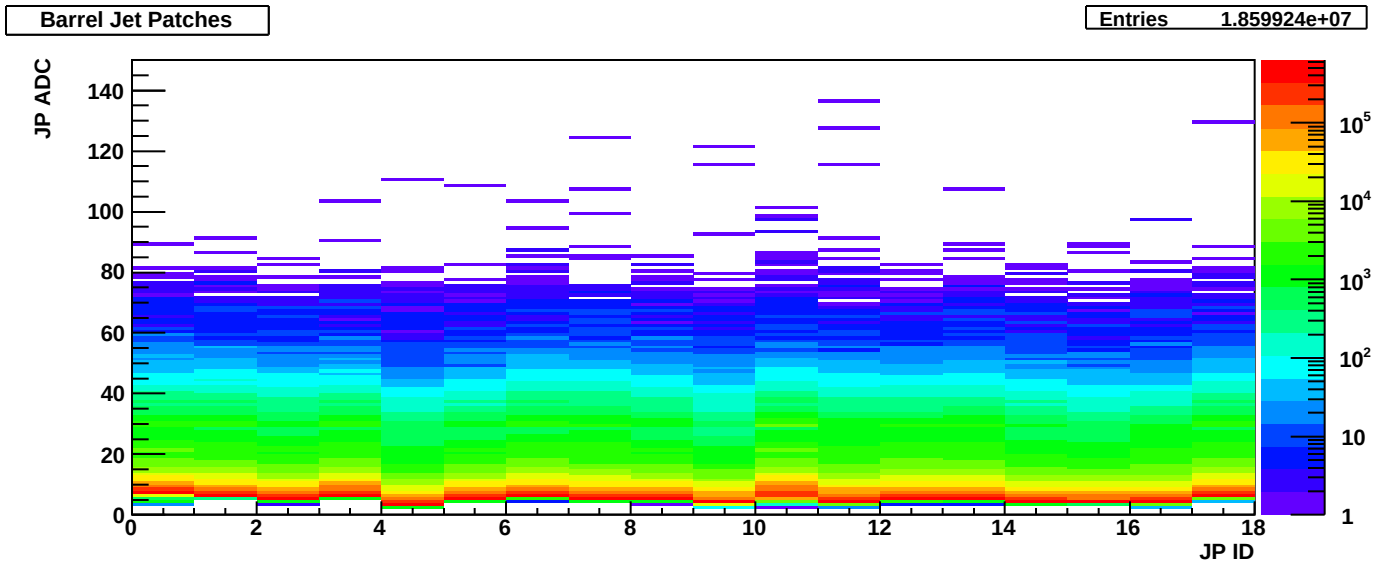

!Yellow-Fill
Entries 33091

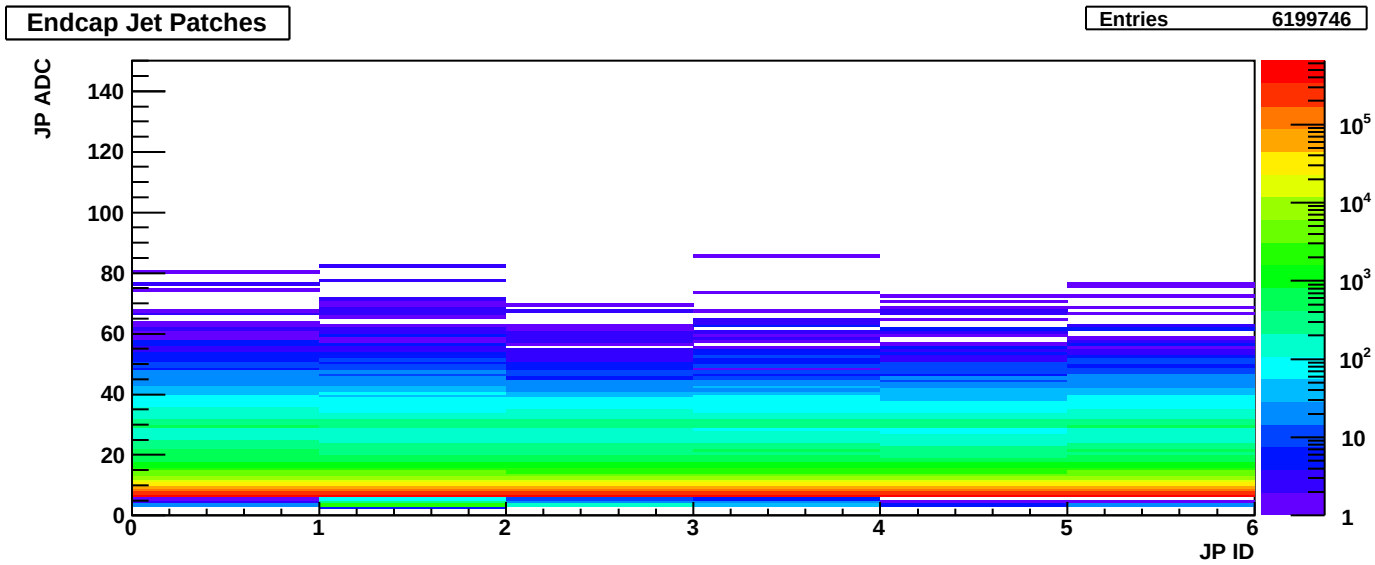

Barrel EMC L0 Input - High Tower
Entries 3.099873e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

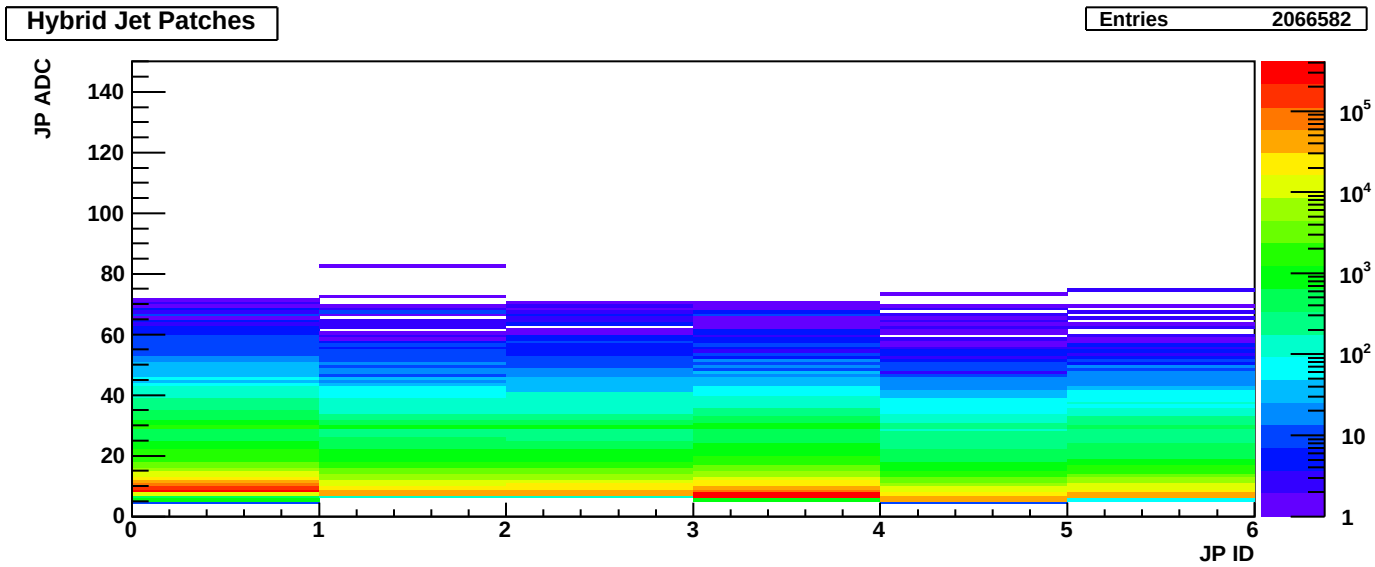

Barrel EMC L0 Input - Patch Sum
Entries 3.099873e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

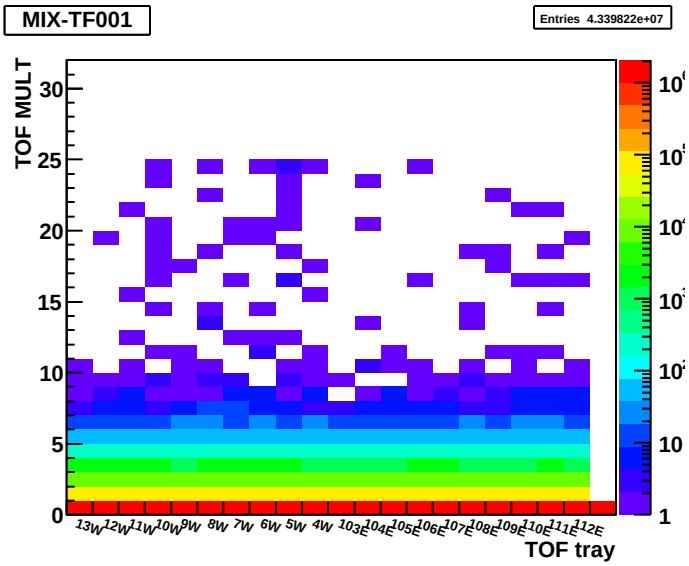

Endcap EMC L0 Input - High Tower
Entries 9.299619e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

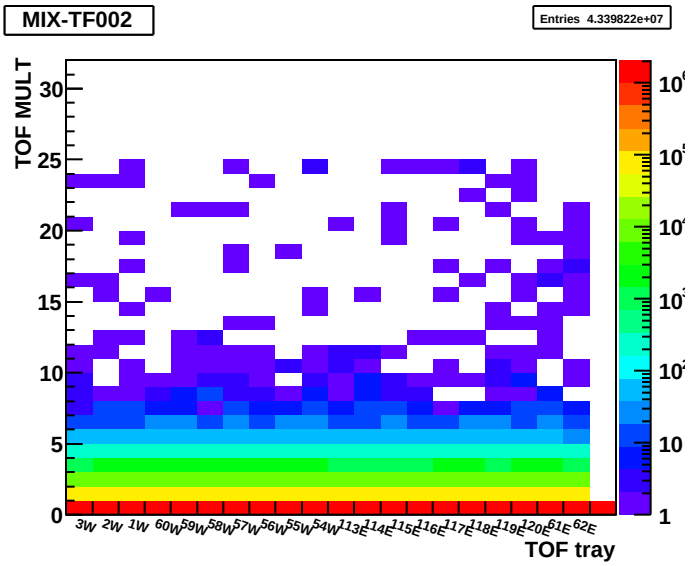

Endcap EMC L0 Input - Patch Sum
Entries 9.299619e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

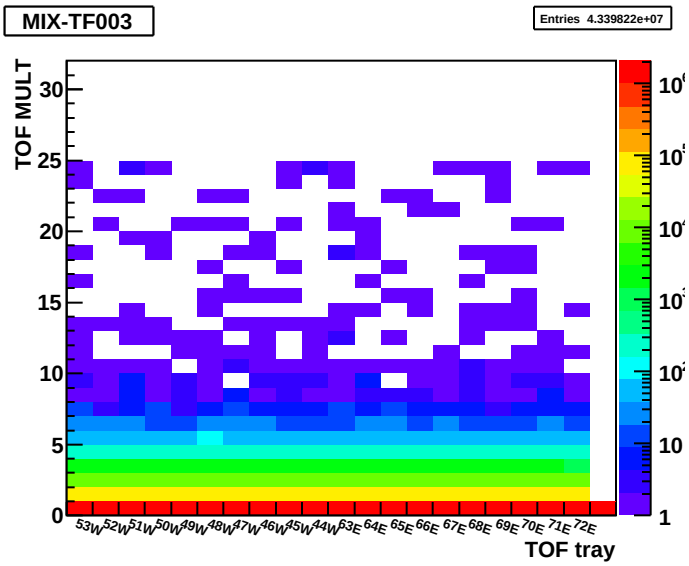

Barrel Jet Patches
Entries 1.859924e+07
JP ADC
JP ID

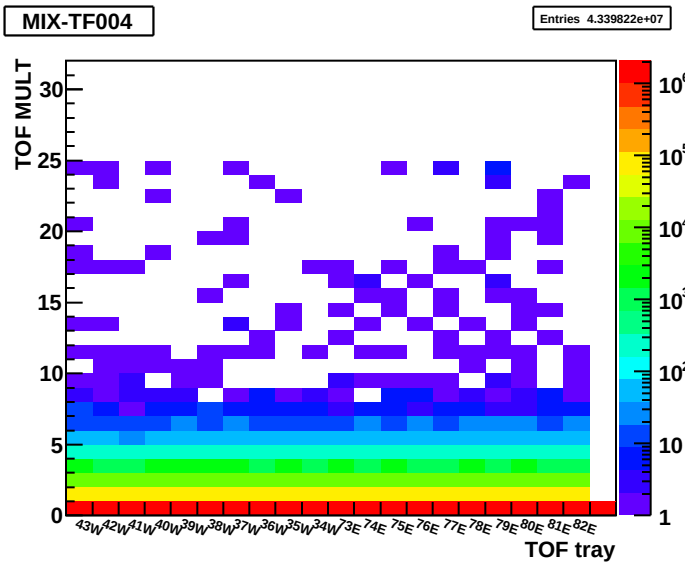

Endcap Jet Patches
Entries 6199746
JP ADC
JP ID

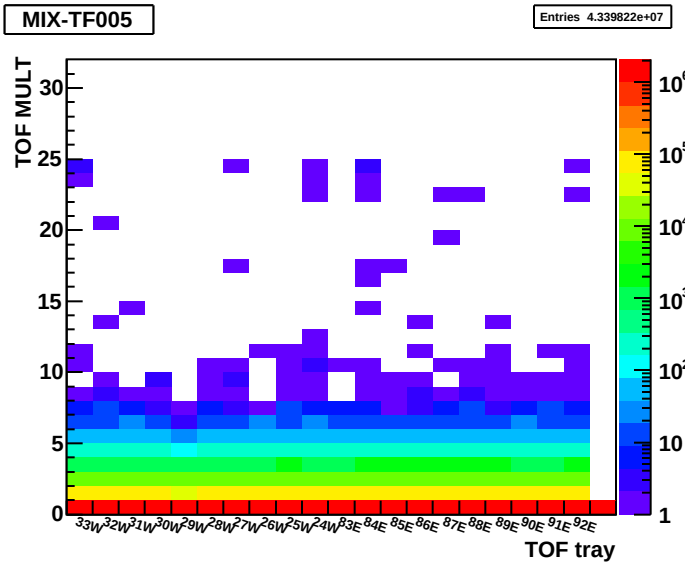

Hybrid Jet Patches
Entries 2066582
JP ADC
JP ID

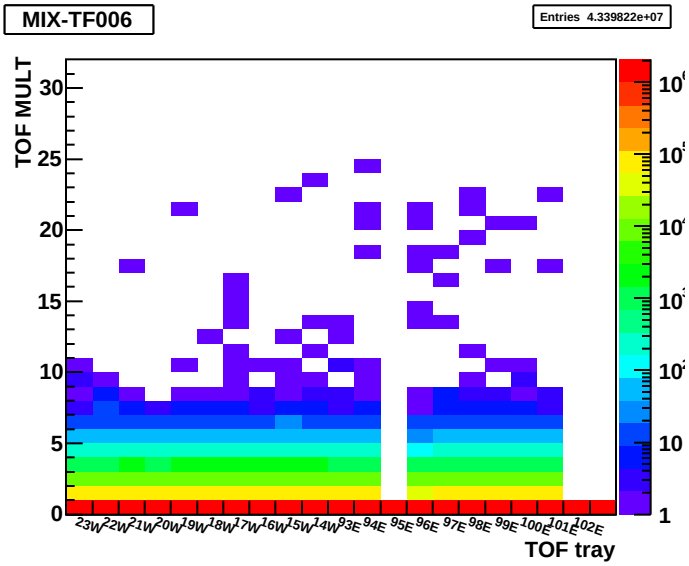

MIX-TF001
Entries 4.339822e+07
TOF MULT
TOF tray

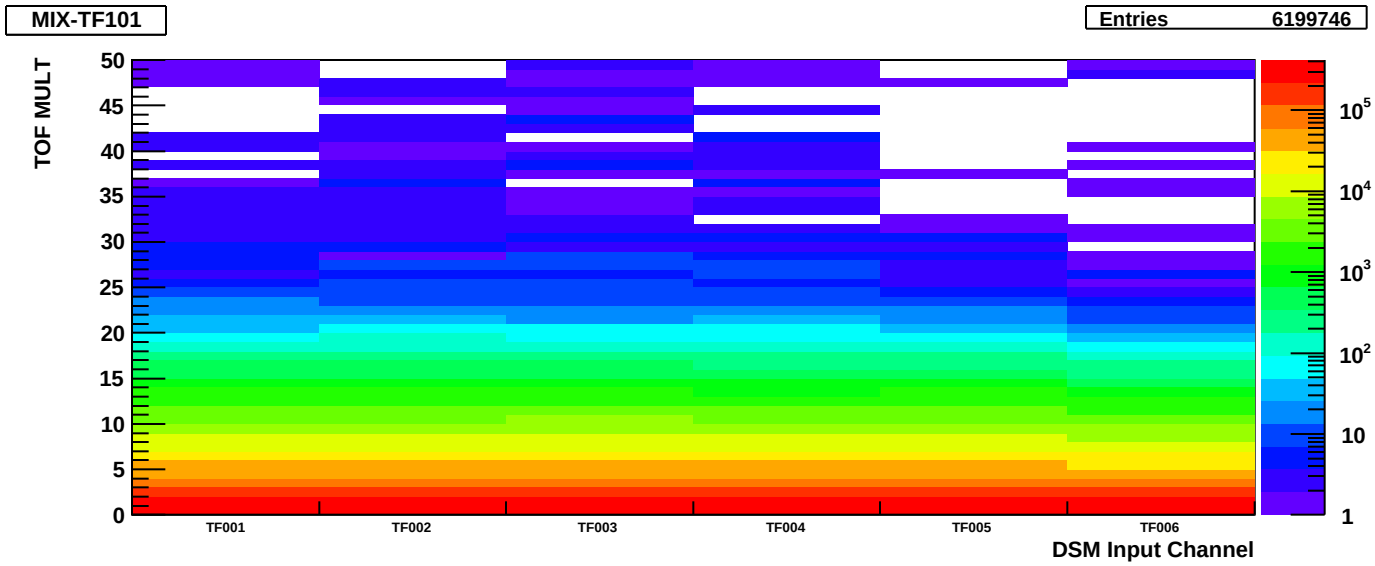

MIX-TF002
Entries 4.339822e+07
TOF MULT
TOF tray

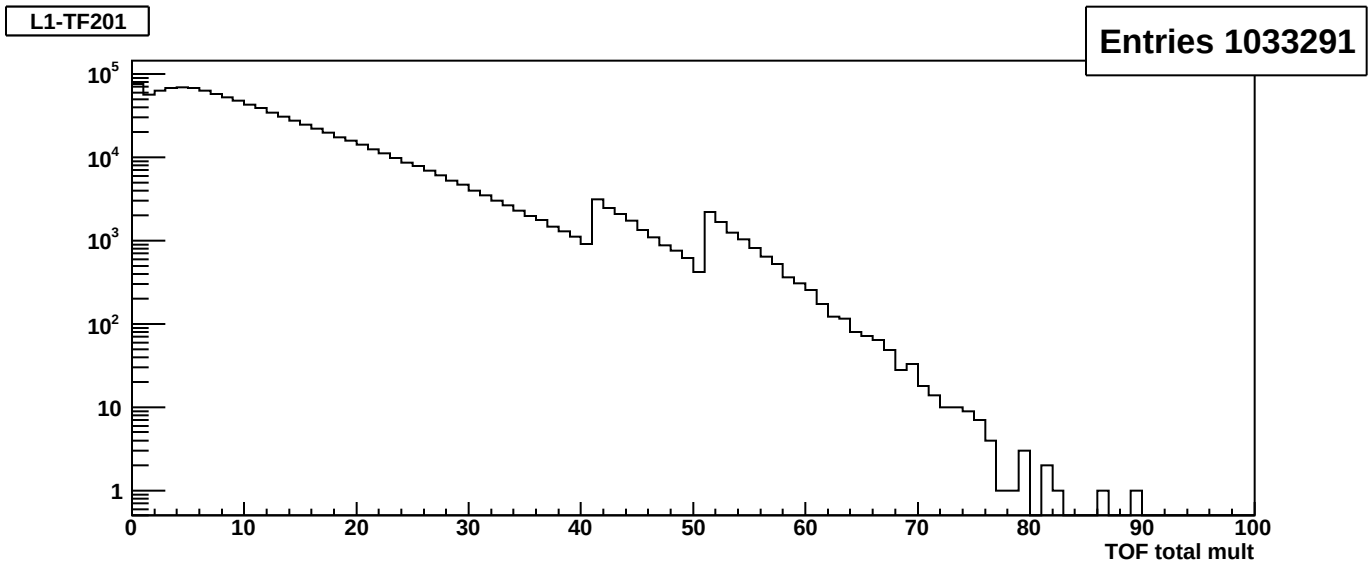

MIX-TF003
Entries 4.339822e+07
TOF MULT
TOF tray

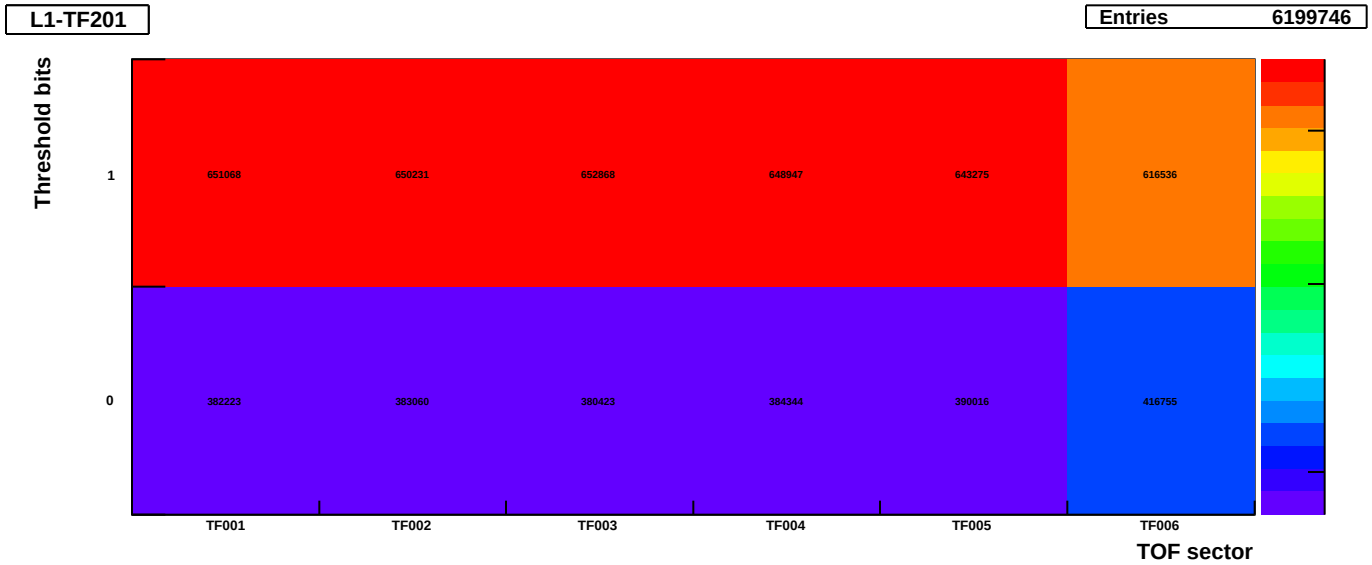

MIX-TF004
Entries 4.339822e+07
TOF MULT
TOF tray

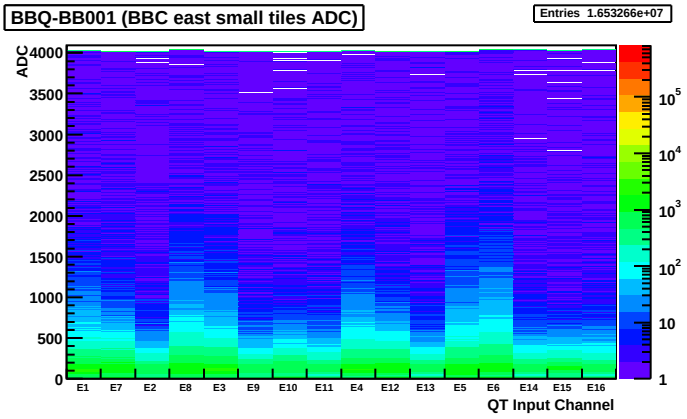

MIX-TF005
Entries 4.339822e+07
TOF MULT
TOF tray

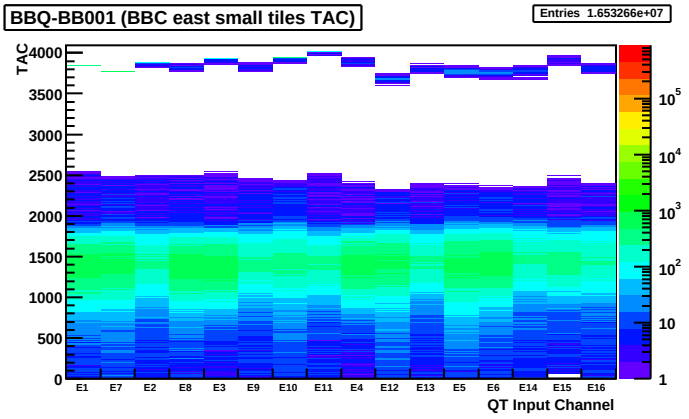

MIX-TF006
Entries 4.339822e+07
TOF MULT
TOF tray

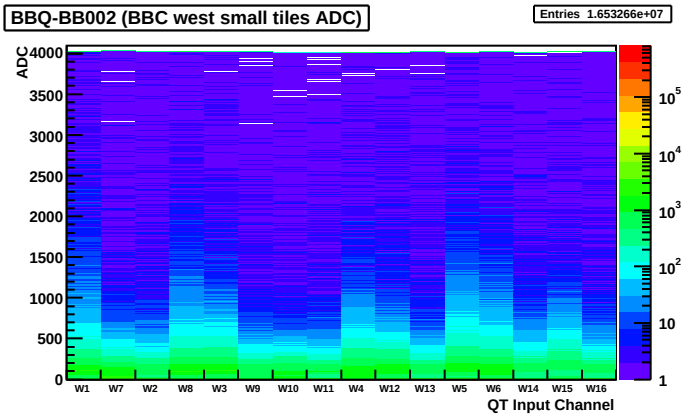

MIX-TF101
Entries 6199746
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

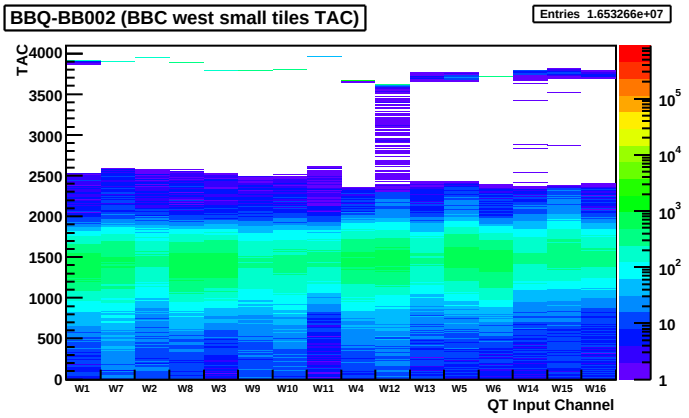

L1-TF201
Entries 1033291
TOF total mult

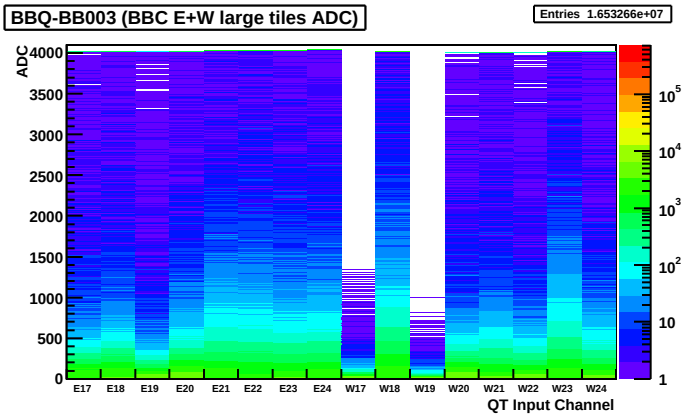

L1-TF201
Entries 6199746
Threshold bits
651068
650231
652868
648947
643275
616536
382223
383060
380423
384344
390016
416755
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

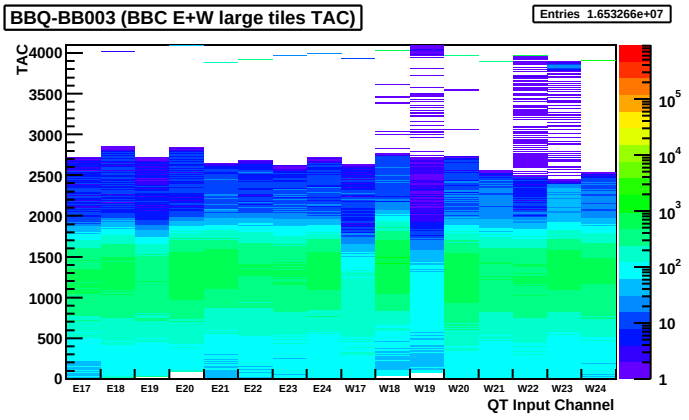

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.653266e+07
ADC
QT Input Channel

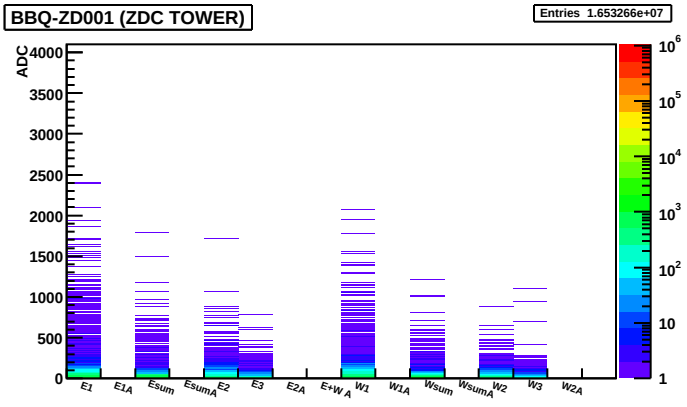

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.653266e+07
TAC
QT Input Channel

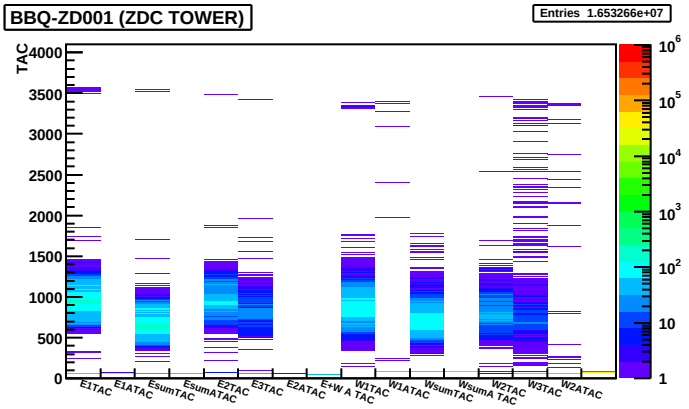

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.653266e+07
ADC
QT Input Channel

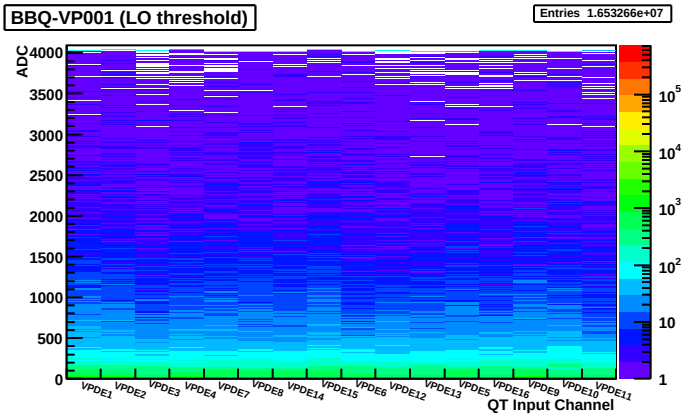

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.653266e+07
TAC
QT Input Channel

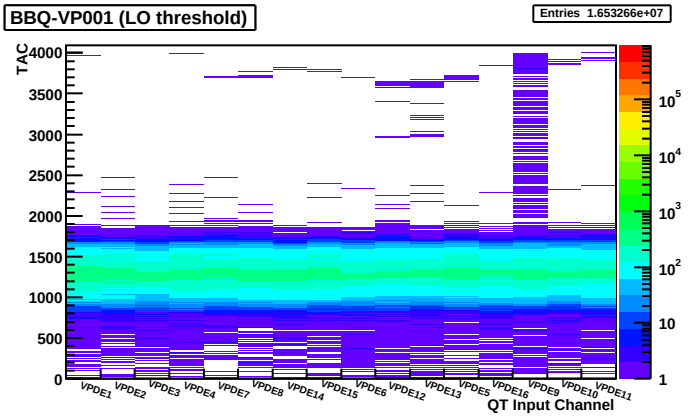

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.653266e+07
ADC
QT Input Channel

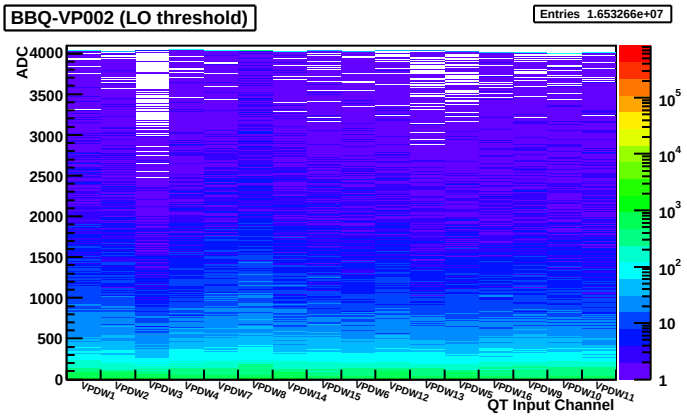

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.653266e+07
TAC
QT Input Channel

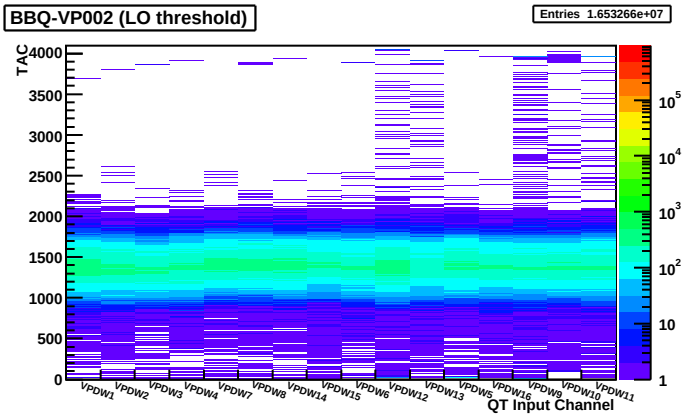

BBQ-ZD001 (ZDC TOWER)
Entries 1.653266e+07
ADC

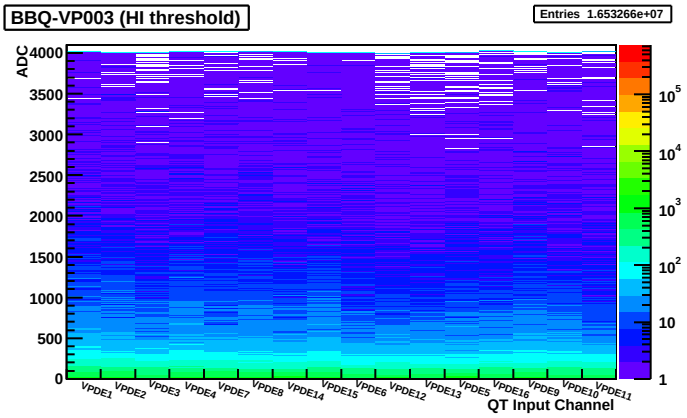

BBQ-ZD001 (ZDC TOWER)
Entries 1.653266e+07
TAC

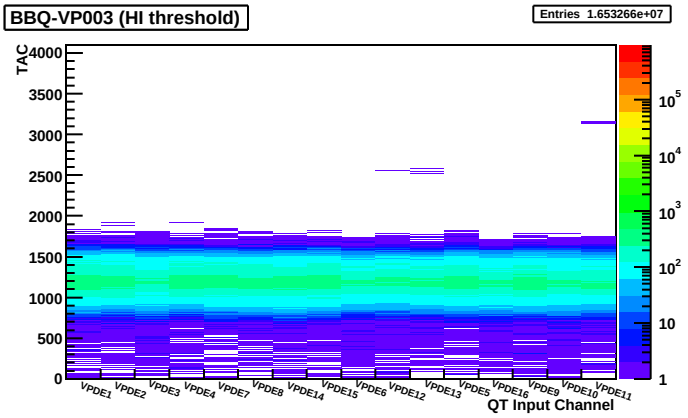

BBQ-VP001 (LO threshold)
Entries 1.653266e+07
ADC
QT Input Channel

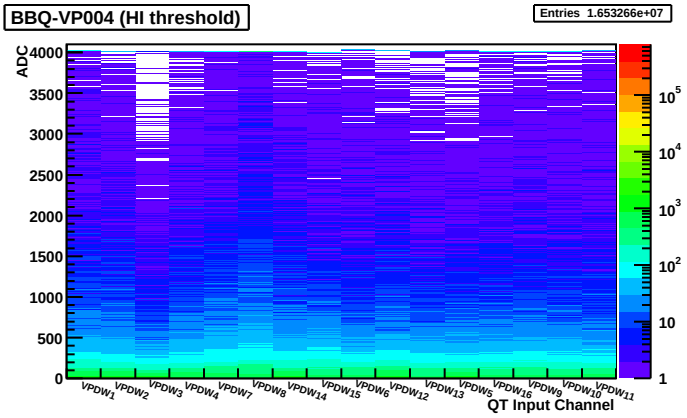

BBQ-VP001 (LO threshold)
Entries 1.653266e+07
TAC
QT Input Channel

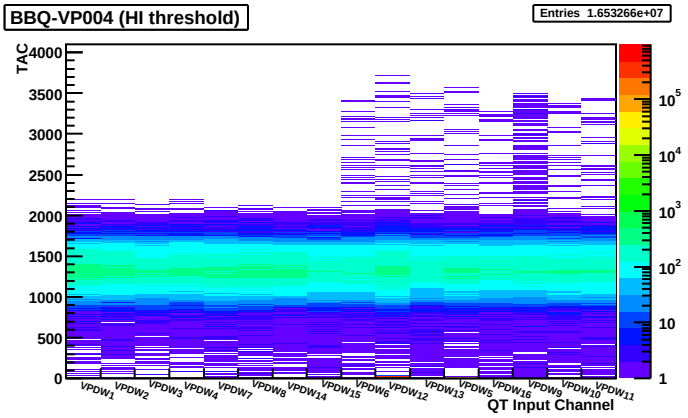

BBQ-VP002 (LO threshold)
Entries 1.653266e+07
ADC
QT Input Channel

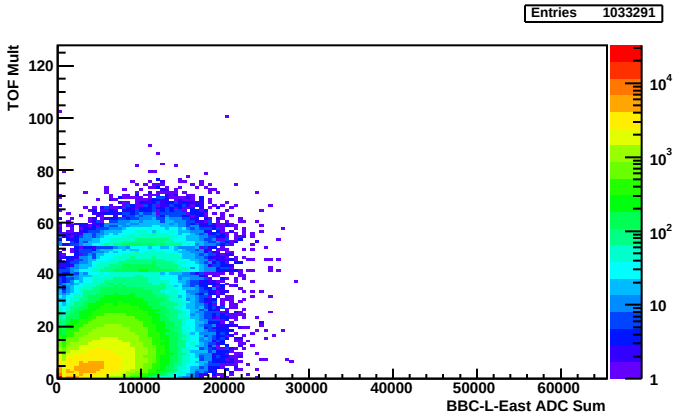

BBQ-VP002 (LO threshold)
Entries 1.653266e+07
TAC
QT Input Channel

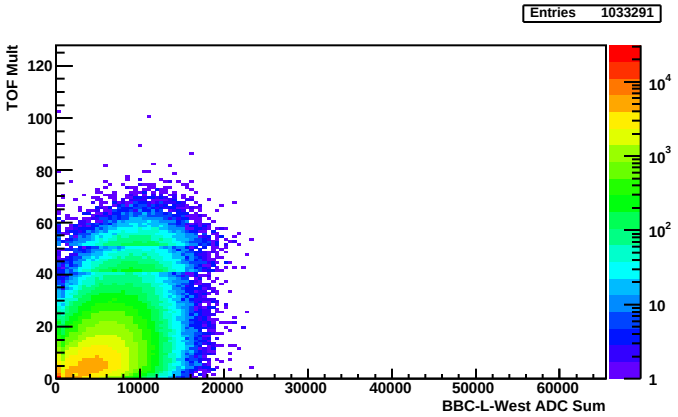

BBQ-VP003 (HI threshold)
Entries 1.653266e+07
ADC
QT Input Channel

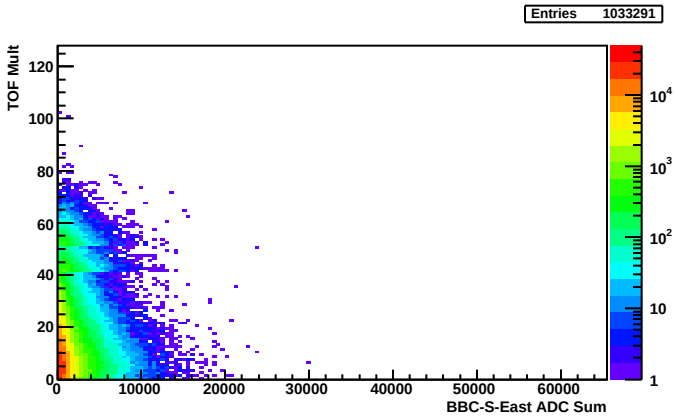

BBQ-VP003 (HI threshold)
Entries 1.653266e+07
TAC
QT Input Channel

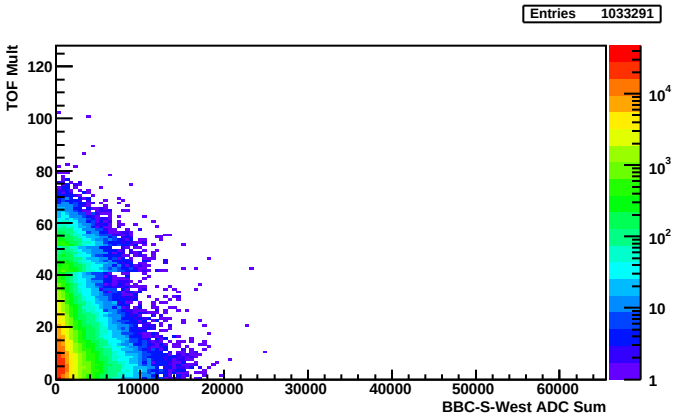

BBQ-VP004 (HI threshold)
Entries 1.653266e+07
ADC
QT Input Channel

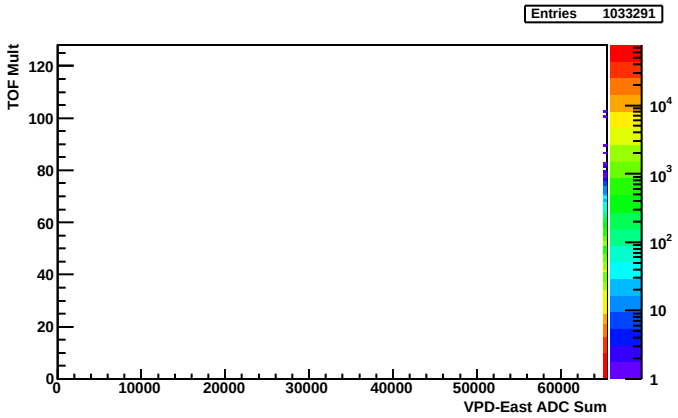

BBQ-VP004 (HI threshold)
Entries 1.653266e+07
TAC
QT Input Channel

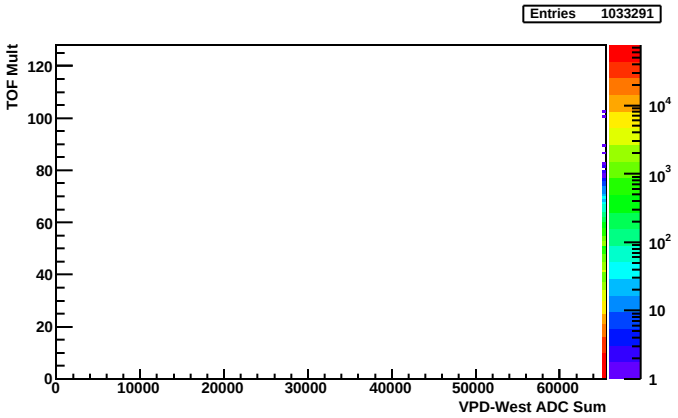

Entries 1033291
TOF Mult
BBC-L-East ADC Sum

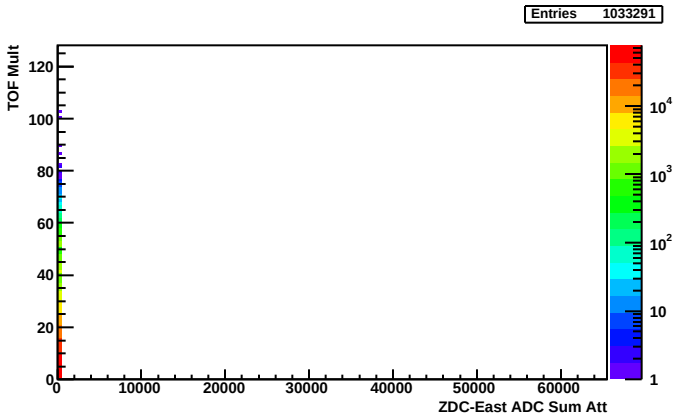

Entries 1033291
TOF Mult
BBC-L-West ADC Sum

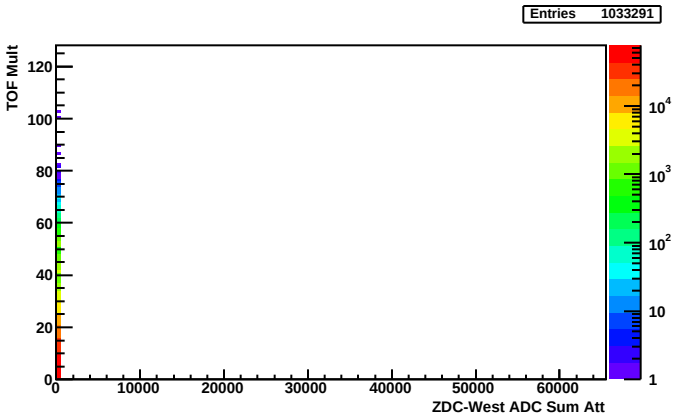

Entries 1033291
TOF Mult
BBC-S-East ADC Sum

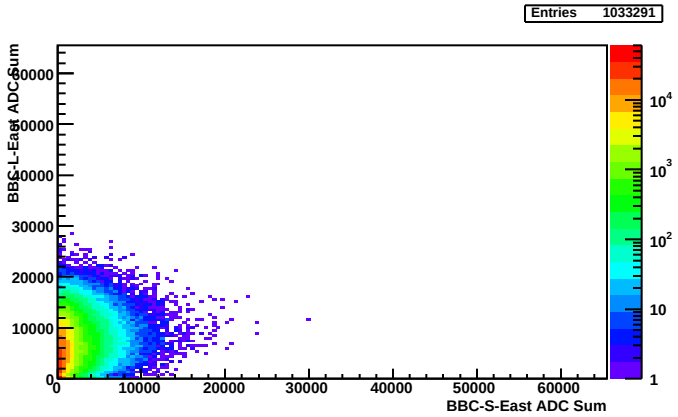

Entries 1033291
TOF Mult
BBC-S-West ADC Sum

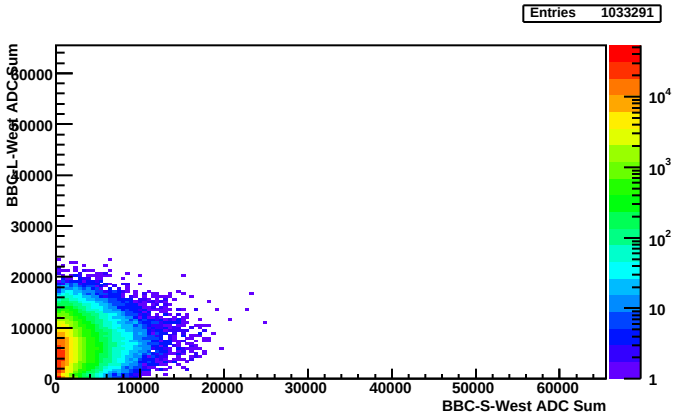

Entries 1033291
TOF Mult
VPD-East ADC Sum

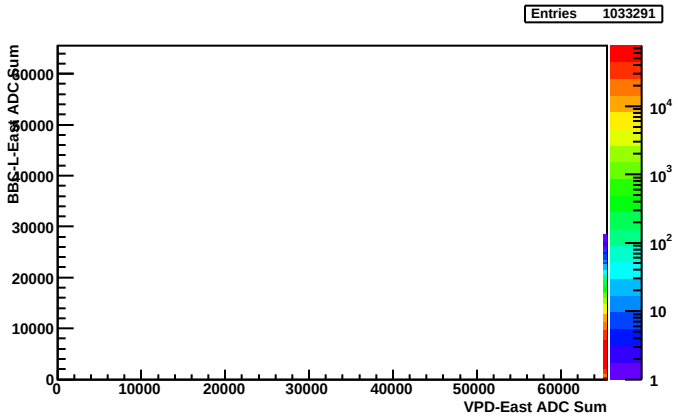

Entries 1033291
TOF Mult
VPD-West ADC Sum

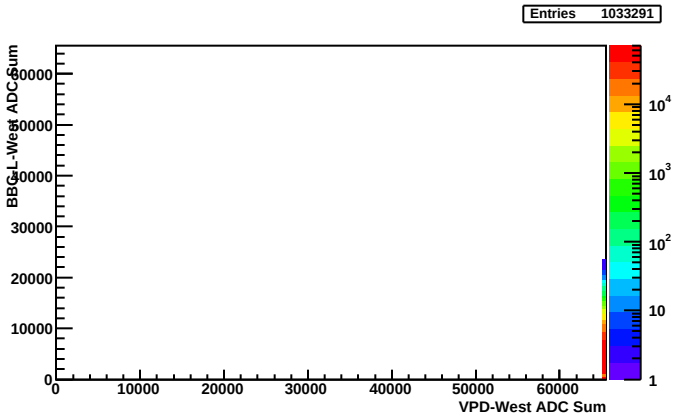

Entries 1033291
TOF Mult
ZDC-East ADC Sum Att

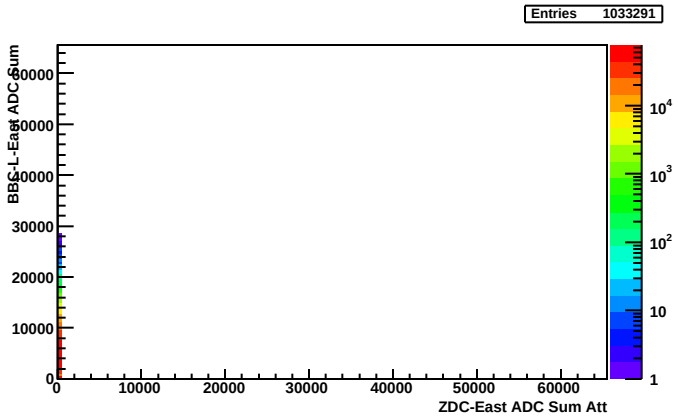

Entries 1033291
TOF Mult
ZDC-West ADC Sum Att

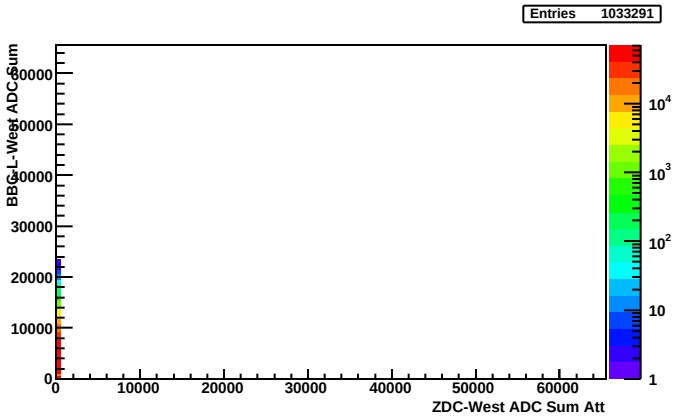

Entries 1033291
BBC-L-East ADC Sum
BBC-S-East ADC Sum

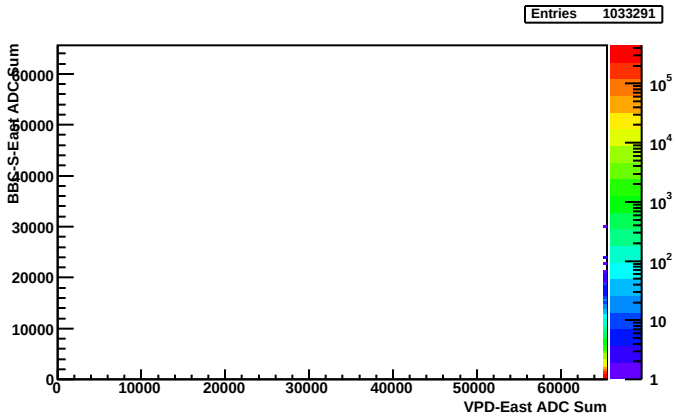

Entries 1033291
BBC-L-West ADC Sum
BBC-S-West ADC Sum

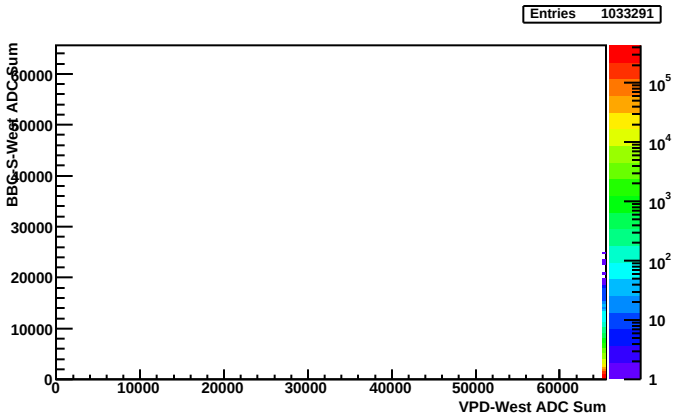

Entries 1033291
BBC-L-East ADC Sum
VPD-East ADC Sum

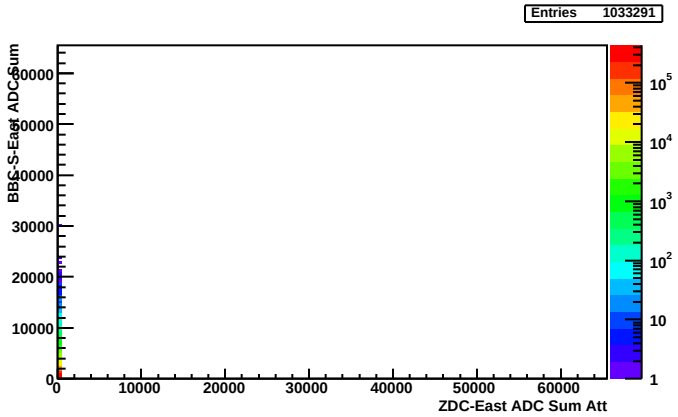

Entries 1033291
BBC-L-West ADC Sum
VPD-West ADC Sum

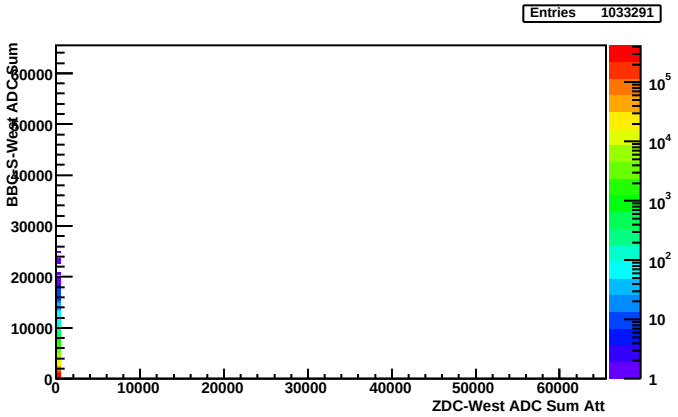

Entries 1033291
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

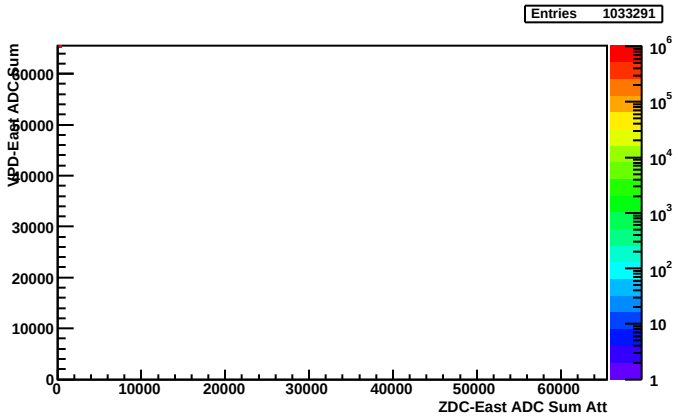

Entries 1033291
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

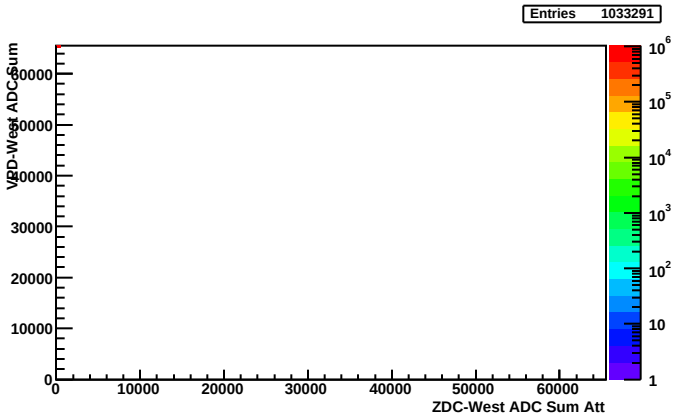

Entries 1033291
BBC-S-East ADC Sum
VPD-East ADC Sum

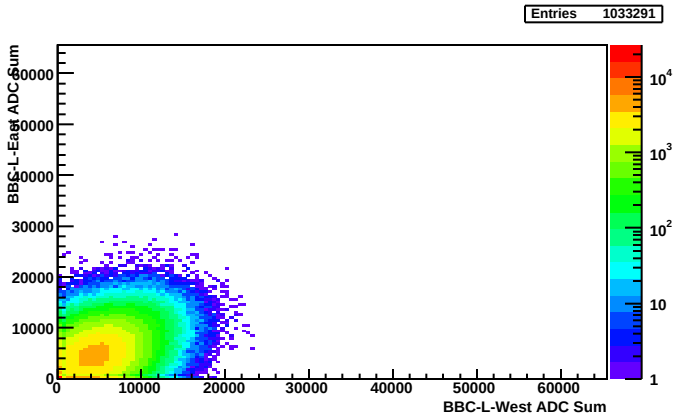

Entries 1033291
BBC-S-West ADC Sum
VPD-West ADC Sum

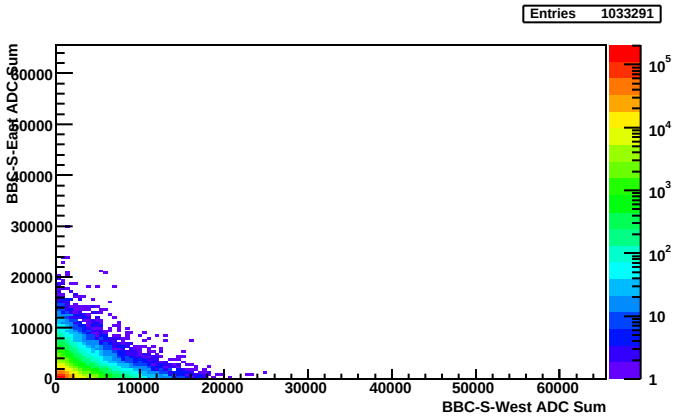

Entries 1033291
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

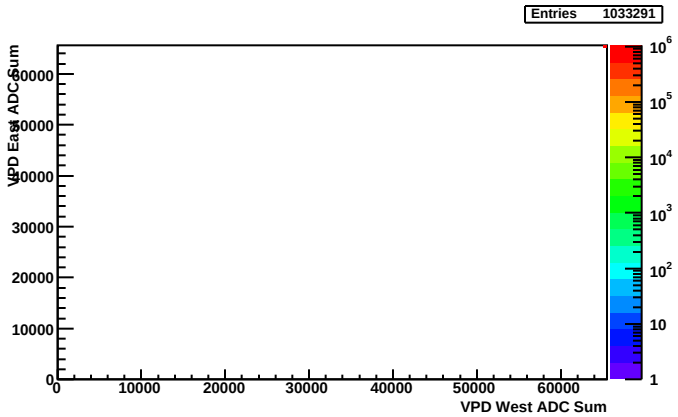

Entries 1033291
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

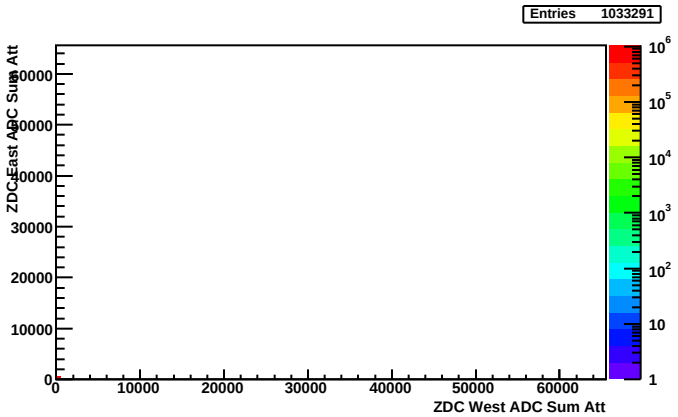

Entries 1033291
VPD-East ADC Sum
ZDC-East ADC Sum Att

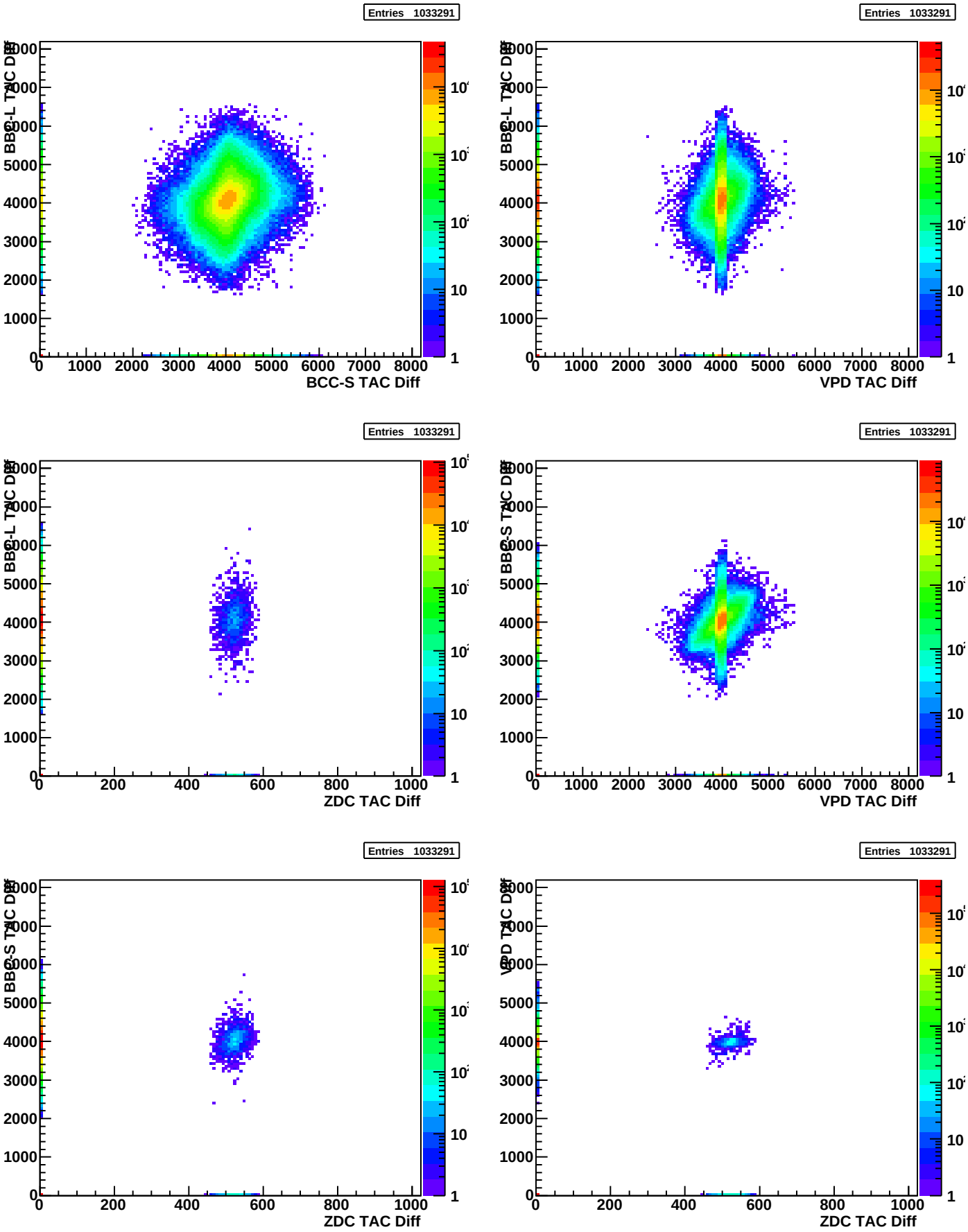

Entries 1033291
VPD-West ADC Sum
ZDC-West ADC Sum Att

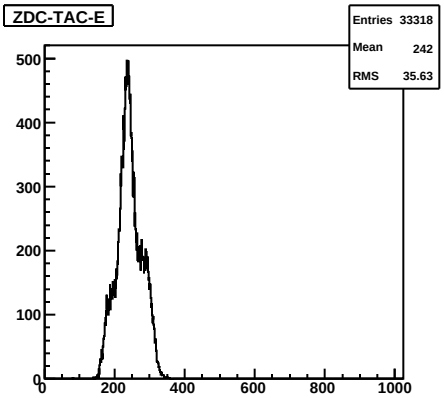

Entries 1033291
BBC-L-East ADC Sum
BBC-L-West ADC Sum

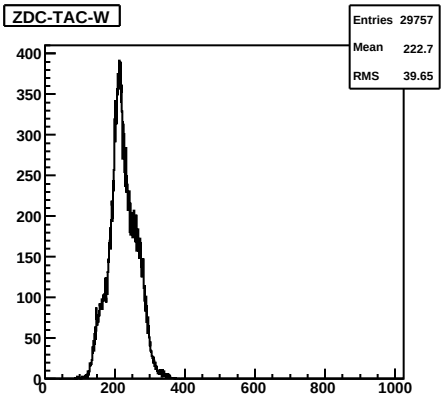

Entries 1033291
BBC-S-East ADC Sum
BBC-S-West ADC Sum

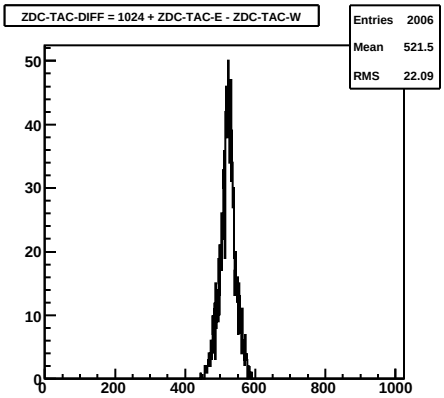

Entries 1033291
VPD East ADC Sum
VPD West ADC Sum

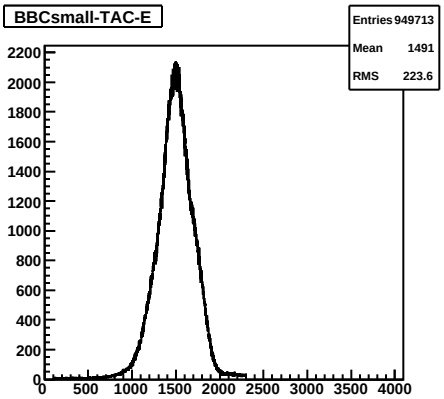

Entries 1033291
ZDC East ADC Sum Att
ZDC West ADC Sum Att

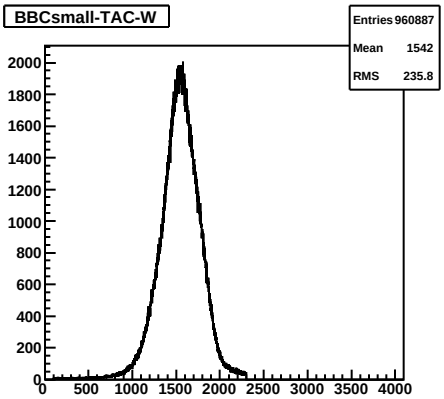

Entries 1033291
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1033291
BBC-L TAC Diff
VPD TAC Diff
Entries 1033291
BBC-L TAC Diff
ZDC TAC Diff
Entries 1033291
BBC-S TAC Diff
VPD TAC Diff
Entries 1033291
BBC-S TAC Diff
ZDC TAC Diff
Entries 1033291
VPD TAC Diff
ZDC TAC Diff

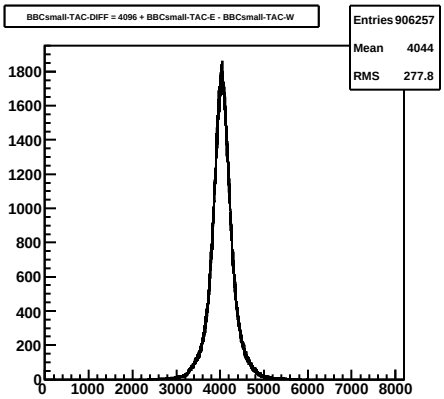

ZDC-TAC-E
Entries 33318
Mean 242
RMS 35.63

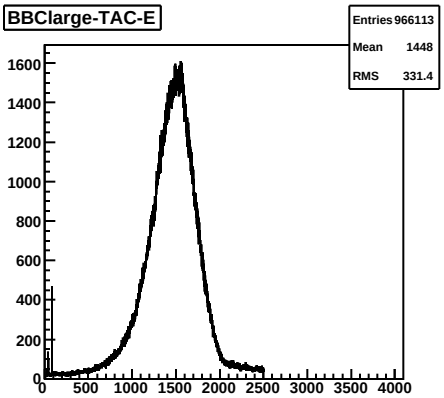

ZDC-TAC-W
Entries 29757
Mean 222.7
RMS 39.65

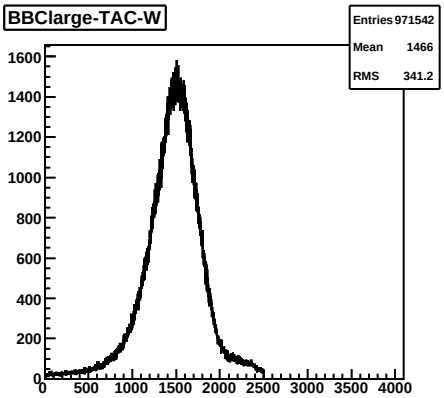

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2006
Mean 521.5
RMS 22.09

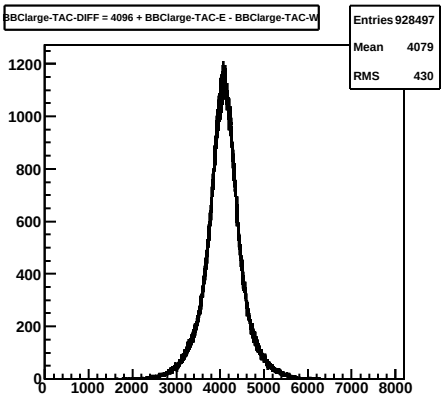

BBCsmall-TAC-E
Entries 949713
Mean 1491
RMS 223.6

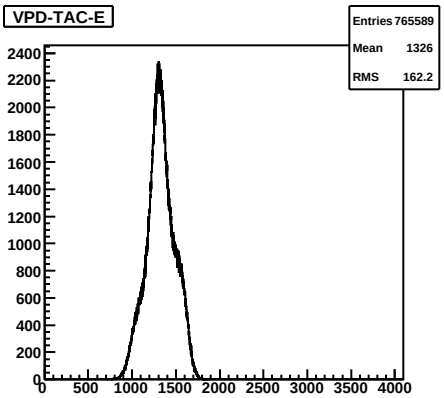

BBCsmall-TAC-W
Entries 960887
Mean 1542
RMS 235.8

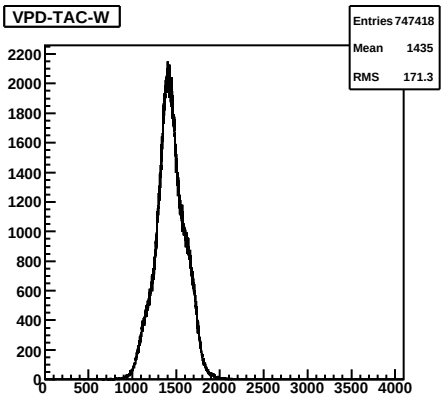

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 906257
Mean 4044
RMS 277.8

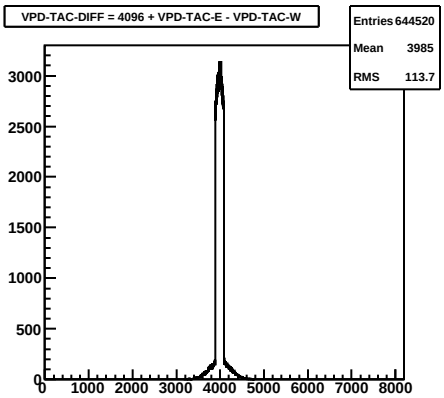

BBClarge-TAC-E
Entries 966113
Mean 1448
RMS 331.4

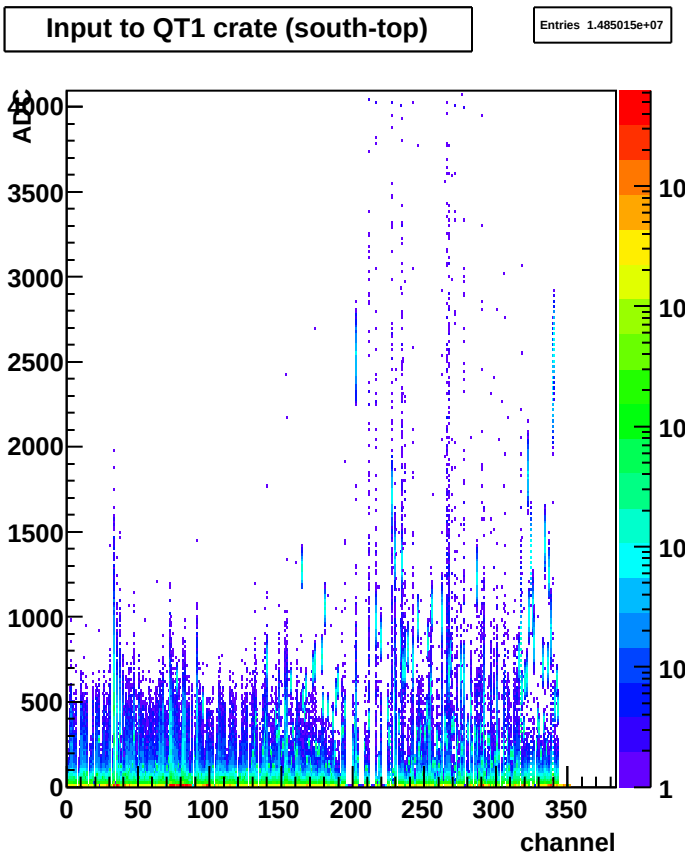

BBClarge-TAC-W
Entries 971542
Mean 1466
RMS 341.2

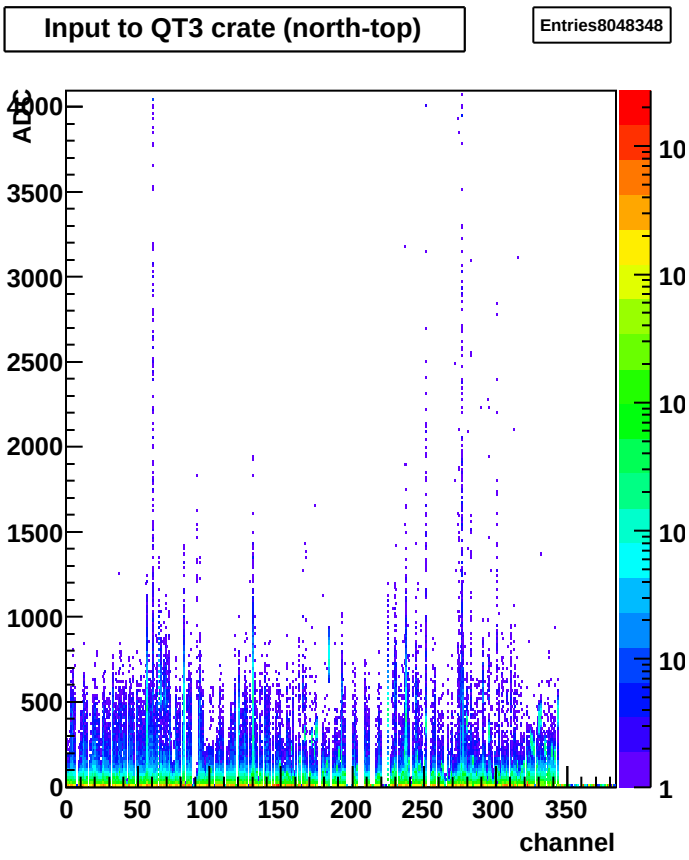

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 928497
Mean 4079
RMS 430

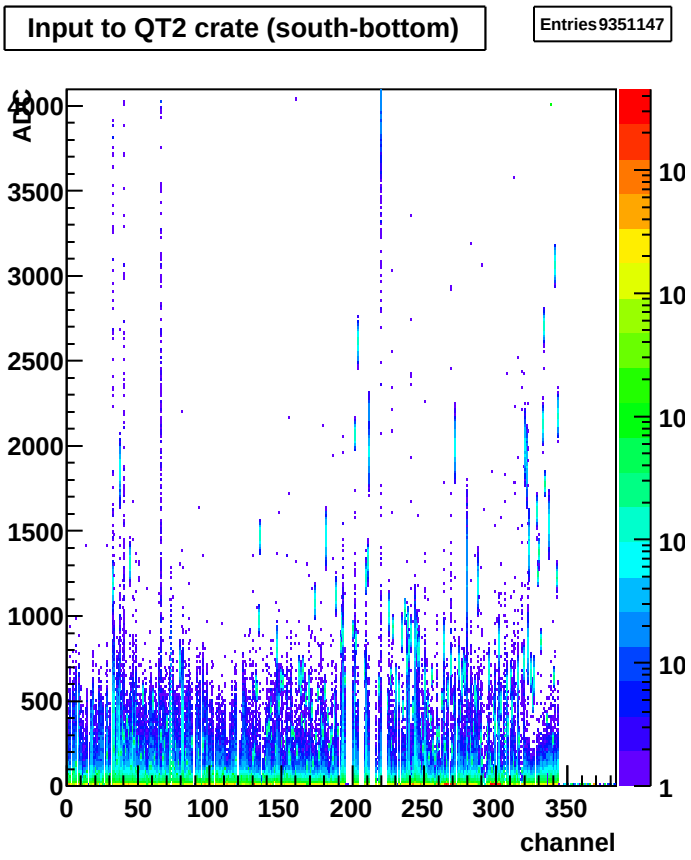

VPD-TAC-E
Entries 765589
Mean 1326
RMS 162.2

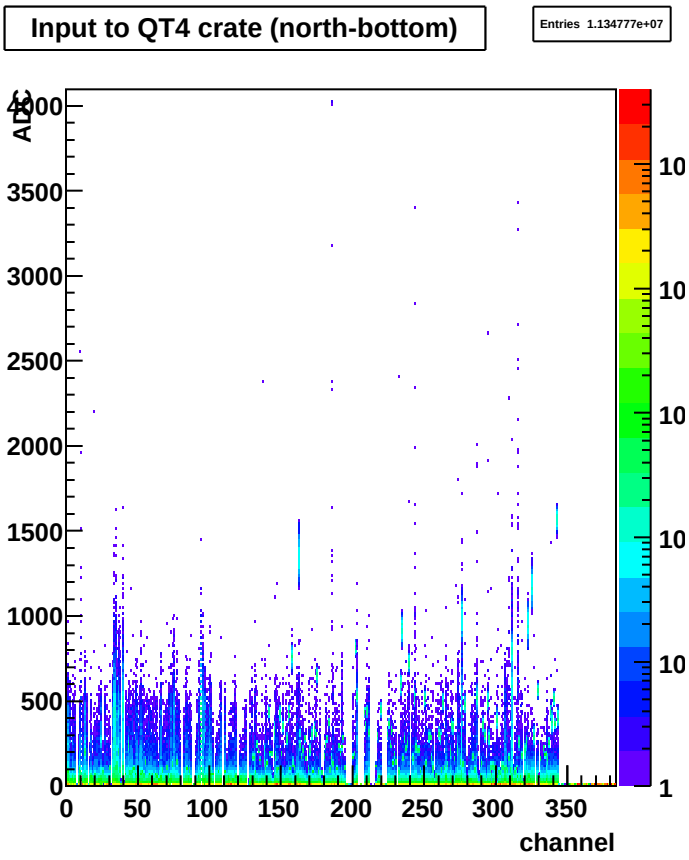

VPD-TAC-W
Entries 747418
Mean 1435
RMS 171.3

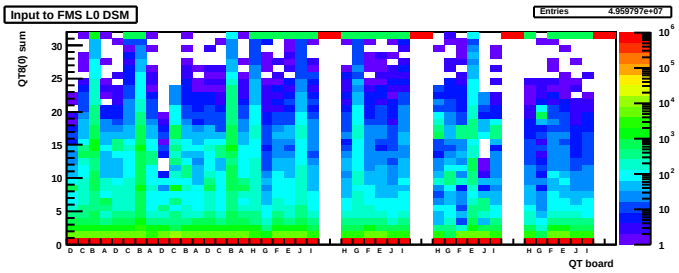

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 644520
Mean 3985
RMS 113.7

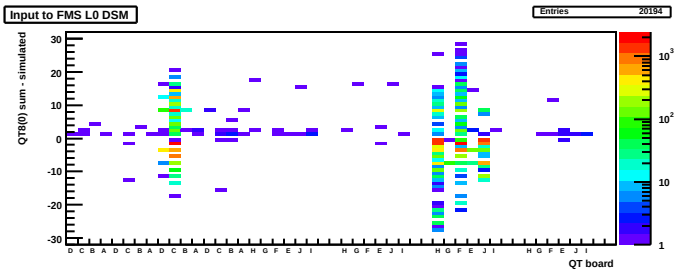

Input to QT1 crate (south-top)
Entries 1.485015e+07
ADC
channel

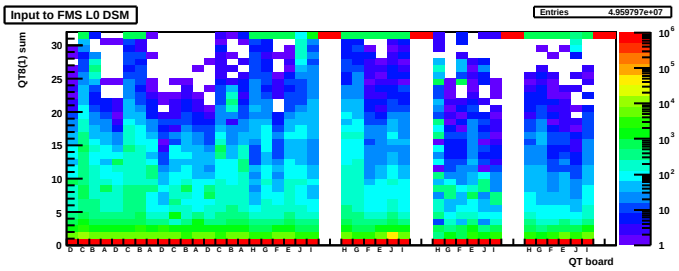

Input to QT3 crate (north-top)
Entries8048348
ADC
channel

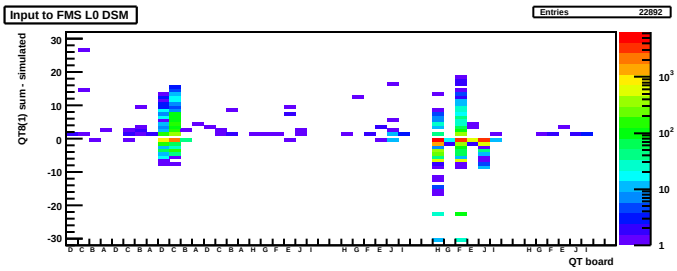

Input to QT2 crate (south-bottom)
Entries9351147
ADC
channel

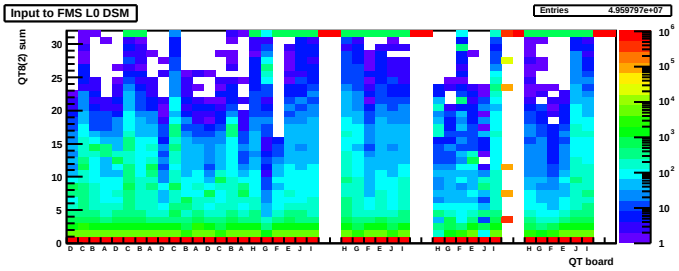

Input to QT4 crate (north-bottom)
Entries 1.134777e+07
ADC
channel

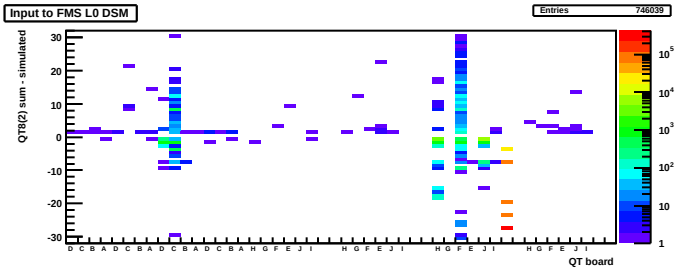

Input to FMS L0 DSM
Entries 4.959797e+07
QT8(0) sum
QT board

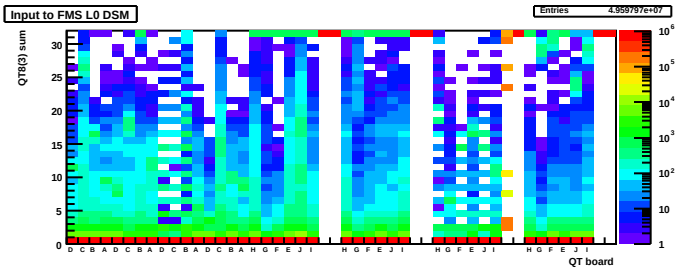

Input to FMS L0 DSM
Entries 20194
QT8(0) sum - simulated
QT board

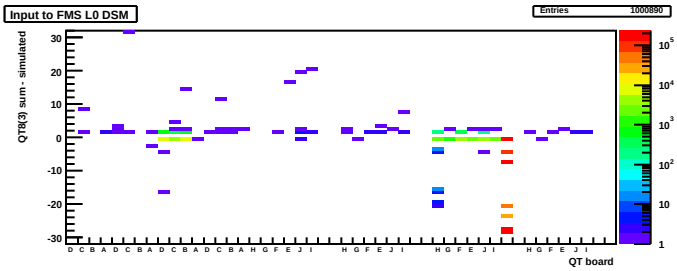

Input to FMS L0 DSM
Entries 4.959797e+07
QT8(1) sum
QT board

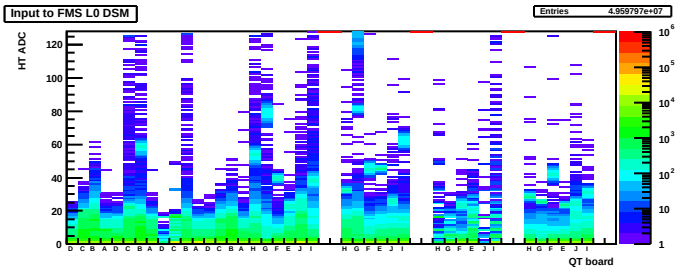

Input to FMS L0 DSM
Entries 22892
QT8(1) sum - simulated
QT board

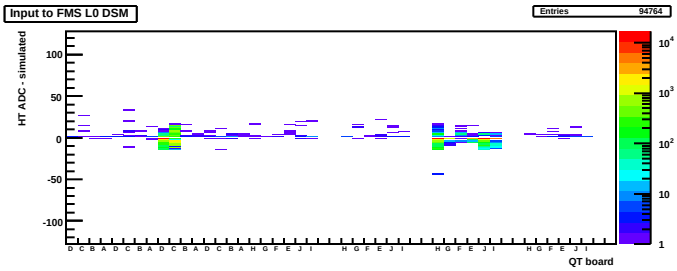

Input to FMS L0 DSM
Entries 4.959797e+07
QT8(2) sum
QT board

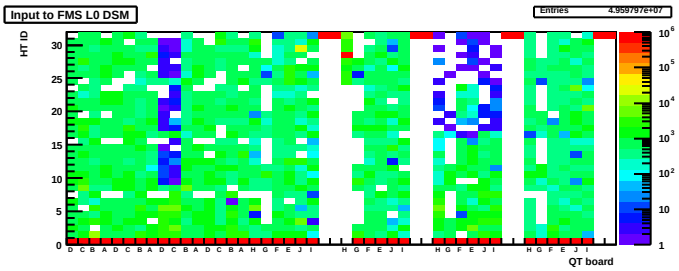

Input to FMS L0 DSM
Entries 746039
QT8(2) sum - simulated
QT board

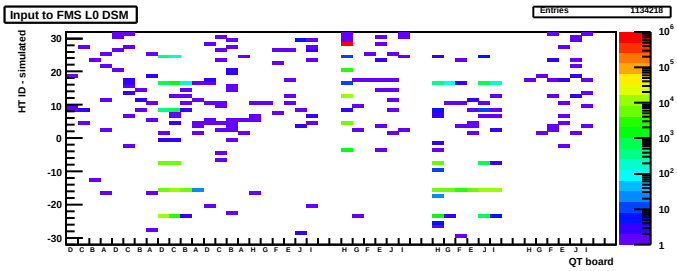

Input to FMS L0 DSM
Entries 4.959797e+07
QT8(3) sum
QT board

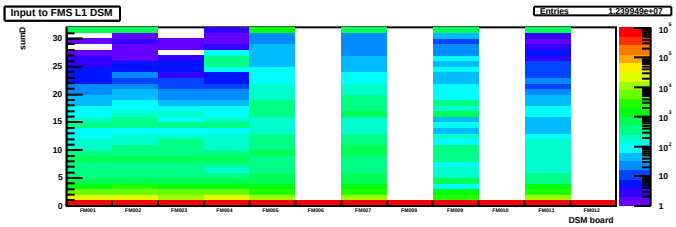

Input to FMS L0 DSM
Entries 1000890
QT8(3) sum - simulated
QT board

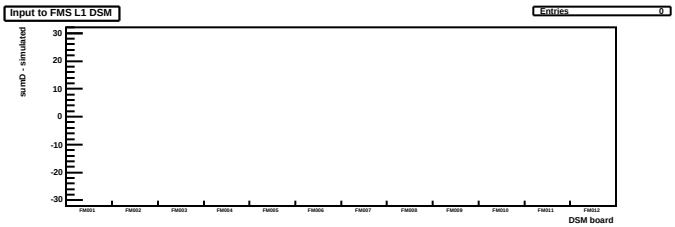

Input to FMS L0 DSM
Entries 4.959797e+07
HT ADC
QT board

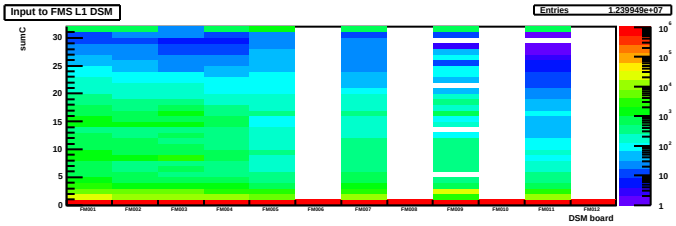

Input to FMS L0 DSM
Entries 94764
HT ADC - simulated
QT board

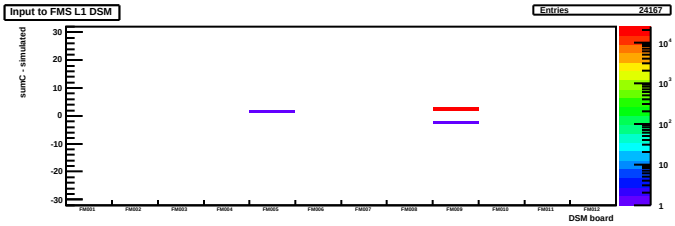

Input to FMS L0 DSM
Entries 4.959797e+07
HT ID
QT board

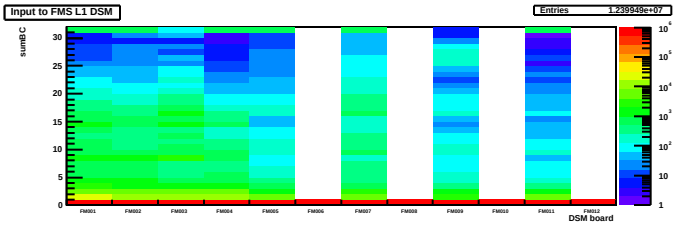

Input to FMS L0 DSM
Entries 1134218
HT ID - simulated
QT board

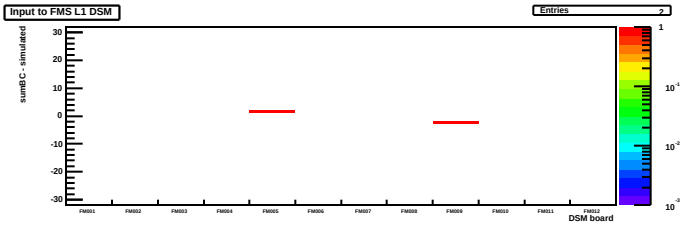

Input to FMS L1 DSM
Entries 1.239949e+07
sumD
DSM board

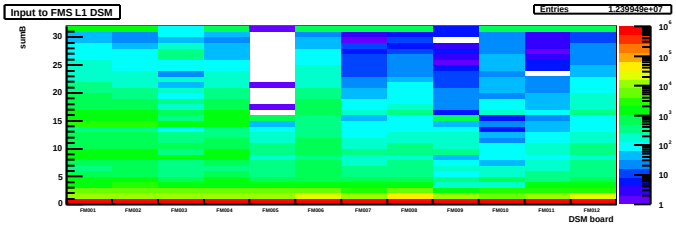

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

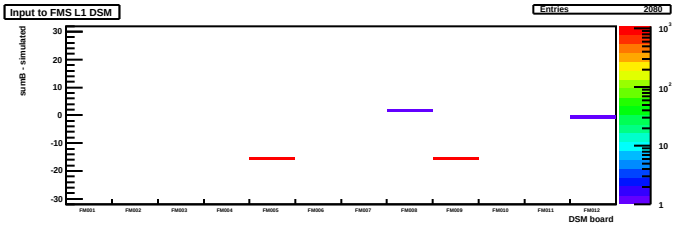

Input to FMS L1 DSM
Entries 1.239949e+07
sumC
DSM board

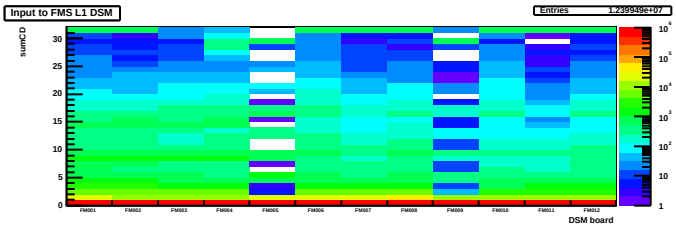

Input to FMS L1 DSM
Entries 24167
sumC - simulated
DSM board

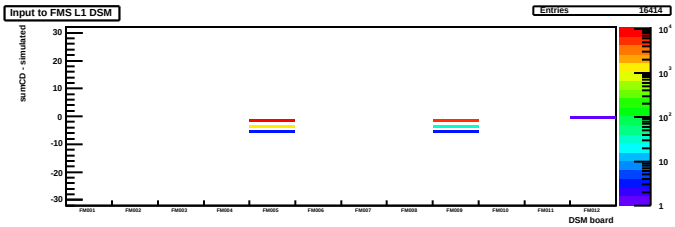

Input to FMS L1 DSM
Entries 1.239949e+07
sumBC
DSM board

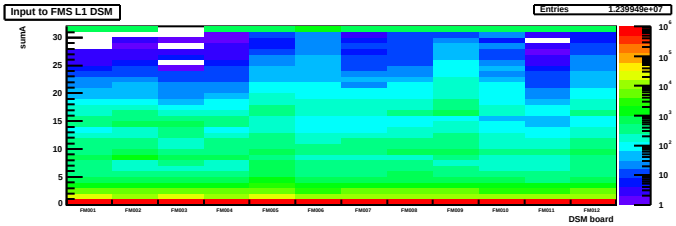

Input to FMS L1 DSM
Entries 2
sumBC - simulated
DSM board

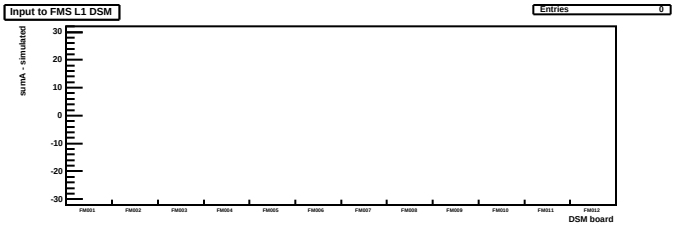

Input to FMS L1 DSM
Entries 1.239949e+07
sumB
DSM board

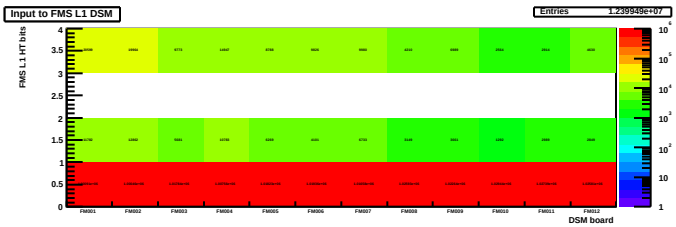

Input to FMS L1 DSM
Entries 2080
sumB - simulated
DSM board

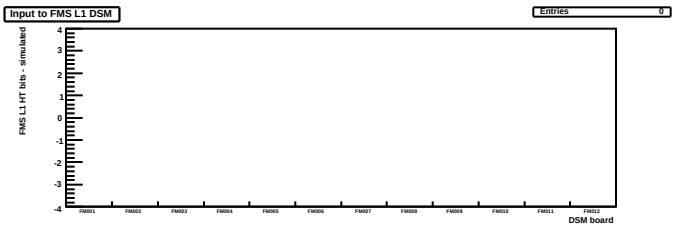

Input to FMS L1 DSM
Entries 1.239949e+07
sumCD
DSM board

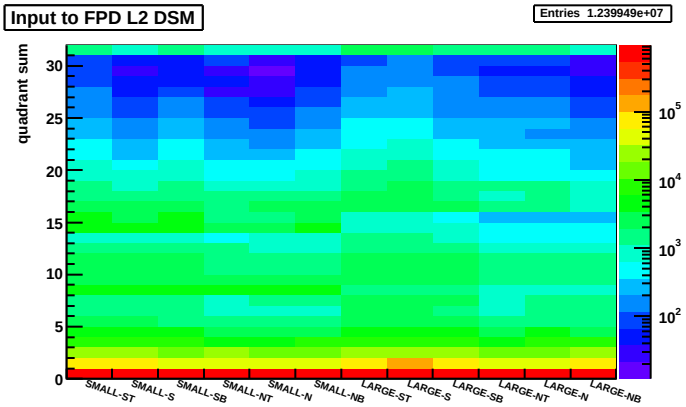

Input to FMS L1 DSM
Entries 16414
sumCD - simulated
DSM board

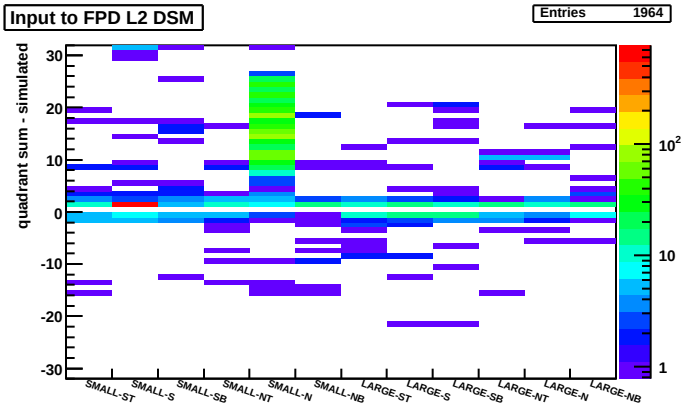

Input to FMS L1 DSM
Entries 1.239949e+07
sumA
DSM board

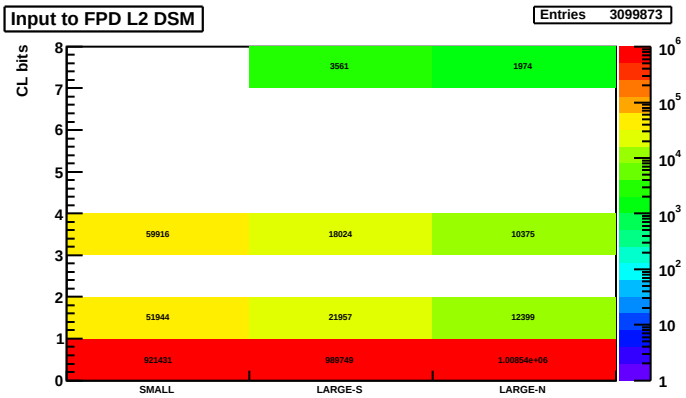

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

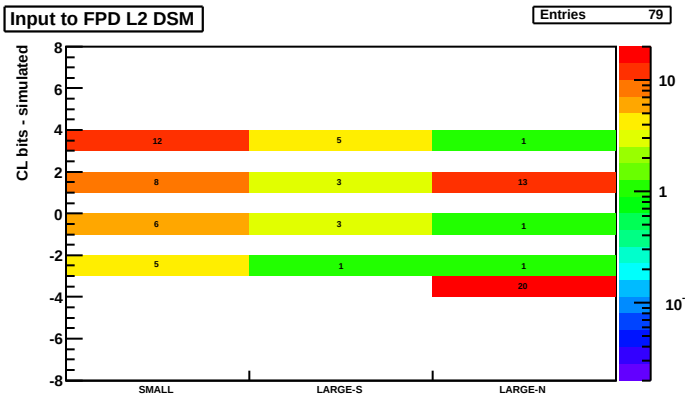

Input to FMS L1 DSM
Entries 1.239949e+07
FMS L1 HT bits
DSM board

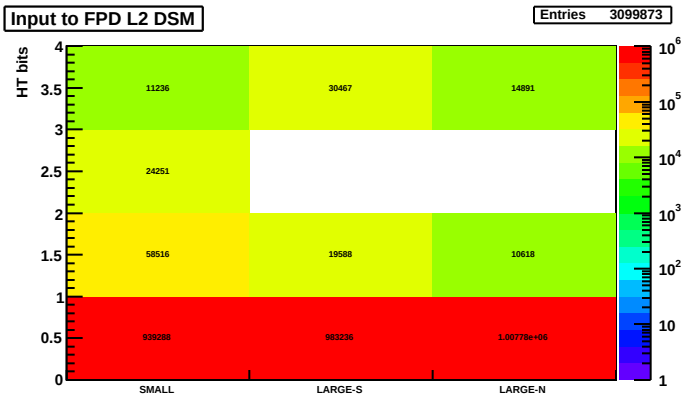

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

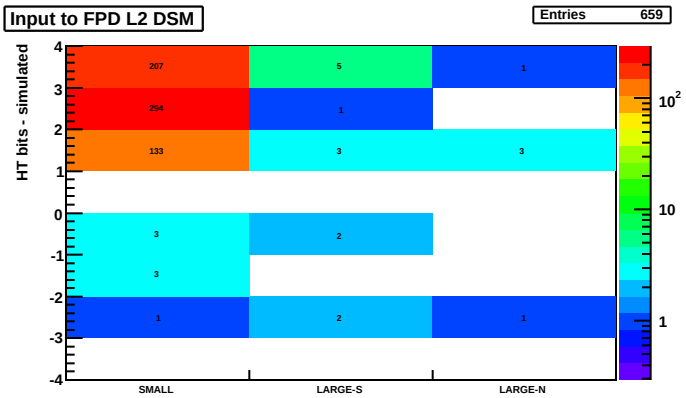

Input to FPD L2 DSM
Entries 1.239949e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

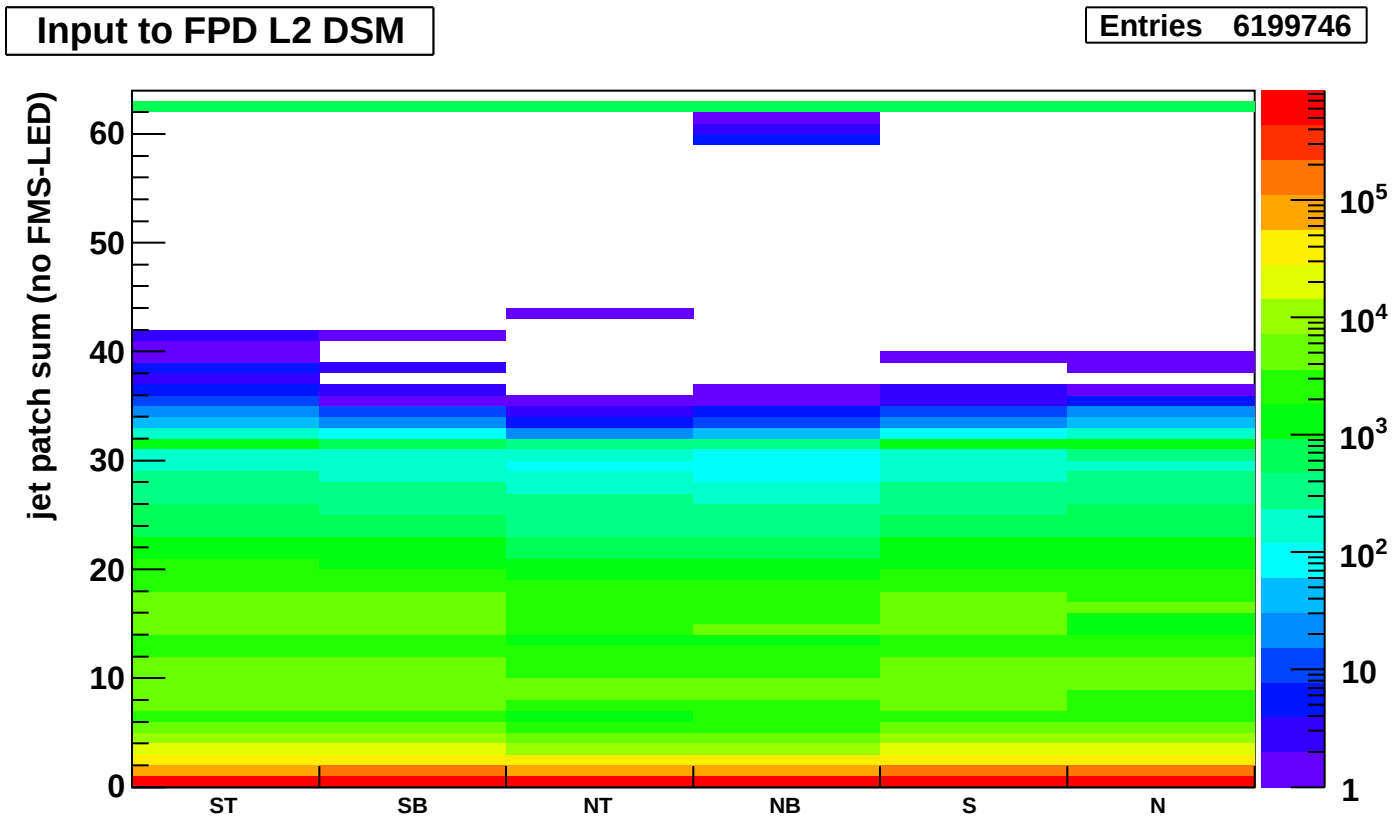

Input to FPD L2 DSM
Entries 1964
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

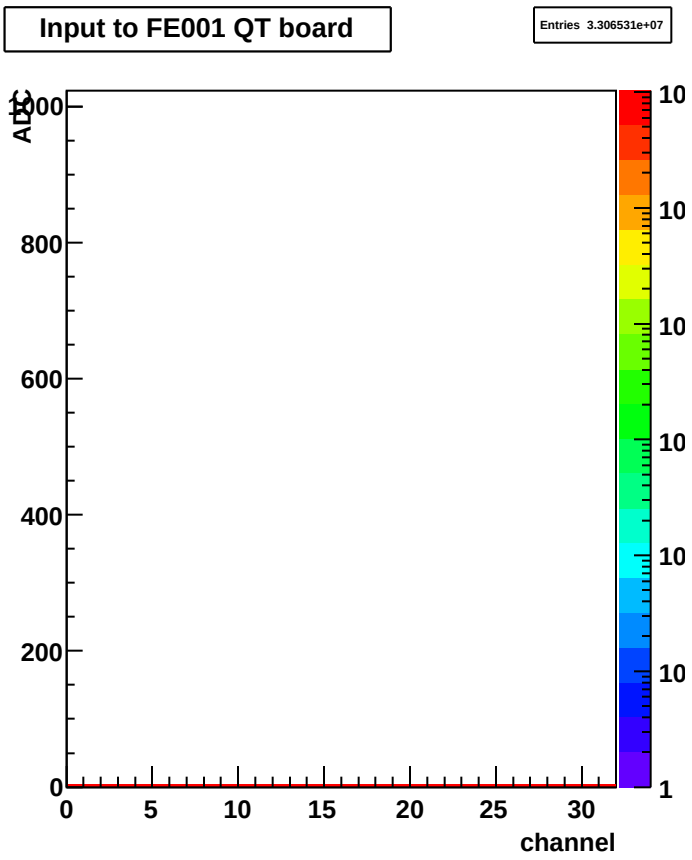

Input to FPD L2 DSM
Entries 3099873
CL bits
3561
1974
59916
18024
10375
51944
21957
12399
921431
989749
1.00854e+06
SMALL
LARGE-S
LARGE-N

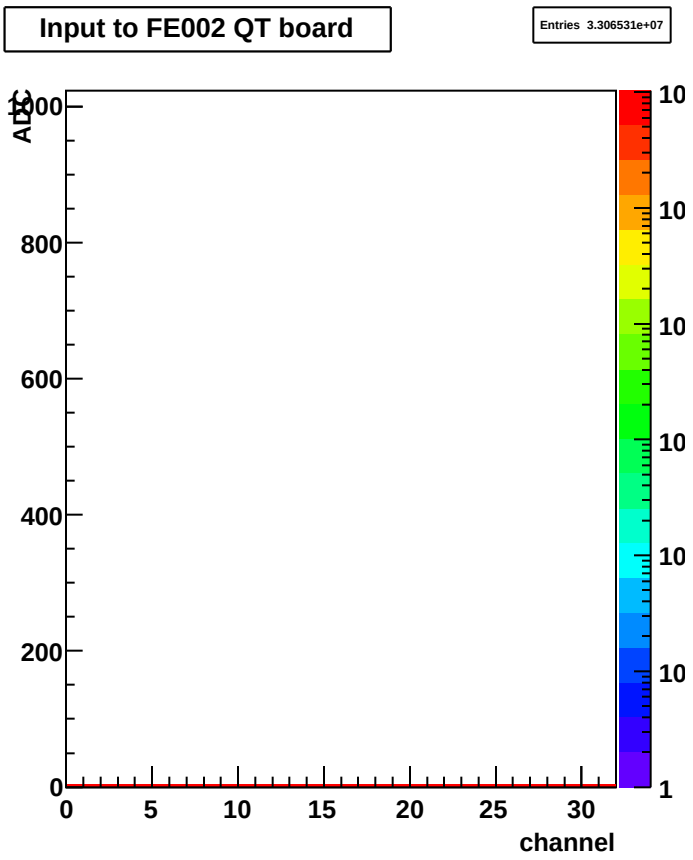

Input to FPD L2 DSM
Entries 79
CL bits - simulated
12
5
1
8
3
13
6
3
1
5
1
1
20
SMALL
LARGE-S
LARGE-N

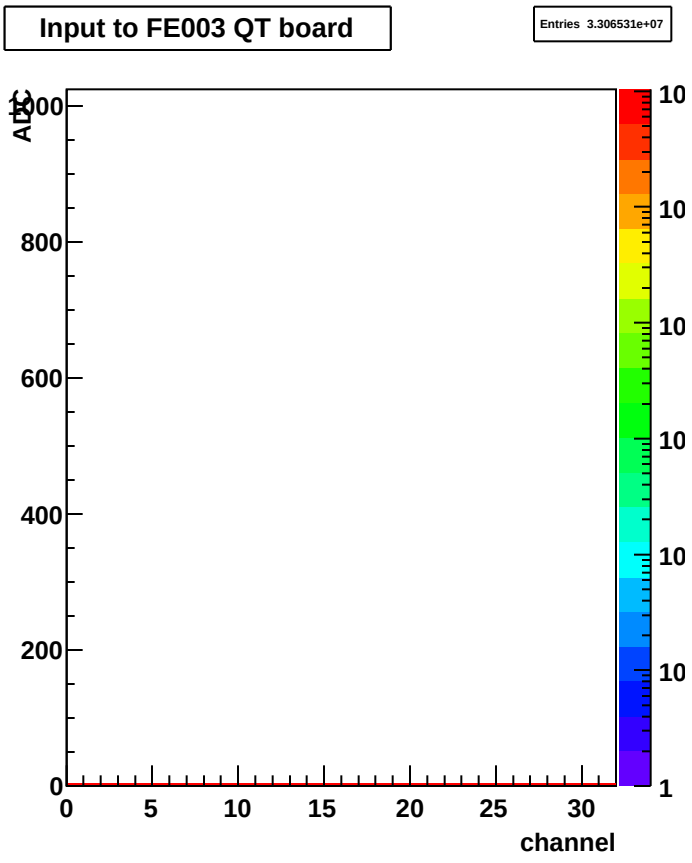

Input to FPD L2 DSM
Entries 3099873
HT bits
11236
30467
14891
24251
58516
19588
10618
939288
983236
1.00778e+06
SMALL
LARGE-S
LARGE-N

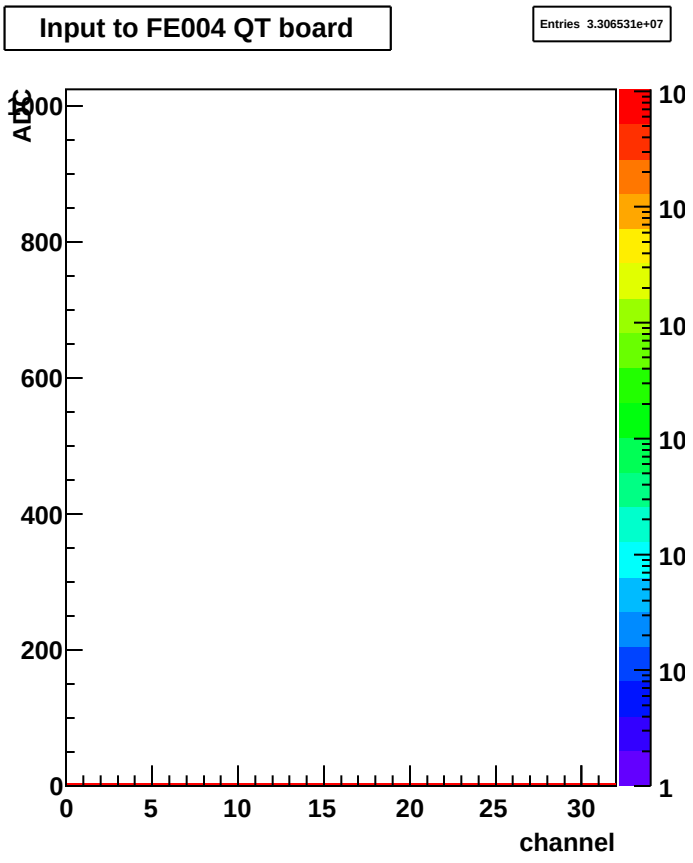

Input to FPD L2 DSM
Entries 659
HT bits - simulated
207
5
1
294
1
133
3
3
3
2
3
1
2
1
SMALL
LARGE-S
LARGE-N

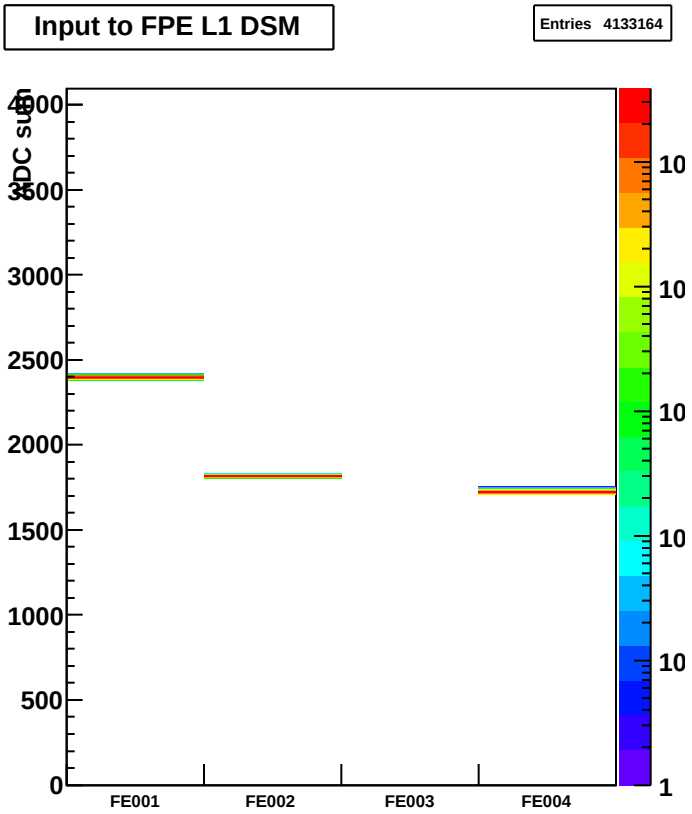

Input to FPD L2 DSM
Entries 6199746
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

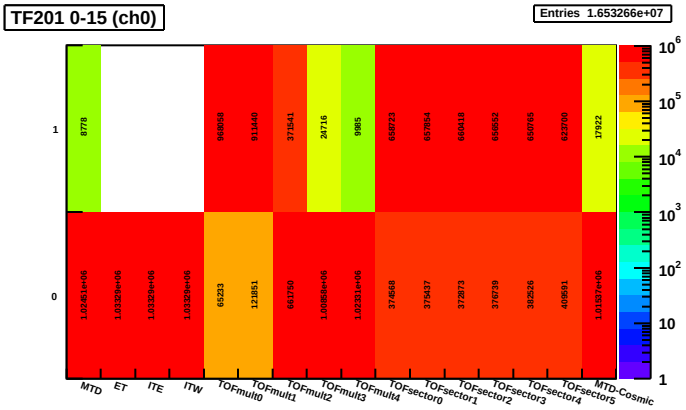

Input to FE001 QT board
Entries 3.306531e+07
ADC
channel

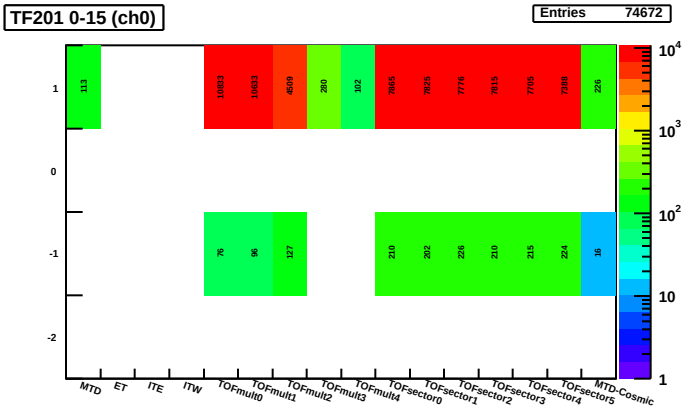

Input to FE002 QT board
Entries 3.306531e+07
ADC
channel

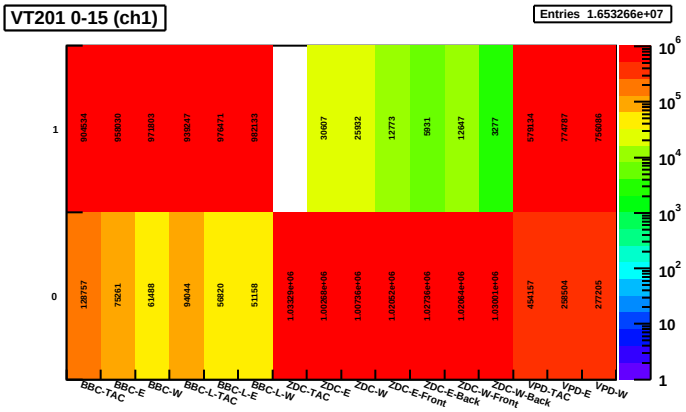

Input to FE003 QT board
Entries 3.306531e+07
ADC
channel

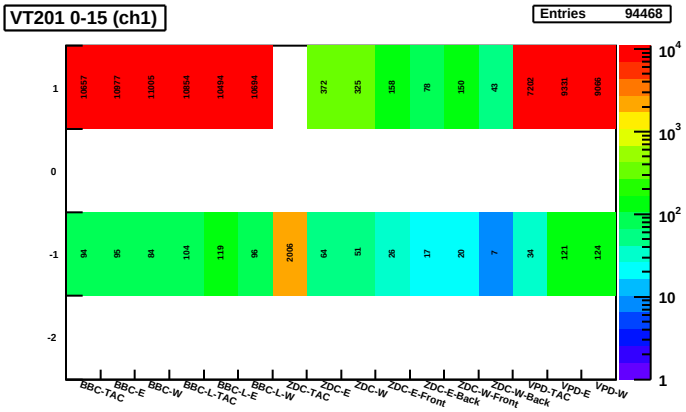

Input to FE004 QT board
Entries 3.306531e+07
ADC
channel

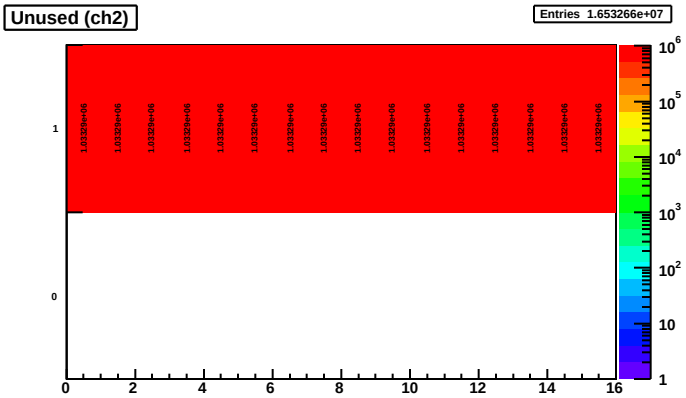

Input to FPE L1 DSM
Entries 4133164
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

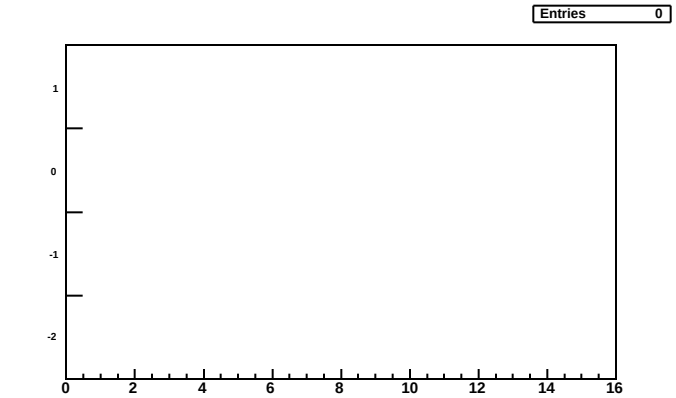

TF201 0-15 (ch0)
Entries 1.653266e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

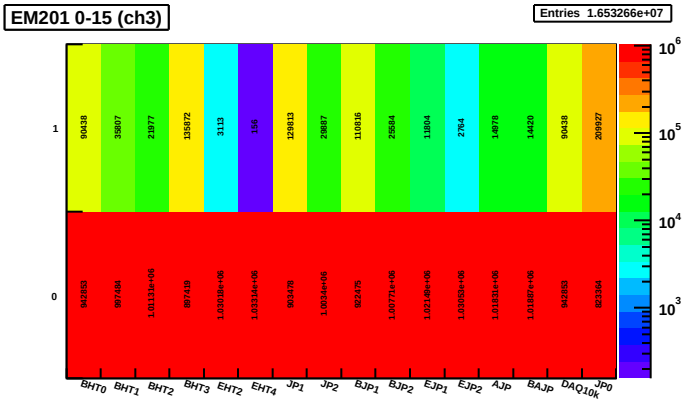

TF201 0-15 (ch0)
Entries 74672
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

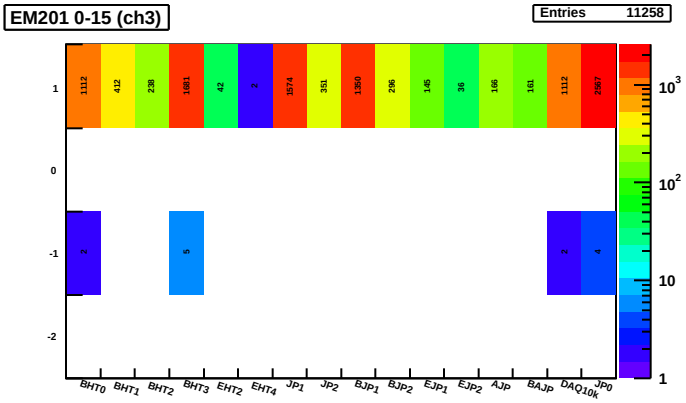

VT201 0-15 (ch1)
Entries 1.653266e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 94468
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

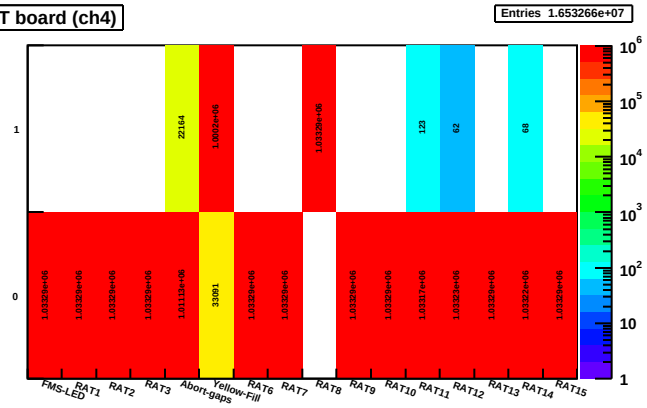

Unused (ch2)
Entries 1.653266e+07

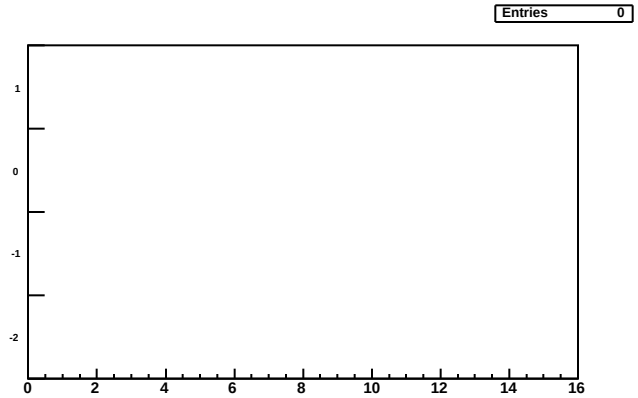

Entries 0


EM201 0-15 (ch3)
Entries 1.653266e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

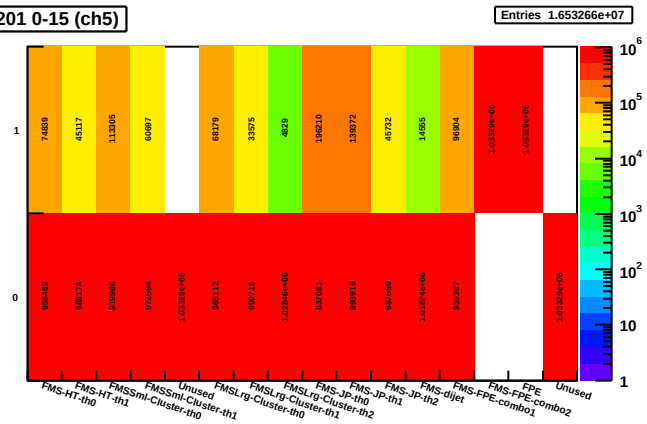

EM201 0-15 (ch3)
Entries 11258
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

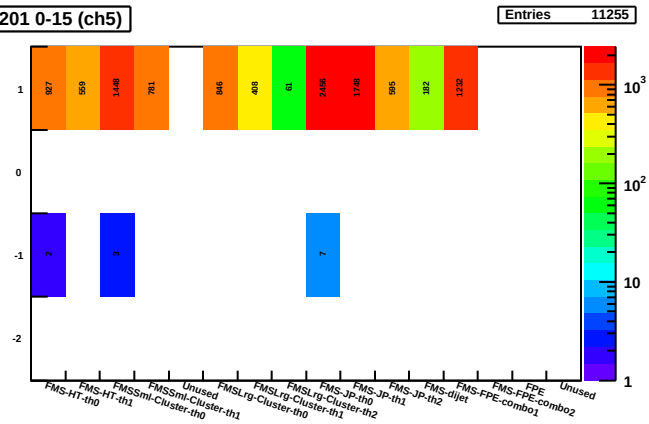




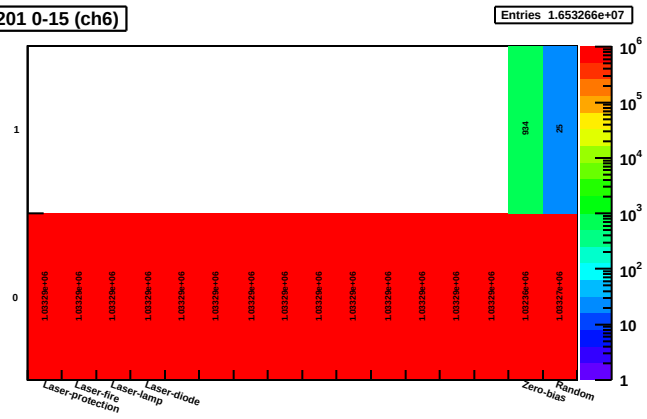
FP201 0-15 (ch5)

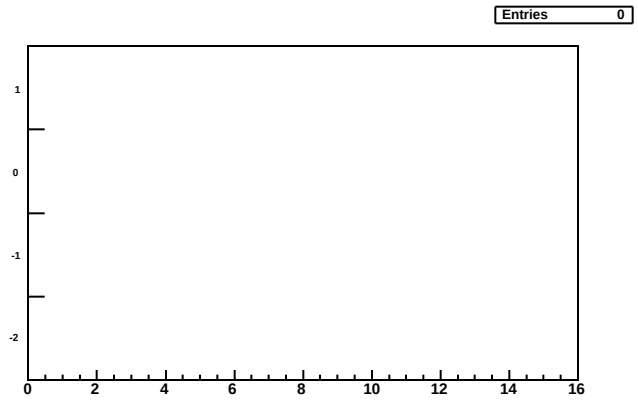
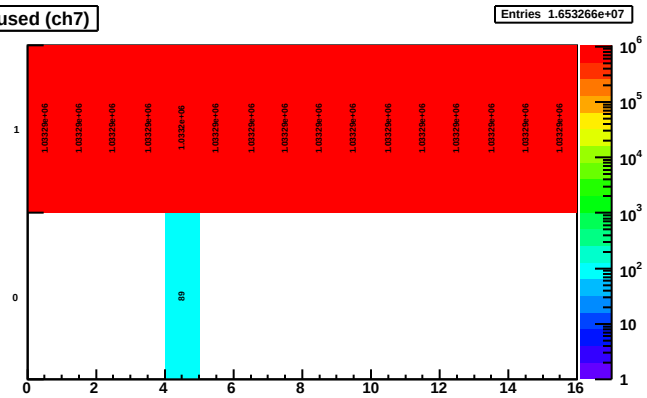
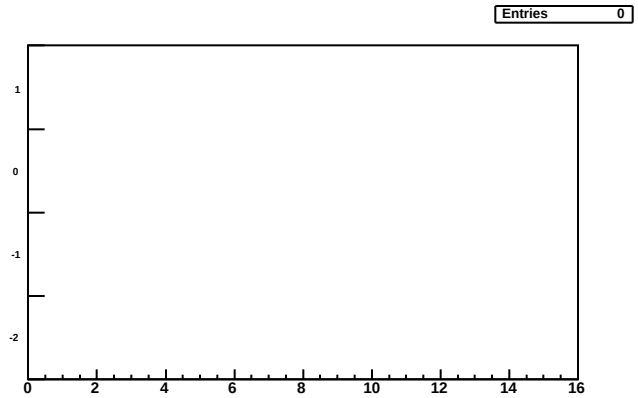


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)