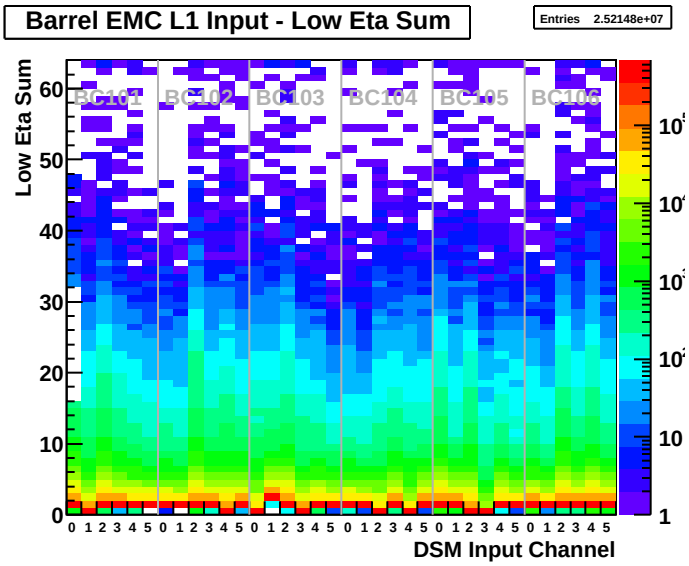
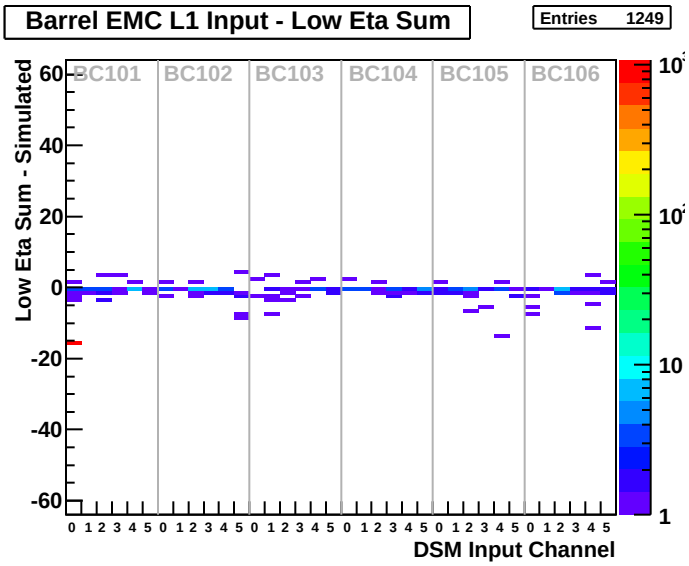
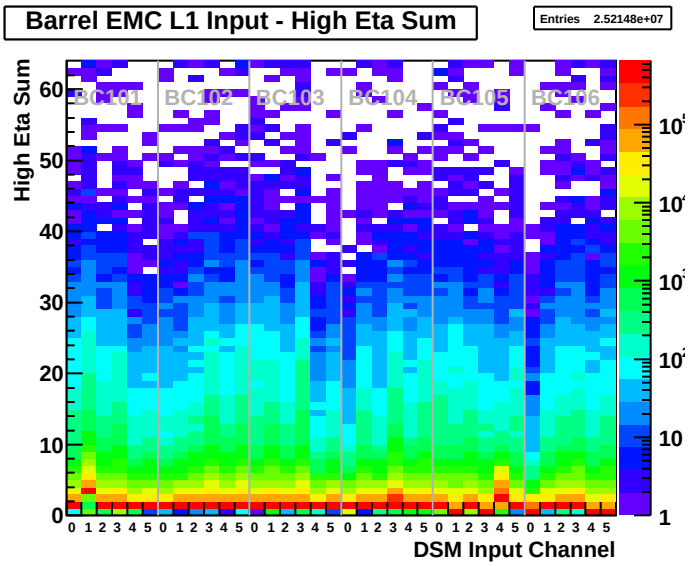
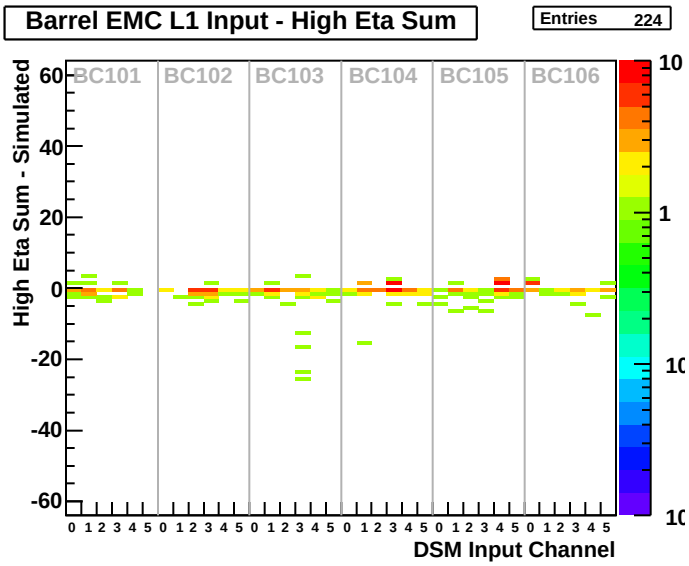
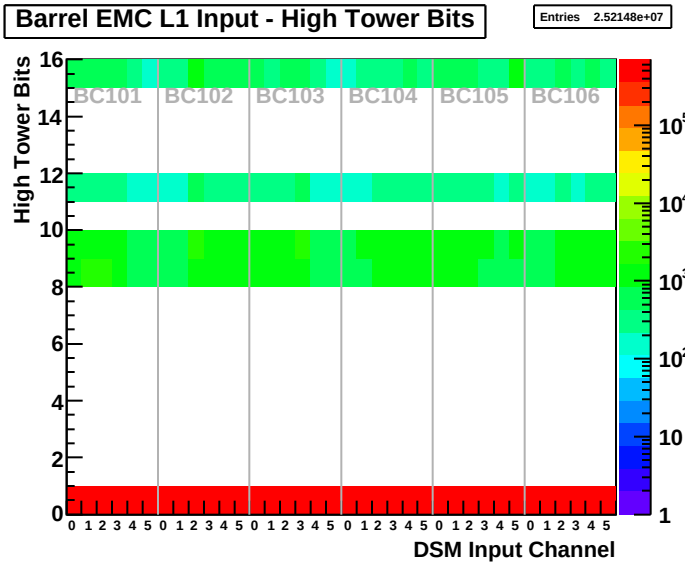
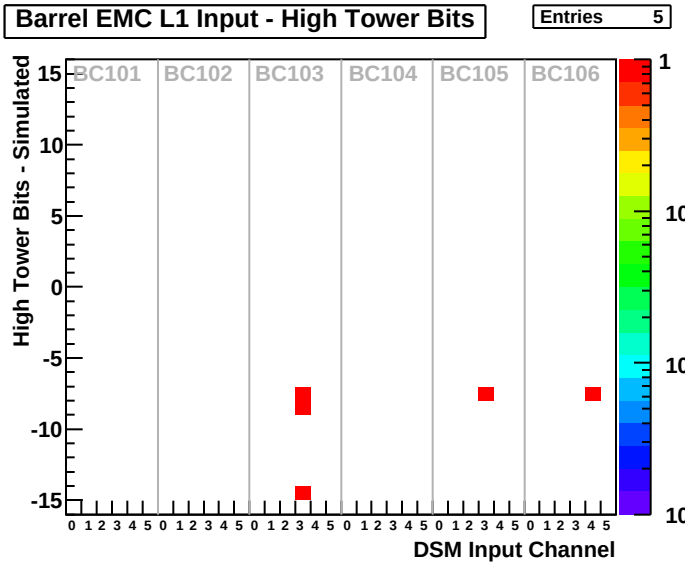

Barrel EMC L1 Input - Low Eta Sum
Entries 2.52148e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

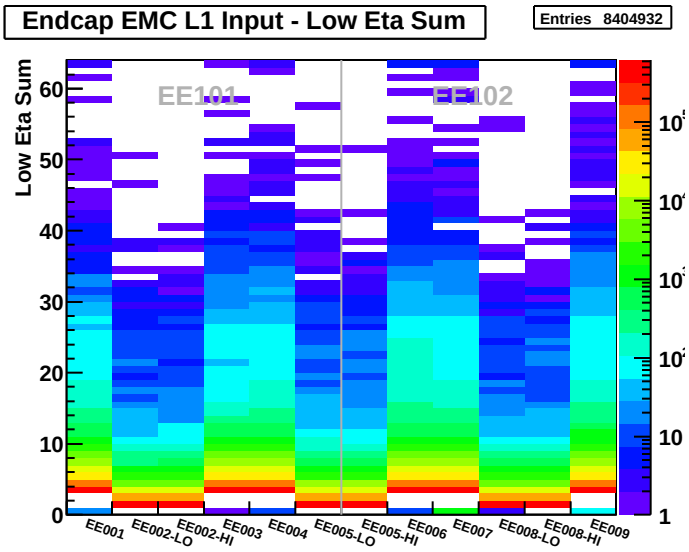

Barrel EMC L1 Input - Low Eta Sum
Entries 1249
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

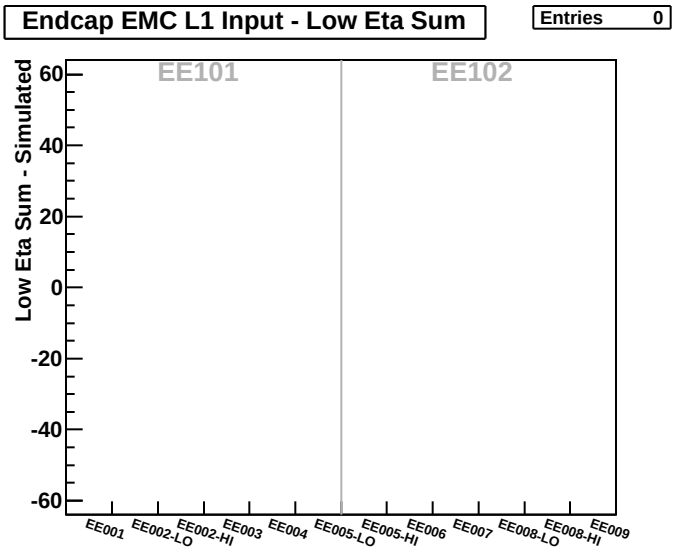

Barrel EMC L1 Input - High Eta Sum
Entries 2.52148e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

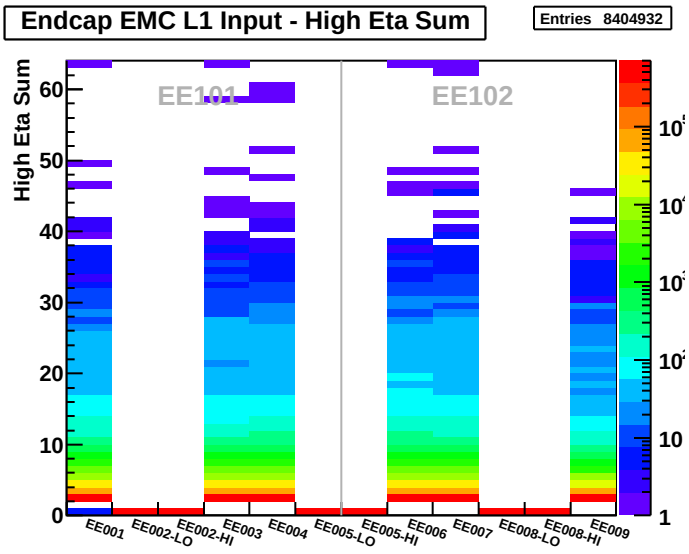

Barrel EMC L1 Input - High Eta Sum
Entries 224
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

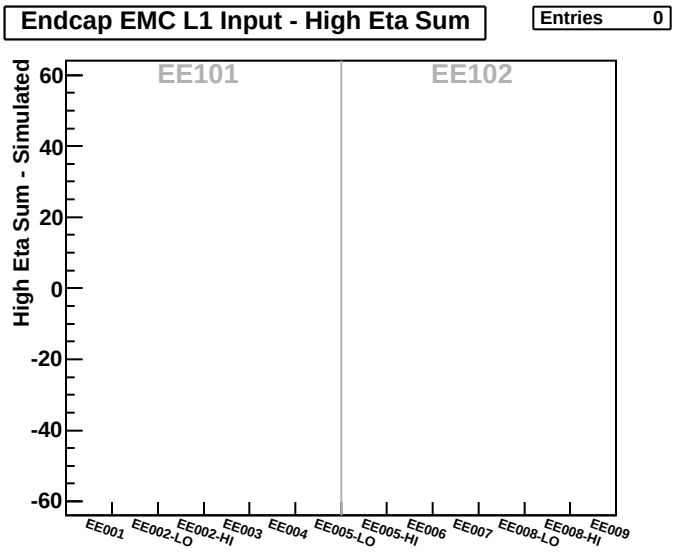

Barrel EMC L1 Input - High Tower Bits
Entries 2.52148e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

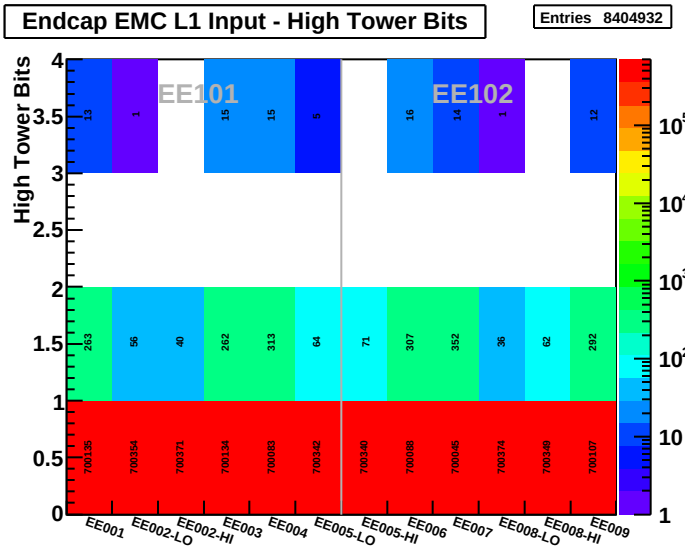

Barrel EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

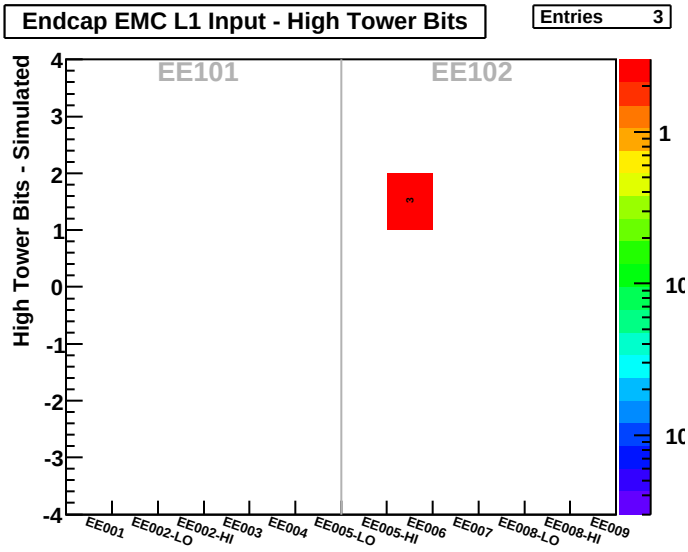

Endcap EMC L1 Input - Low Eta Sum
Entries 8404932
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

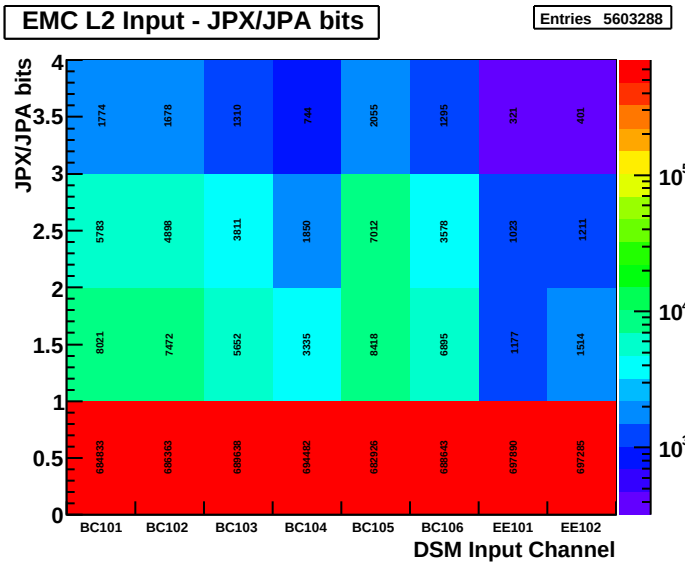

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

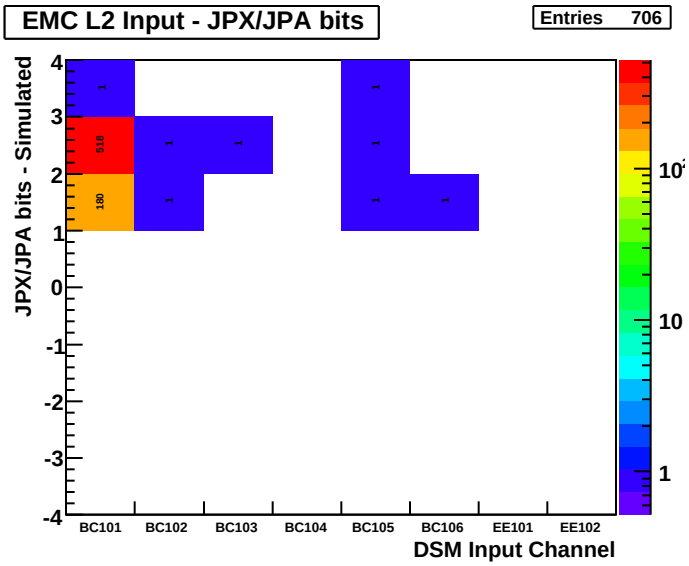

Endcap EMC L1 Input - High Eta Sum
Entries 8404932
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

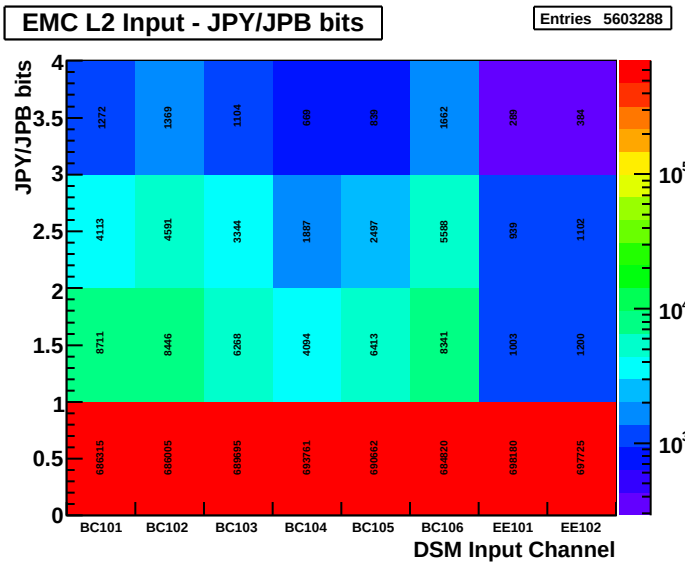

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

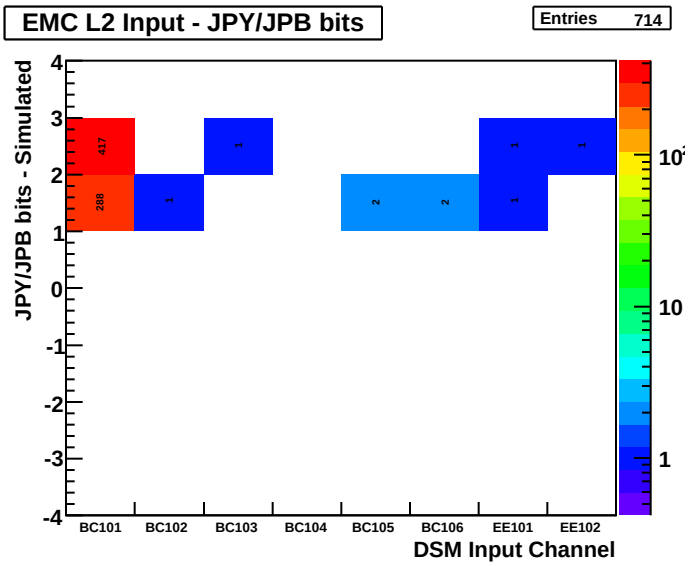

Endcap EMC L1 Input - High Tower Bits
Entries 8404932
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

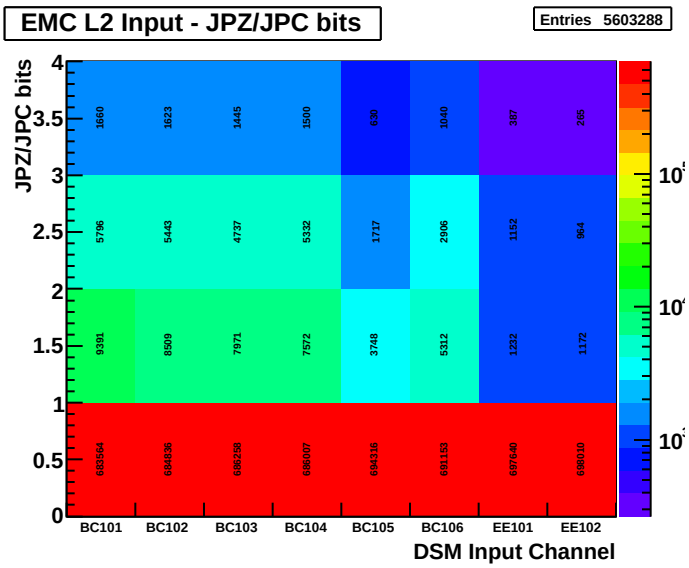

Endcap EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

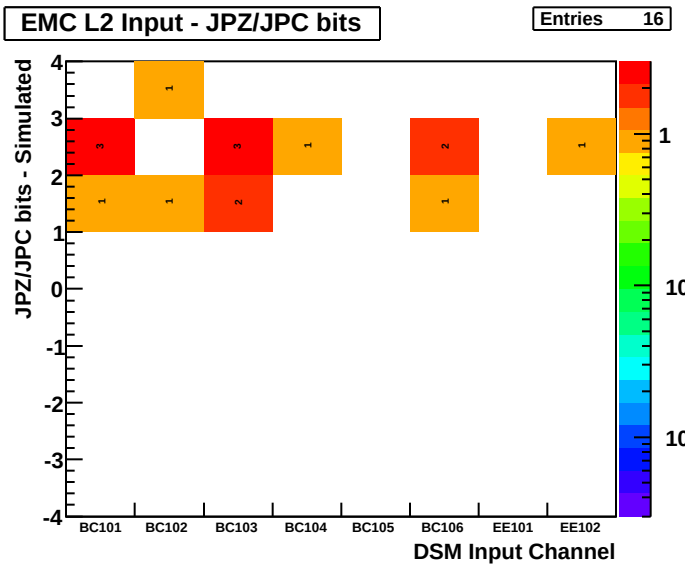

EMC L2 Input - JPX/JPA bits
Entries 5603288
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1774 1678 1310 744 2055 1295 321 401
5783 4898 3911 1850 7012 3578 1023 1211
8021 7472 5652 3335 8418 6895 1177 1514
684833 685363 689638 694482 682936 688643 697890 697285

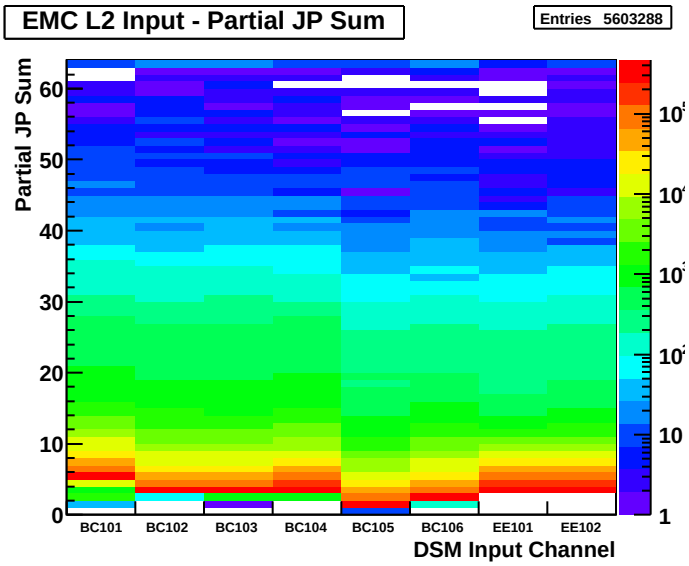

EMC L2 Input - JPX/JPA bits
Entries 706
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
518 1 1 1
180 1 1 1

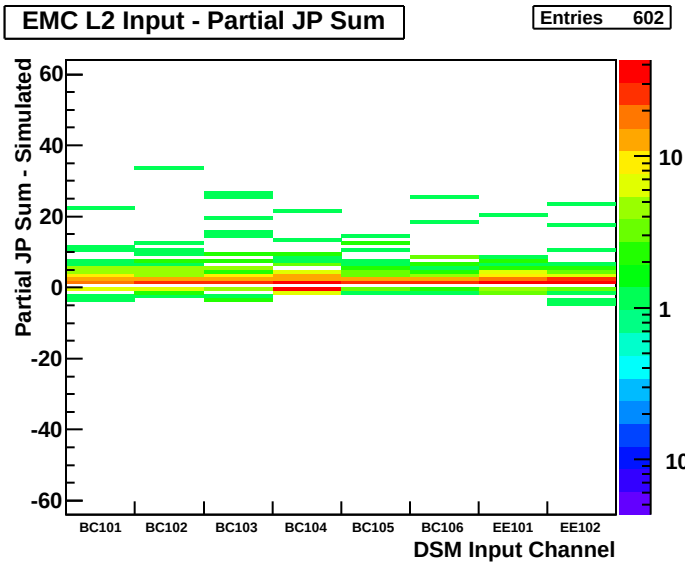

EMC L2 Input - JPY/JPB bits
Entries 5603288
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1272 1369 1104 668 839 1662 288 384
4113 4591 3344 1887 2497 5588 939 1102
8711 8446 6268 4094 6413 8341 1003 1200
686315 686005 689695 693761 690662 684820 698180 697725

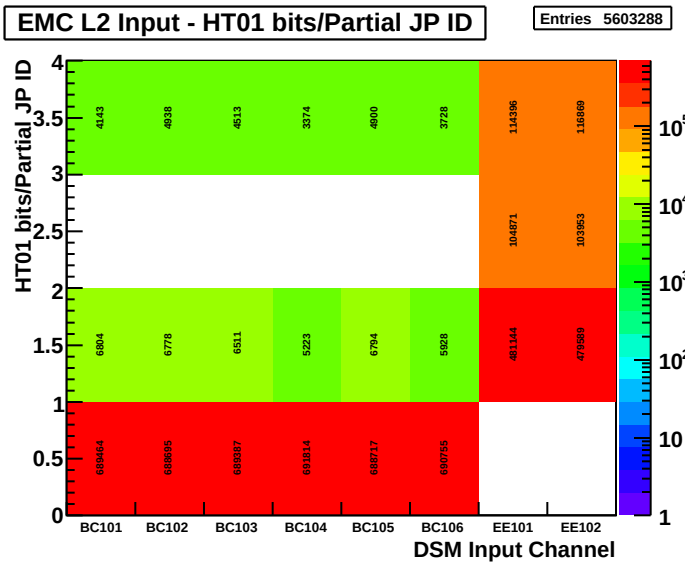

EMC L2 Input - JPY/JPB bits
Entries 714
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417 1 1 1
288 1 2 2 1

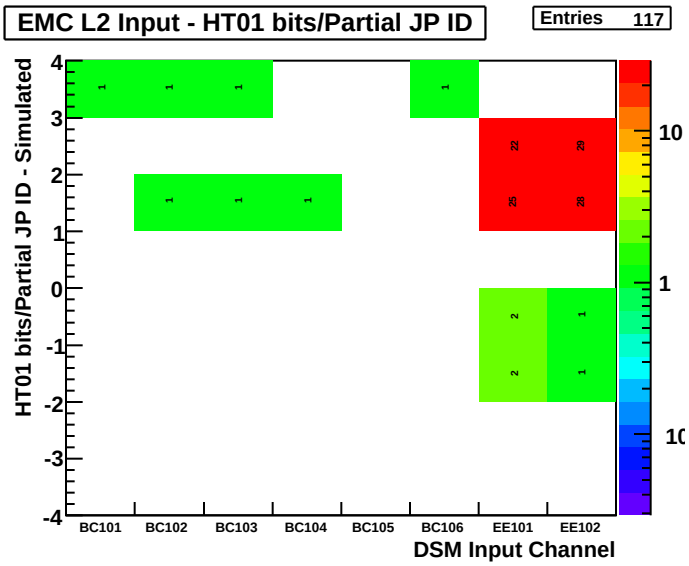

EMC L2 Input - JPZ/JPC bits
Entries 5603288
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1660 1623 1445 1500 630 1040 387 265
5796 5443 4737 5332 1717 2906 1152 964
9391 8509 7971 7572 3748 5312 1232 1172
683564 684336 686259 686007 694316 691153 697640 698010

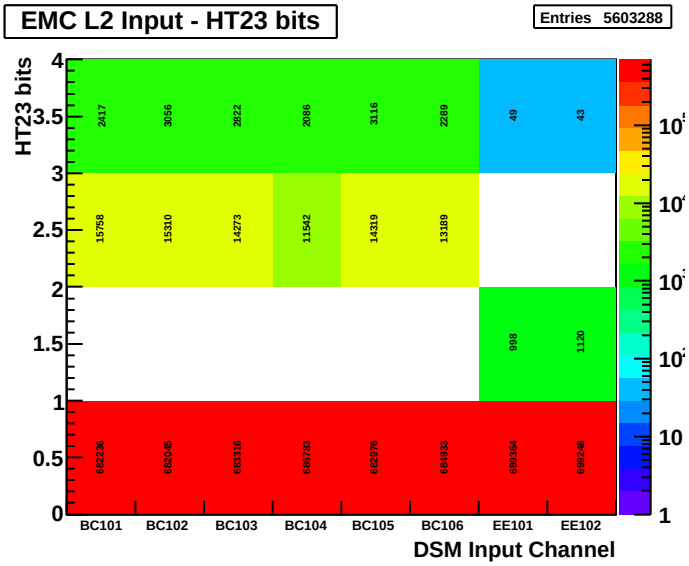

EMC L2 Input - JPZ/JPC bits
Entries 16
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
3 3 1 2 1
1 1 2 1

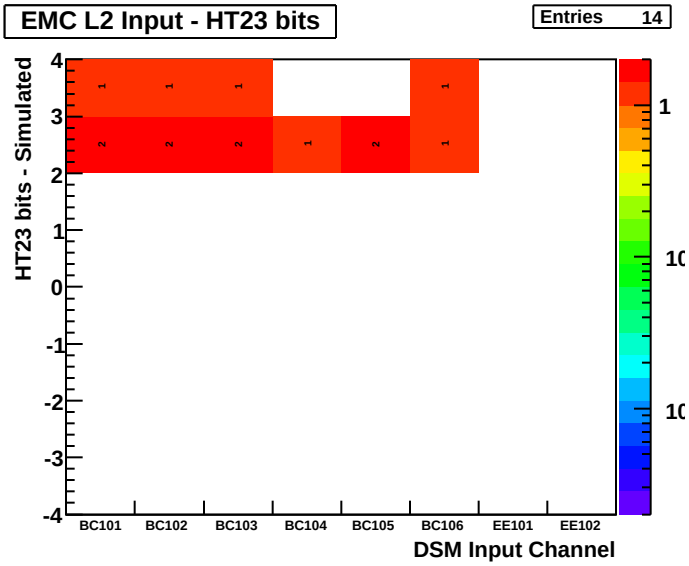

EMC L2 Input - Partial JP Sum
Entries 5603288
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

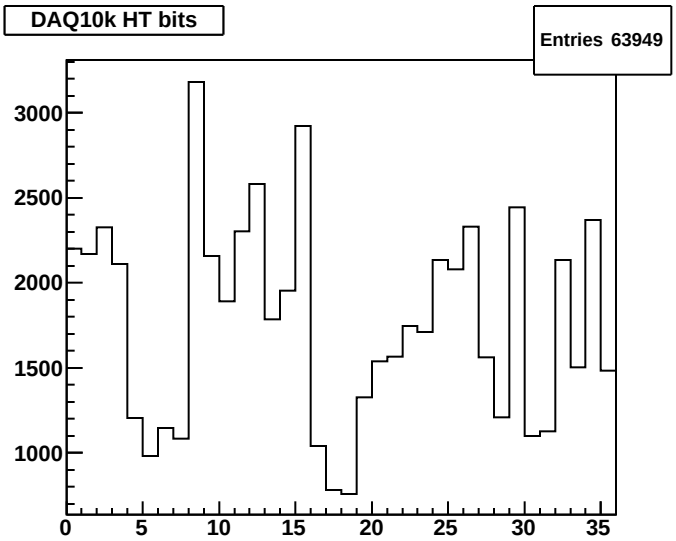

EMC L2 Input - Partial JP Sum
Entries 602
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

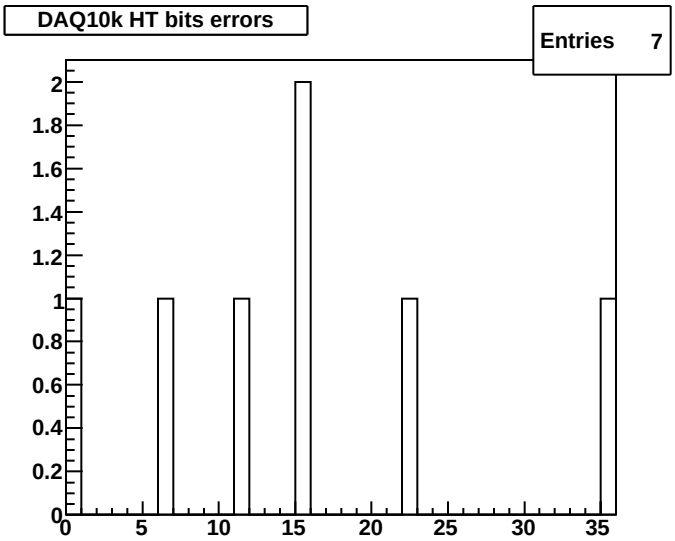

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5603288
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

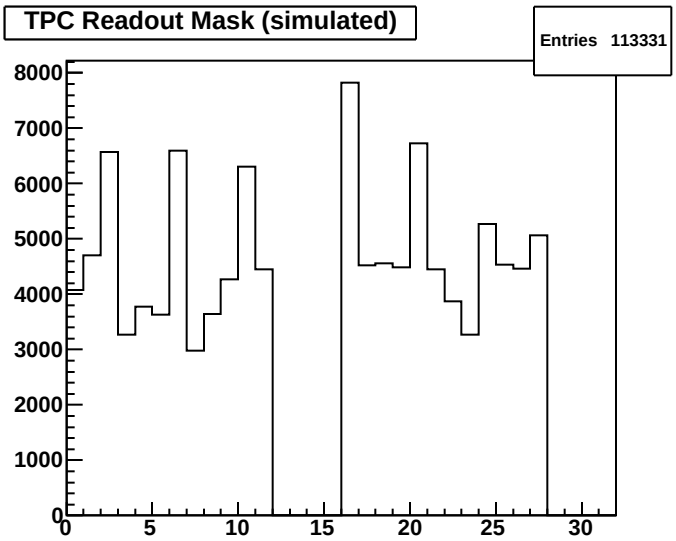

EMC L2 Input - HT01 bits/Partial JP ID
Entries 117
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

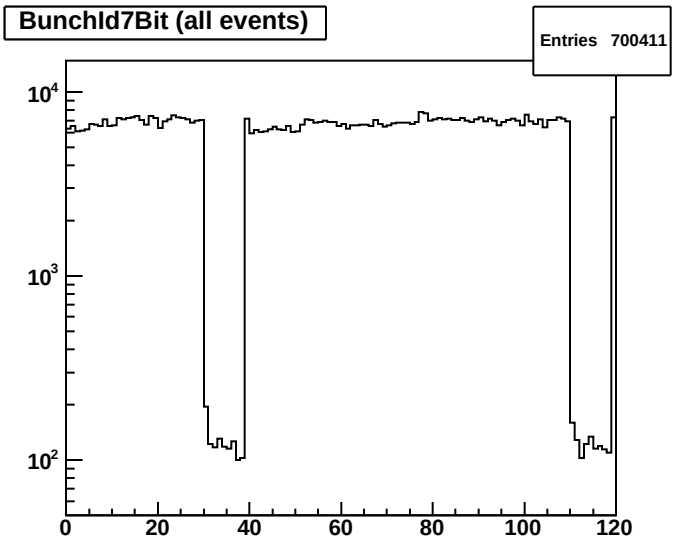

EMC L2 Input - HT23 bits
Entries 5603288
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

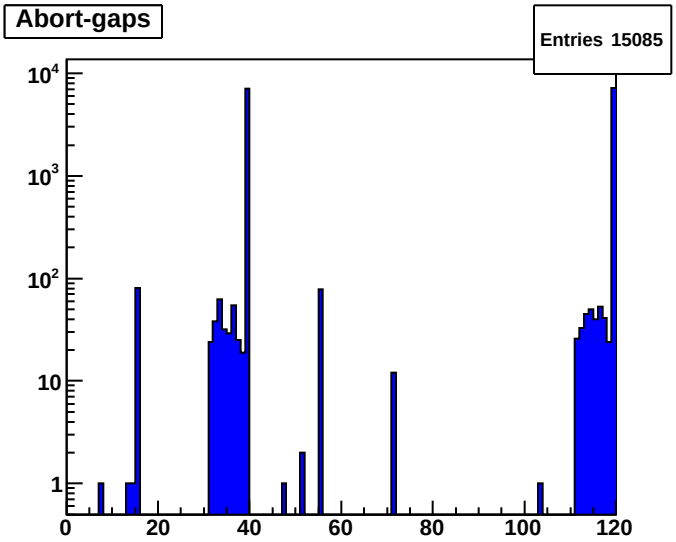

EMC L2 Input - HT23 bits
Entries 14
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

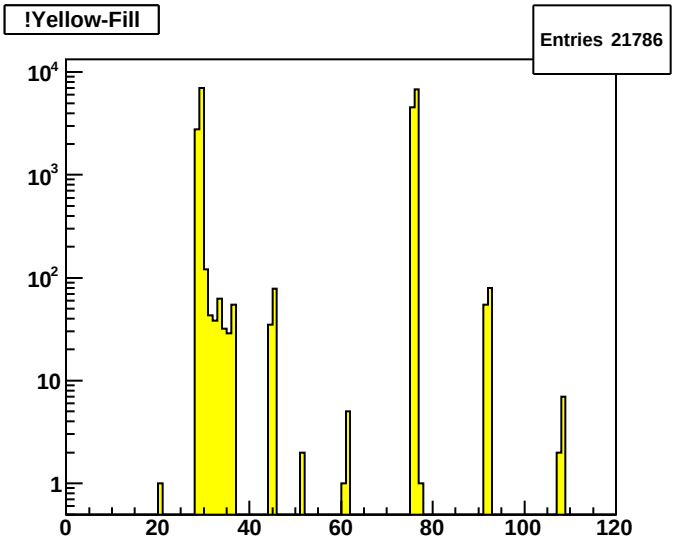

DAQ10k HT bits
Entries 63949

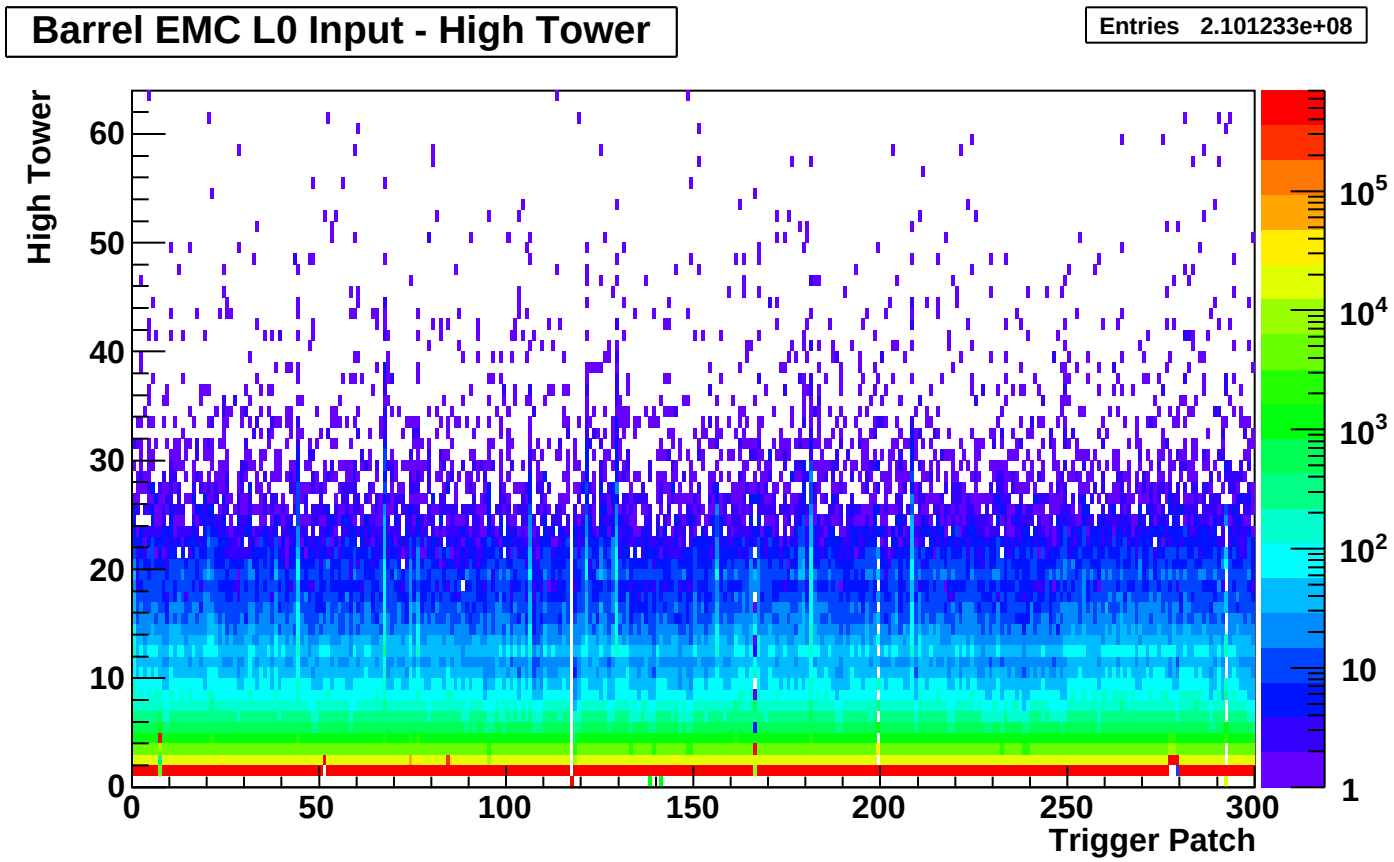

DAQ10k HT bits errors
Entries 7

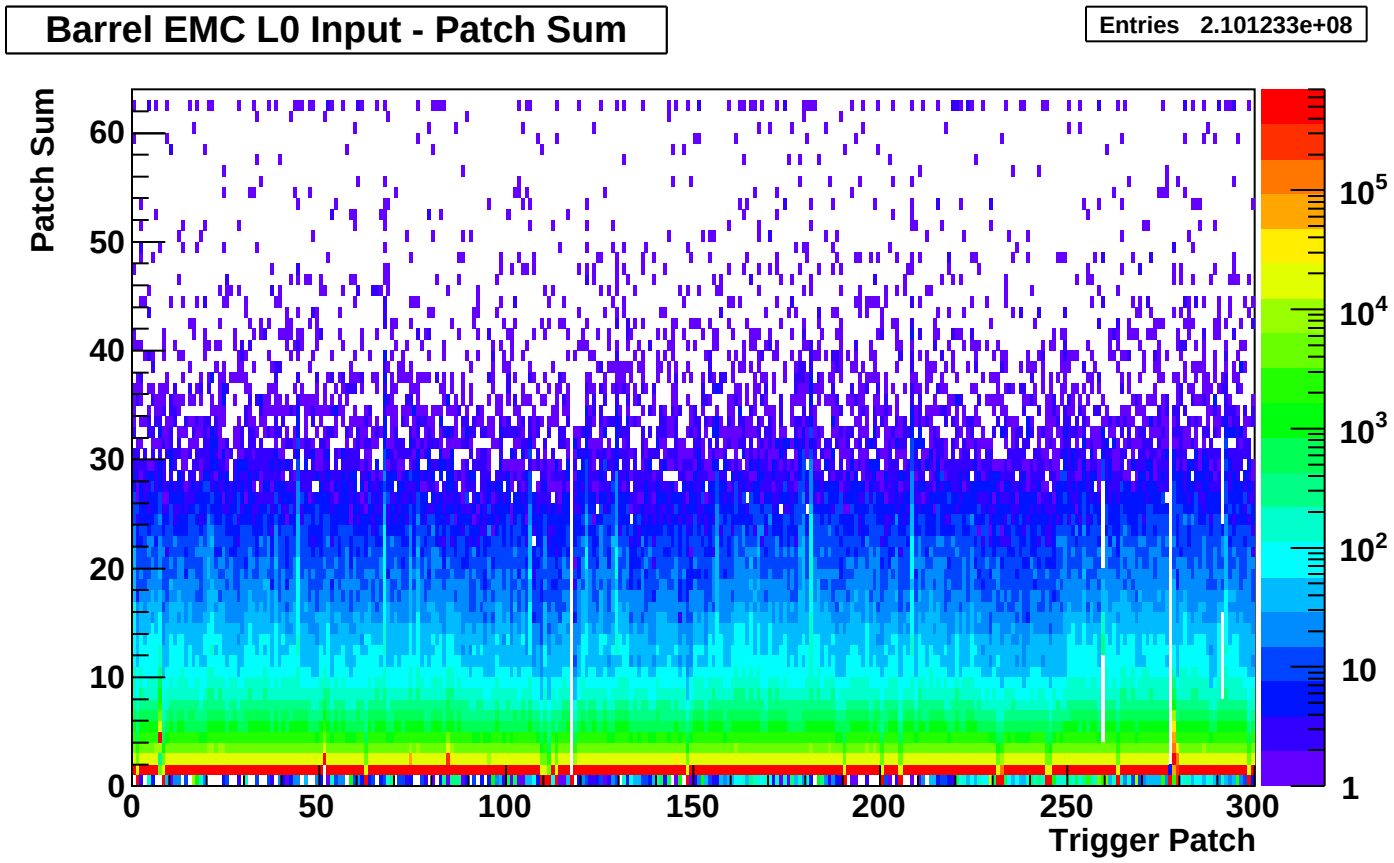

TPC Readout Mask (simulated)
Entries 113331

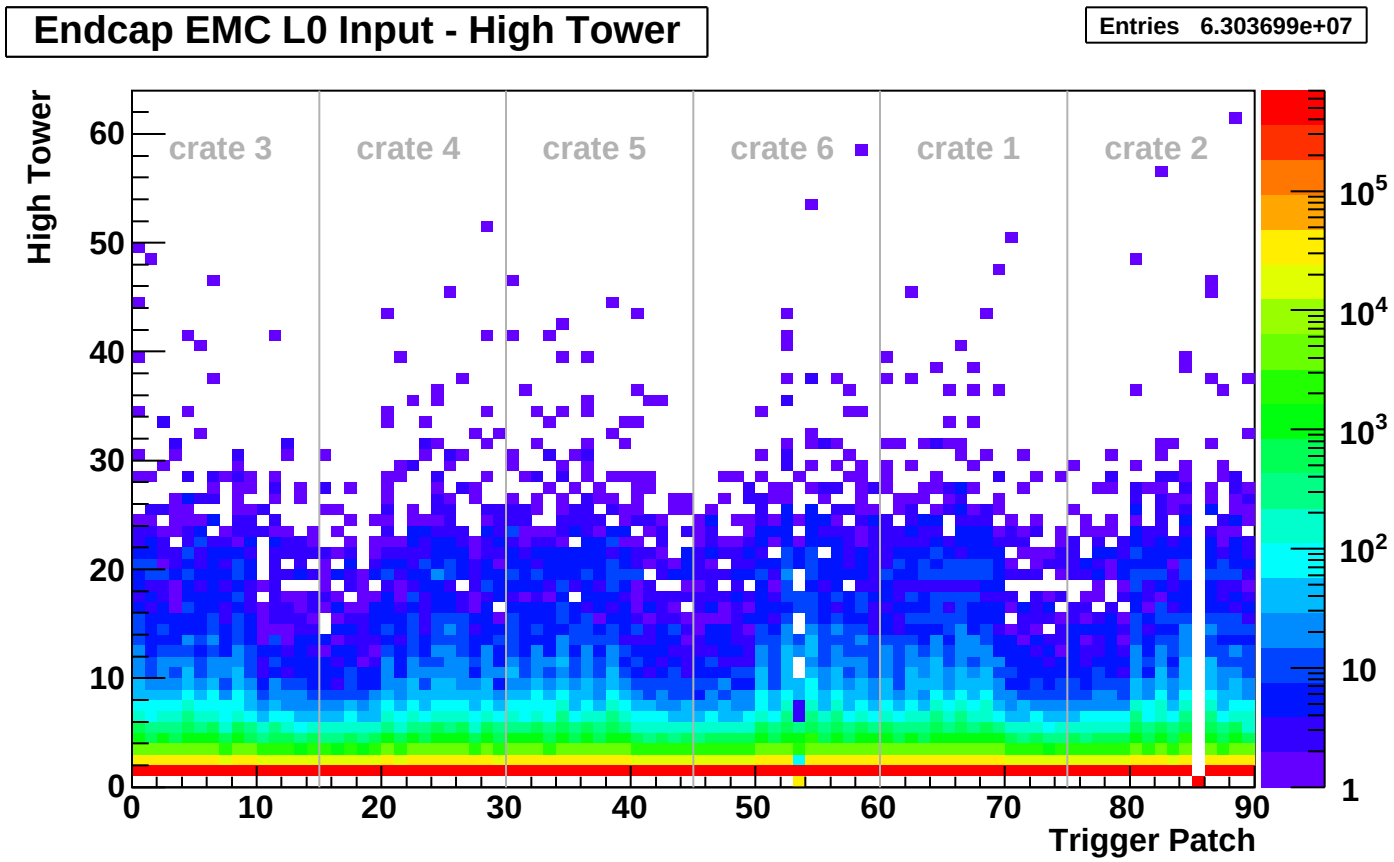

BunchId7Bit (all events)
Entries 700411

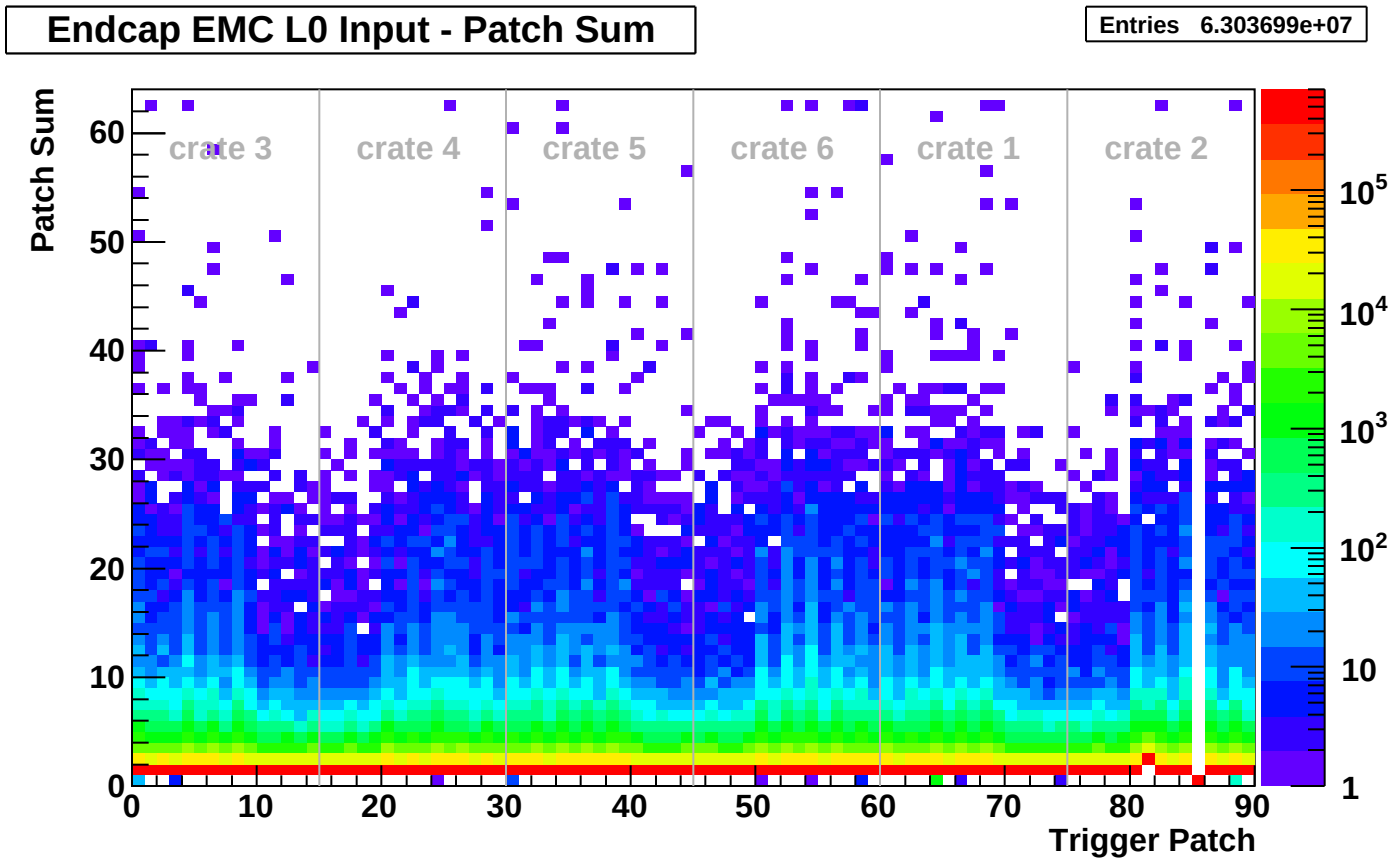

Abort-gaps
Entries 15085

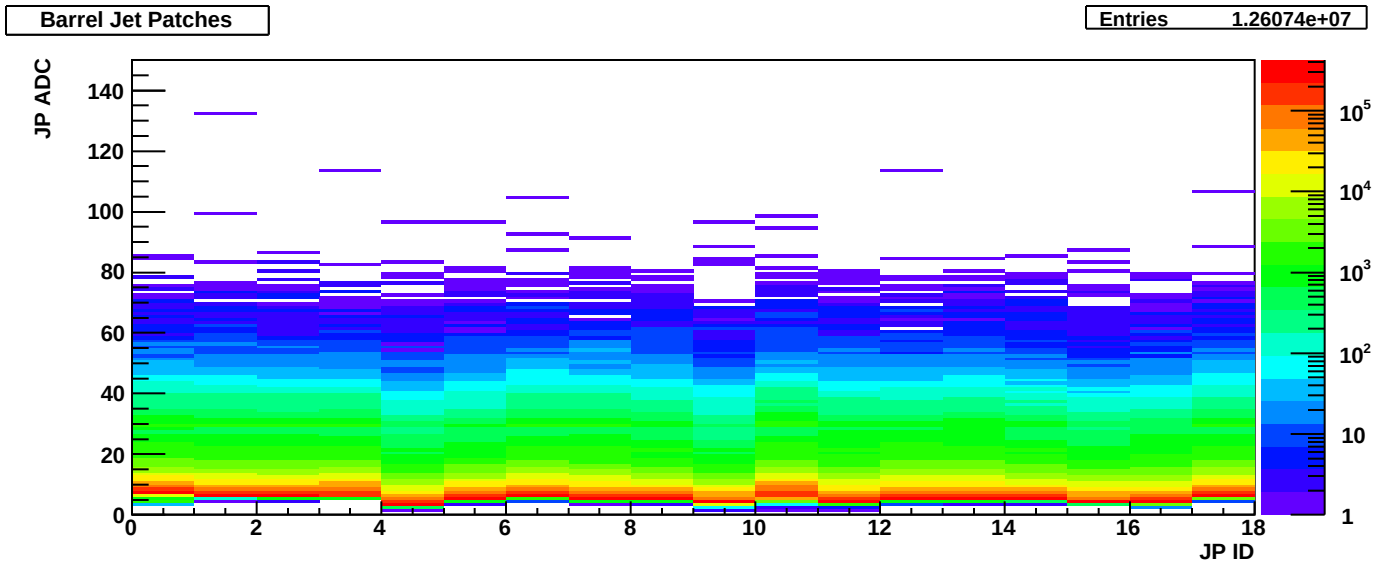

!Yellow-Fill
Entries 21786

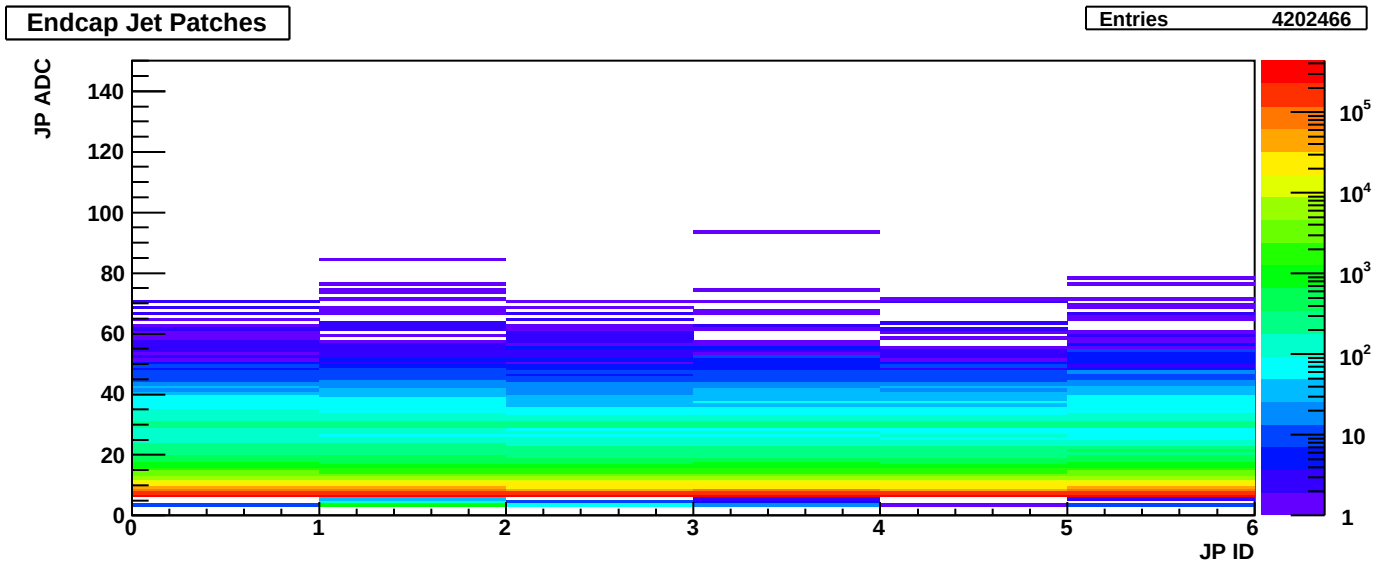

Barrel EMC L0 Input - High Tower
Entries 2.101233e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

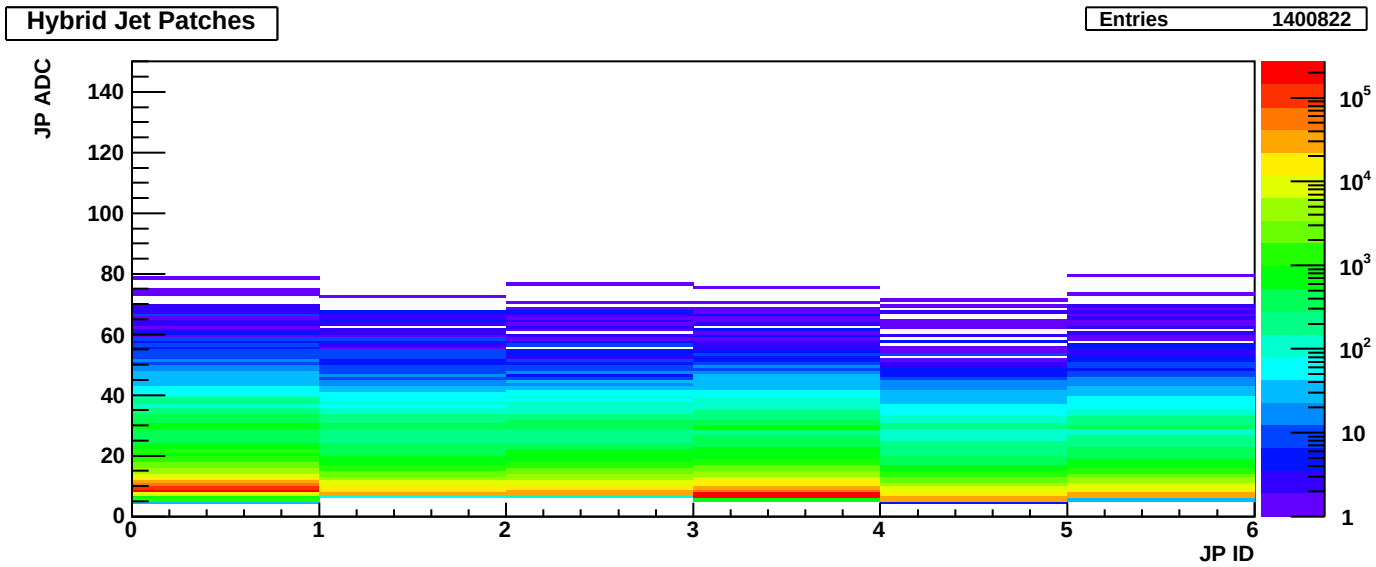

Barrel EMC L0 Input - Patch Sum
Entries 2.101233e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

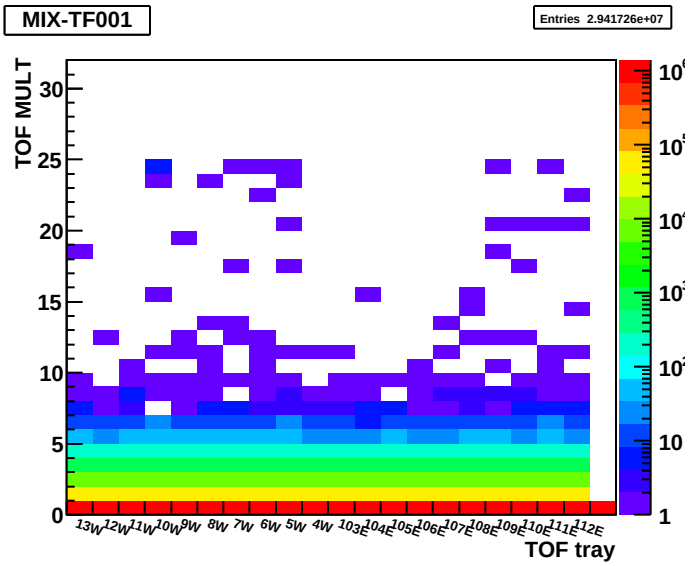

Endcap EMC L0 Input - High Tower
Entries 6.303699e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

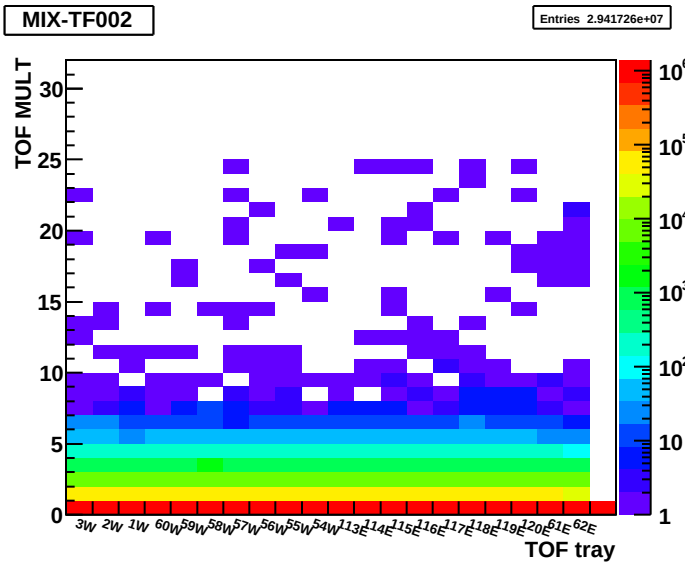

Endcap EMC L0 Input - Patch Sum
Entries 6.303699e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

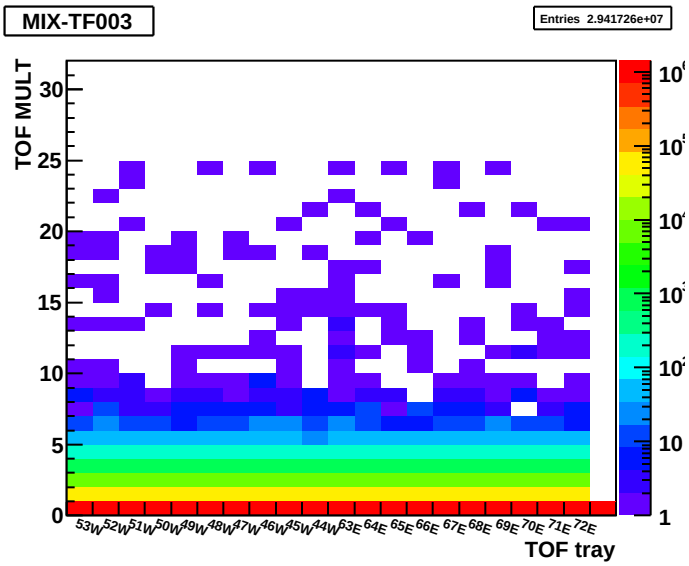

Barrel Jet Patches
Entries 1.26074e+07
JP ADC
JP ID

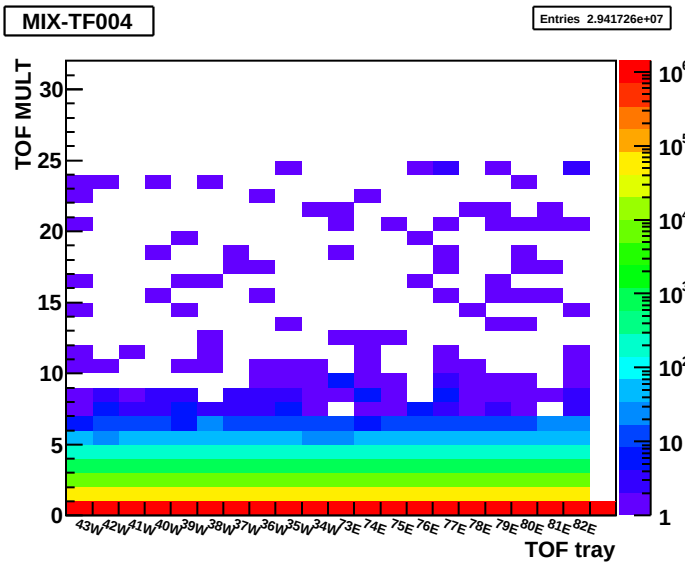

Endcap Jet Patches
Entries 4202466
JP ADC
JP ID

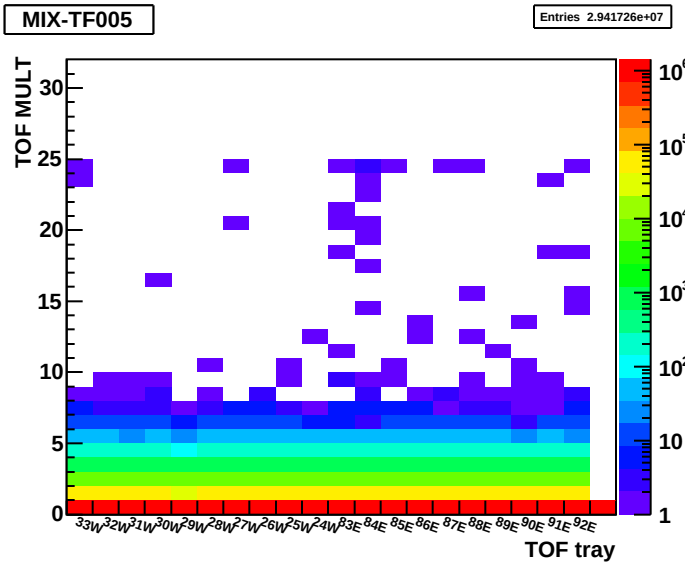

Hybrid Jet Patches
Entries 1400822
JP ADC
JP ID

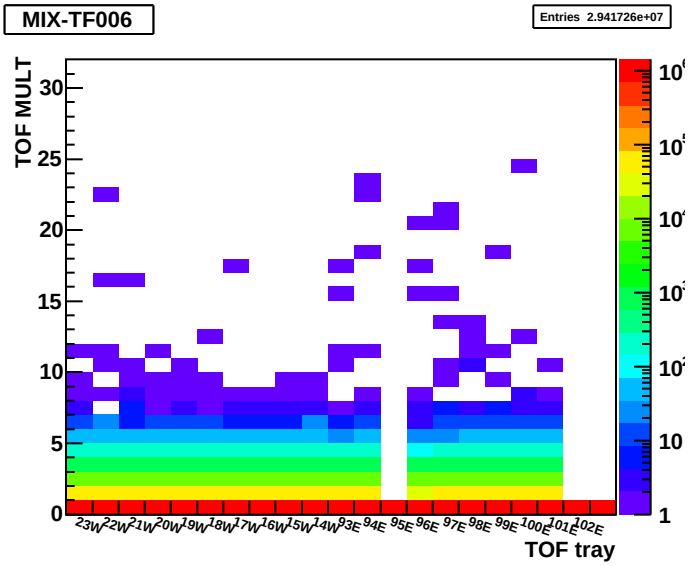

MIX-TF001
Entries 2.941726e+07
TOF MULT
TOF tray

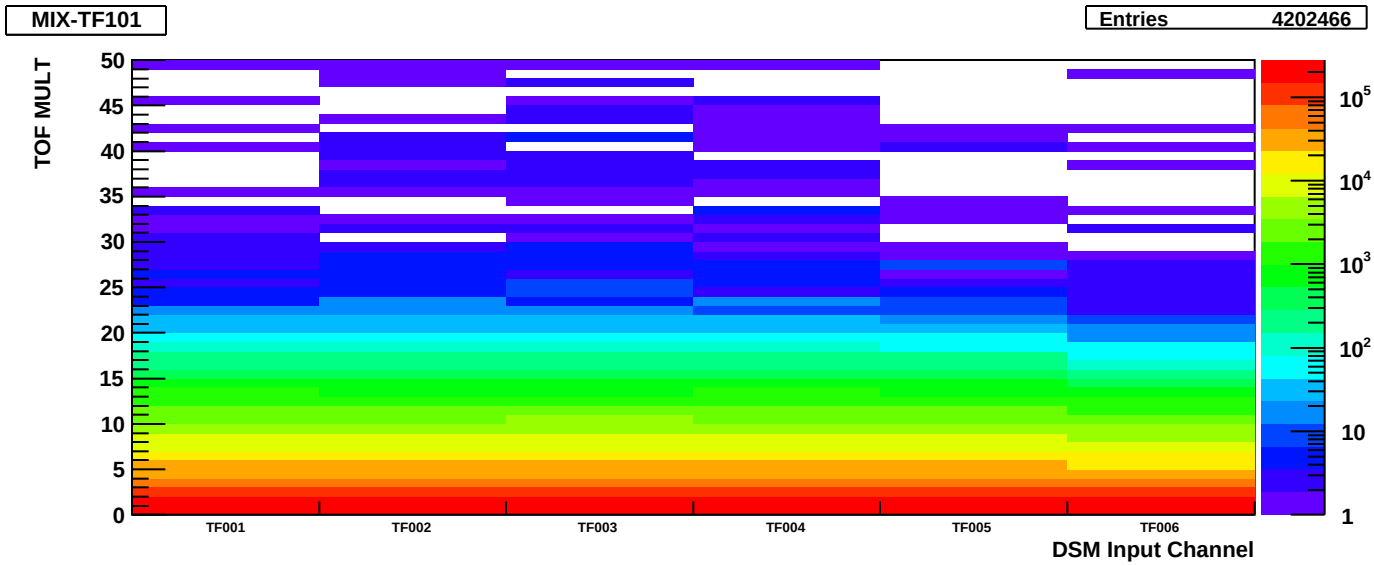

MIX-TF002
Entries 2.941726e+07
TOF MULT
TOF tray

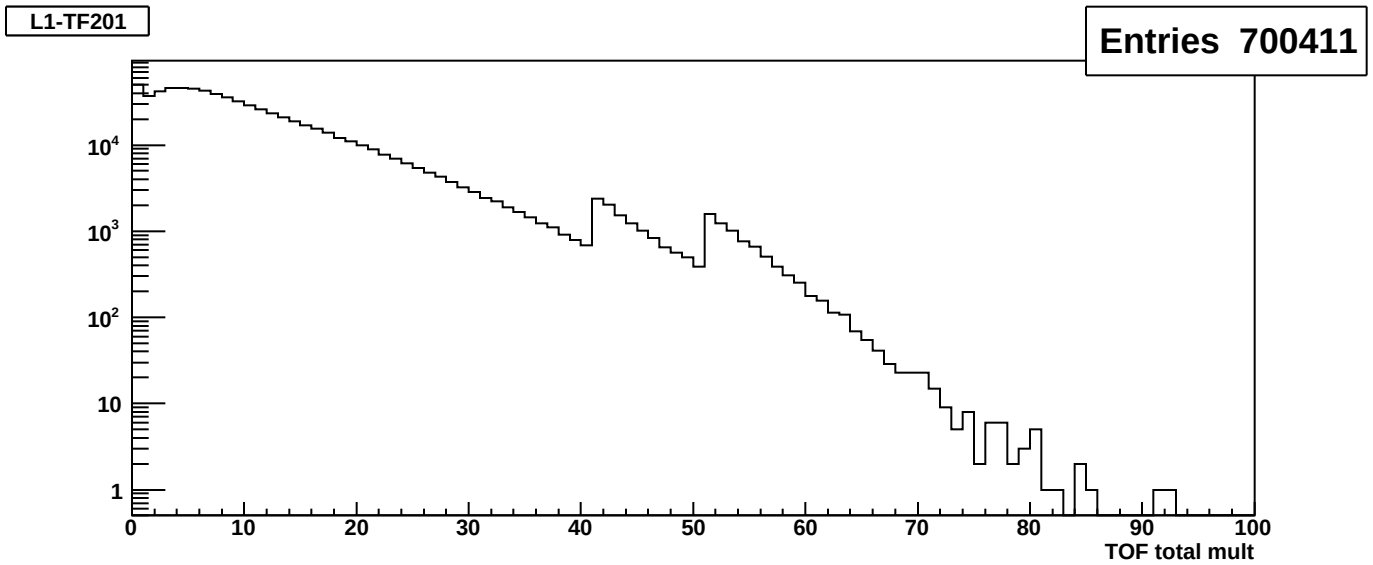

MIX-TF003
Entries 2.941726e+07
TOF MULT
TOF tray

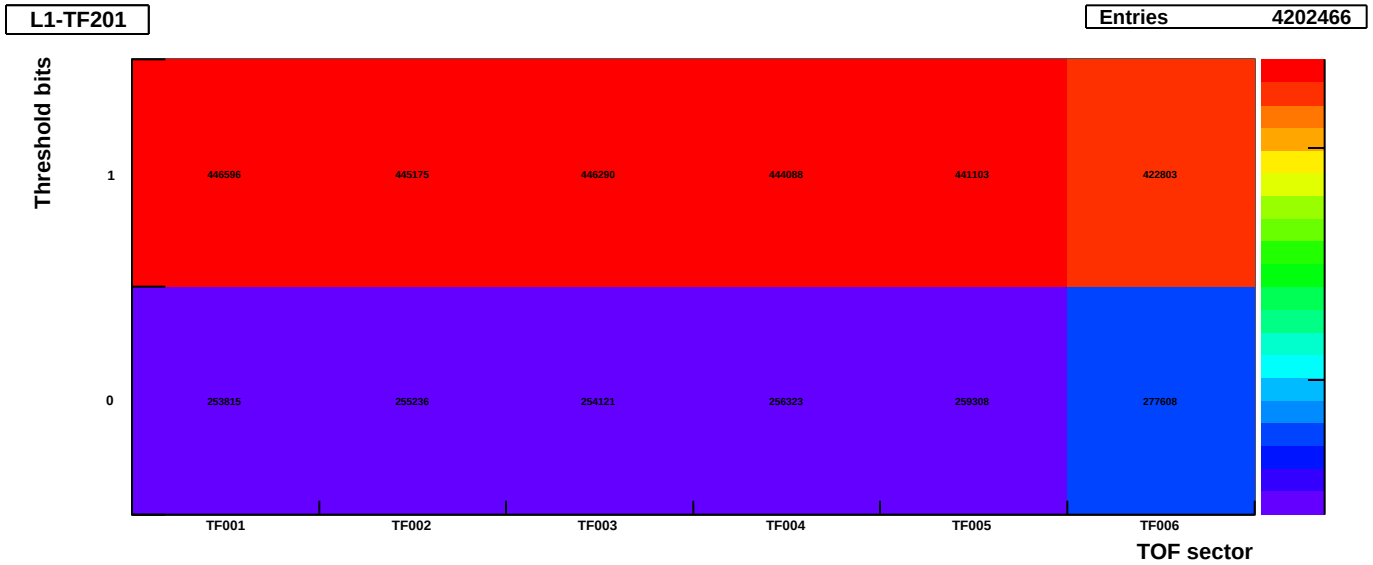

MIX-TF004
Entries 2.941726e+07
TOF MULT
TOF tray

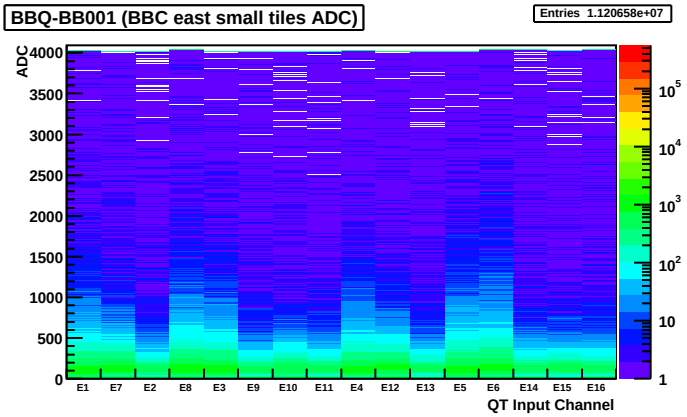

MIX-TF005
Entries 2.941726e+07
TOF MULT
TOF tray

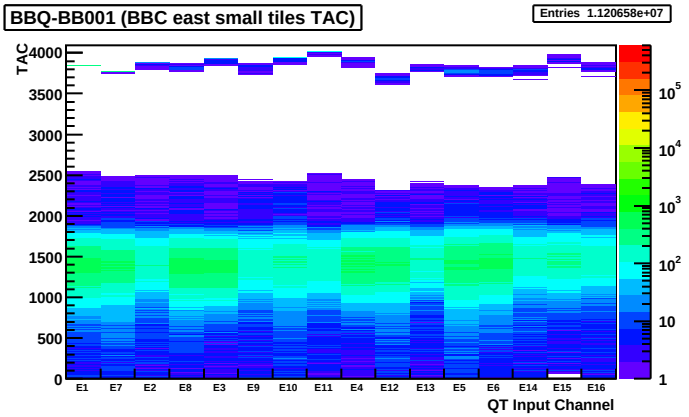

MIX-TF006
Entries 2.941726e+07
TOF MULT
TOF tray

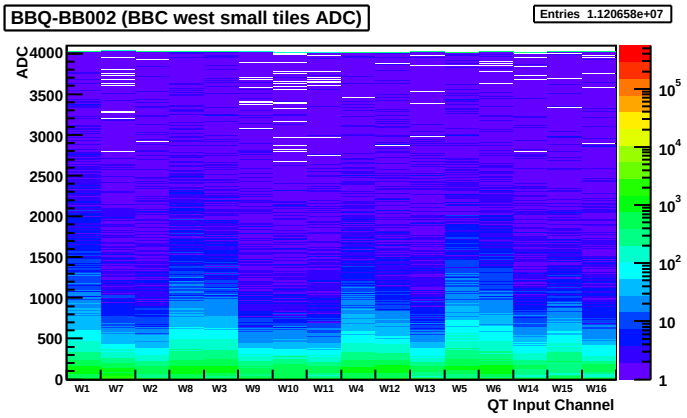

MIX-TF101
Entries 4202466
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

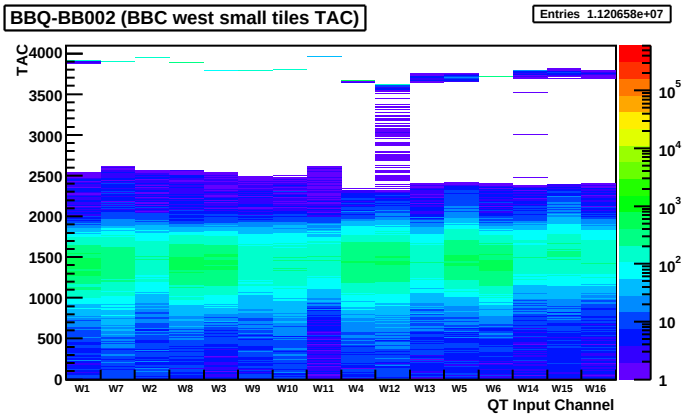

L1-TF201
Entries 700411
TOF total mult

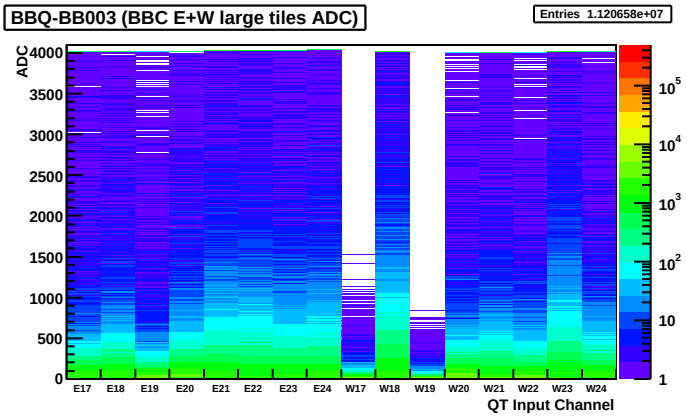

L1-TF201
Entries 4202466
Threshold bits
446596
445175
446290
444088
441103
422803
253815
255236
254121
256323
259308
277608
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

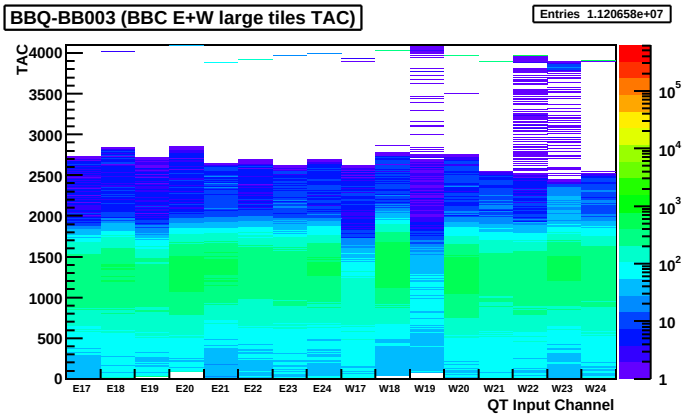

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.120658e+07
ADC
QT Input Channel

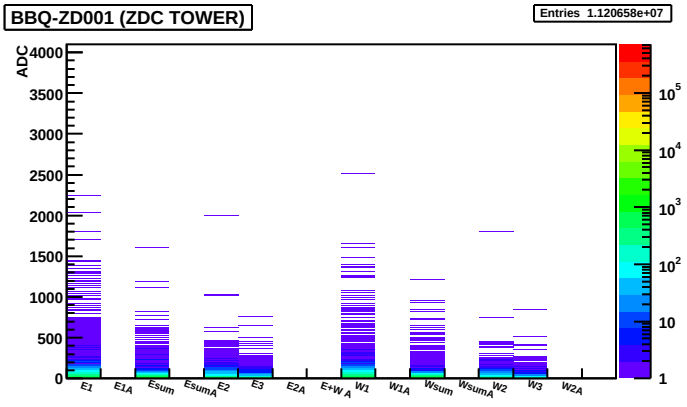

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.120658e+07
TAC
QT Input Channel

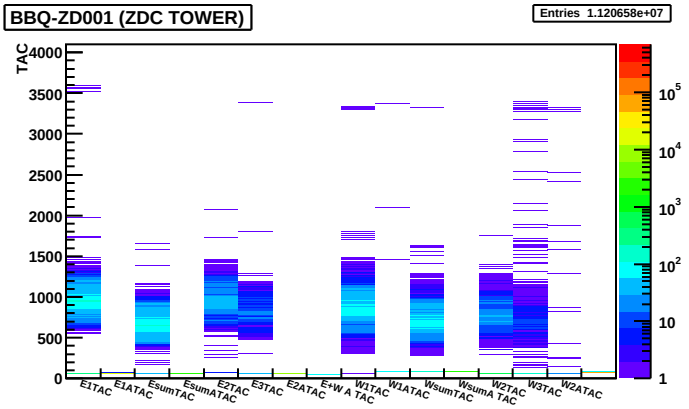

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.120658e+07
ADC
QT Input Channel

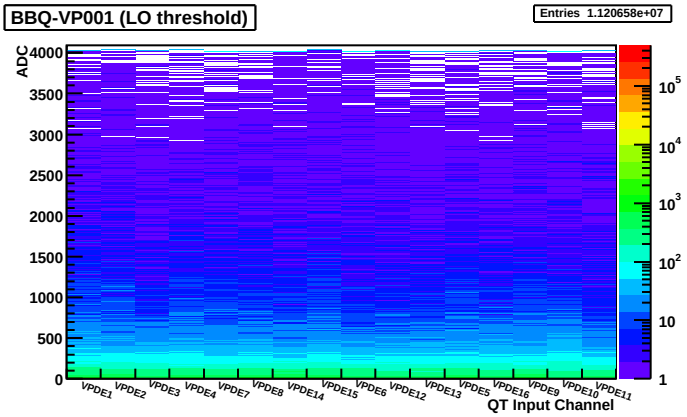

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.120658e+07
TAC
QT Input Channel

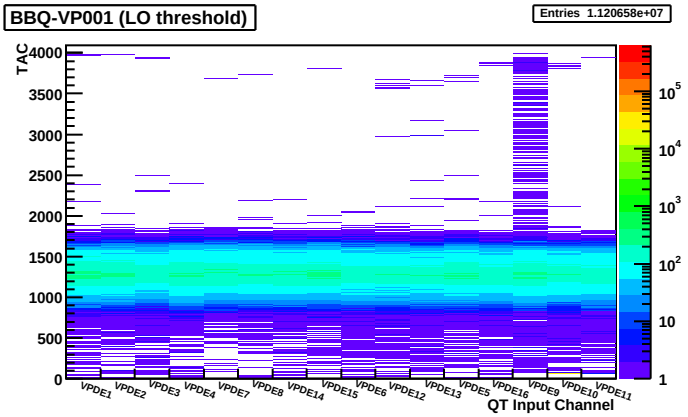

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.120658e+07
ADC
QT Input Channel

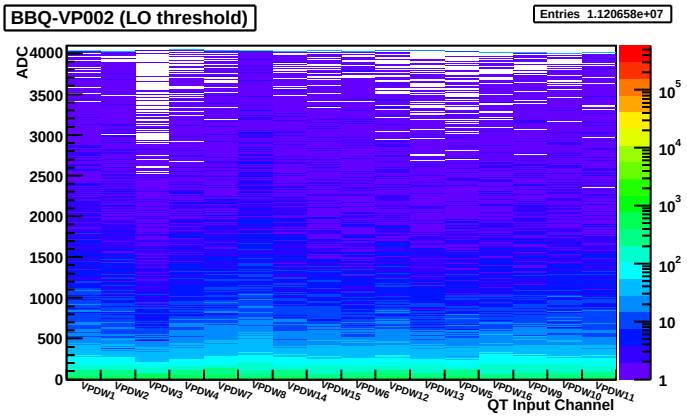

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.120658e+07
TAC
QT Input Channel

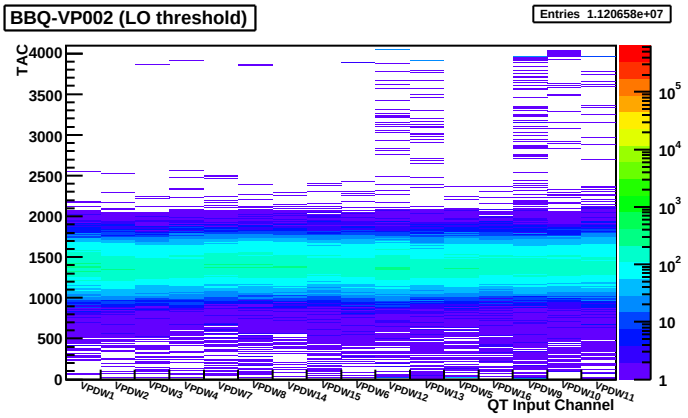

BBQ-ZD001 (ZDC TOWER)
Entries 1.120658e+07
ADC

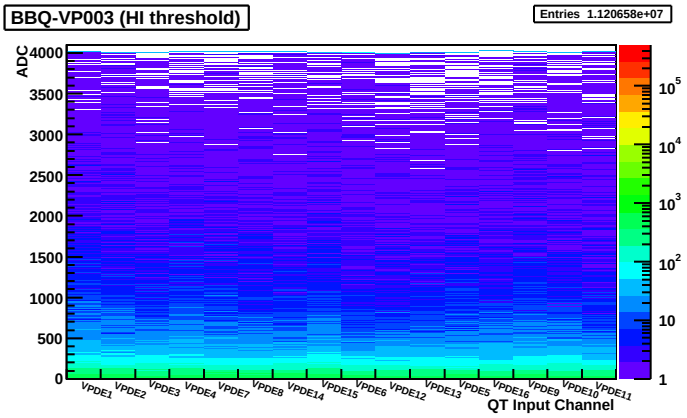

BBQ-ZD001 (ZDC TOWER)
Entries 1.120658e+07
TAC

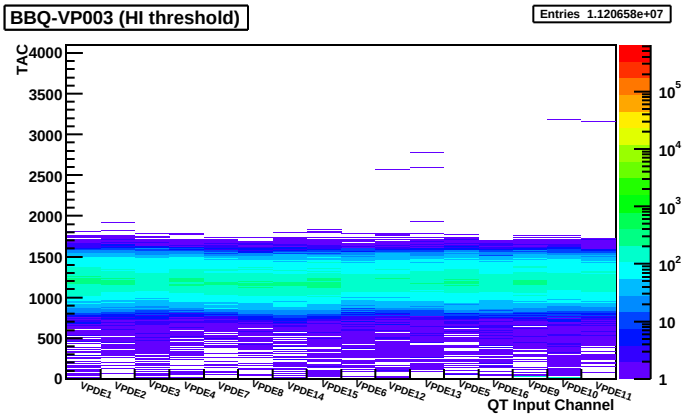

BBQ-VP001 (LO threshold)
Entries 1.120658e+07
ADC
QT Input Channel

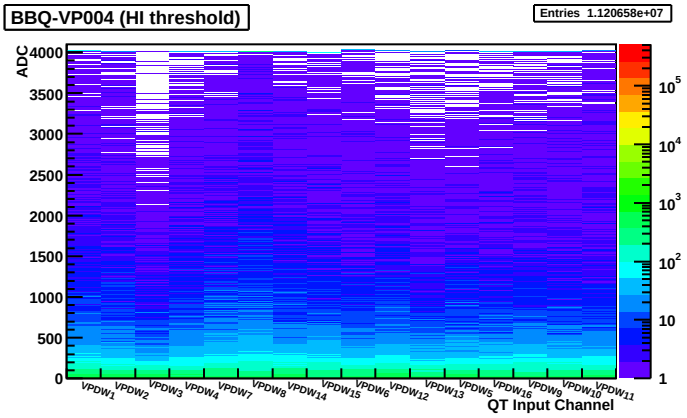

BBQ-VP001 (LO threshold)
Entries 1.120658e+07
TAC
QT Input Channel

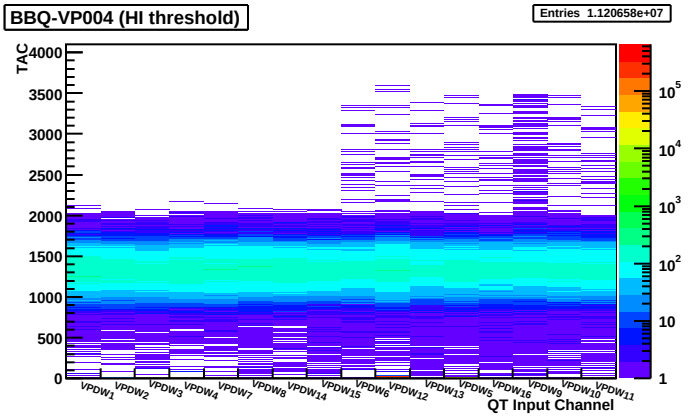

BBQ-VP002 (LO threshold)
Entries 1.120658e+07
ADC
QT Input Channel

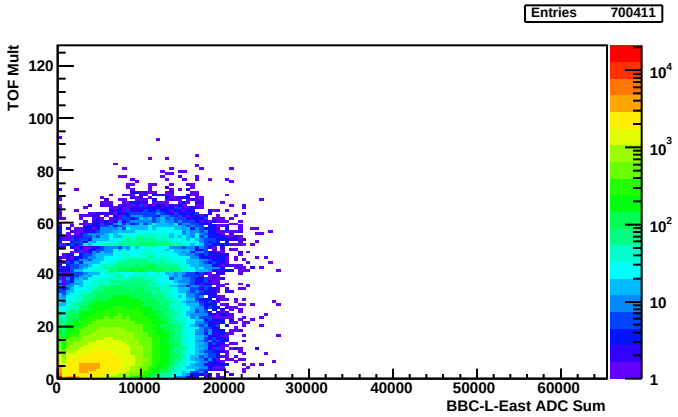

BBQ-VP002 (LO threshold)
Entries 1.120658e+07
TAC
QT Input Channel

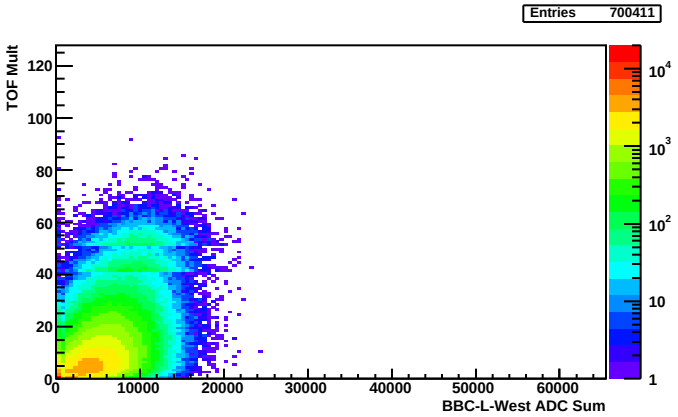

BBQ-VP003 (HI threshold)
Entries 1.120658e+07
ADC
QT Input Channel

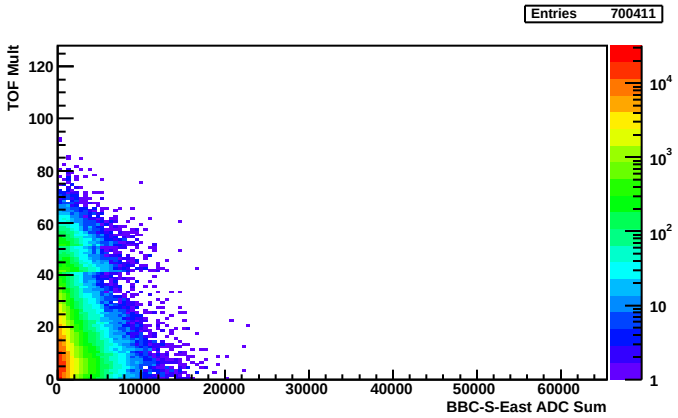

BBQ-VP003 (HI threshold)
Entries 1.120658e+07
TAC
QT Input Channel

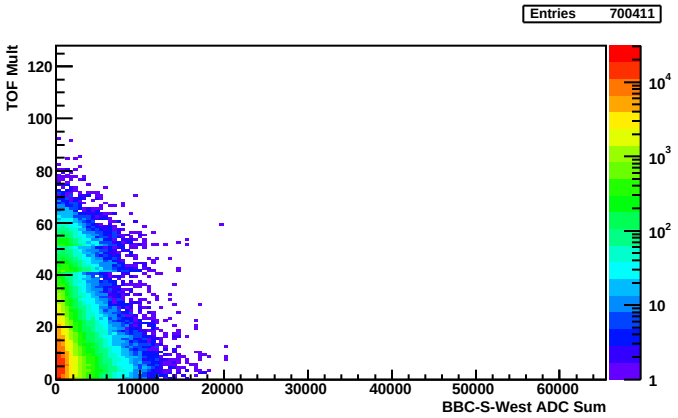

BBQ-VP004 (HI threshold)
Entries 1.120658e+07
ADC
QT Input Channel

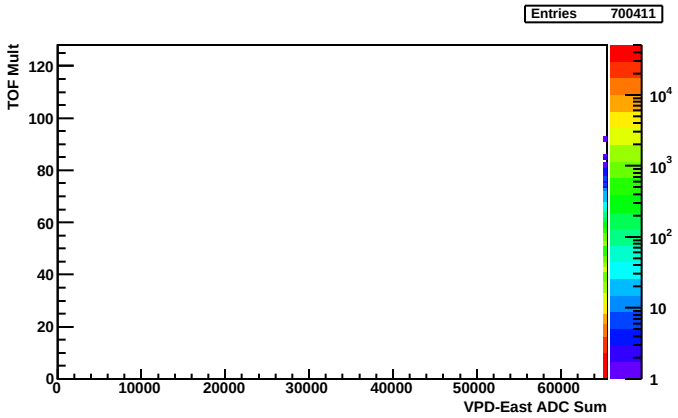

BBQ-VP004 (HI threshold)
Entries 1.120658e+07
TAC
QT Input Channel

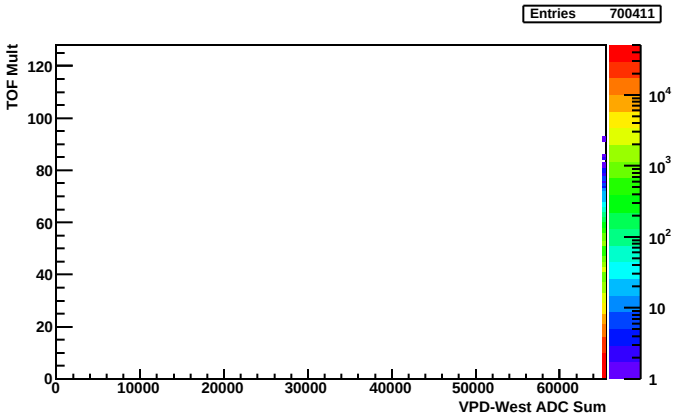

Entries 700411
TOF Mult
BBC-L-East ADC Sum

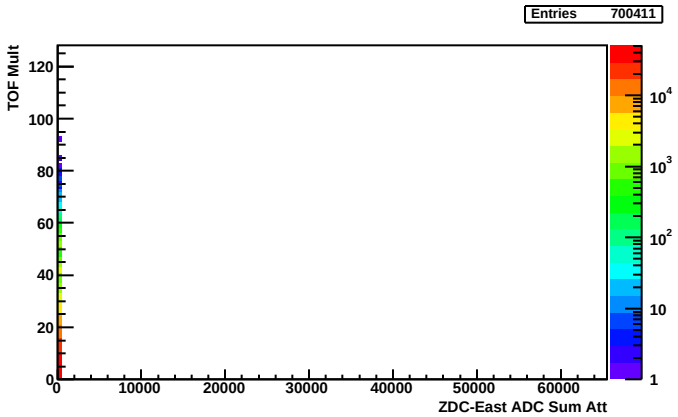

Entries 700411
TOF Mult
BBC-L-West ADC Sum

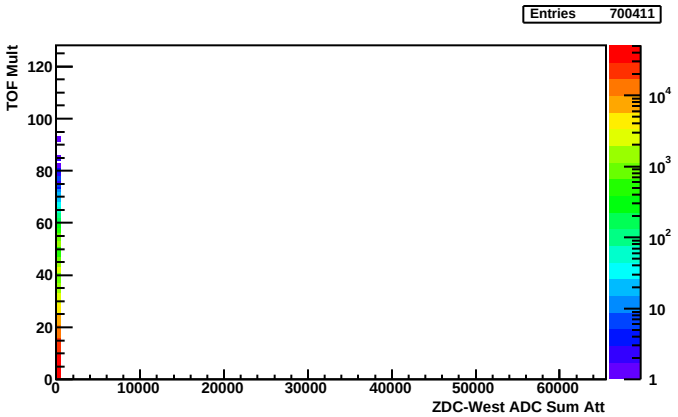

Entries 700411
TOF Mult
BBC-S-East ADC Sum

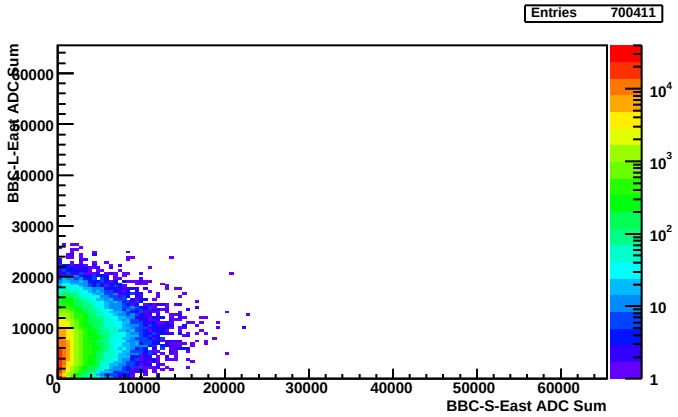

Entries 700411
TOF Mult
BBC-S-West ADC Sum

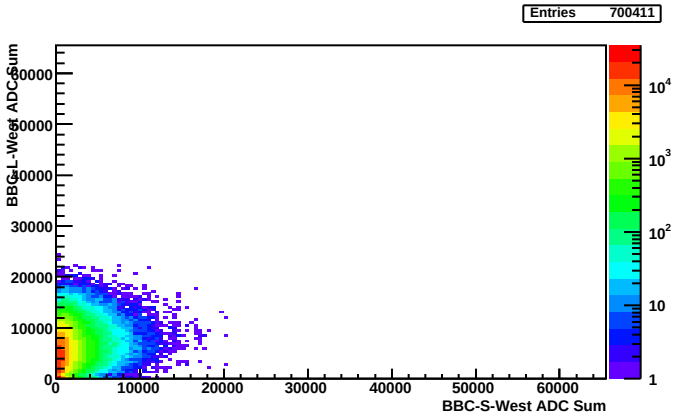

Entries 700411
TOF Mult
VPD-East ADC Sum

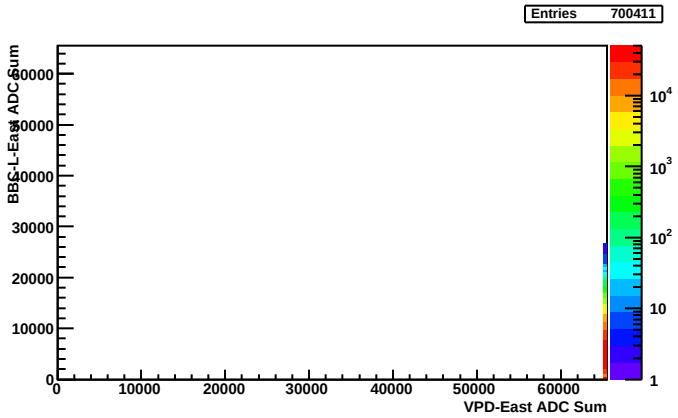

Entries 700411
TOF Mult
VPD-West ADC Sum

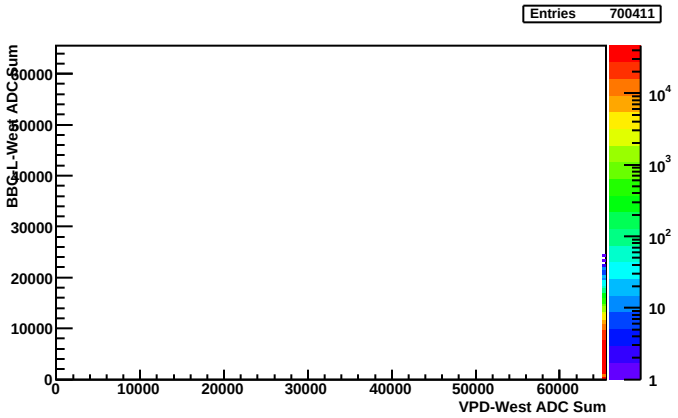

Entries 700411
TOF Mult
ZDC-East ADC Sum Att

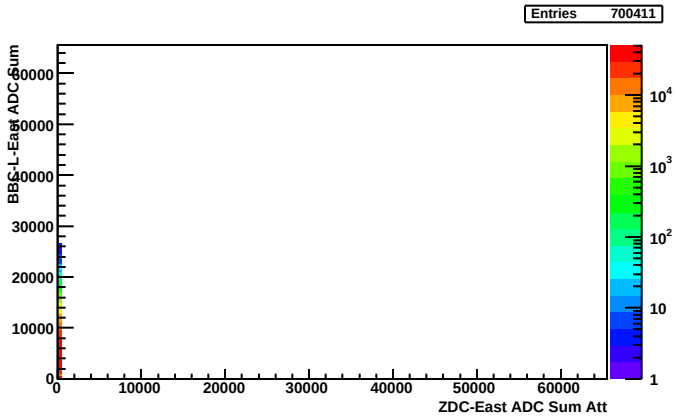

Entries 700411
TOF Mult
ZDC-West ADC Sum Att

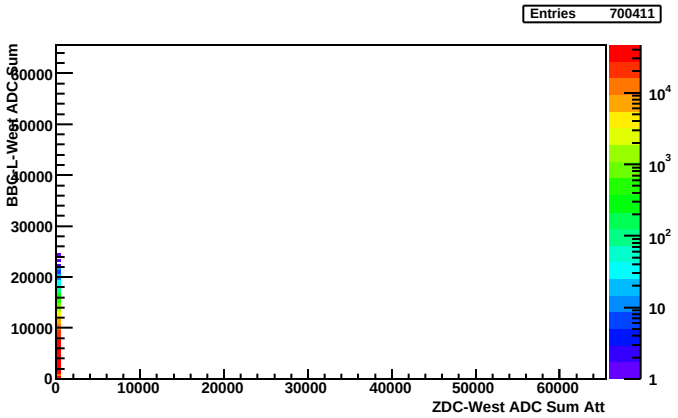

Entries 700411
BBC-L-East ADC Sum
BBC-S-East ADC Sum

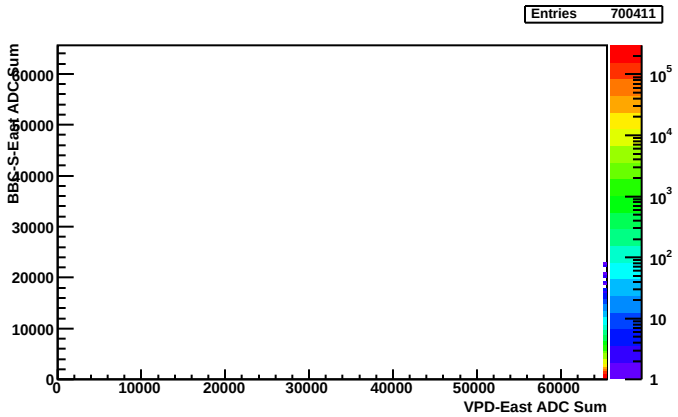

Entries 700411
BBC-L-West ADC Sum
BBC-S-West ADC Sum

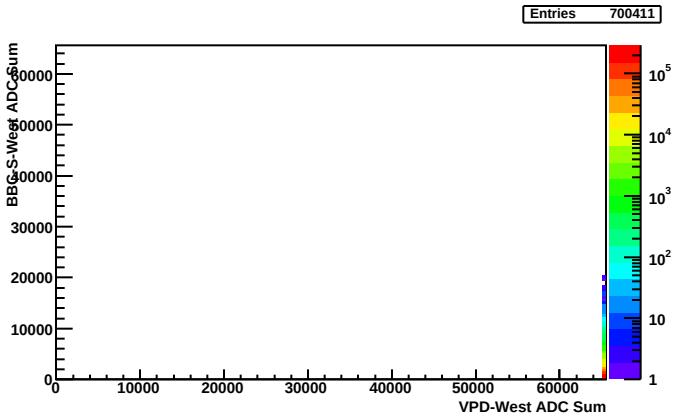

Entries 700411
BBC-L-East ADC Sum
VPD-East ADC Sum

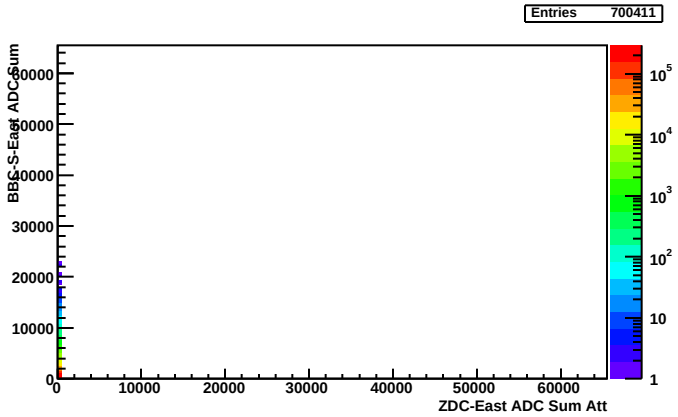

Entries 700411
BBC-L-West ADC Sum
VPD-West ADC Sum

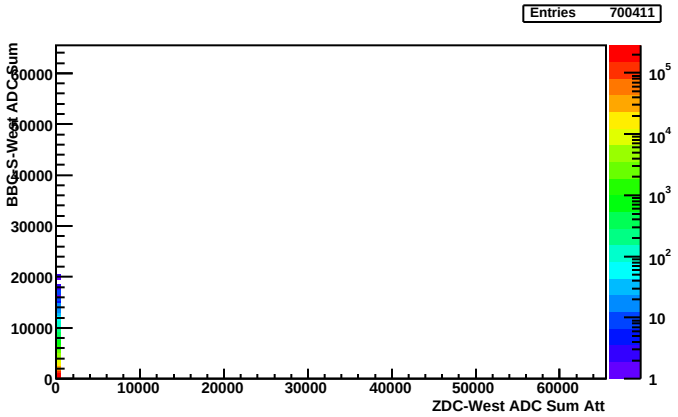

Entries 700411
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

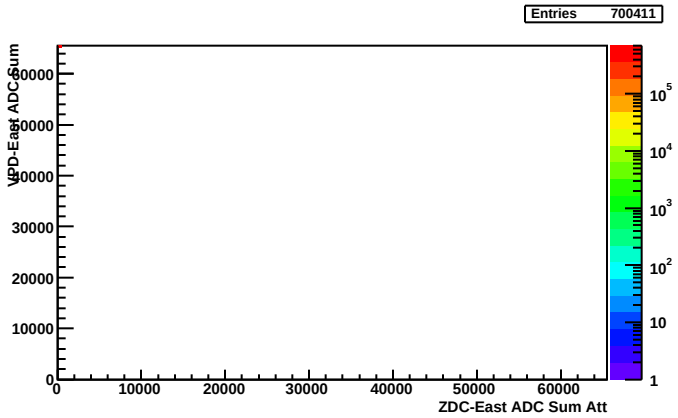

Entries 700411
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

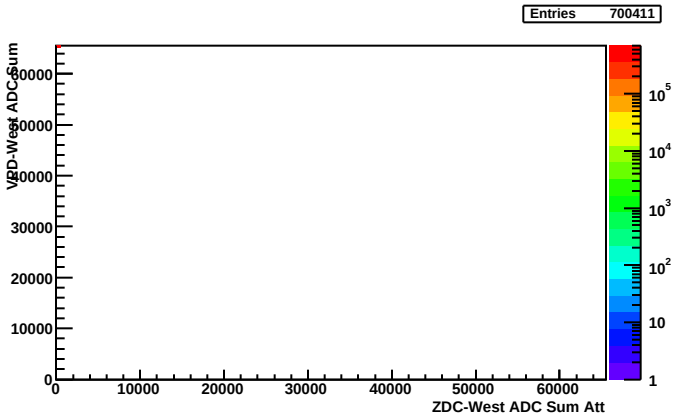

Entries 700411
BBC-S-East ADC Sum
VPD-East ADC Sum

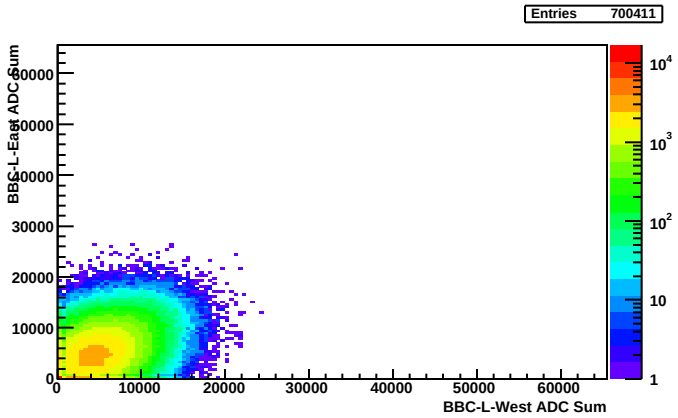

Entries 700411
BBC-S-West ADC Sum
VPD-West ADC Sum

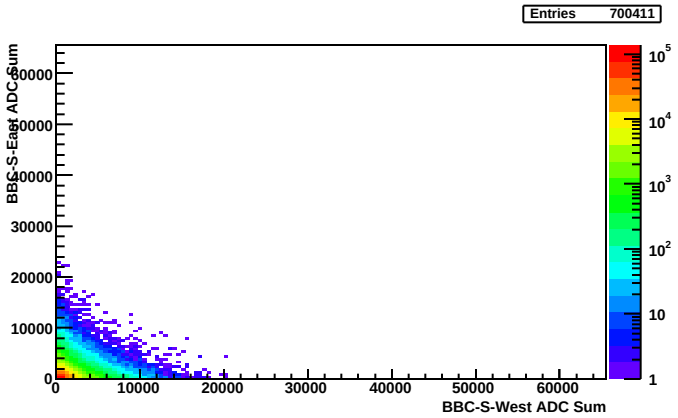

Entries 700411
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

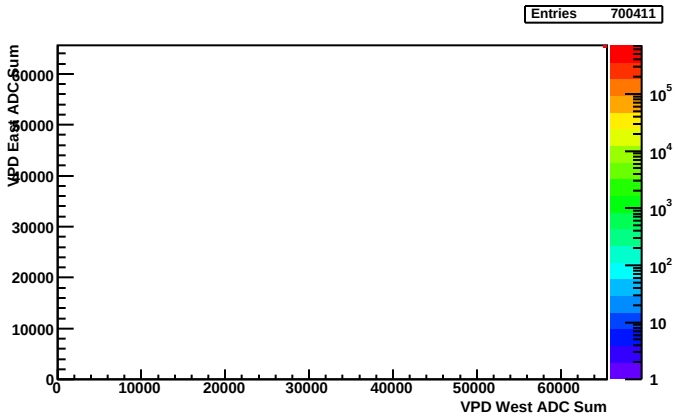

Entries 700411
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

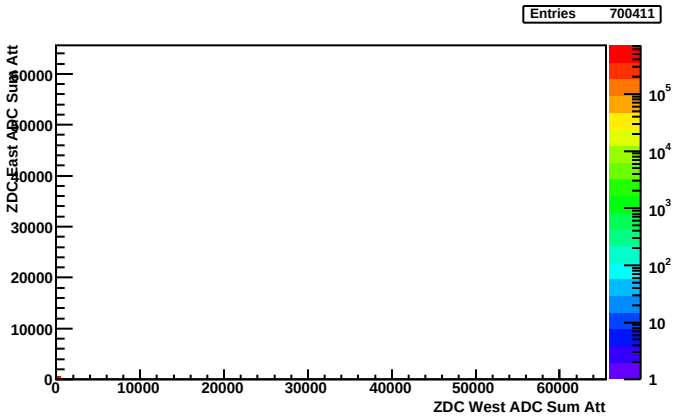

Entries 700411
VPD-East ADC Sum
ZDC-East ADC Sum Att

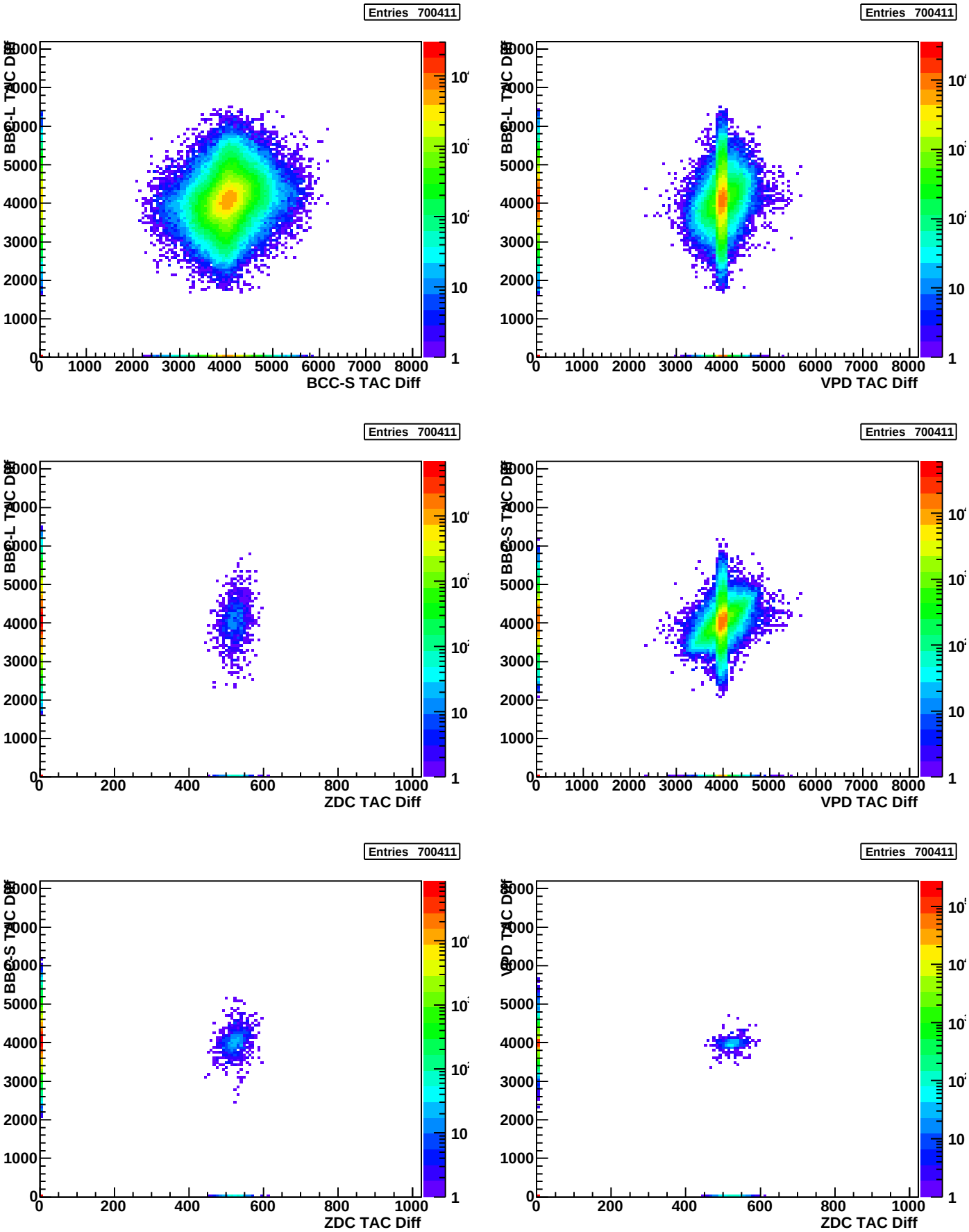

Entries 700411
VPD-West ADC Sum
ZDC-West ADC Sum Att

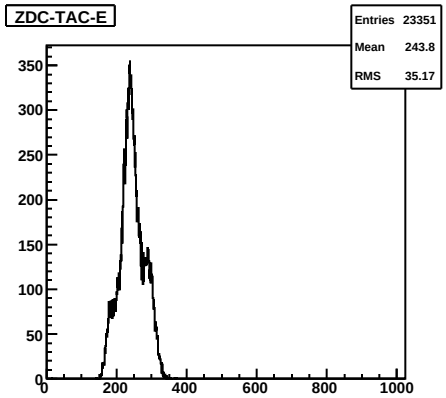

Entries 700411
BBC-L-East ADC Sum
BBC-L-West ADC Sum

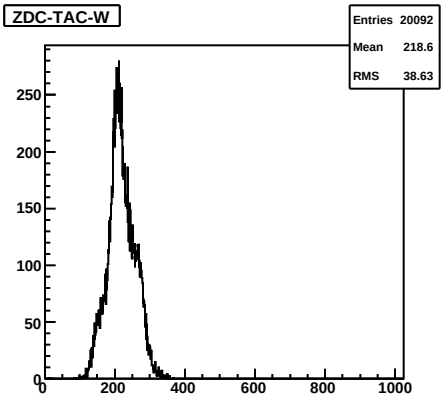

Entries 700411
BBC-S-East ADC Sum
BBC-S-West ADC Sum

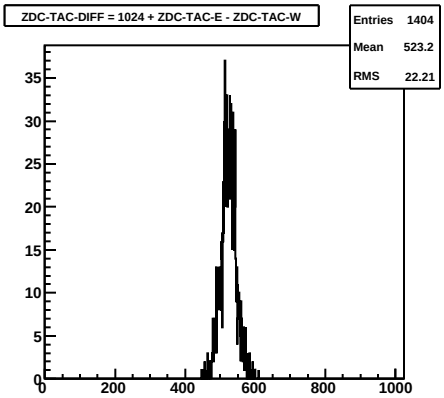

Entries 700411
VPD East ADC Sum
VPD West ADC Sum

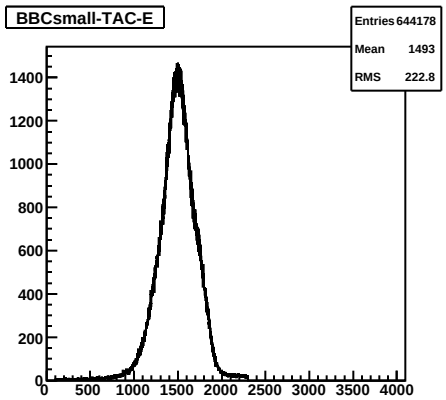

Entries 700411
ZDC East ADC Sum Att
ZDC West ADC Sum Att

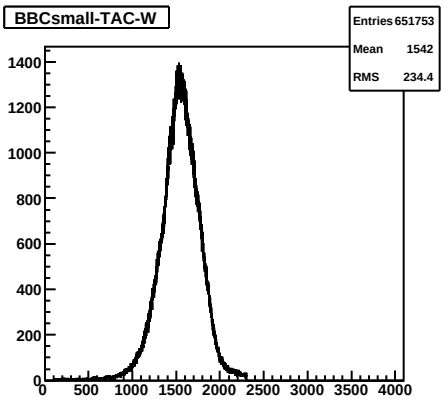

Entries 700411
BBC-L TAC Diff
BCC-S TAC Diff
Entries 700411
BBC-L TAC Diff
VPD TAC Diff
Entries 700411
BBC-L TAC Diff
ZDC TAC Diff
Entries 700411
BBC-S TAC Diff
VPD TAC Diff
Entries 700411
BBC-S TAC Diff
ZDC TAC Diff
Entries 700411
VPD TAC Diff
ZDC TAC Diff

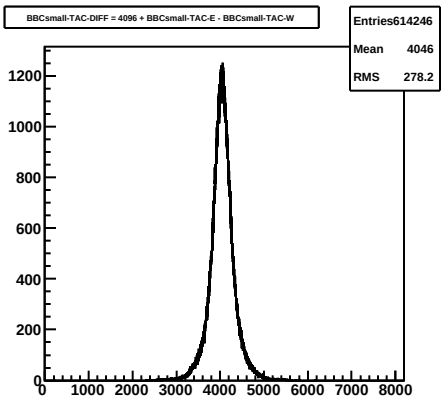

ZDC-TAC-E
Entries 23351
Mean 243.8
RMS 35.17

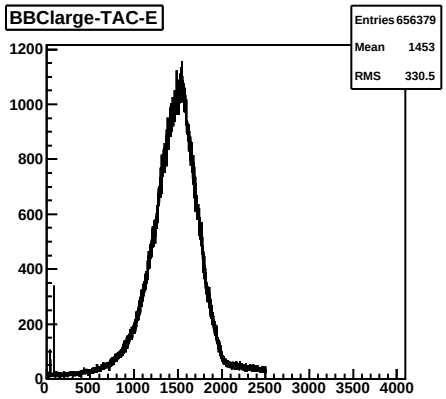

ZDC-TAC-W
Entries 20092
Mean 218.6
RMS 38.63

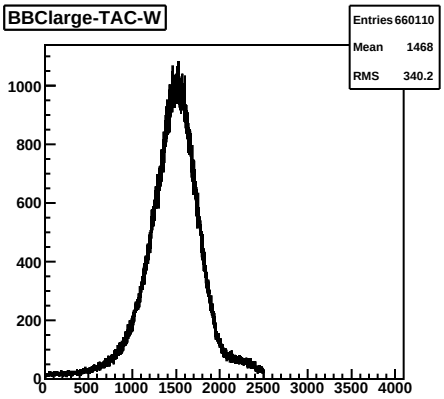

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1404
Mean 523.2
RMS 22.21

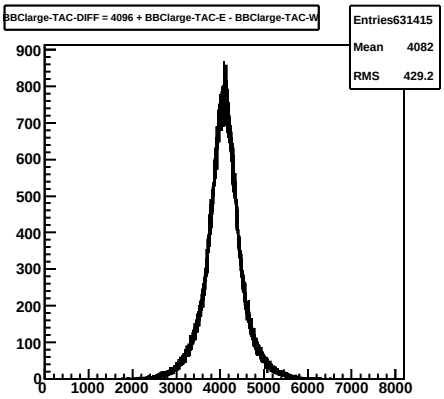

BBCsmall-TAC-E
Entries 644178
Mean 1493
RMS 222.8

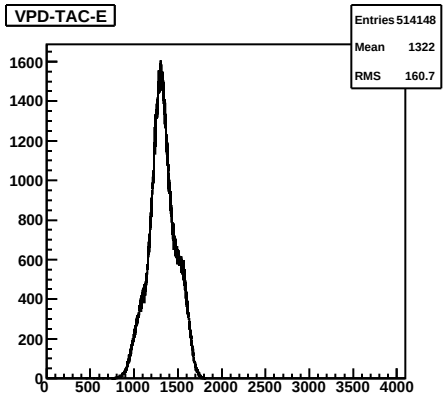

BBCsmall-TAC-W
Entries 651753
Mean 1542
RMS 234.4

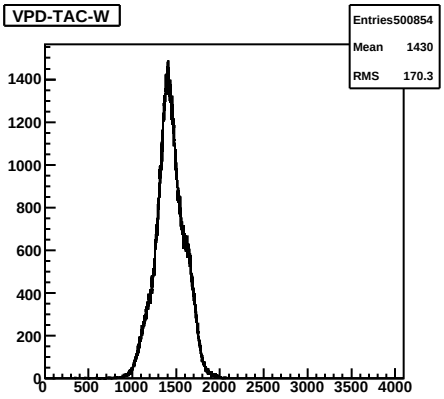

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries614246
Mean 4046
RMS 278.2

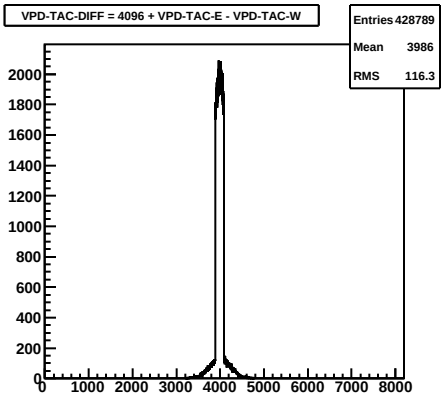

BBClarge-TAC-E
Entries 656379
Mean 1453
RMS 330.5

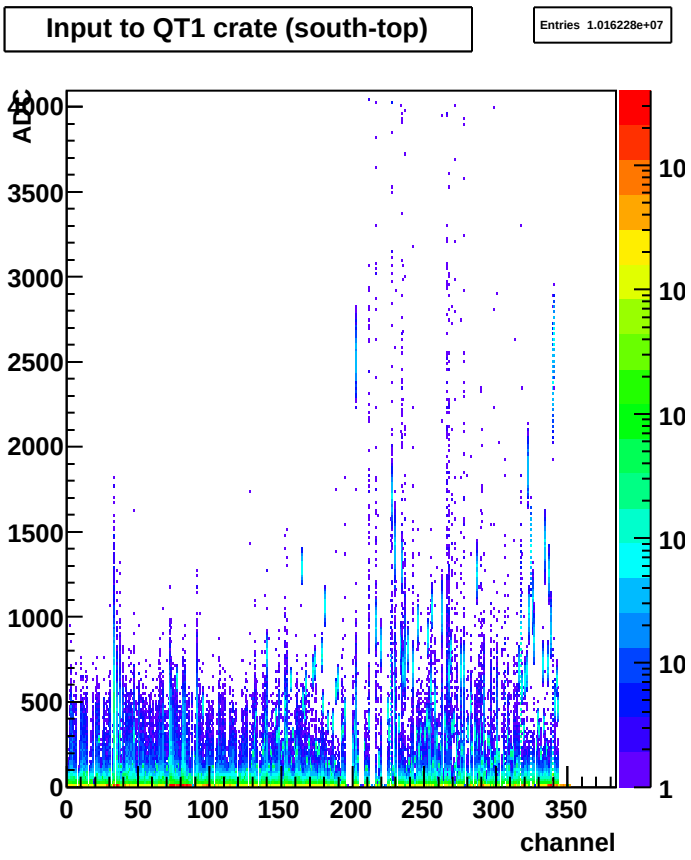

BBClarge-TAC-W
Entries 660110
Mean 1468
RMS 340.2

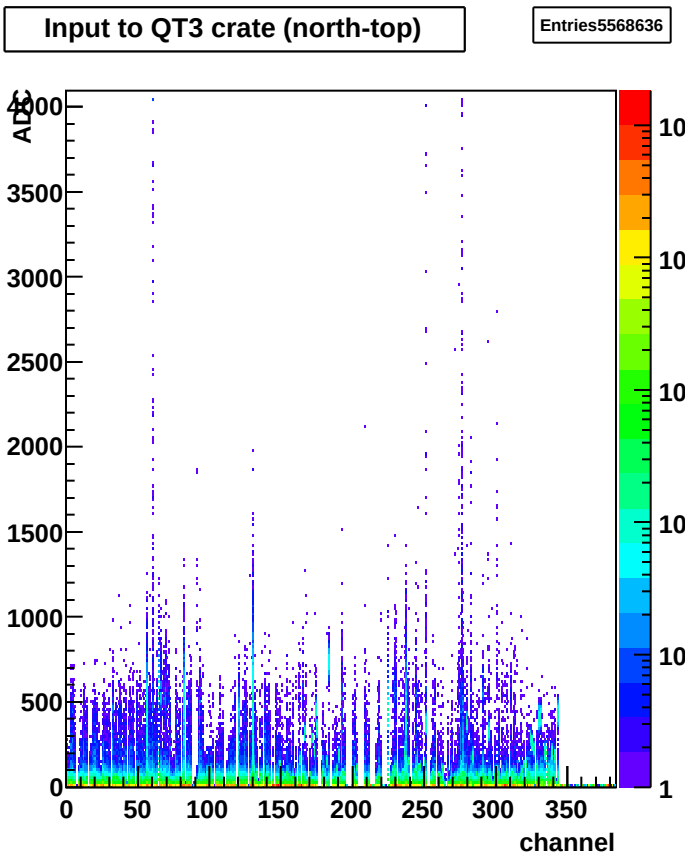

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries631415
Mean 4082
RMS 429.2

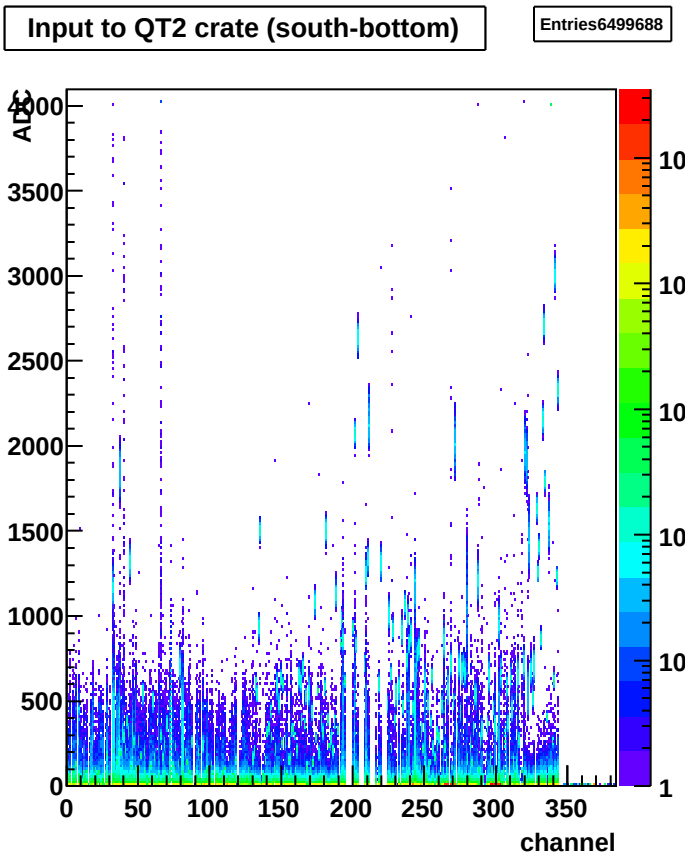

VPD-TAC-E
Entries 514148
Mean 1322
RMS 160.7

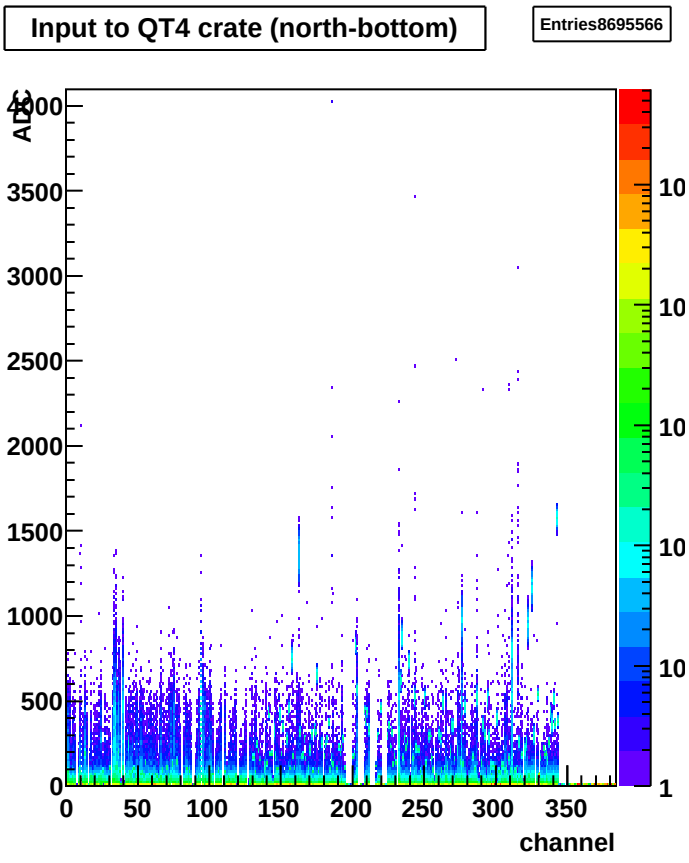

VPD-TAC-W
Entries500854
Mean 1430
RMS 170.3

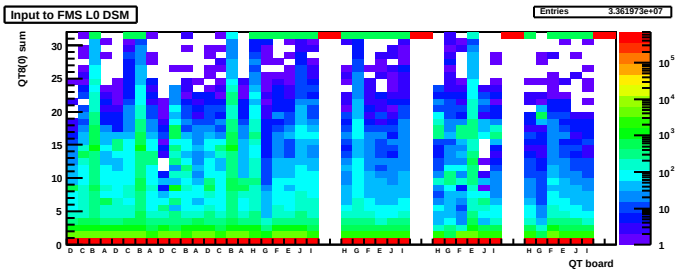

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 428789
Mean 3986
RMS 116.3

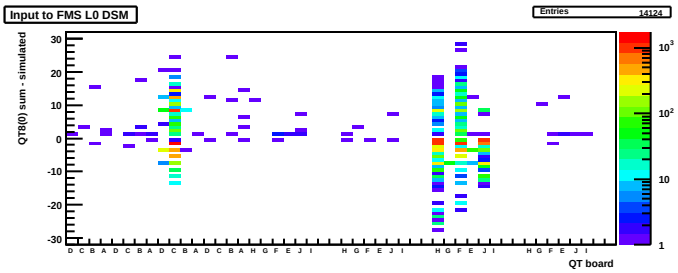

Input to QT1 crate (south-top)
Entries 1.016228e+07
ADC
channel

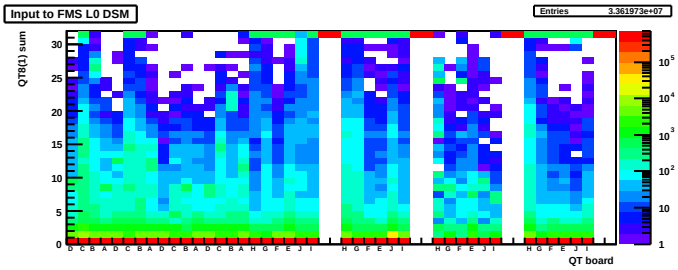

Input to QT3 crate (north-top)
Entries5568636
ADC
channel

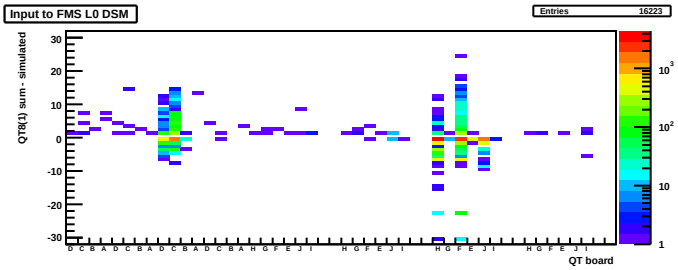

Input to QT2 crate (south-bottom)
Entries6499688
ADC
channel

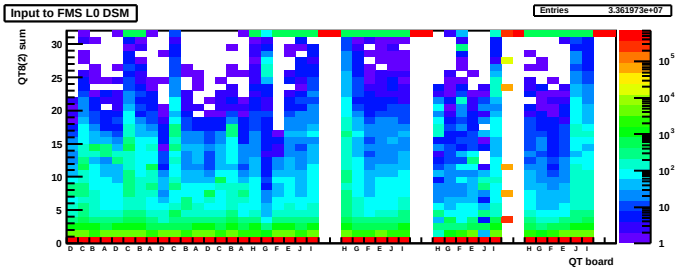

Input to QT4 crate (north-bottom)
Entries8695566
ADC
channel

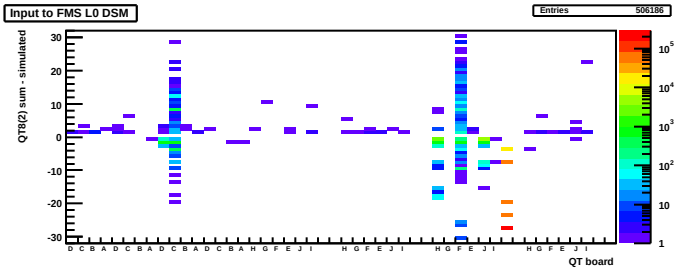

Input to FMS L0 DSM
Entries 3.361973e+07
QT8(0) sum
QT board

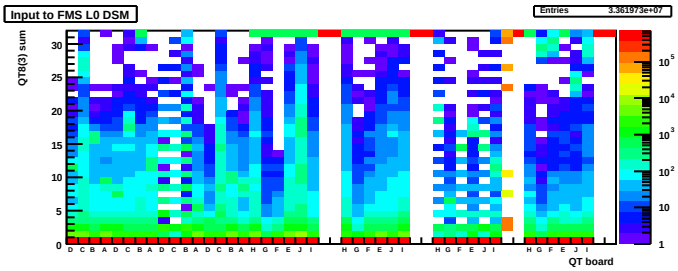

Input to FMS L0 DSM
Entries 14124
QT8(0) sum - simulated
QT board

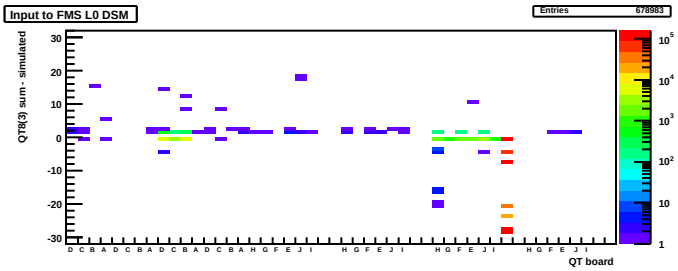

Input to FMS L0 DSM
Entries 3.361973e+07
QT8(1) sum
QT board

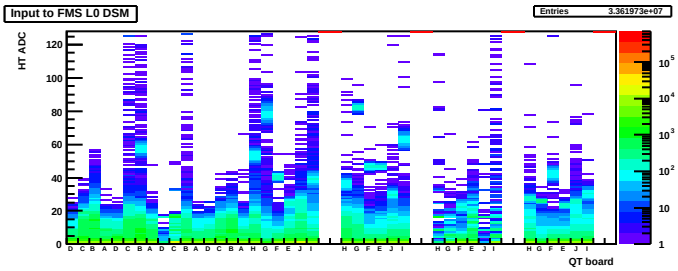

Input to FMS L0 DSM
Entries 16223
QT8(1) sum - simulated
QT board

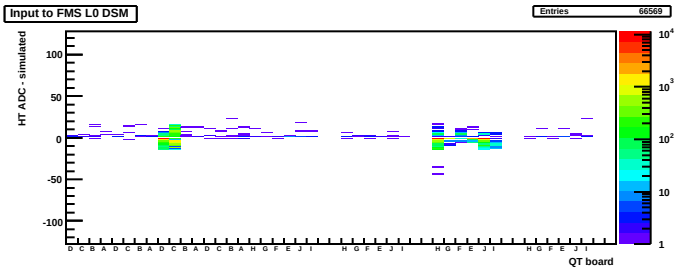

Input to FMS L0 DSM
Entries 3.361973e+07
QT8(2) sum
QT board

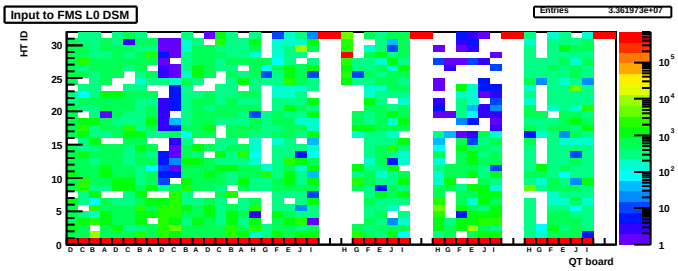

Input to FMS L0 DSM
Entries 506186
QT8(2) sum - simulated
QT board

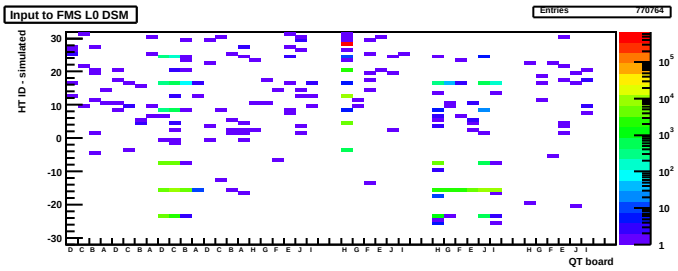

Input to FMS L0 DSM
Entries 3.361973e+07
QT8(3) sum
QT board

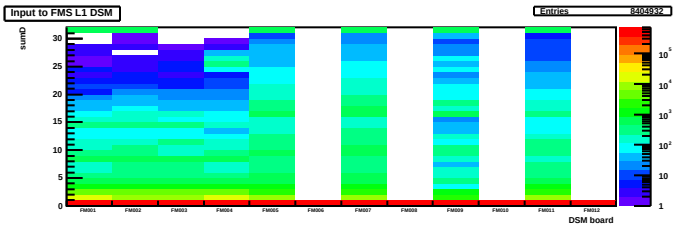

Input to FMS L0 DSM
Entries 678983
QT8(3) sum - simulated
QT board

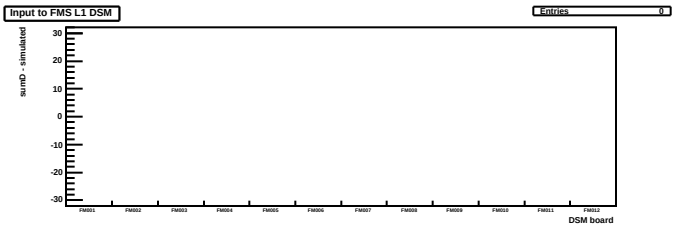

Input to FMS L0 DSM
Entries 3.361973e+07
HT ADC
QT board

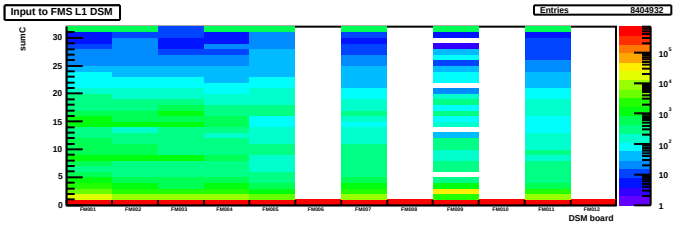

Input to FMS L0 DSM
Entries 66569
HT ADC - simulated
QT board

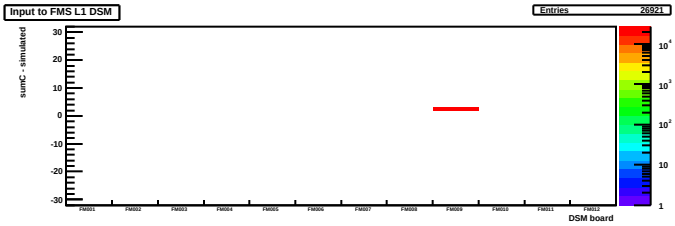

Input to FMS L0 DSM
Entries 3.361973e+07
HT ID
QT board

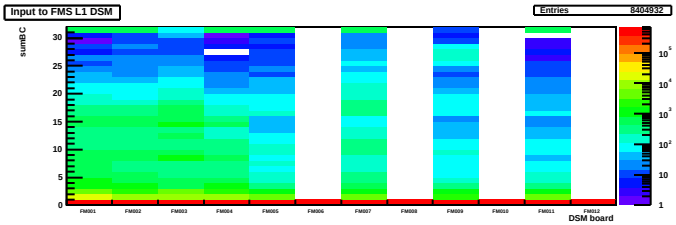

Input to FMS L0 DSM
Entries 770764
HT ID - simulated
QT board

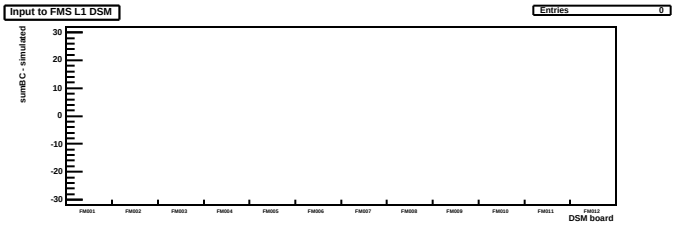

Input to FMS L1 DSM
Entries 8404932
sumD
DSM board

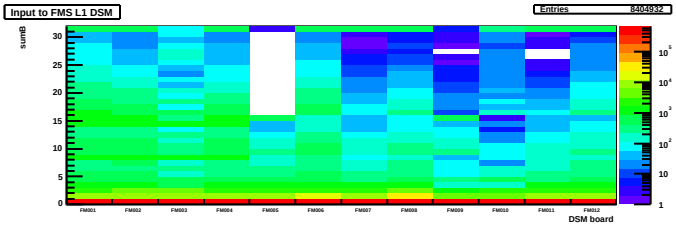

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

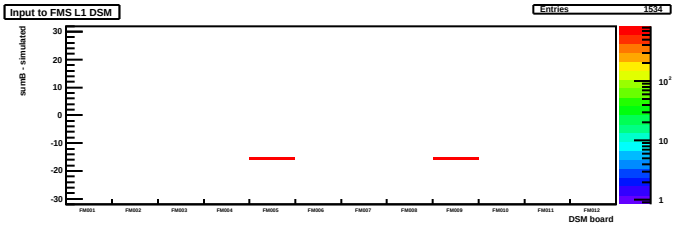

Input to FMS L1 DSM
Entries 8404932
sumC
DSM board

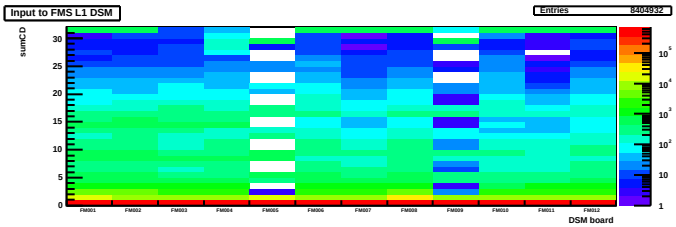

Input to FMS L1 DSM
Entries 26921
sumC - simulated
DSM board

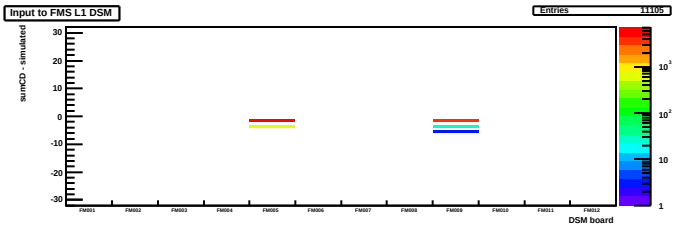

Input to FMS L1 DSM
Entries 8404932
sumBC
DSM board

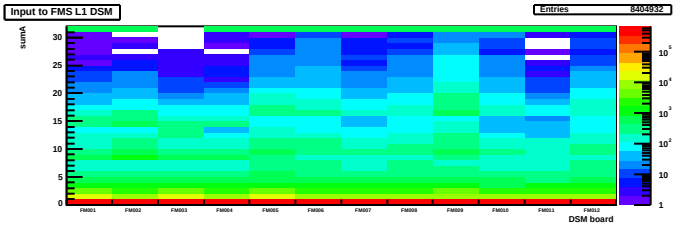

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

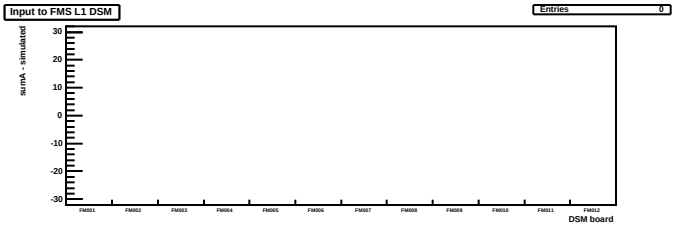

Input to FMS L1 DSM
Entries 8404932
sumB
DSM board

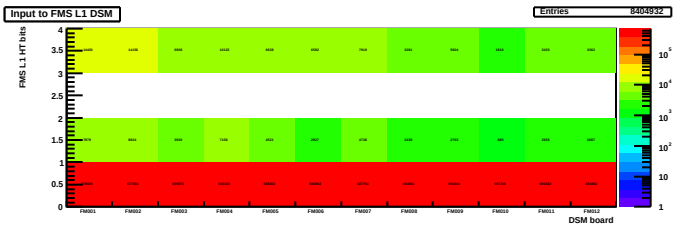

Input to FMS L1 DSM
Entries 1534
sumB - simulated
DSM board

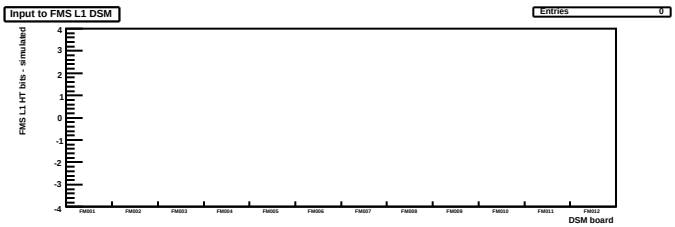

Input to FMS L1 DSM
Entries 8404932
sumCD
DSM board

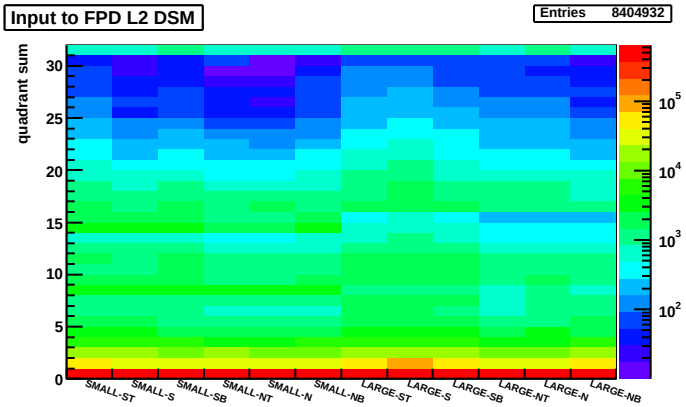

Input to FMS L1 DSM
Entries 11105
sumCD - simulated
DSM board

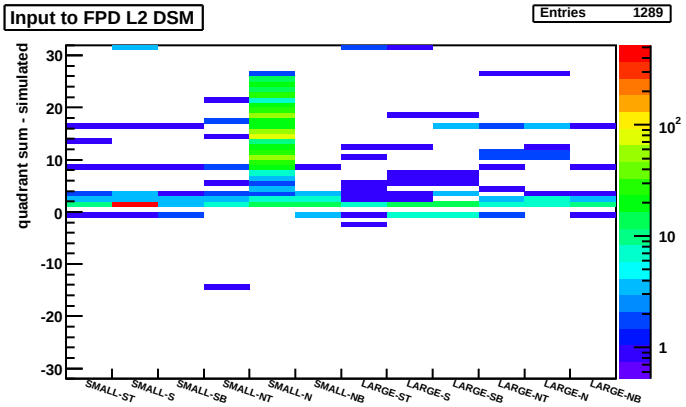

Input to FMS L1 DSM
Entries 8404932
sumA
DSM board

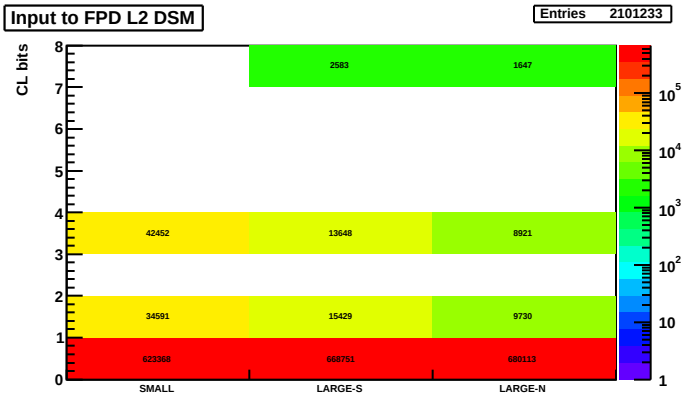

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

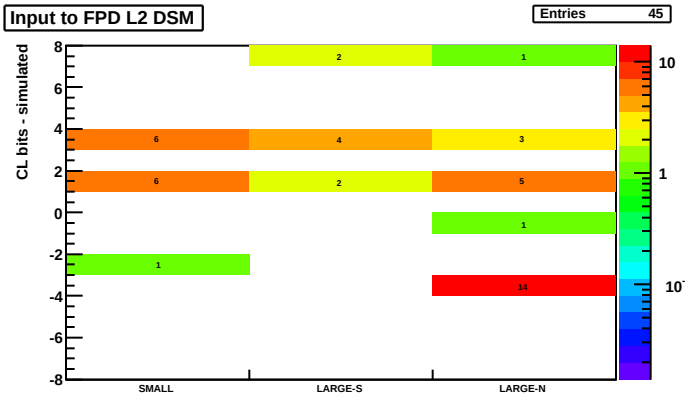

Input to FMS L1 DSM
Entries 8404932
FMS L1 HT bits
DSM board

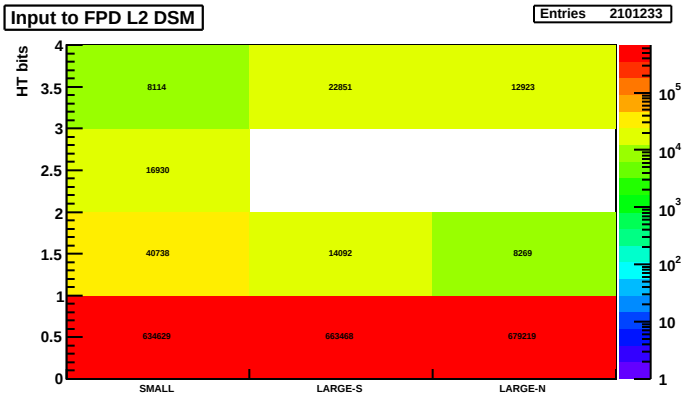

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

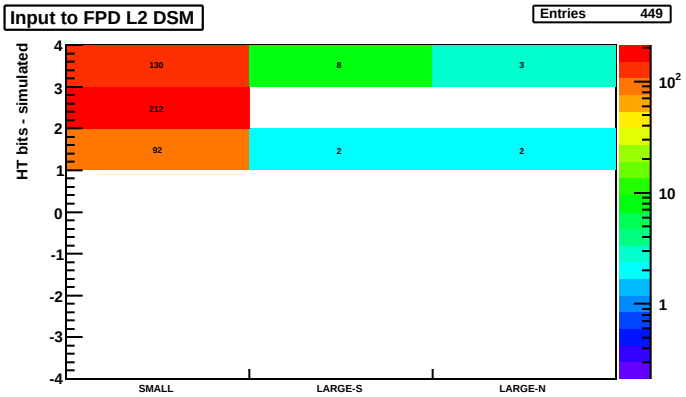

Input to FPD L2 DSM
Entries 8404932
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

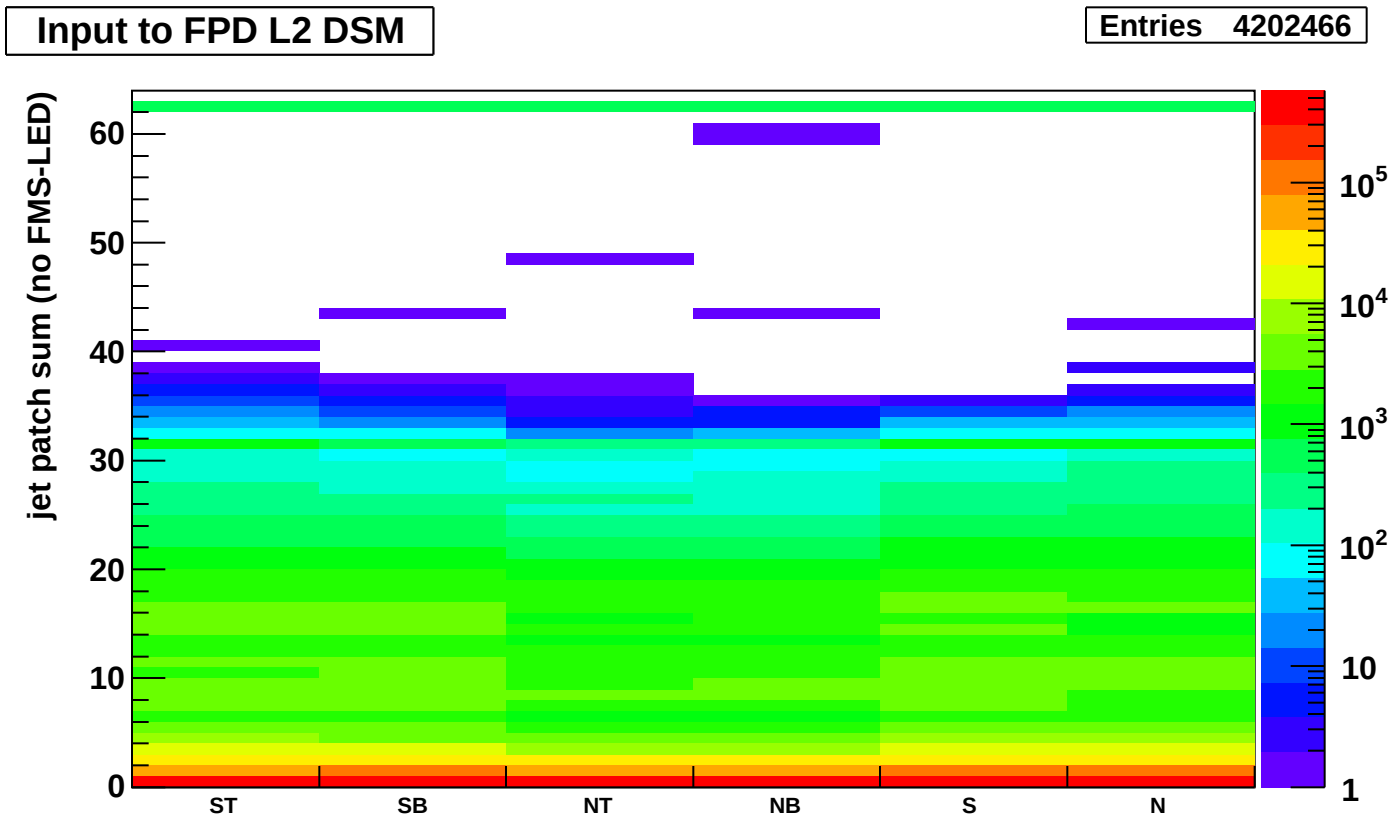

Input to FPD L2 DSM
Entries 1289
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

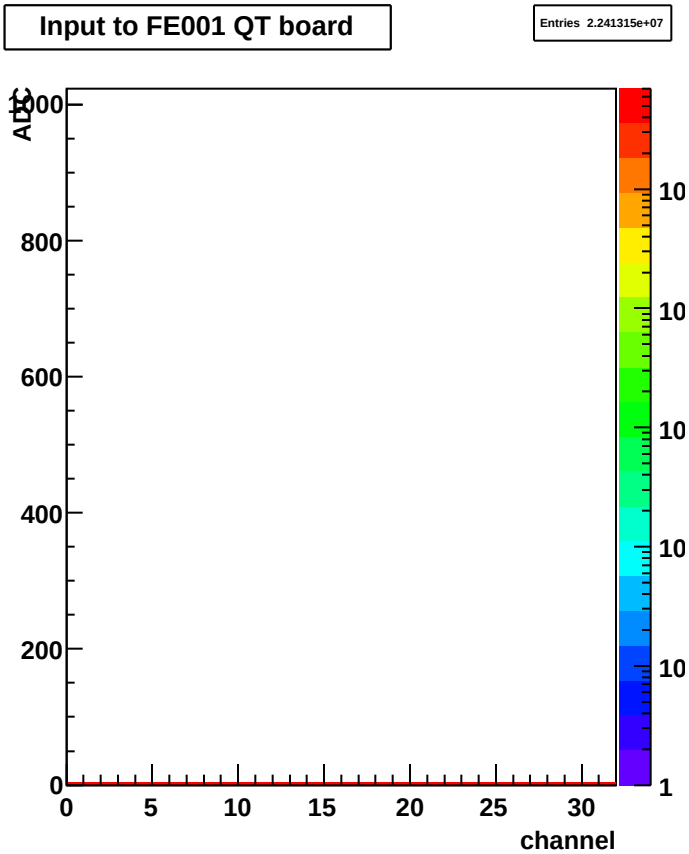

Input to FPD L2 DSM
Entries 2101233
CL bits
2583
1647
42452
13648
8921
34591
15429
9730
623368
668751
680113
SMALL
LARGE-S
LARGE-N

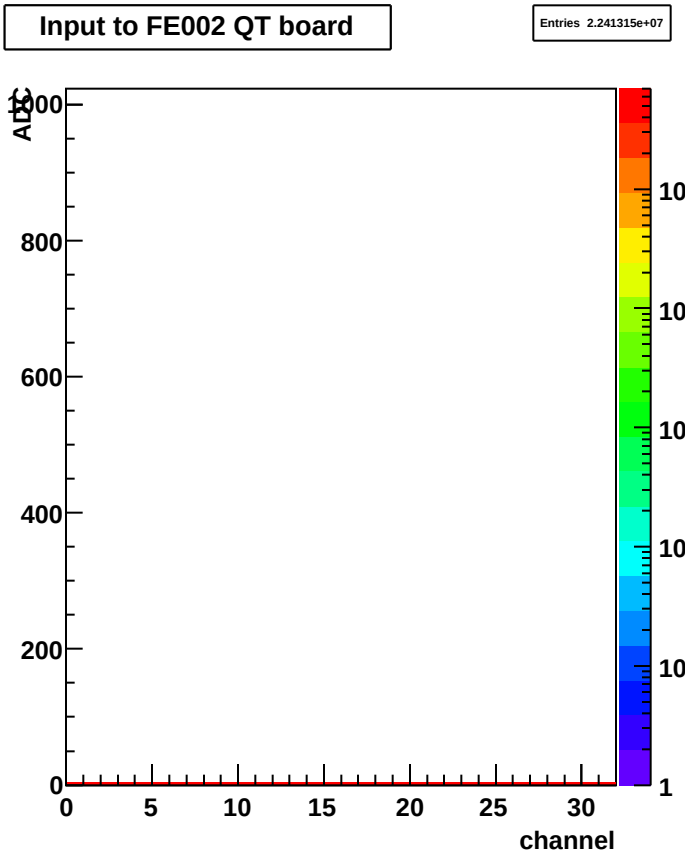

Input to FPD L2 DSM
Entries 45
CL bits - simulated
2
1
6
4
3
6
2
5
1
1
14
SMALL
LARGE-S
LARGE-N

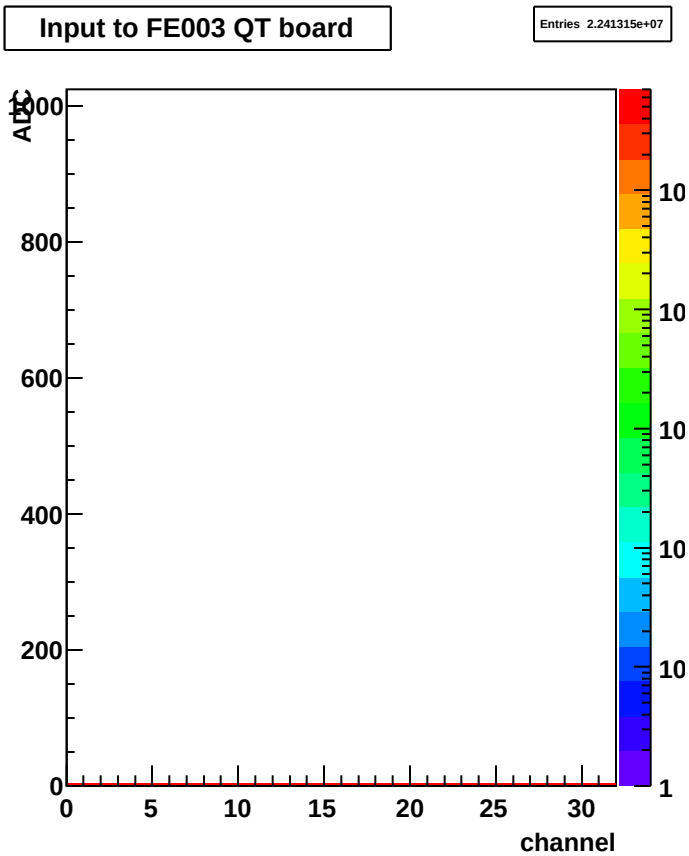

Input to FPD L2 DSM
Entries 2101233
HT bits
8114
22851
12923
16930
40738
14092
8269
634629
663468
679219
SMALL
LARGE-S
LARGE-N

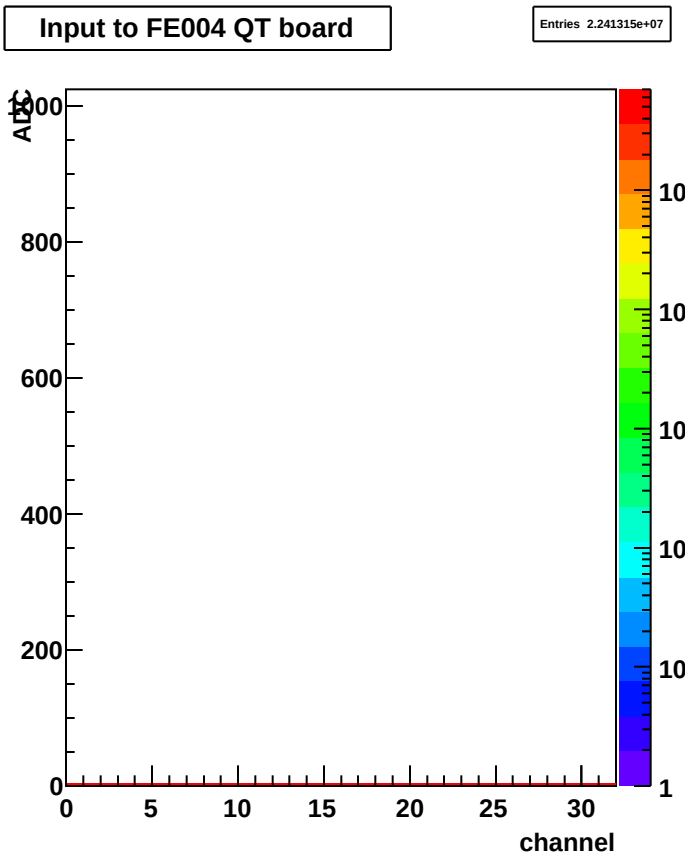

Input to FPD L2 DSM
Entries 449
HT bits - simulated
130
8
3
212
92
2
2
SMALL
LARGE-S
LARGE-N

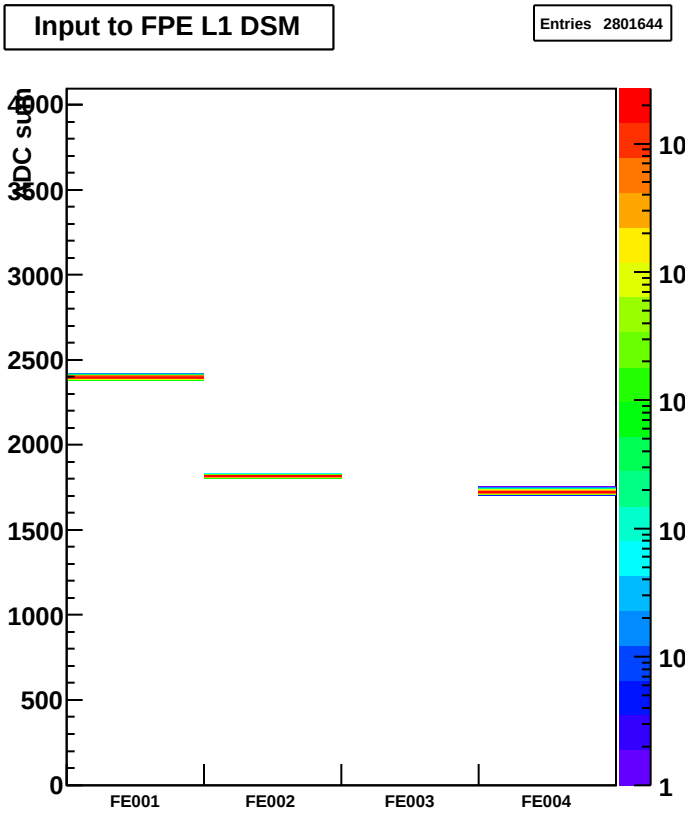

Input to FPD L2 DSM
Entries 4202466
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 2.241315e+07
ADC
channel

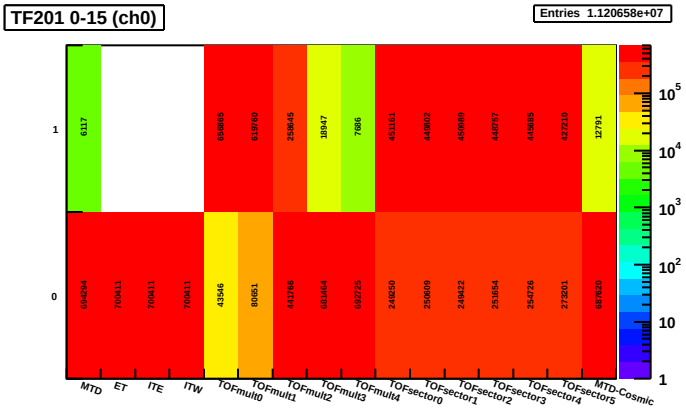

Input to FE002 QT board
Entries 2.241315e+07
ADC
channel


Input to FE003 QT board
Entries 2.241315e+07
ADC
channel

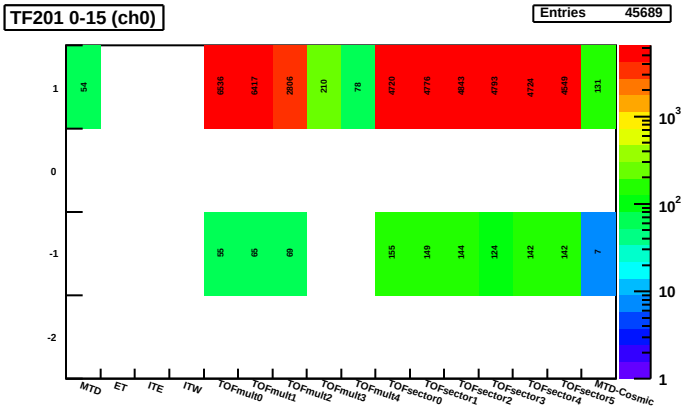

Input to FE004 QT board
Entries 2.241315e+07
ADC
channel


Input to FPE L1 DSM
Entries 2801644
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

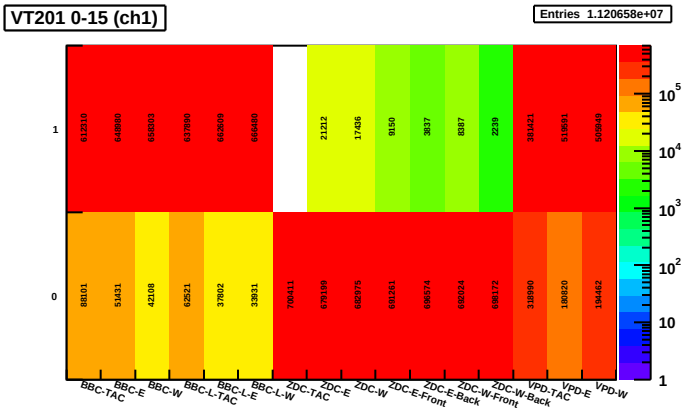
TF201 0-15 (ch0)



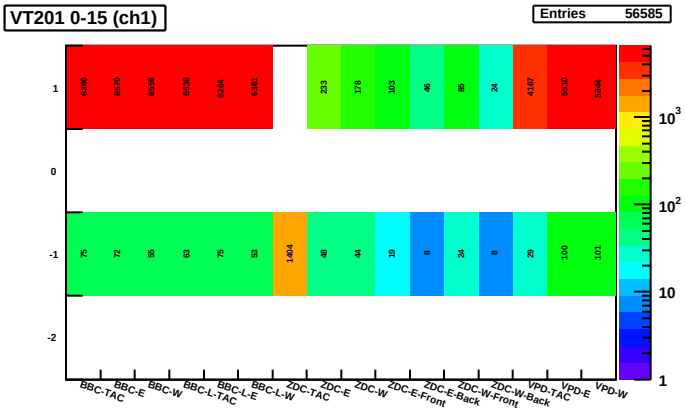
TF201 0-15 (ch0)



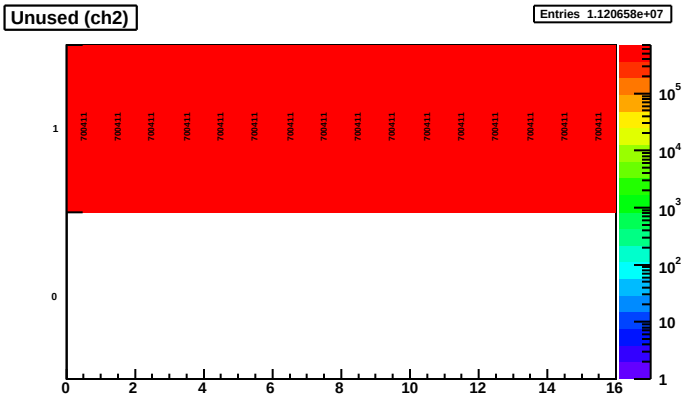
VT201 0-15 (ch1)

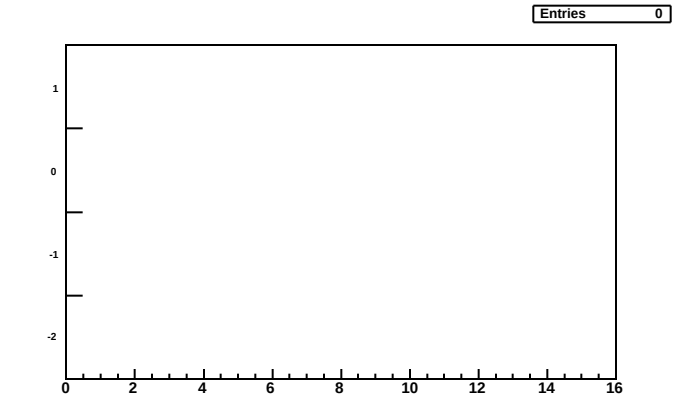


VT201 0-15 (ch1)

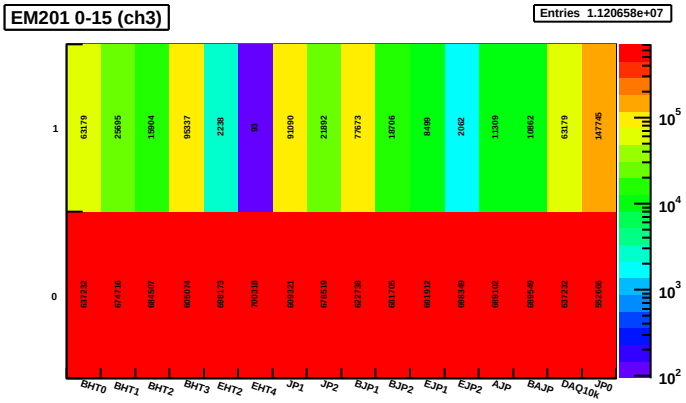


Unused (ch2)

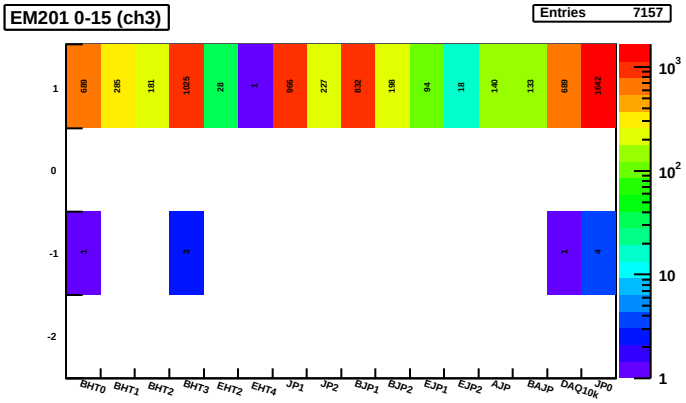




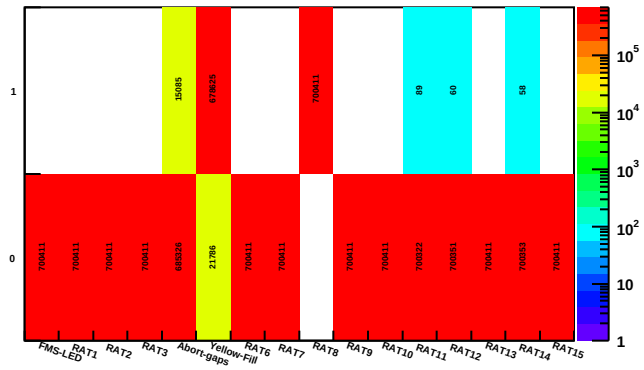
EM201 0-15 (ch3)

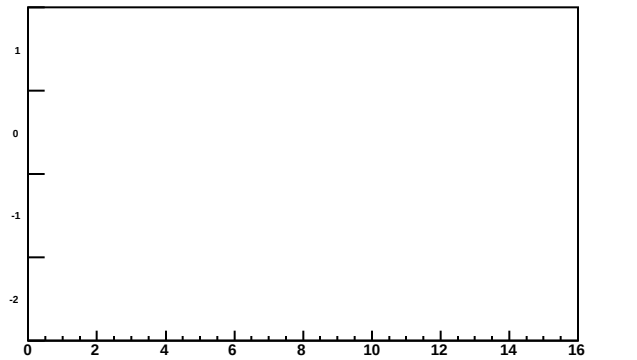


EM201 0-15 (ch3)

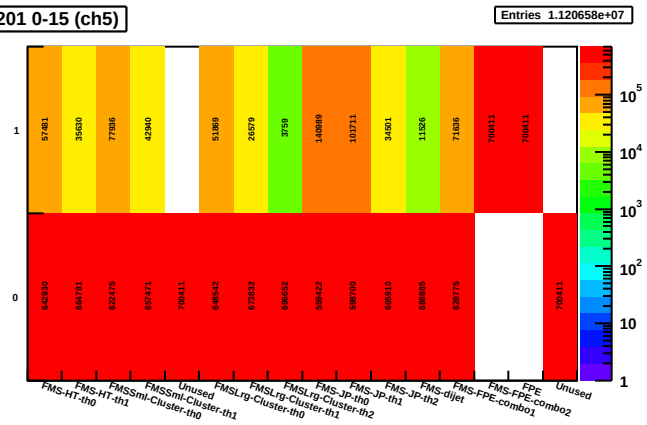


RAT board (ch4)

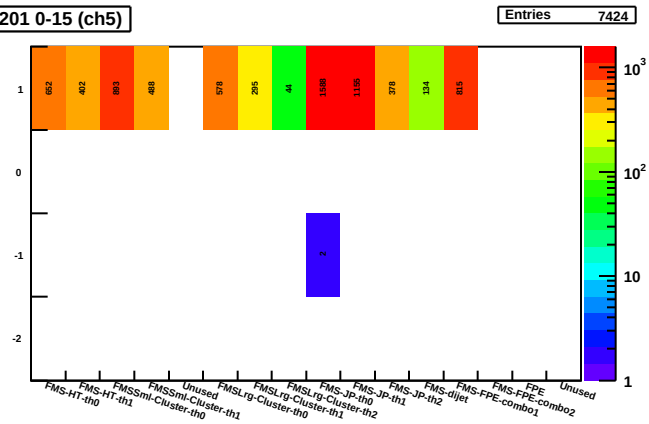




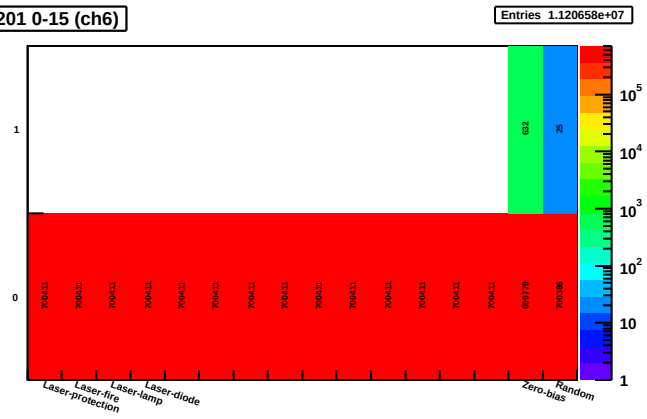
FP201 0-15 (ch5)

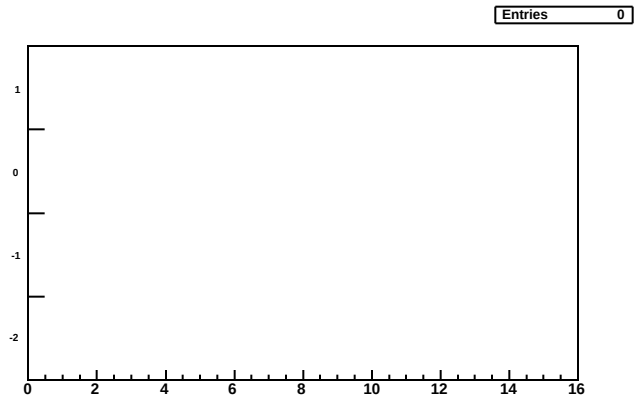
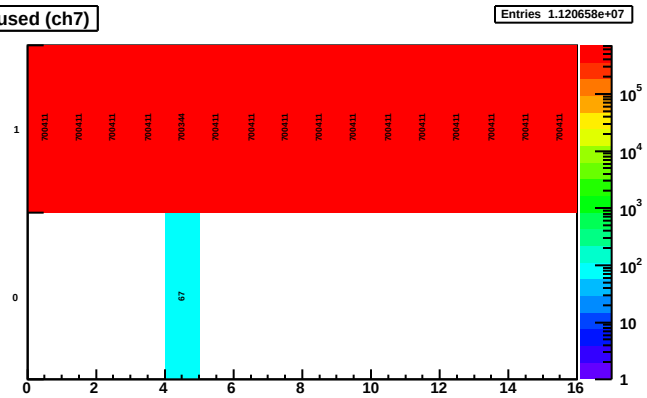
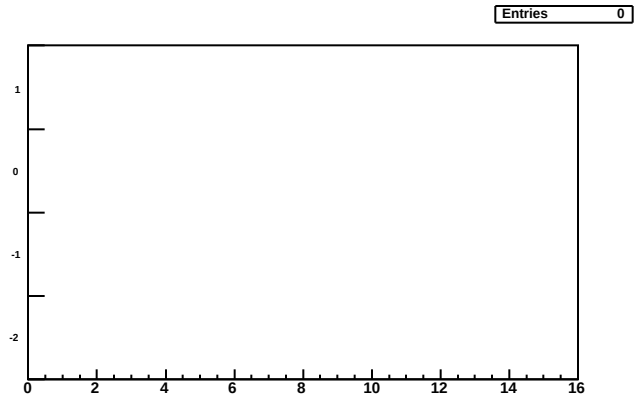


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)