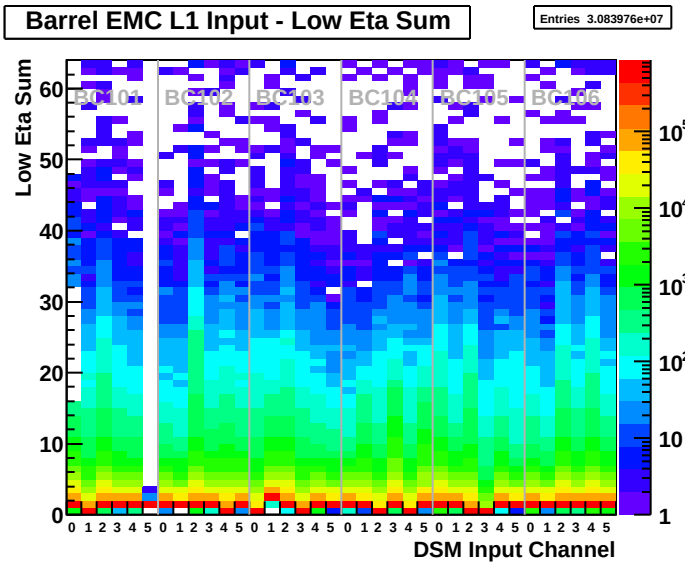
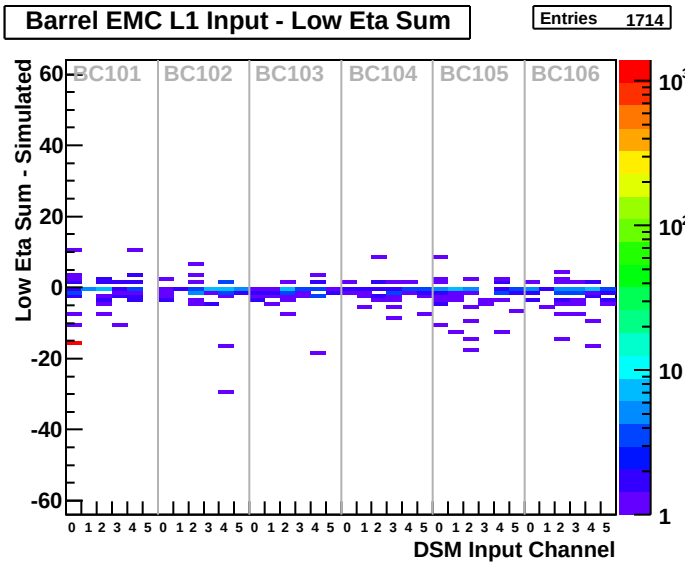
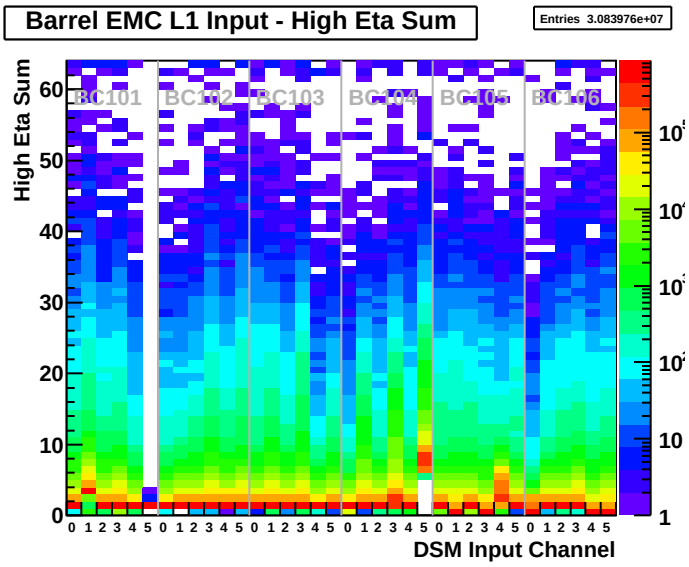
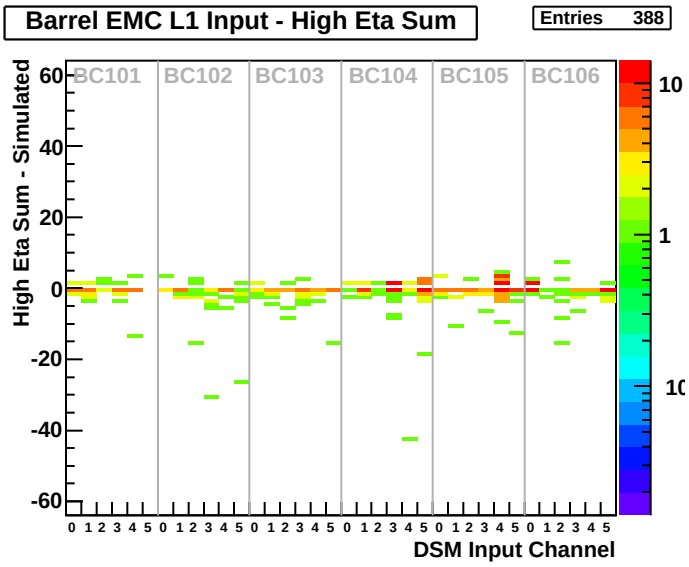
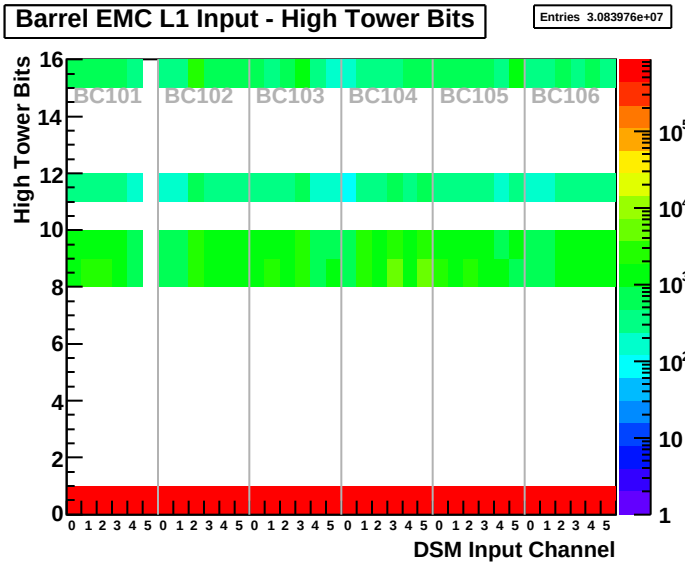
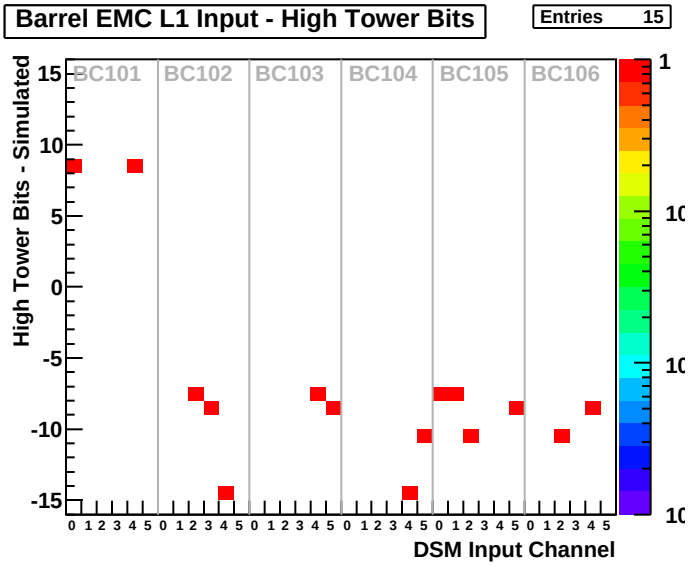

Barrel EMC L1 Input - Low Eta Sum
Entries 3.083976e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

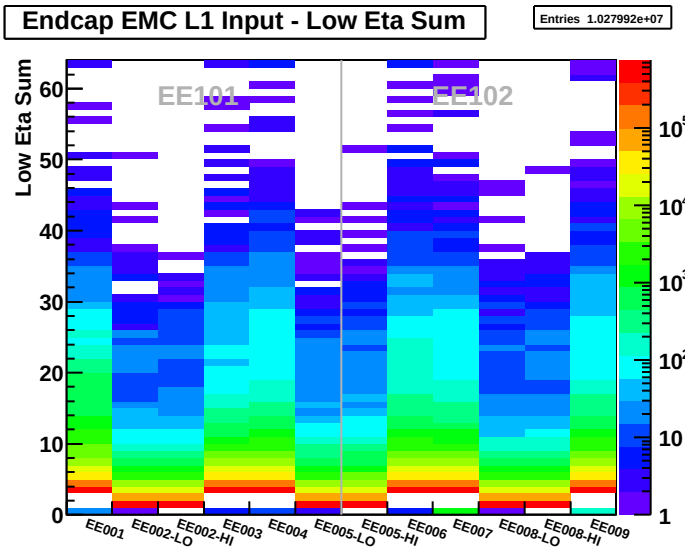

Barrel EMC L1 Input - Low Eta Sum
Entries 1714
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

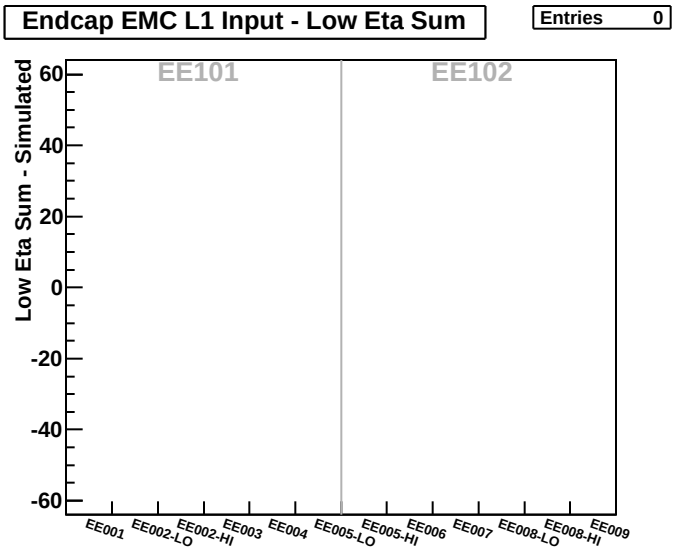

Barrel EMC L1 Input - High Eta Sum
Entries 3.083976e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

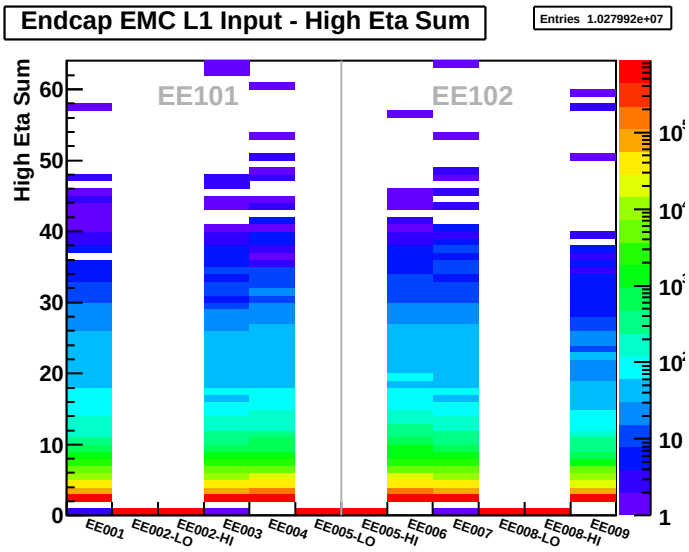

Barrel EMC L1 Input - High Eta Sum
Entries 388
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

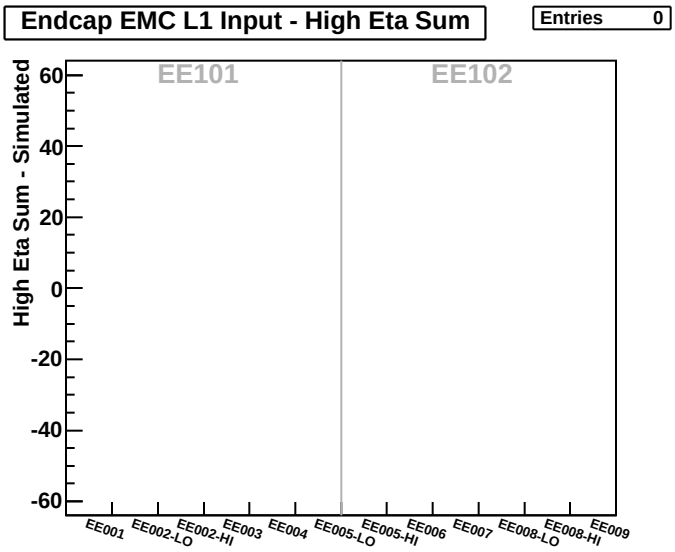

Barrel EMC L1 Input - High Tower Bits
Entries 3.083976e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

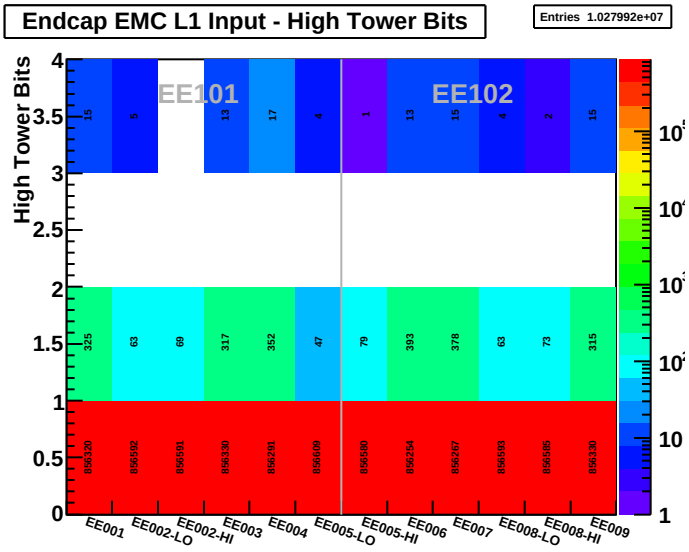

Barrel EMC L1 Input - High Tower Bits
Entries 15
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

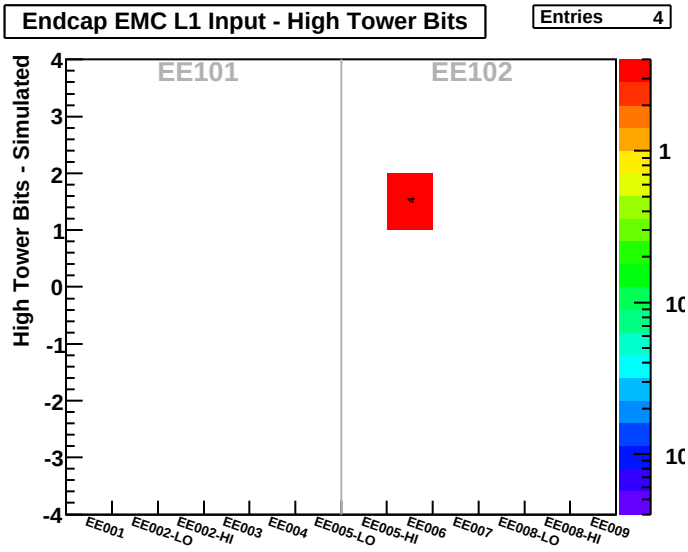

Endcap EMC L1 Input - Low Eta Sum
Entries 1.027992e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

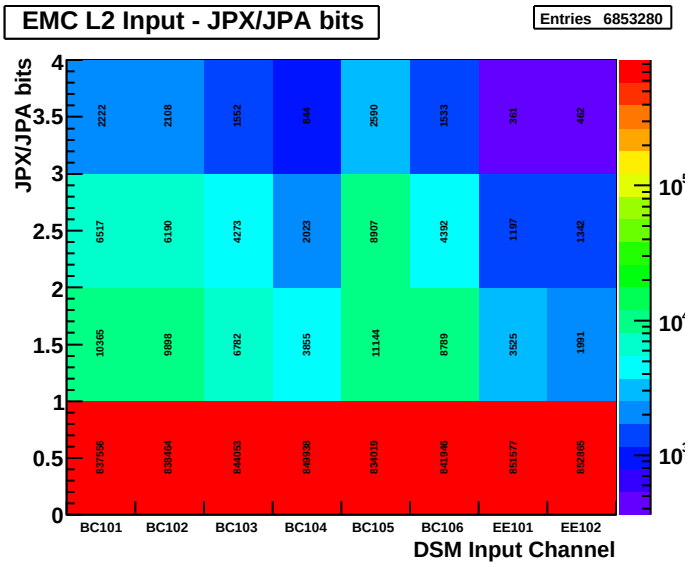

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

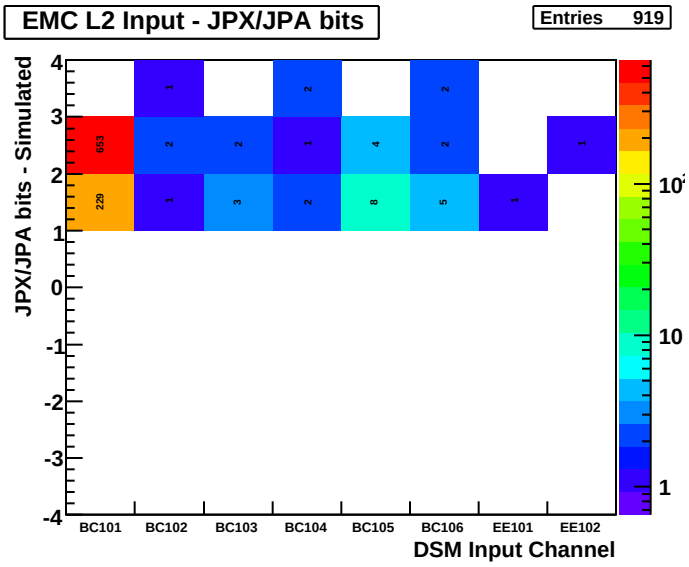

Endcap EMC L1 Input - High Eta Sum
Entries 1.027992e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

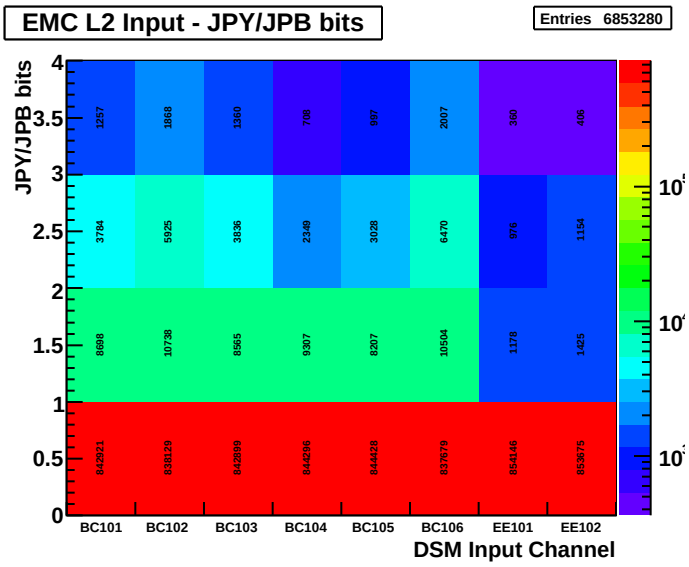

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

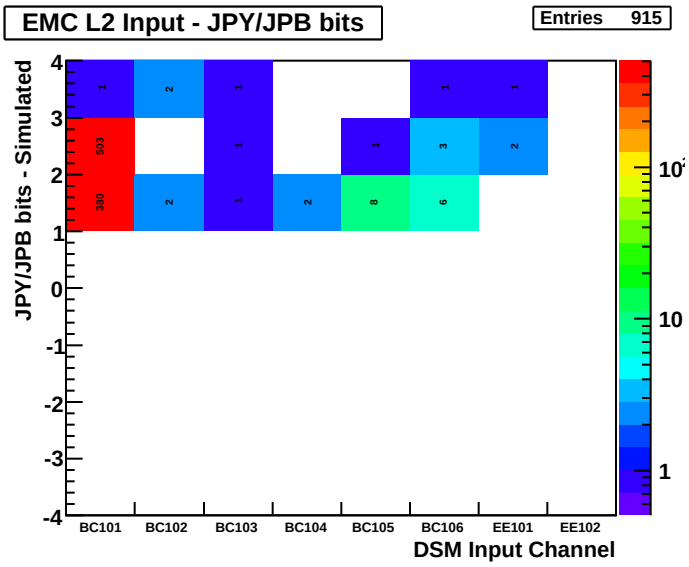

Endcap EMC L1 Input - High Tower Bits
Entries 1.027992e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

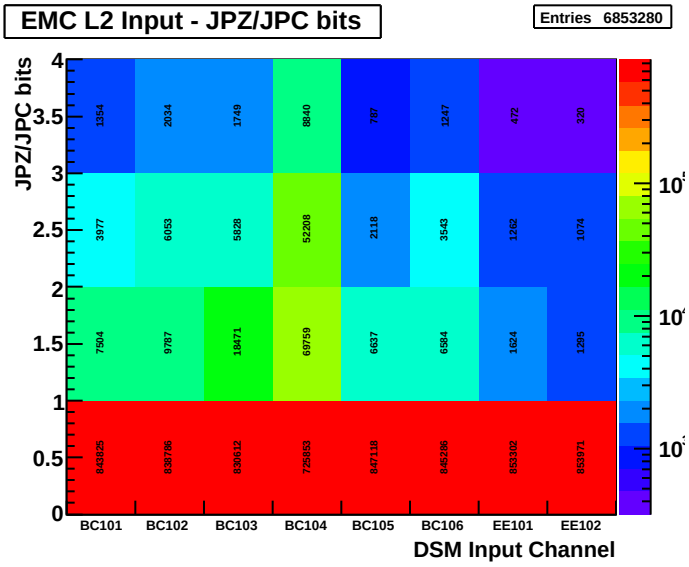

Endcap EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

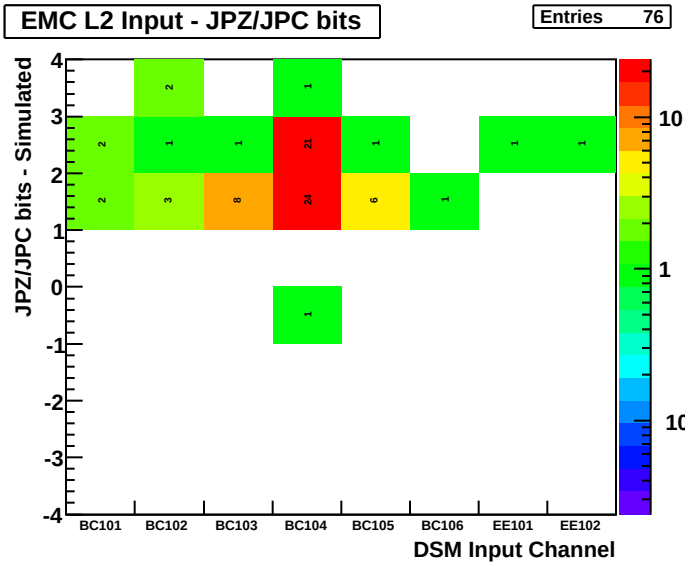

EMC L2 Input - JPX/JPA bits
Entries 6853280
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2222 2108 1552 844 2590 1533 361 462
6517 6190 4273 2023 8907 4392 1197 1342
10365 9898 6782 3855 11144 8789 3525 1991
837556 832464 844053 840938 834019 841946 851577 852865

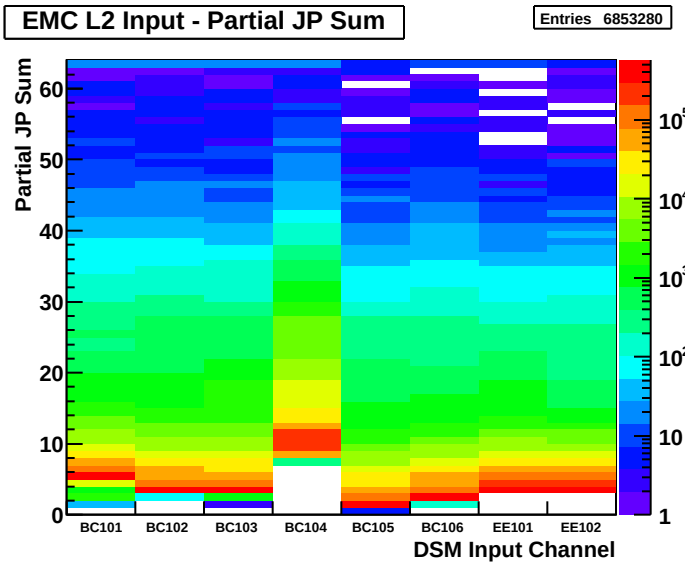

EMC L2 Input - JPX/JPA bits
Entries 919
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
653 1 2 2 1 4 2 1
229 1 3 2 8 5 1

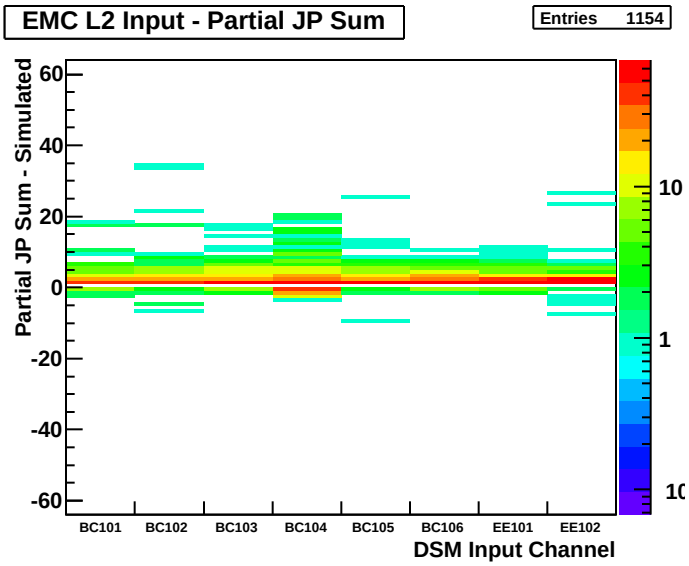

EMC L2 Input - JPY/JPB bits
Entries 6853280
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1257 1868 1360 708 997 2007 360 406
3764 5925 3636 2349 3028 6470 976 1154
8698 10738 8565 9307 8207 10504 1178 1425
842921 838129 842899 844296 844428 837679 854146 853675

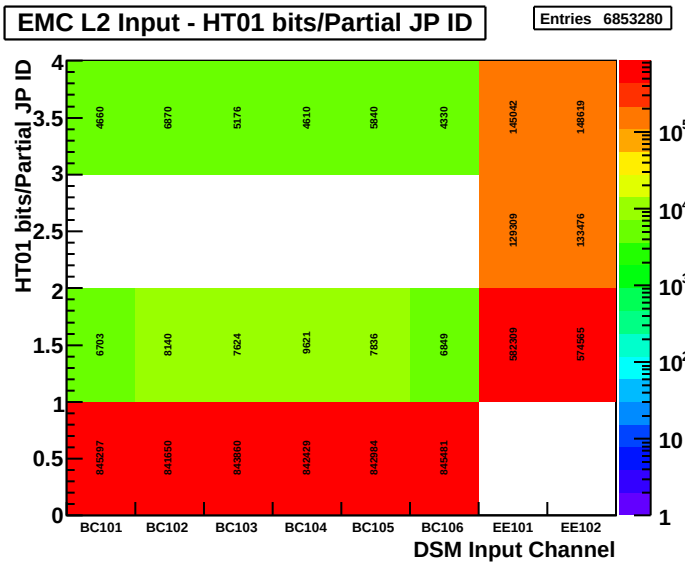

EMC L2 Input - JPY/JPB bits
Entries 915
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 1 1 1
503 1 1 3 2
380 2 1 2 8 6

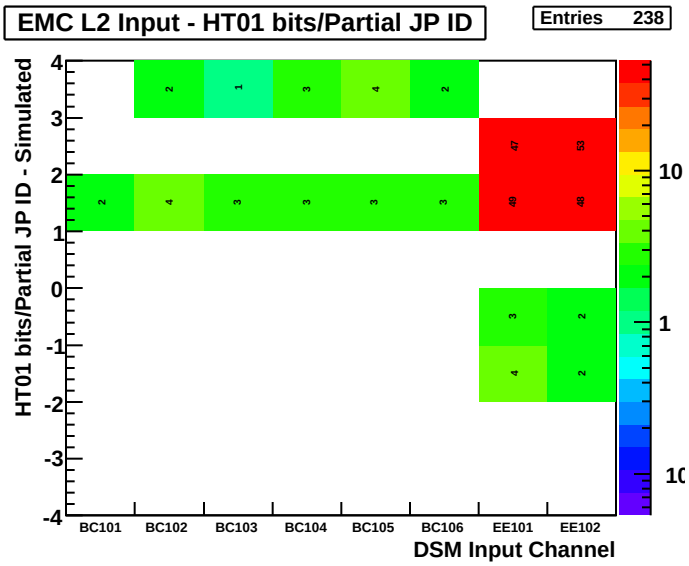

EMC L2 Input - JPZ/JPC bits
Entries 6853280
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1354 2034 1749 8840 787 1247 472 320
3977 6053 5828 52208 2118 3543 1262 1074
7504 9787 18471 69759 6637 6584 1624 1295
843825 838786 830512 725853 847118 845286 853302 853971

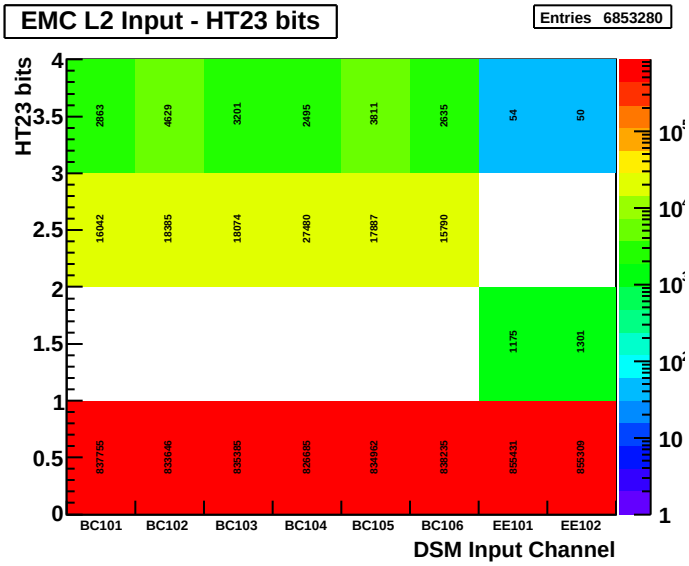

EMC L2 Input - JPZ/JPC bits
Entries 76
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 1
2 1 1 21 1 1 1
2 3 8 24 6 1
1

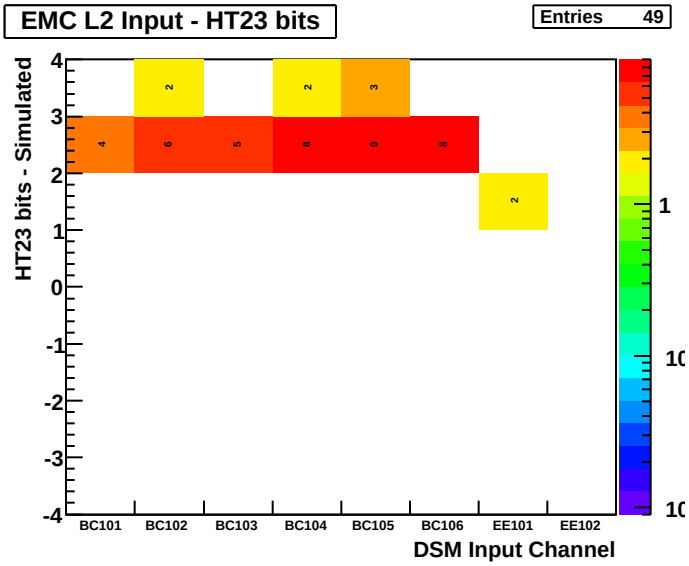

EMC L2 Input - Partial JP Sum
Entries 6853280
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

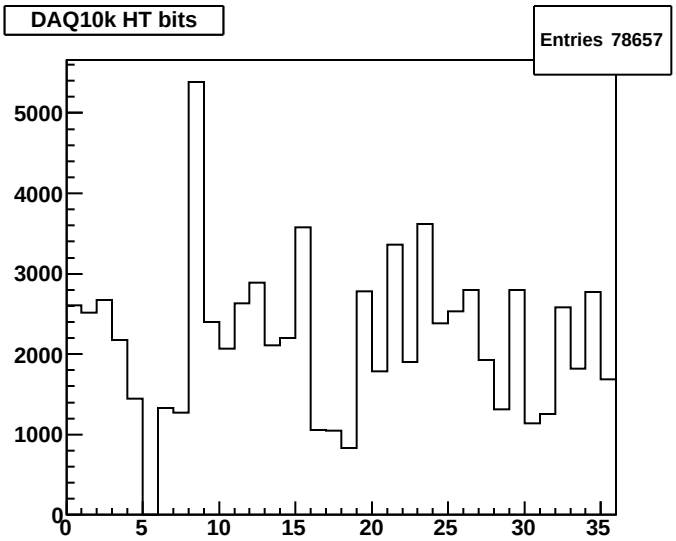

EMC L2 Input - Partial JP Sum
Entries 1154
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

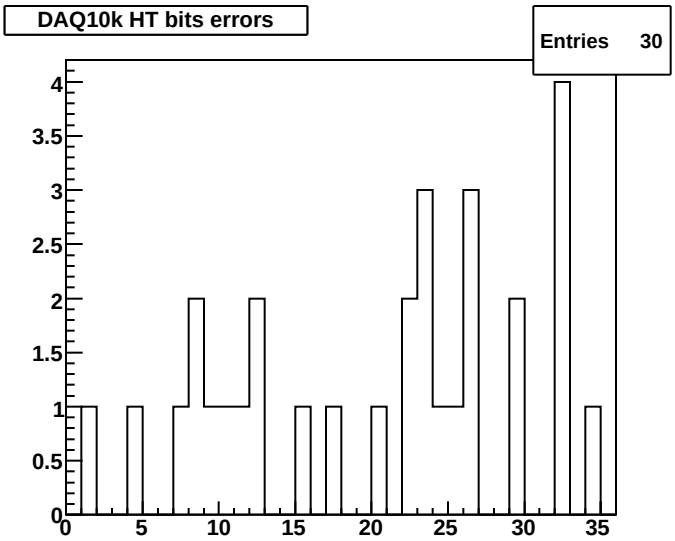

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6853280
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

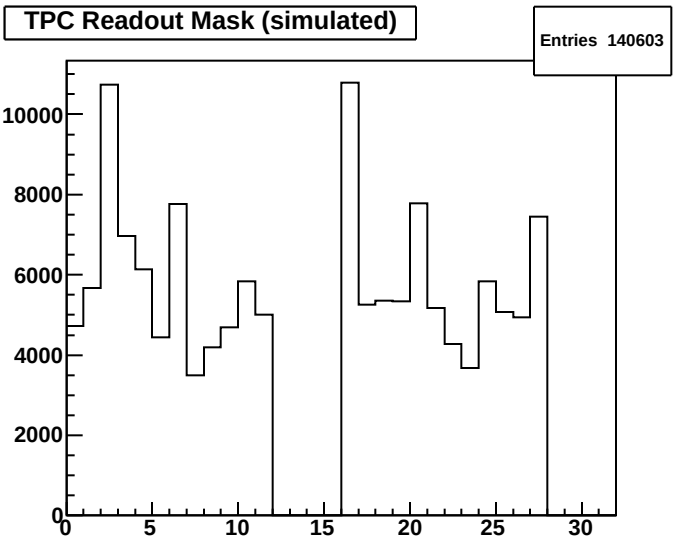

EMC L2 Input - HT01 bits/Partial JP ID
Entries 238
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

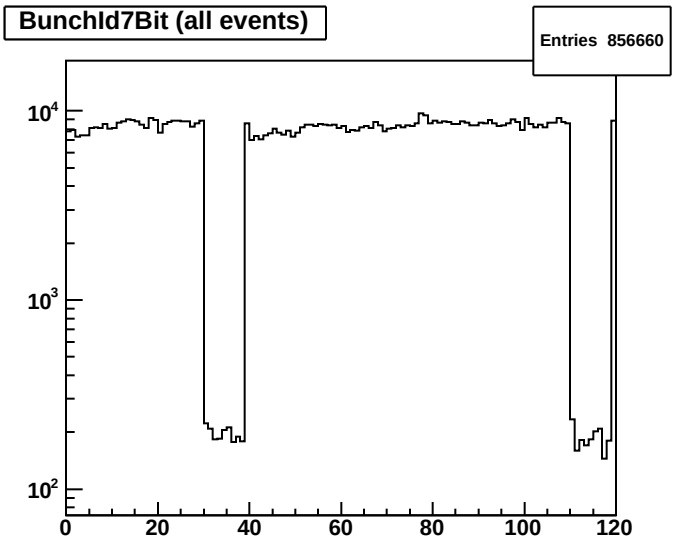

EMC L2 Input - HT23 bits
Entries 6853280
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

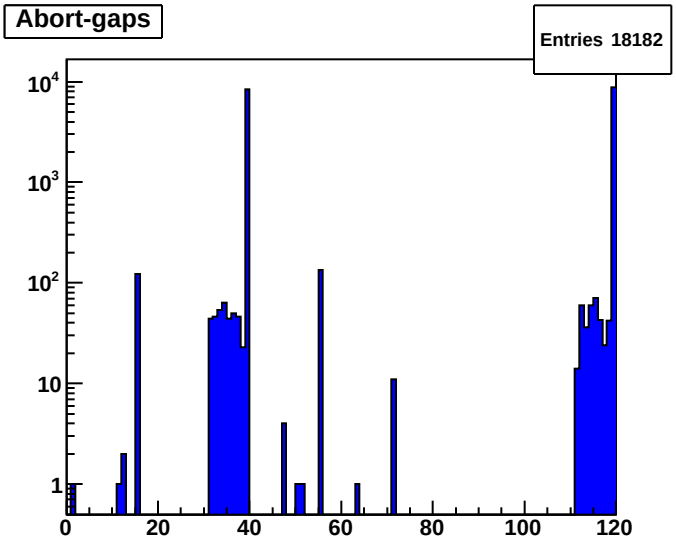

EMC L2 Input - HT23 bits
Entries 49
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

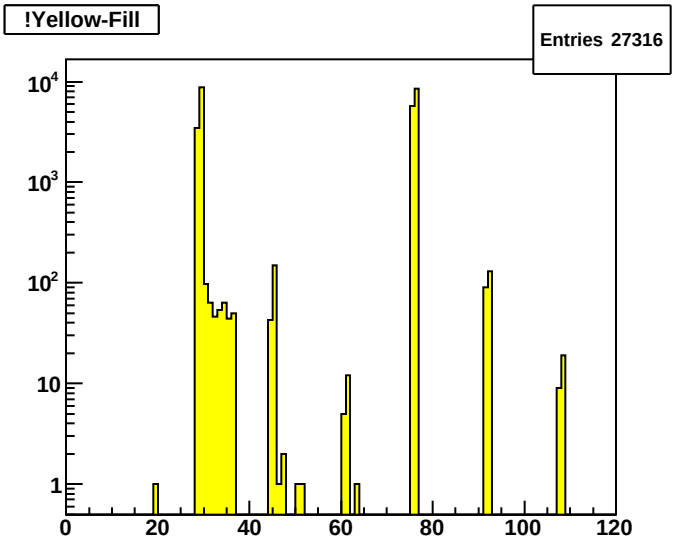

DAQ10k HT bits
Entries 78657

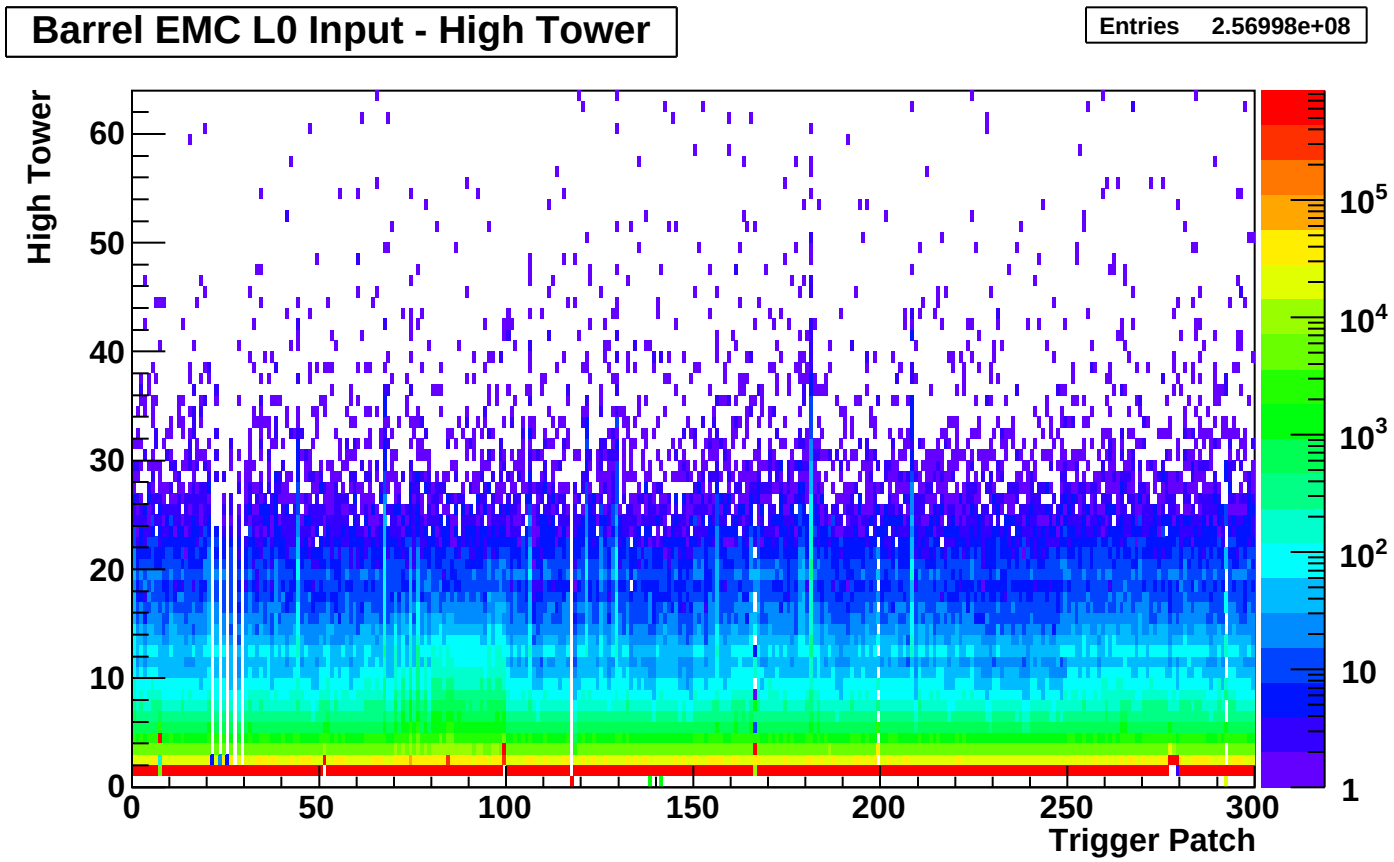

DAQ10k HT bits errors
Entries 30

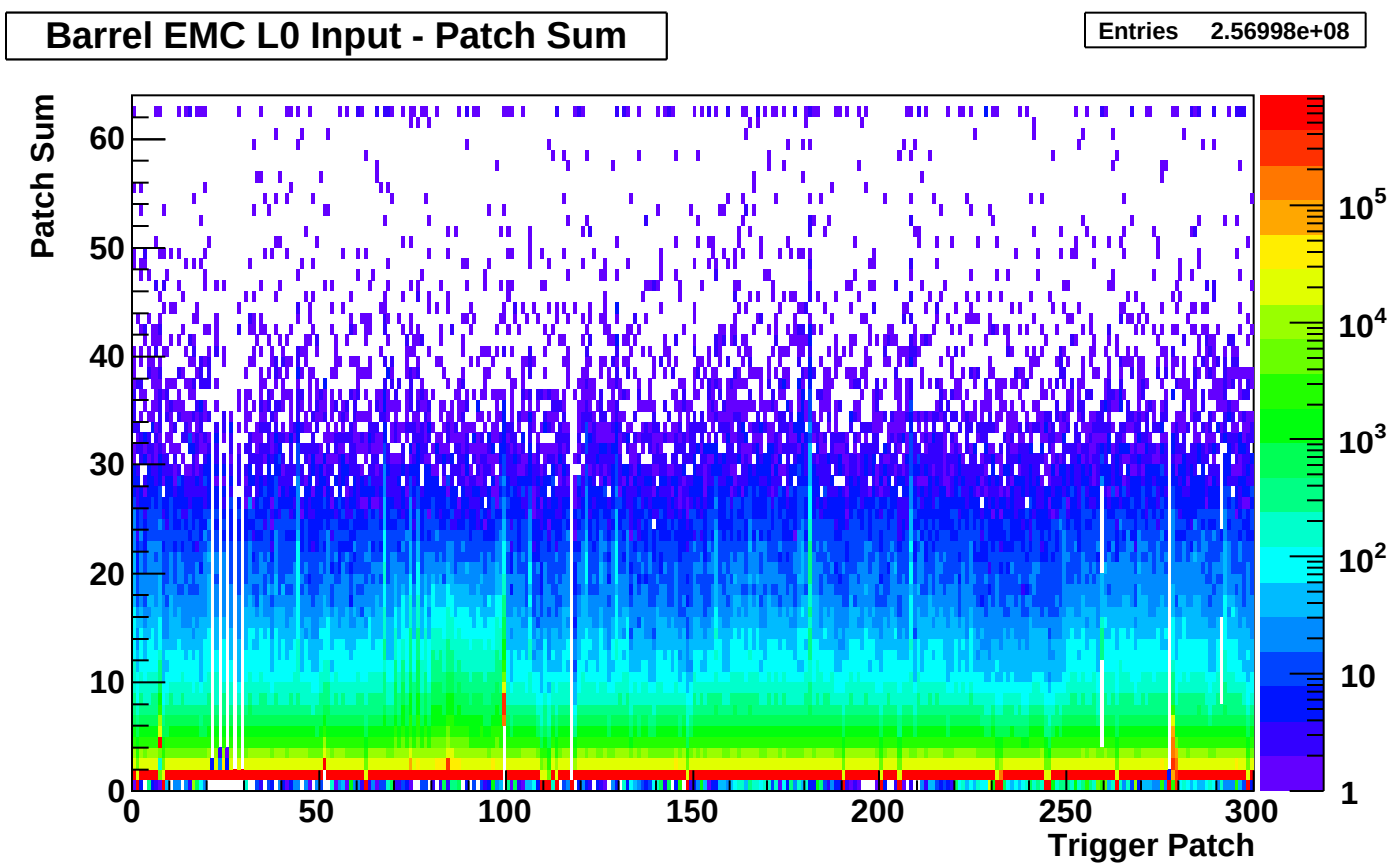

TPC Readout Mask (simulated)
Entries 140603

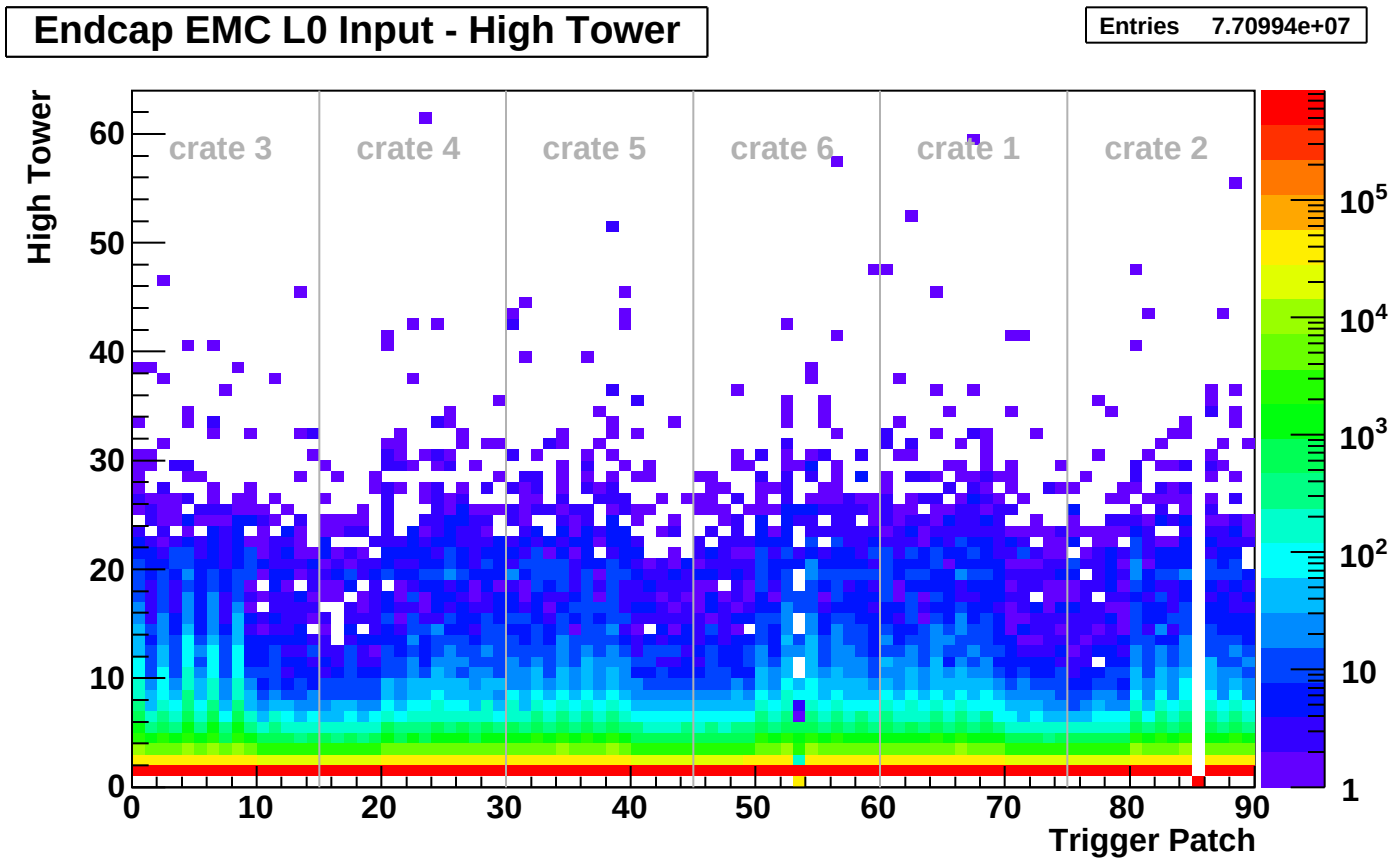

BunchId7Bit (all events)
Entries 856660

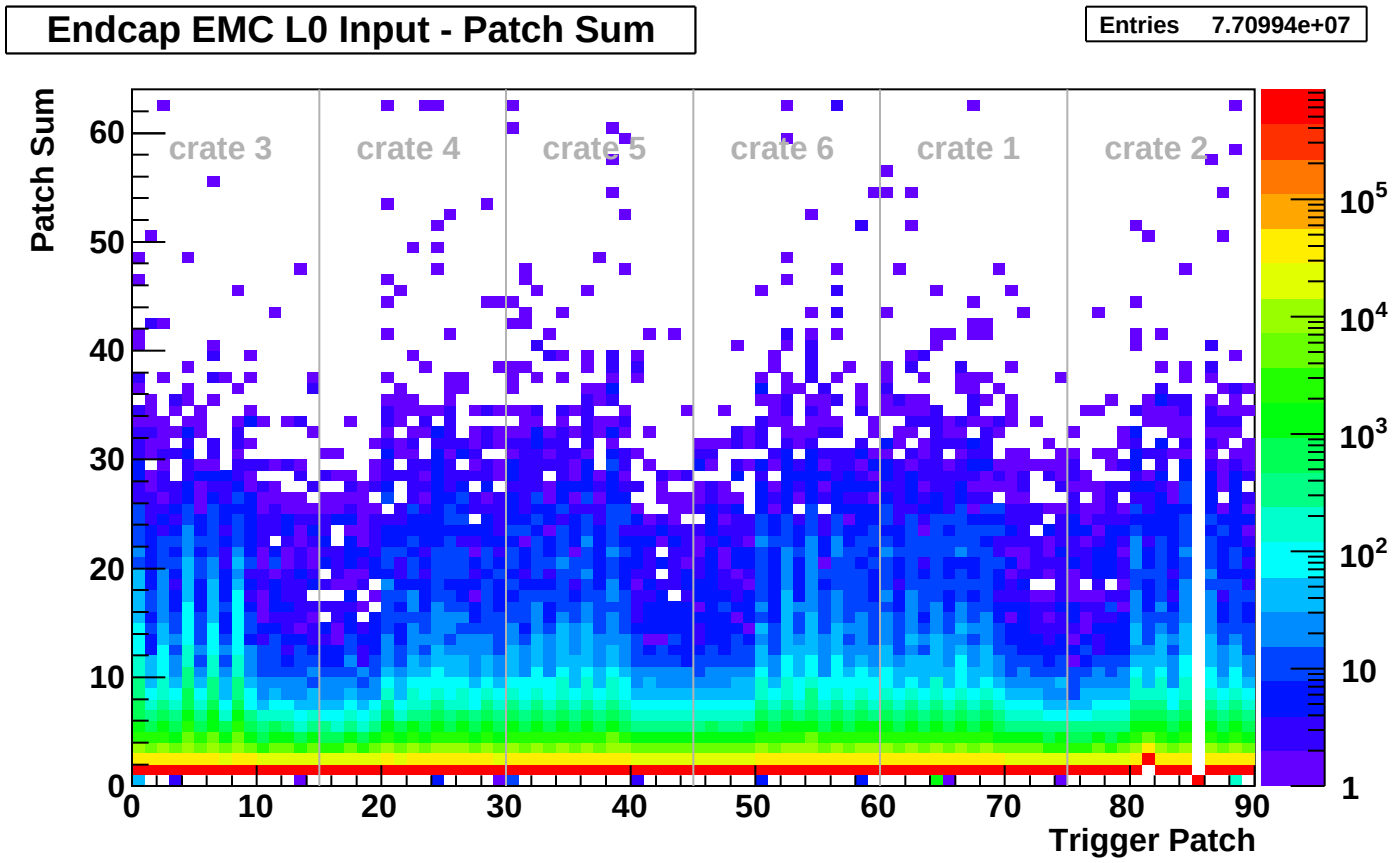

Abort-gaps
Entries 18182

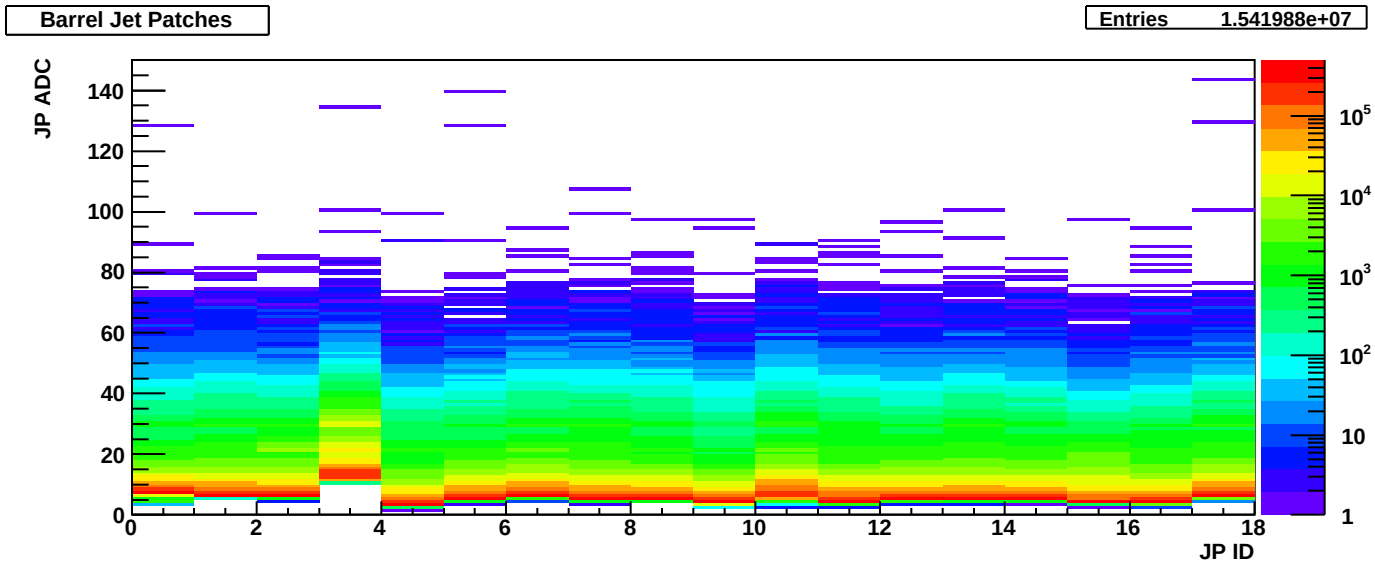

!Yellow-Fill
Entries 27316

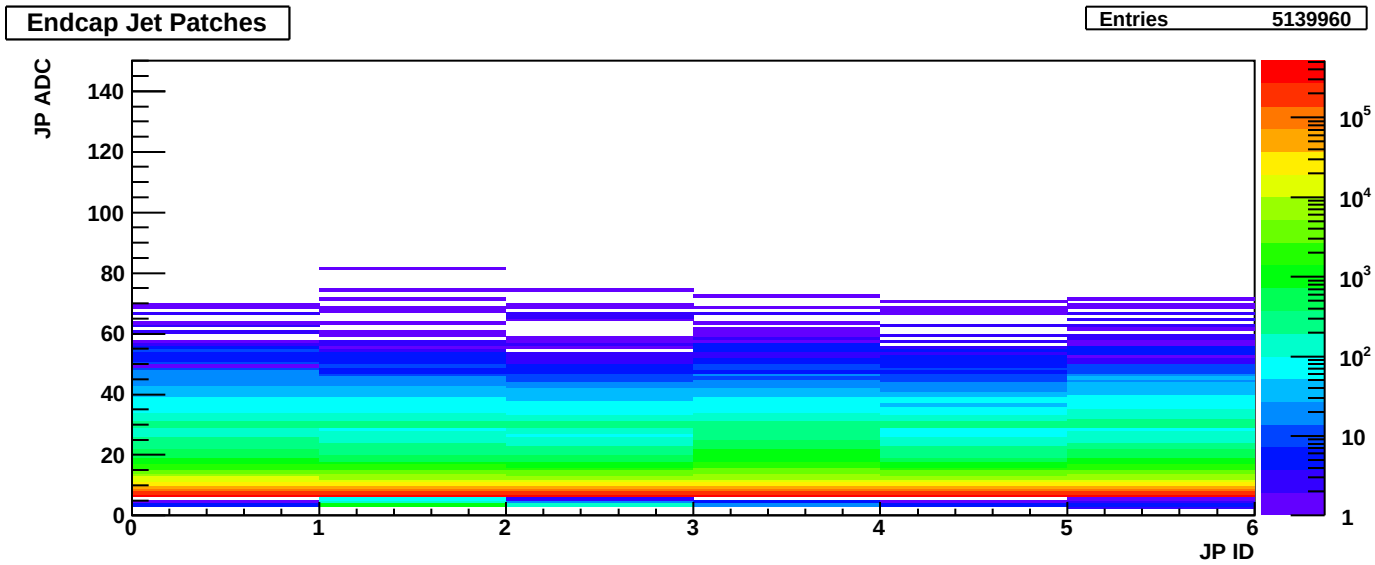

Barrel EMC L0 Input - High Tower
Entries 2.56998e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

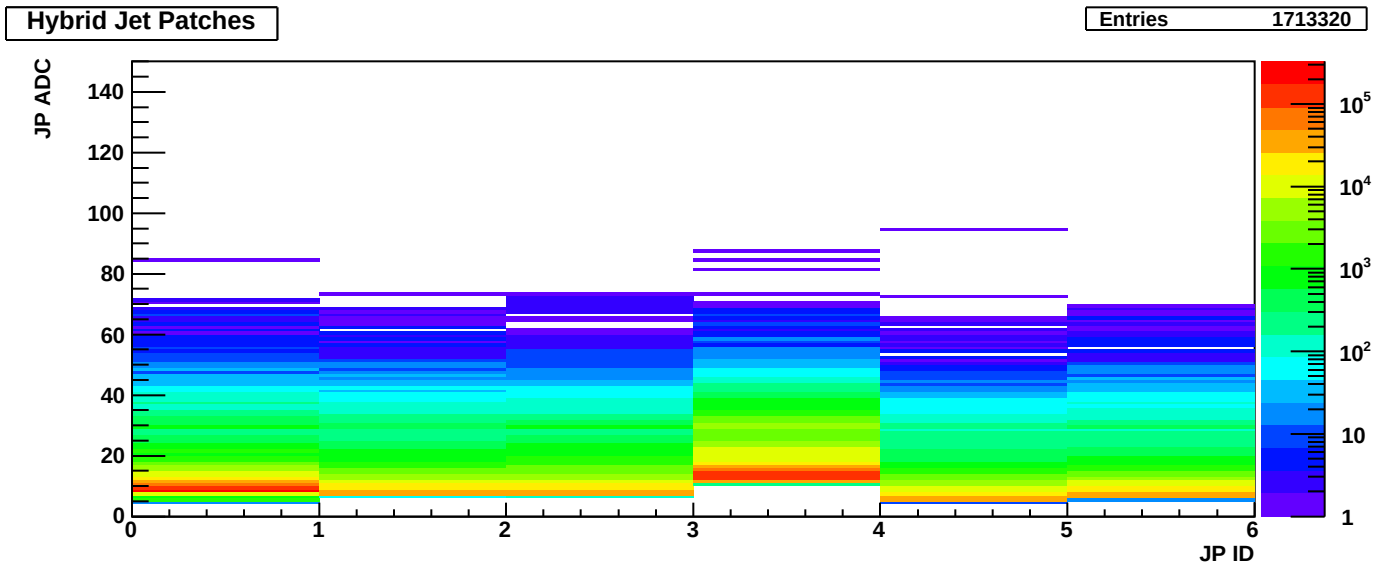

Barrel EMC L0 Input - Patch Sum
Entries 2.56998e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

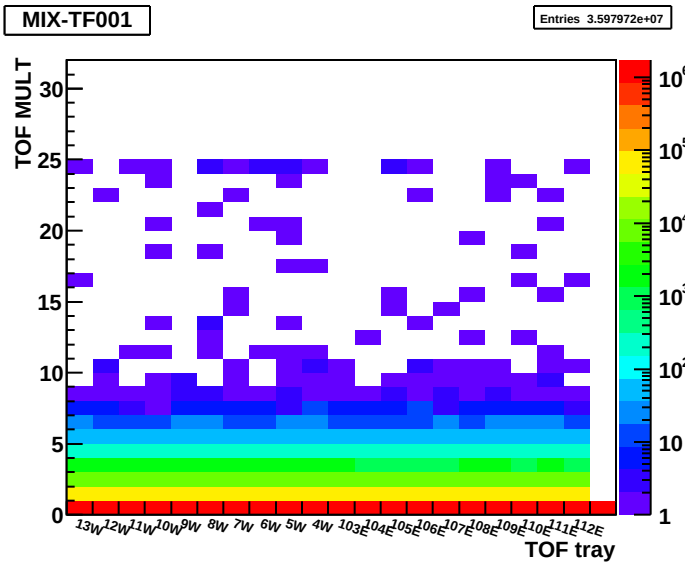

Endcap EMC L0 Input - High Tower
Entries 7.70994e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

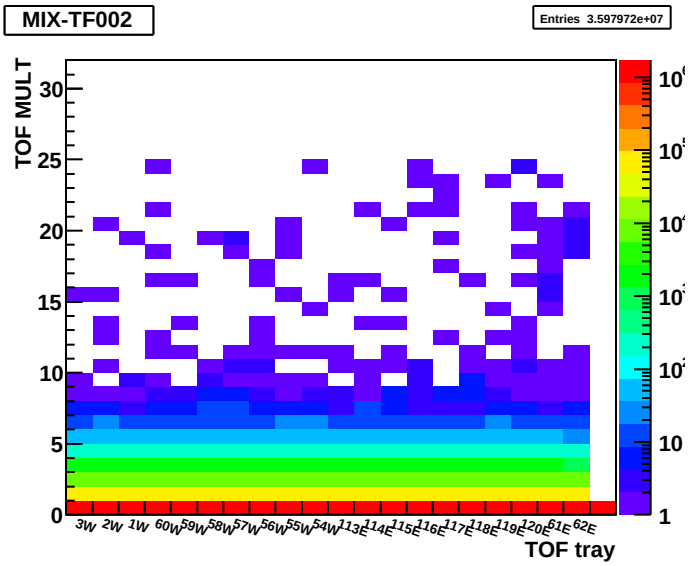

Endcap EMC L0 Input - Patch Sum
Entries 7.70994e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

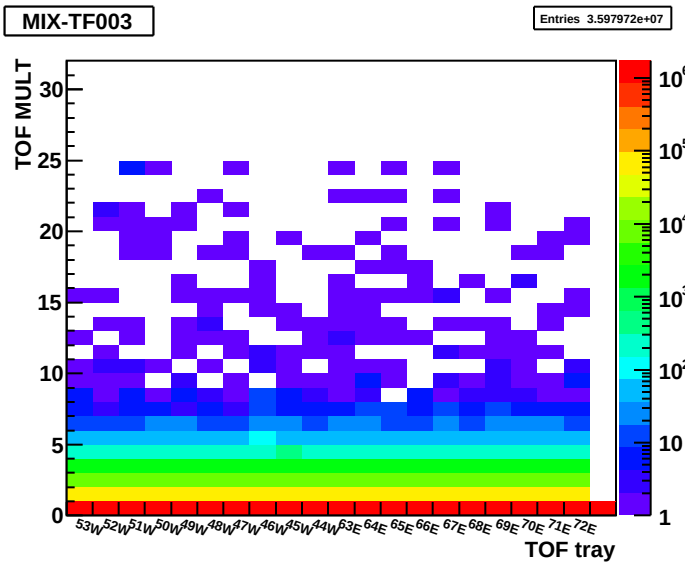

Barrel Jet Patches
Entries 1.541988e+07
JP ADC
JP ID

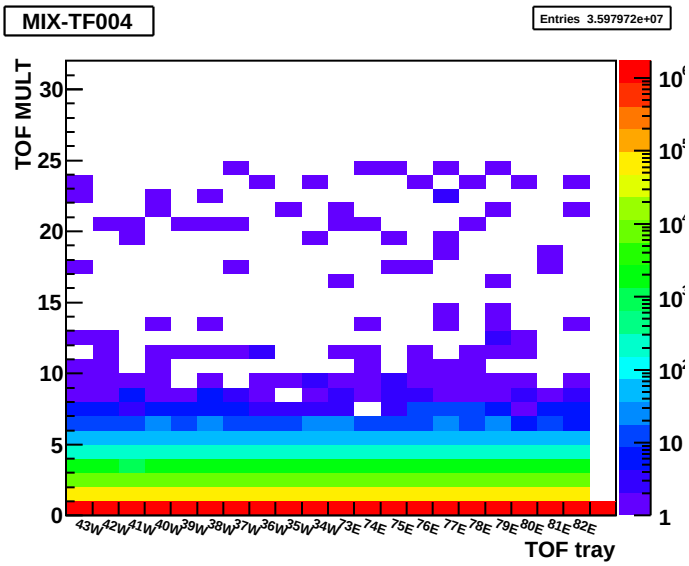

Endcap Jet Patches
Entries 5139960
JP ADC
JP ID

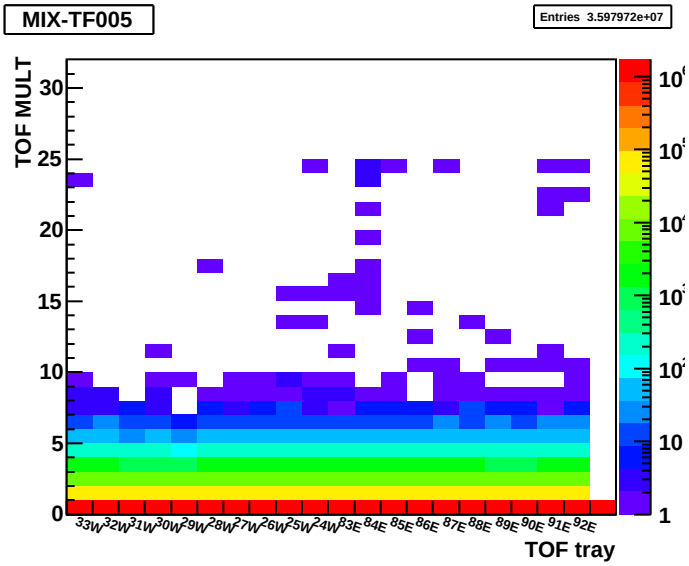

Hybrid Jet Patches
Entries 1713320
JP ADC
JP ID

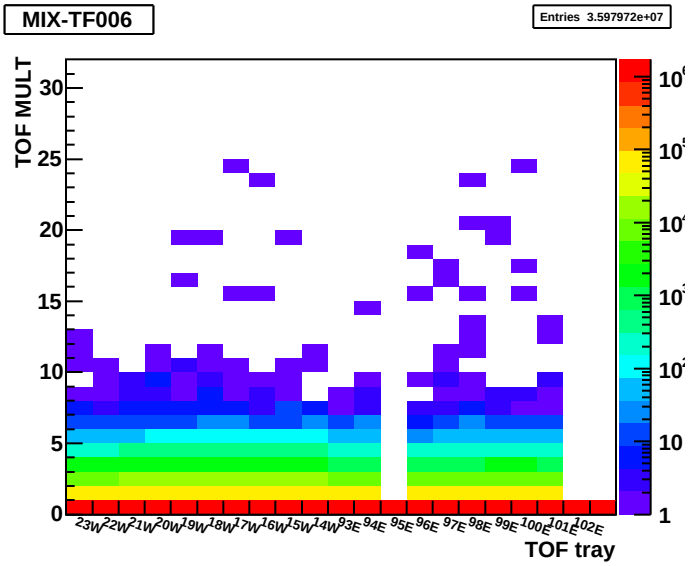

MIX-TF001
Entries 3.597972e+07
TOF MULT
TOF tray

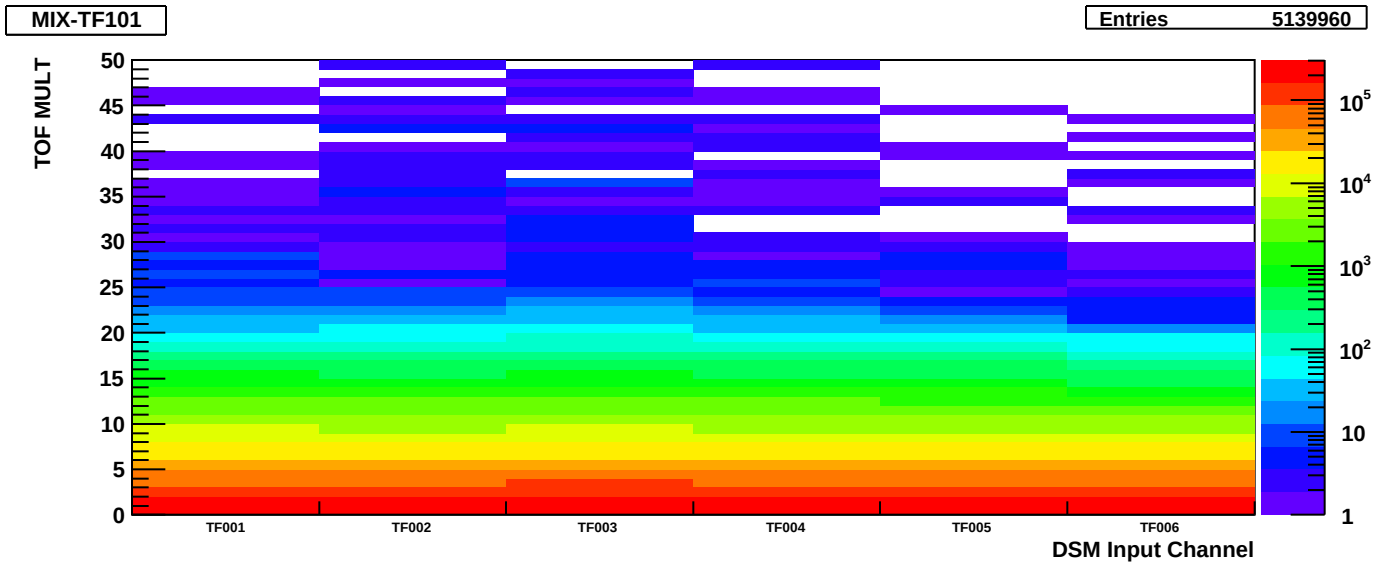

MIX-TF002
Entries 3.597972e+07
TOF MULT
TOF tray

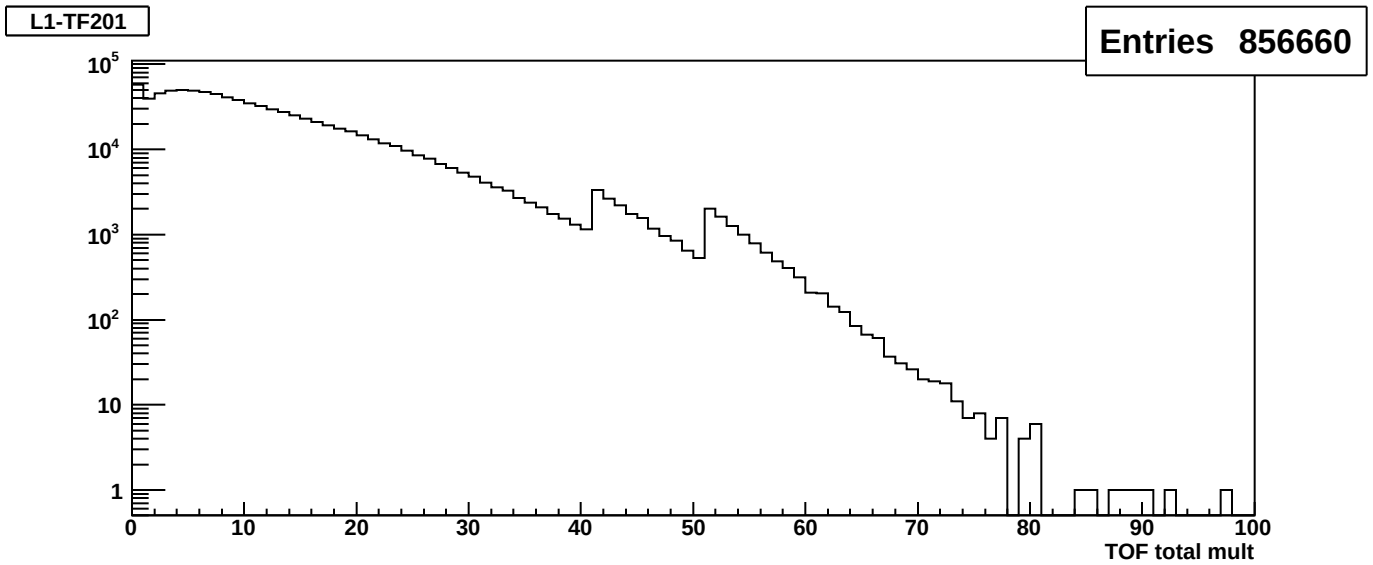

MIX-TF003
Entries 3.597972e+07
TOF MULT
TOF tray

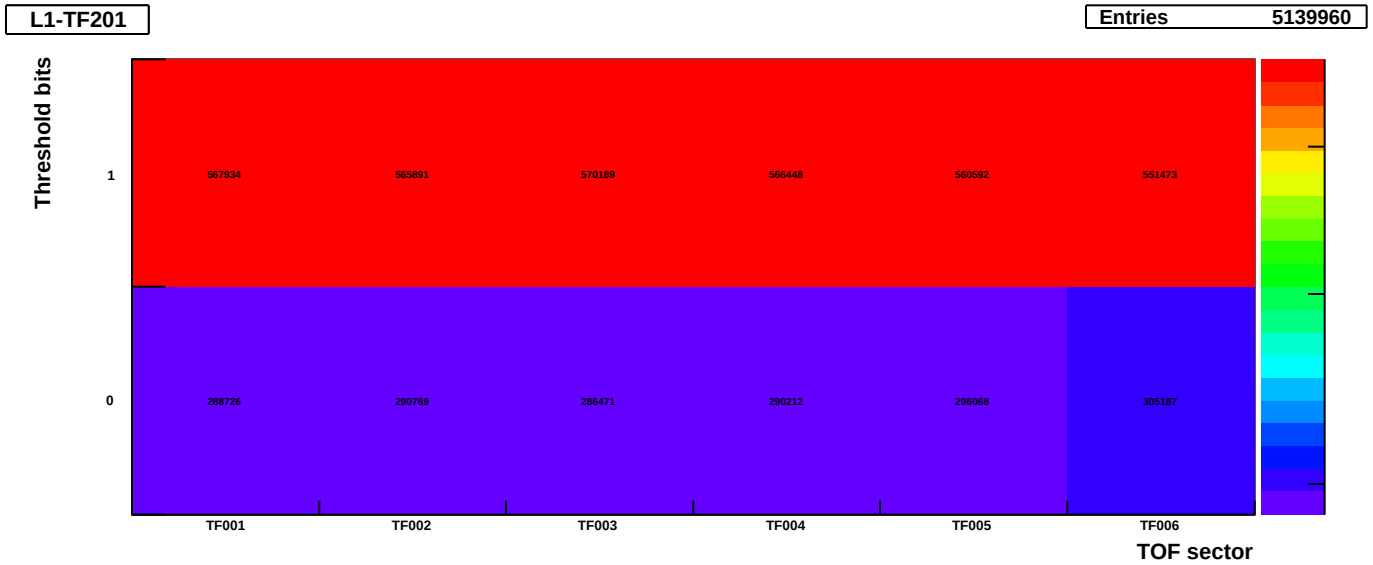

MIX-TF004
Entries 3.597972e+07
TOF MULT
TOF tray

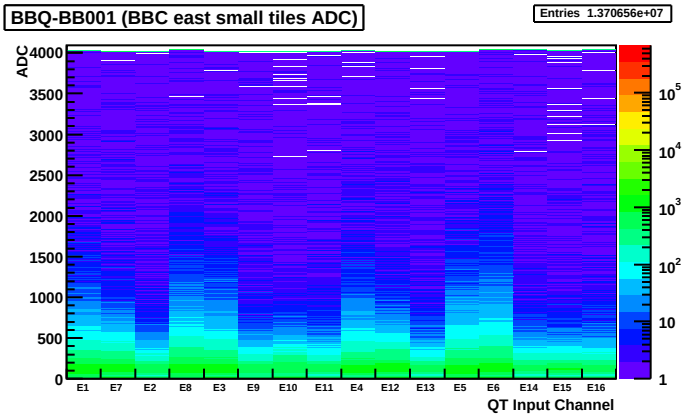

MIX-TF005
Entries 3.597972e+07
TOF MULT
TOF tray

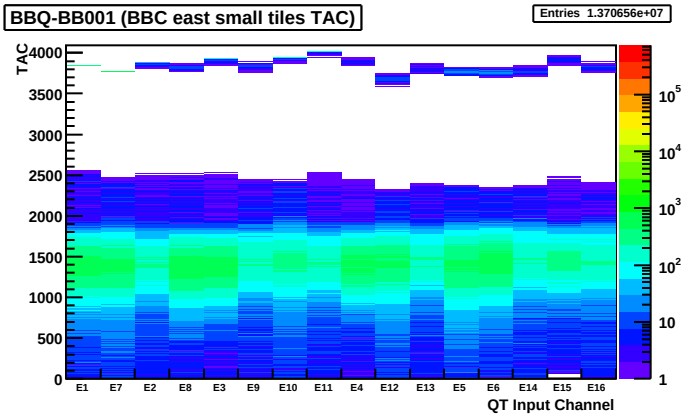

MIX-TF006
Entries 3.597972e+07
TOF MULT
TOF tray

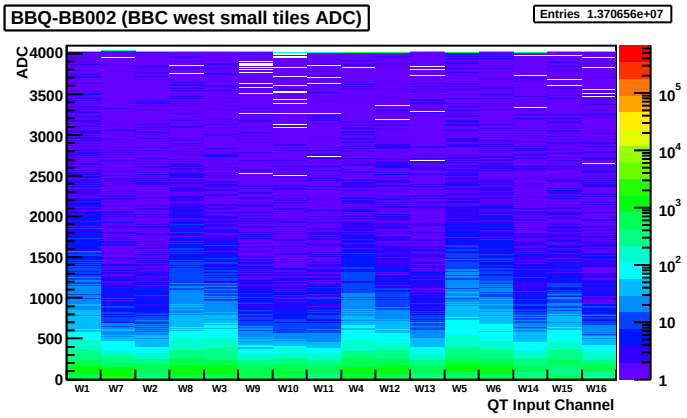

MIX-TF101
Entries 5139960
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

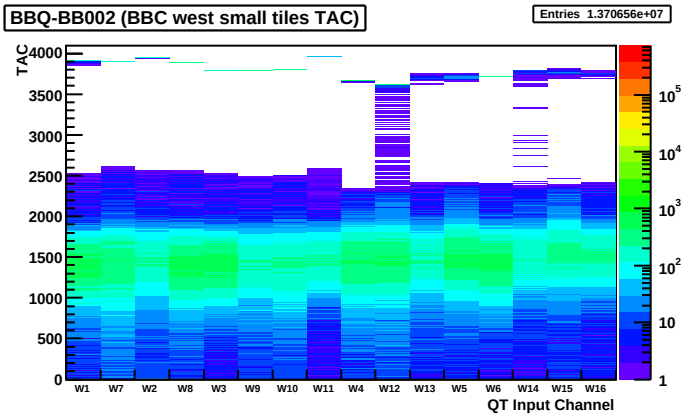

L1-TF201
Entries 856660
TOF total mult

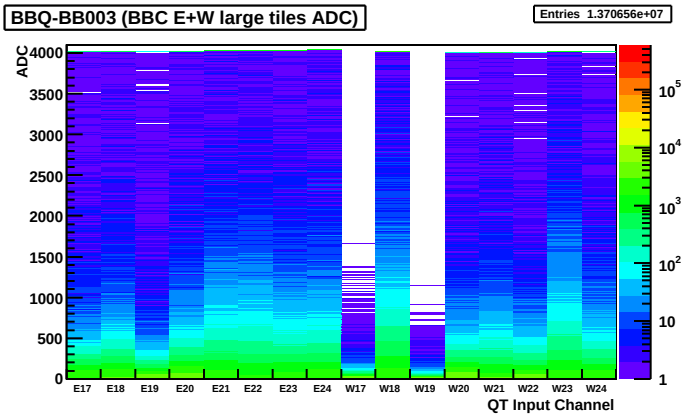

L1-TF201
Entries 5139960
Threshold bits
567934
565891
570189
566448
560592
551473
288726
290769
286471
290212
296068
305187
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

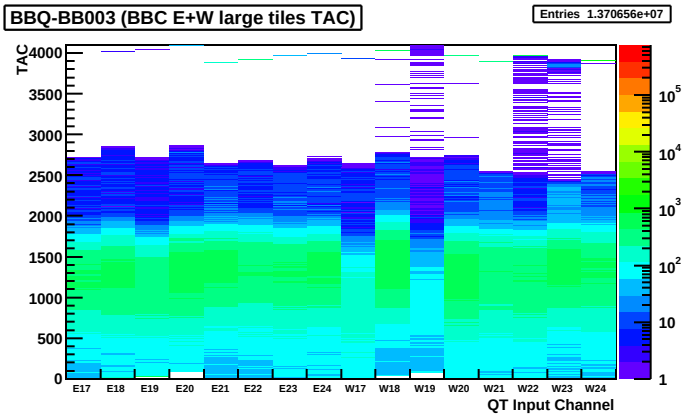

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.370656e+07
ADC
QT Input Channel

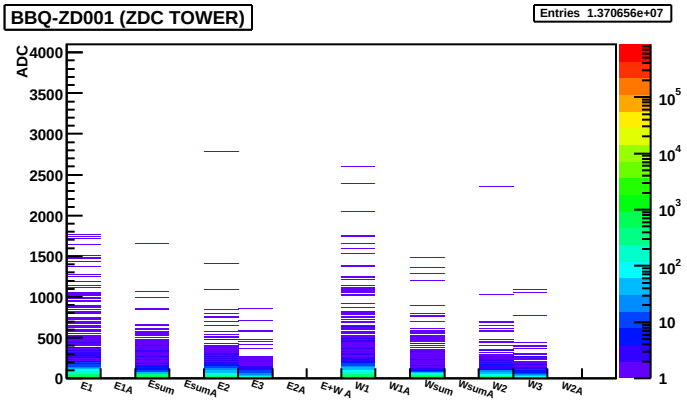

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.370656e+07
TAC
QT Input Channel

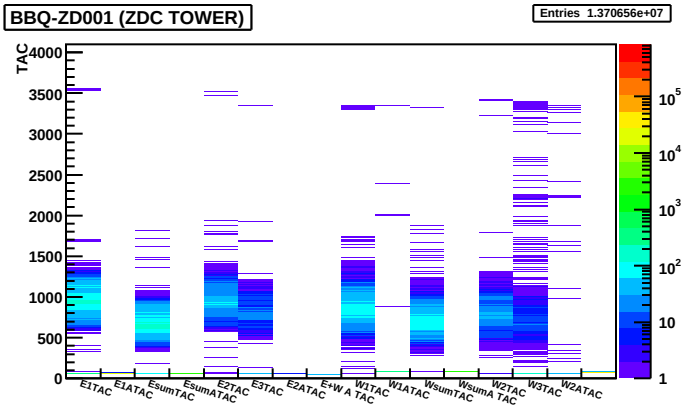

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.370656e+07
ADC
QT Input Channel

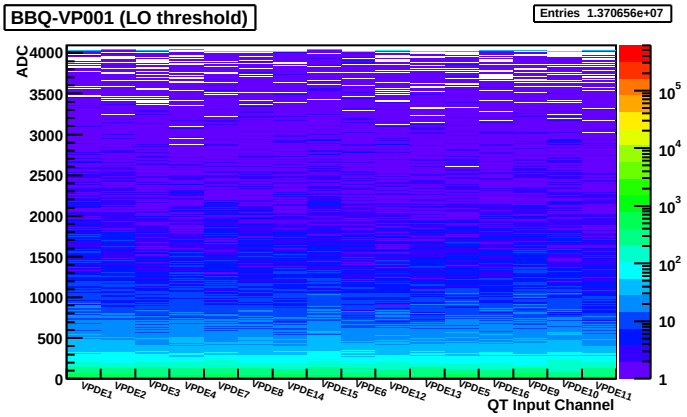

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.370656e+07
TAC
QT Input Channel

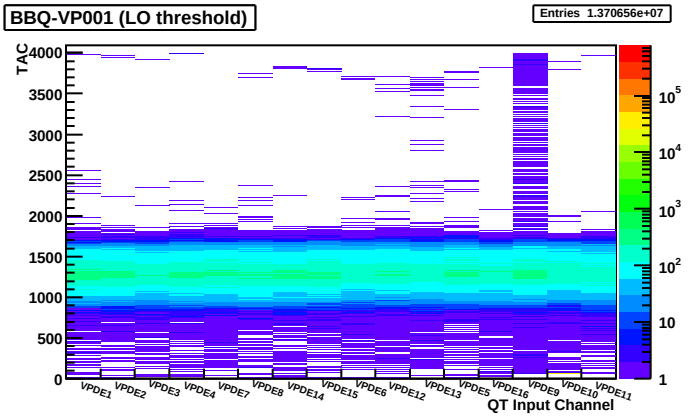

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.370656e+07
ADC
QT Input Channel

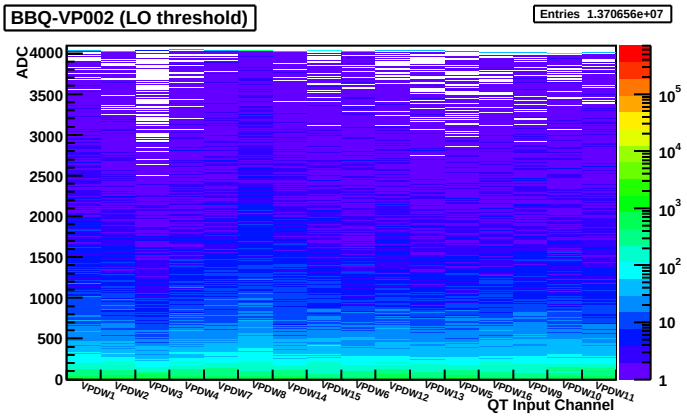

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.370656e+07
TAC
QT Input Channel

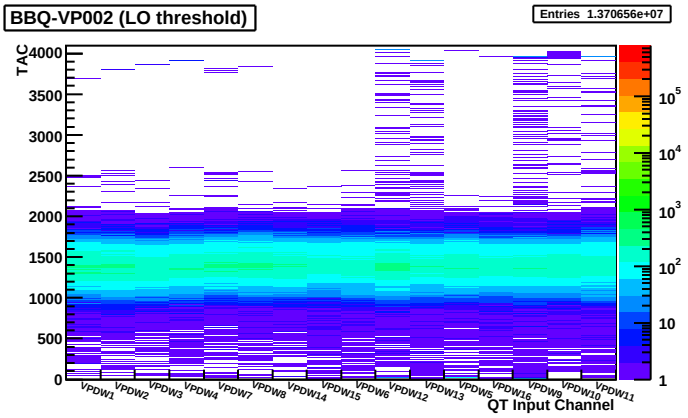

BBQ-ZD001 (ZDC TOWER)
Entries 1.370656e+07
ADC

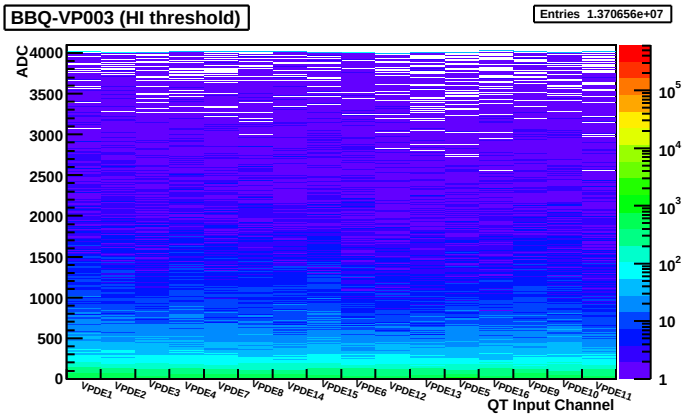

BBQ-ZD001 (ZDC TOWER)
Entries 1.370656e+07
TAC

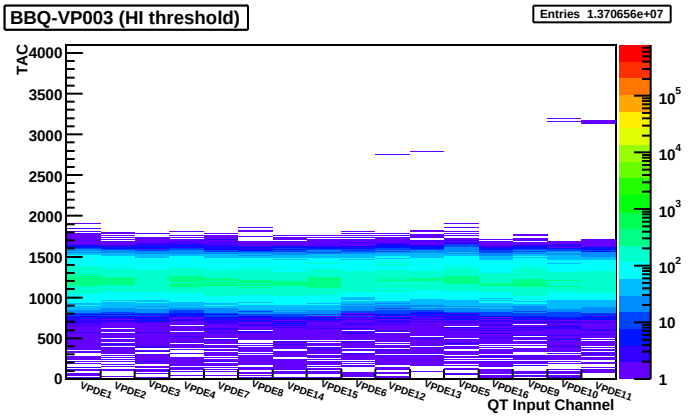

BBQ-VP001 (LO threshold)
Entries 1.370656e+07
ADC
QT Input Channel

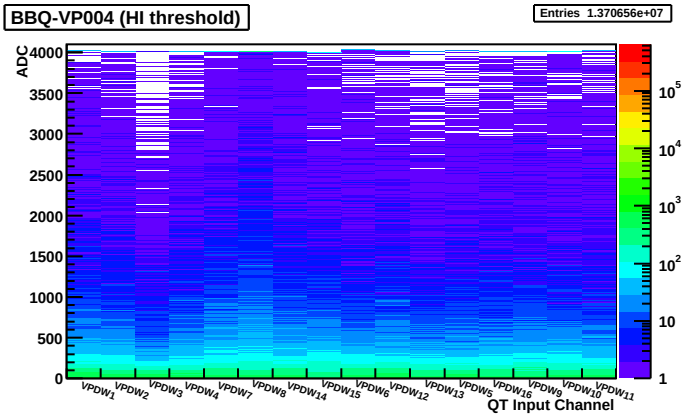

BBQ-VP001 (LO threshold)
Entries 1.370656e+07
TAC
QT Input Channel

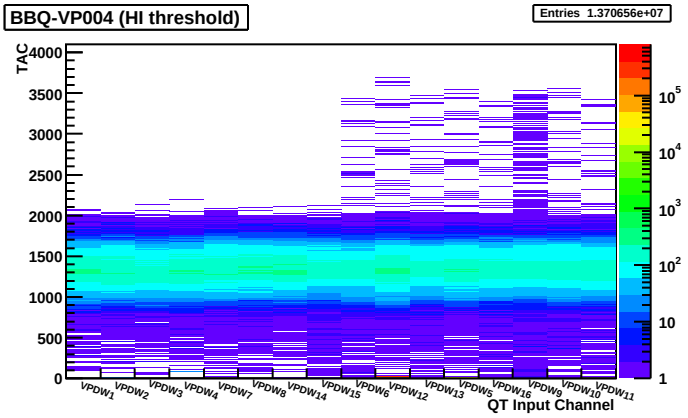

BBQ-VP002 (LO threshold)
Entries 1.370656e+07
ADC
QT Input Channel

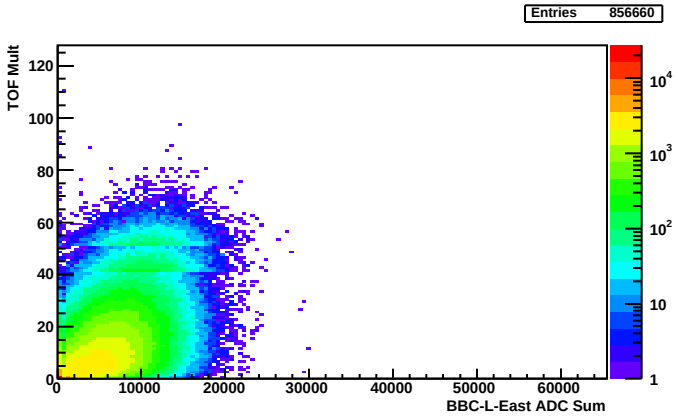

BBQ-VP002 (LO threshold)
Entries 1.370656e+07
TAC
QT Input Channel

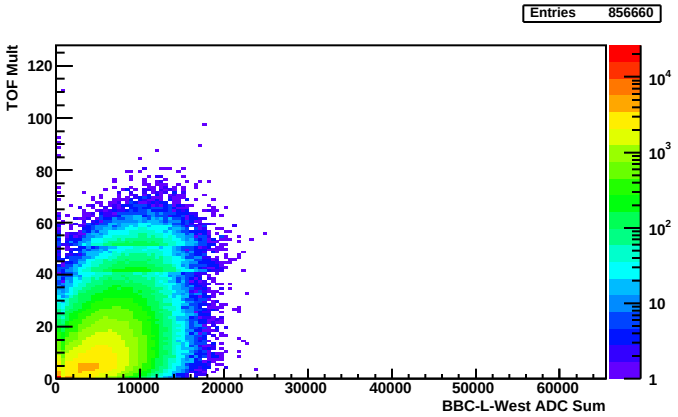

BBQ-VP003 (HI threshold)
Entries 1.370656e+07
ADC
QT Input Channel

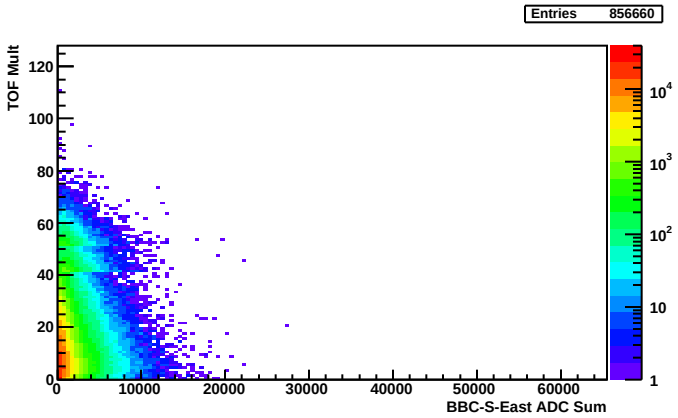

BBQ-VP003 (HI threshold)
Entries 1.370656e+07
TAC
QT Input Channel

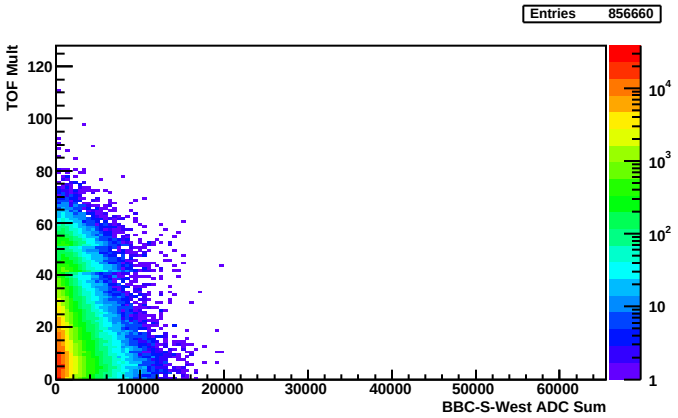

BBQ-VP004 (HI threshold)
Entries 1.370656e+07
ADC
QT Input Channel

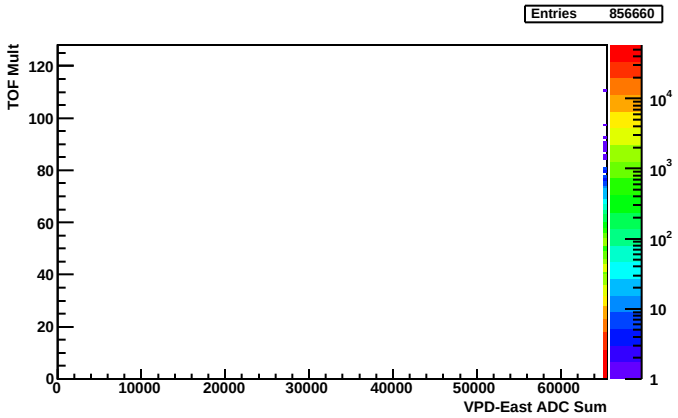

BBQ-VP004 (HI threshold)
Entries 1.370656e+07
TAC
QT Input Channel

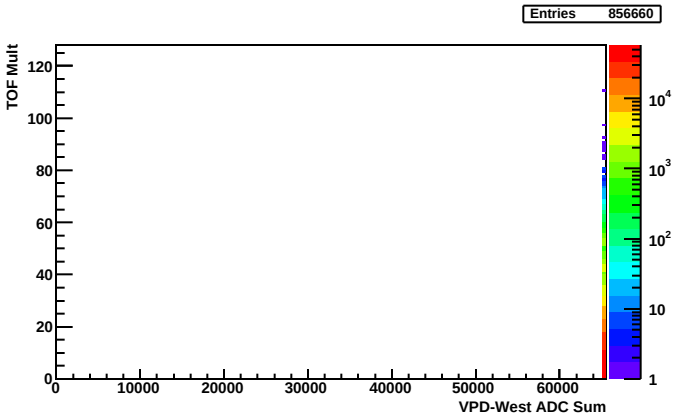

Entries 856660
TOF Mult
BBC-L-East ADC Sum

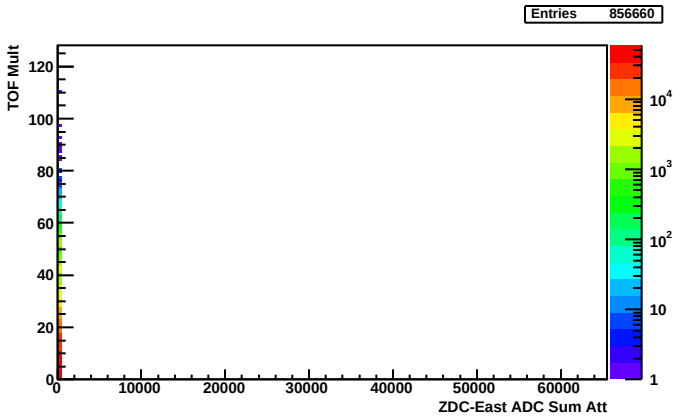

Entries 856660
TOF Mult
BBC-L-West ADC Sum

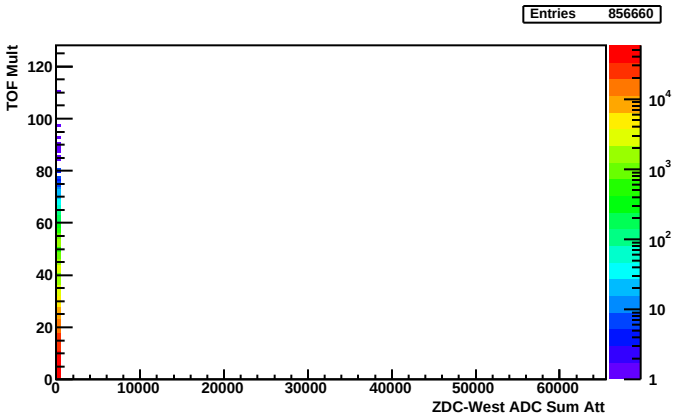

Entries 856660
TOF Mult
BBC-S-East ADC Sum

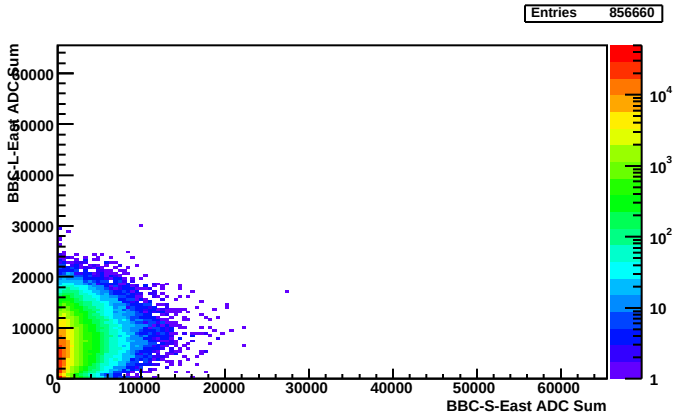

Entries 856660
TOF Mult
BBC-S-West ADC Sum

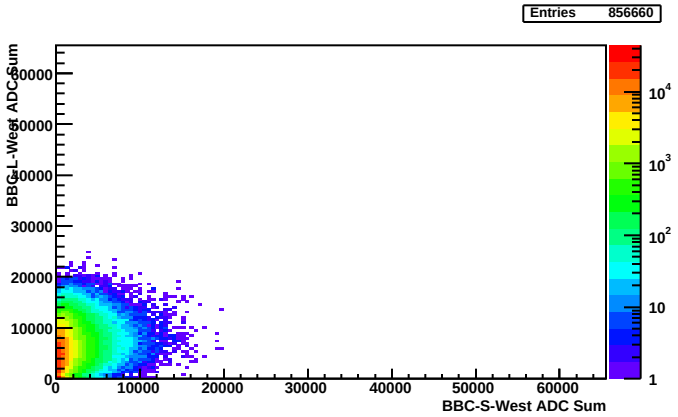

Entries 856660
TOF Mult
VPD-East ADC Sum

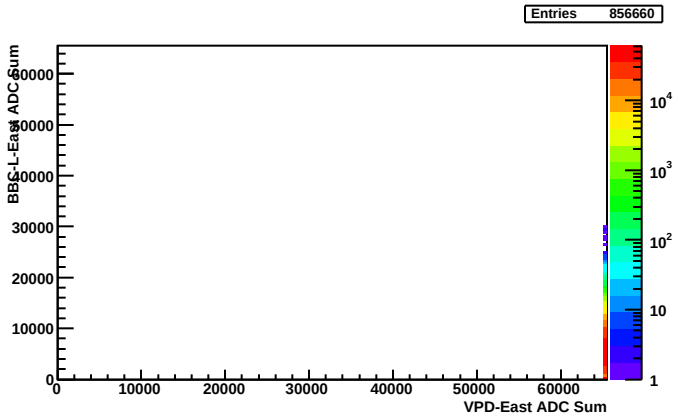

Entries 856660
TOF Mult
VPD-West ADC Sum

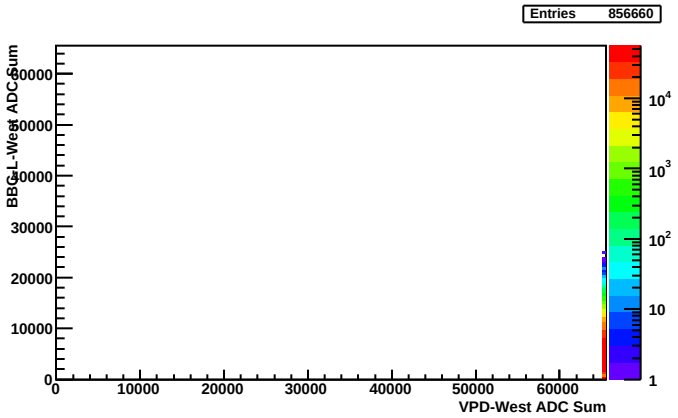

Entries 856660
TOF Mult
ZDC-East ADC Sum Att

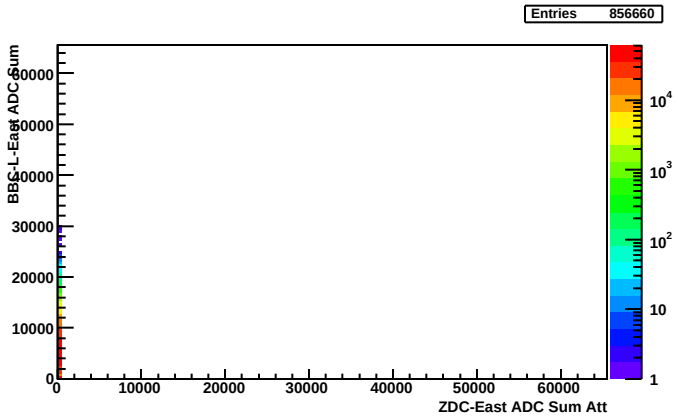

Entries 856660
TOF Mult
ZDC-West ADC Sum Att

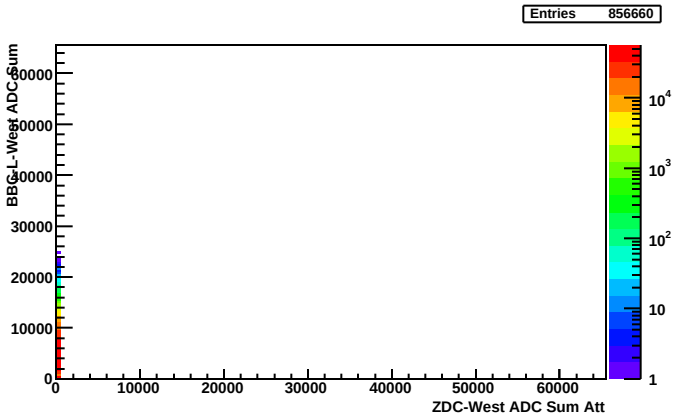

Entries 856660
BBC-L-East ADC Sum
BBC-S-East ADC Sum

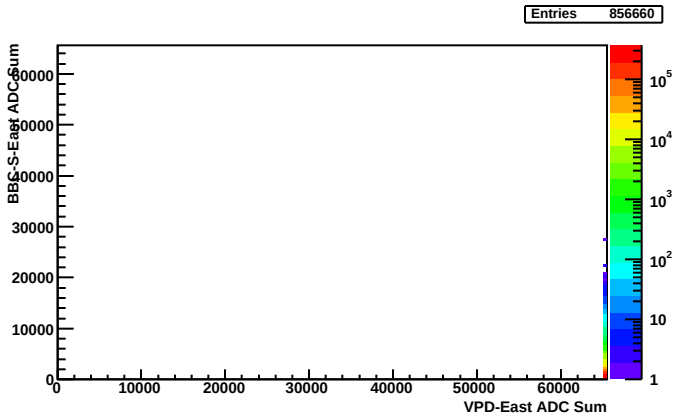

Entries 856660
BBC-L-West ADC Sum
BBC-S-West ADC Sum

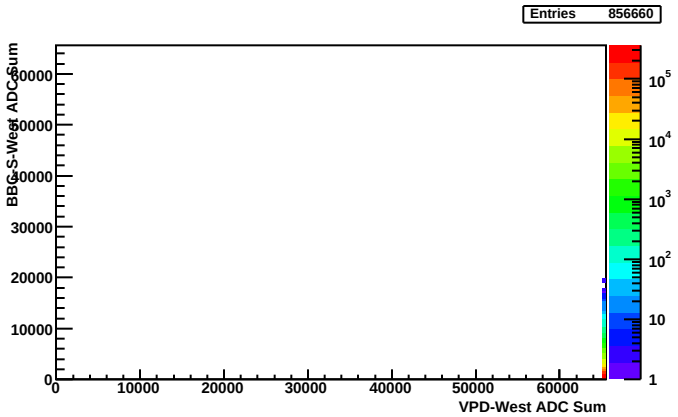

Entries 856660
BBC-L-East ADC Sum
VPD-East ADC Sum

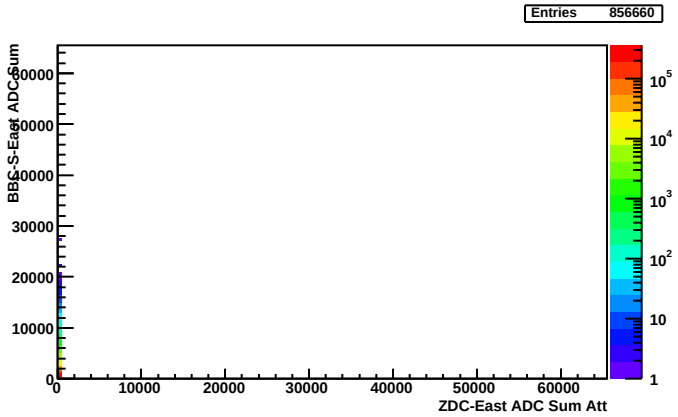

Entries 856660
BBC-L-West ADC Sum
VPD-West ADC Sum

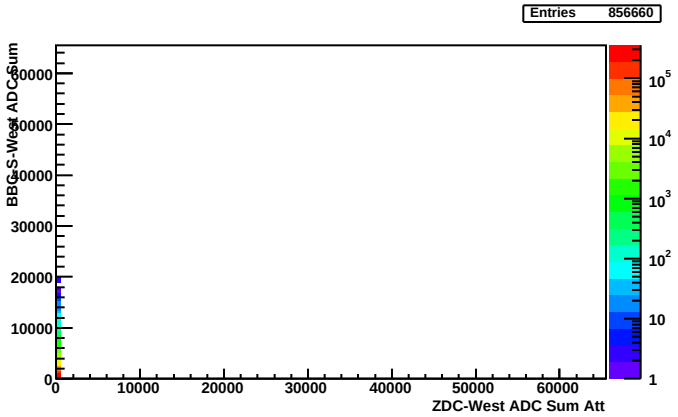

Entries 856660
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

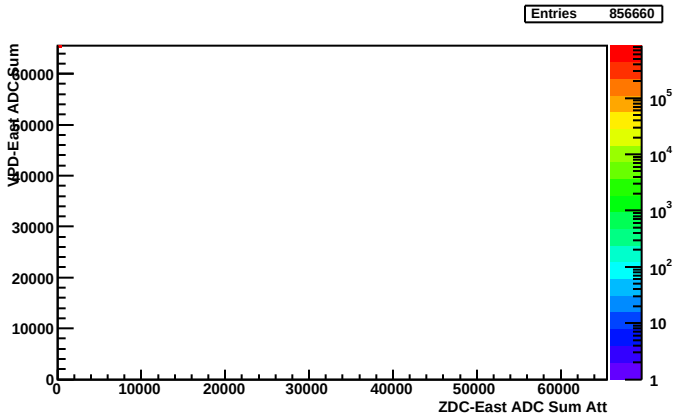

Entries 856660
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

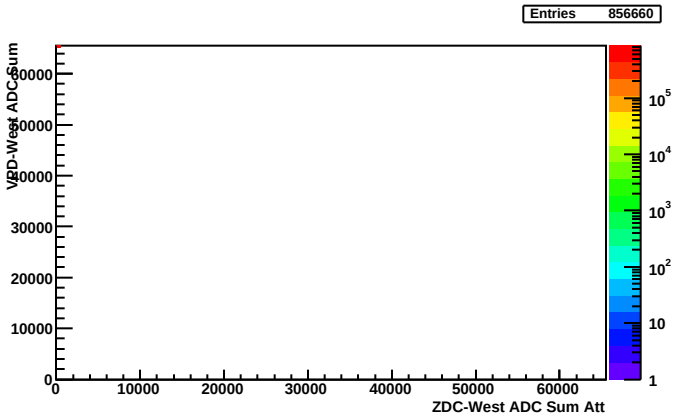

Entries 856660
BBC-S-East ADC Sum
VPD-East ADC Sum

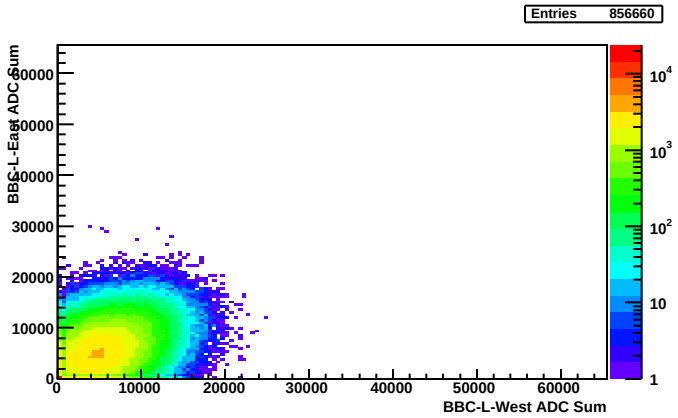

Entries 856660
BBC-S-West ADC Sum
VPD-West ADC Sum

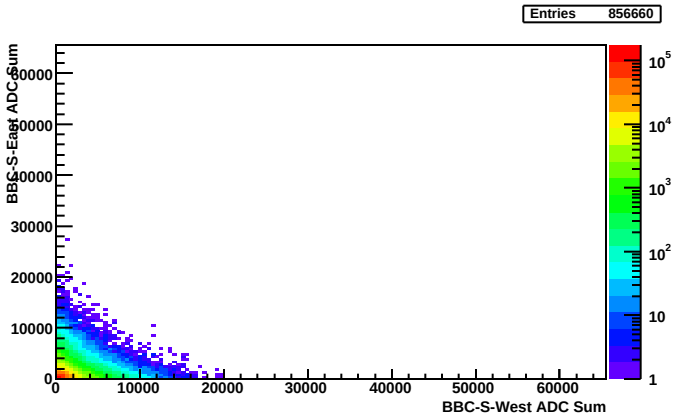

Entries 856660
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

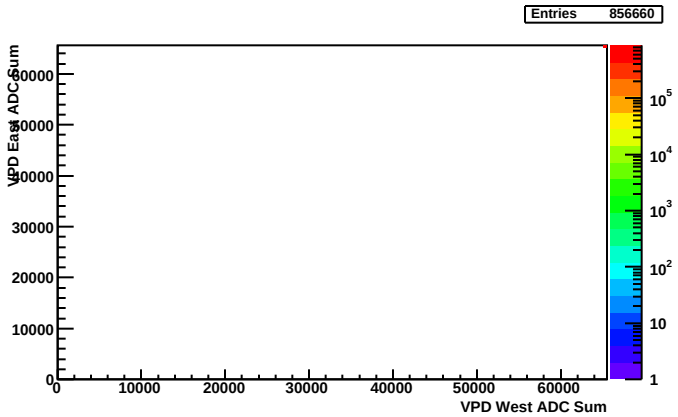

Entries 856660
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

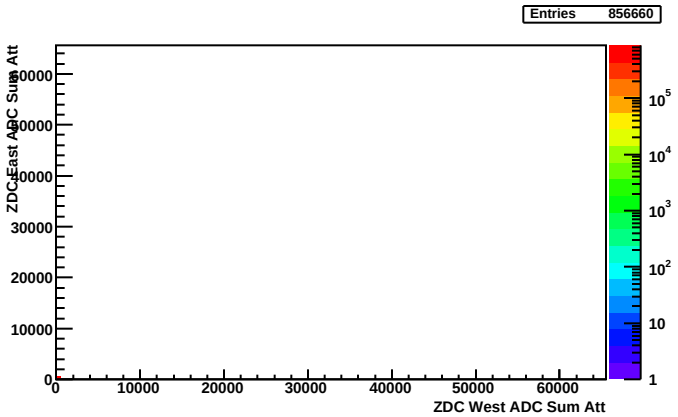

Entries 856660
VPD-East ADC Sum
ZDC-East ADC Sum Att

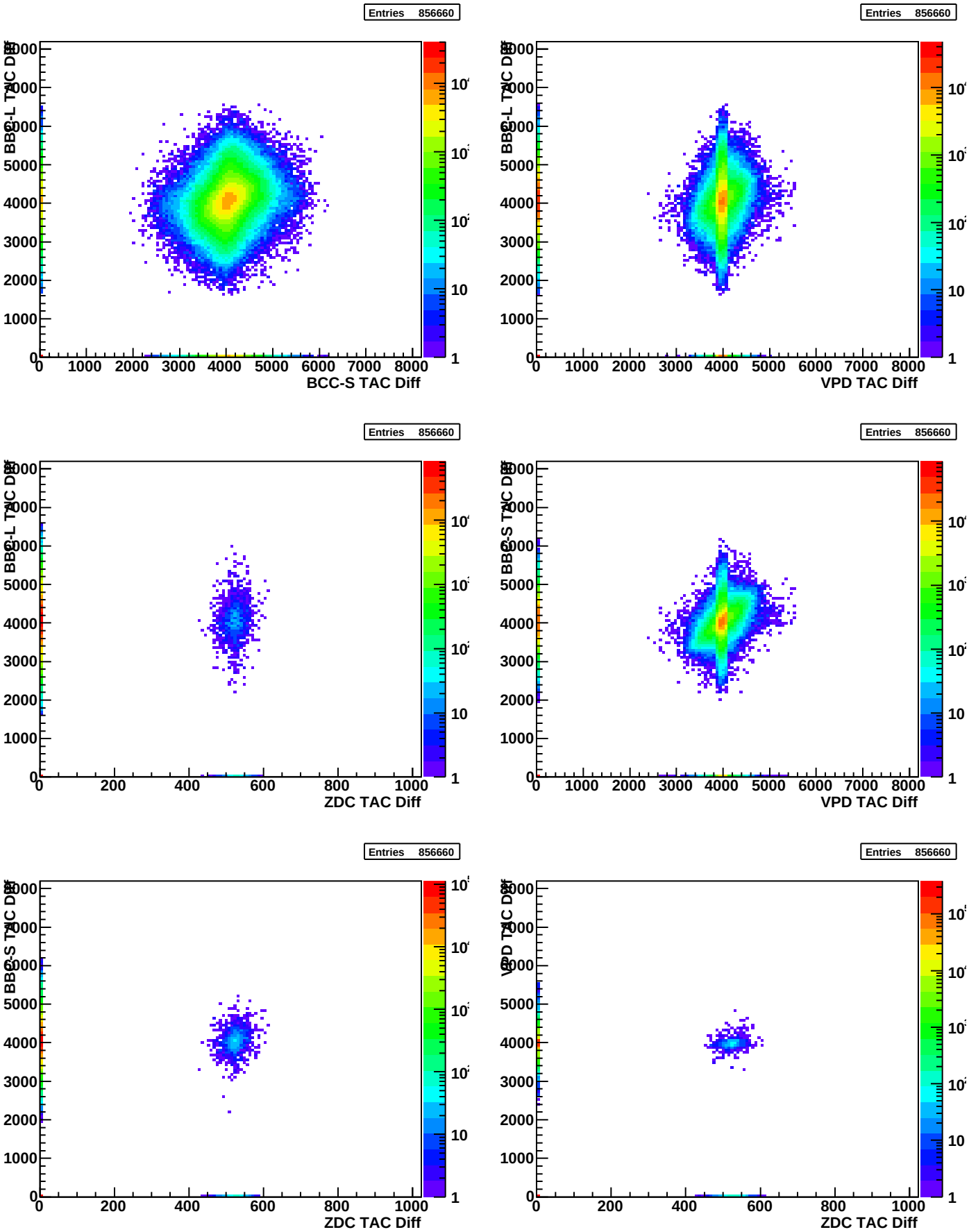

Entries 856660
VPD-West ADC Sum
ZDC-West ADC Sum Att

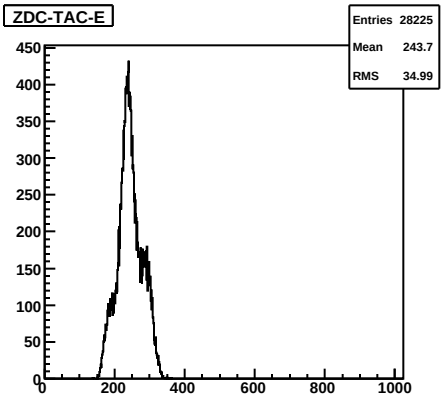

Entries 856660
BBC-L-East ADC Sum
BBC-L-West ADC Sum

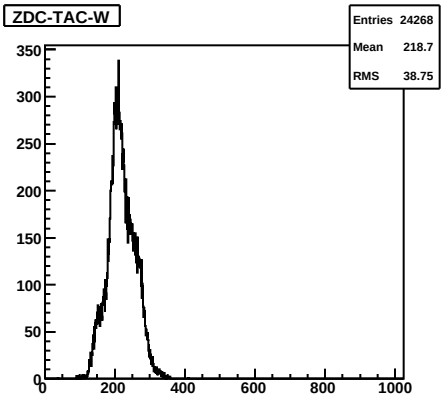

Entries 856660
BBC-S-East ADC Sum
BBC-S-West ADC Sum

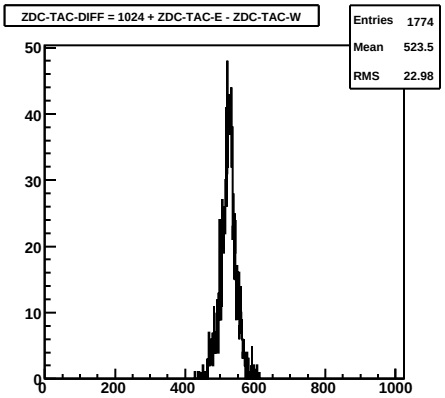

Entries 856660
VPD East ADC Sum
VPD West ADC Sum

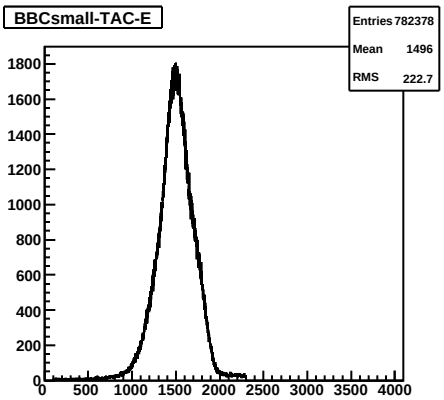

Entries 856660
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

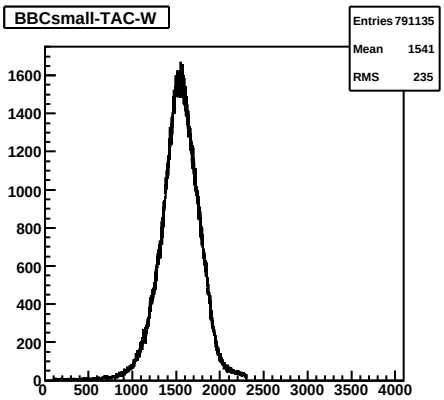

Entries 856660
BBC-L TAC Diff
BCC-S TAC Diff
Entries 856660
BBC-L TAC Diff
VPD TAC Diff
Entries 856660
BBC-L TAC Diff
ZDC TAC Diff
Entries 856660
BBC-S TAC Diff
VPD TAC Diff
Entries 856660
BBC-S TAC Diff
ZDC TAC Diff
Entries 856660
VPD TAC Diff
ZDC TAC Diff

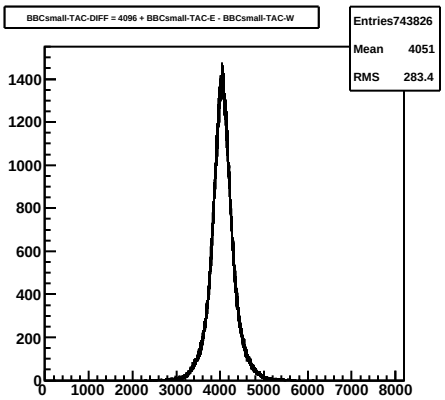

ZDC-TAC-E
Entries 28225
Mean 243.7
RMS 34.99

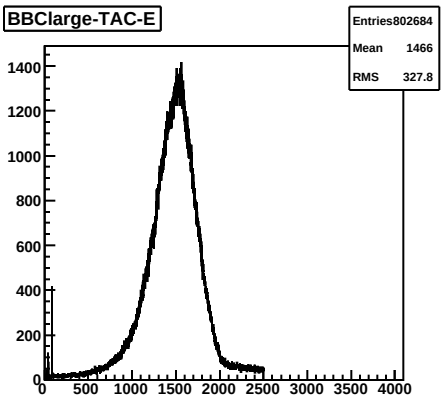

ZDC-TAC-W
Entries 24268
Mean 218.7
RMS 38.75

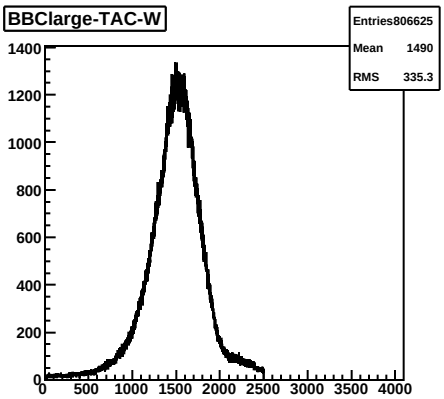

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1774
Mean 523.5
RMS 22.98

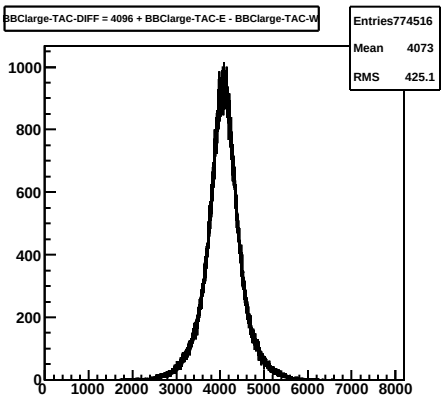

BBCsmall-TAC-E
Entries 782378
Mean 1496
RMS 222.7

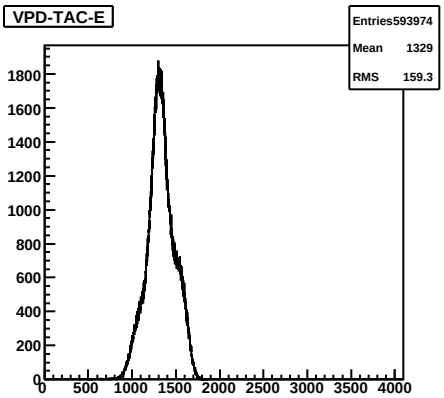

BBCsmall-TAC-W
Entries 791135
Mean 1541
RMS 235

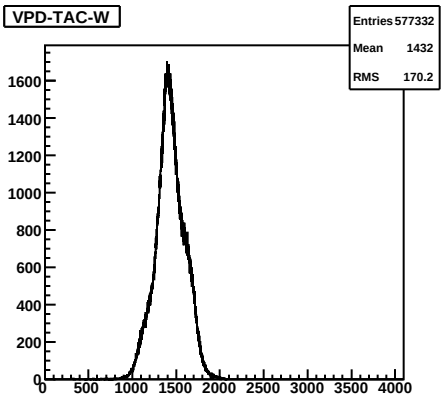

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 743826
Mean 4051
RMS 283.4

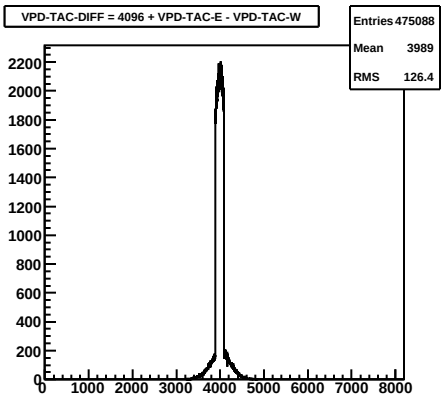

BBClarge-TAC-E
Entries 802684
Mean 1466
RMS 327.8

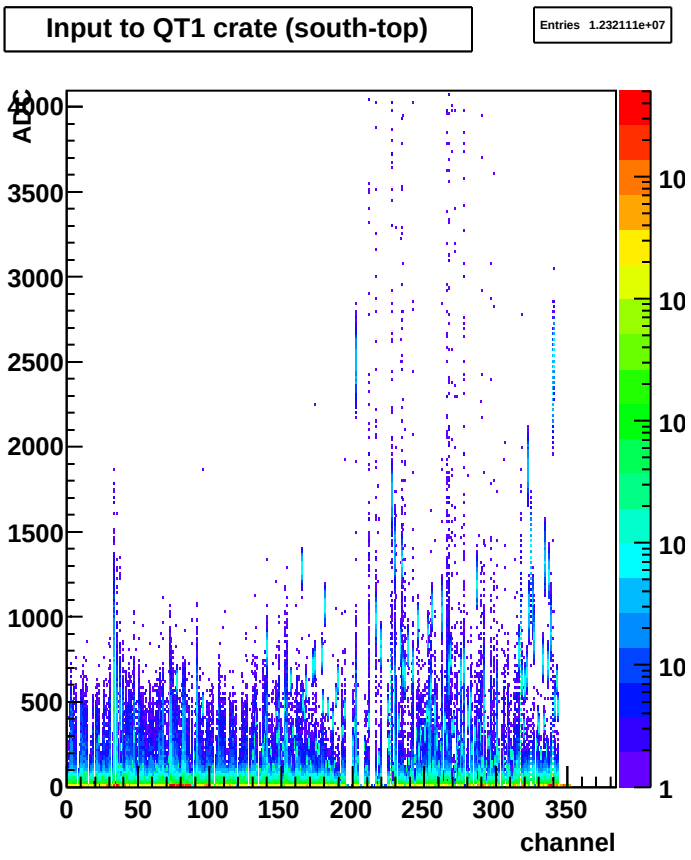

BBClarge-TAC-W
Entries 806625
Mean 1490
RMS 335.3

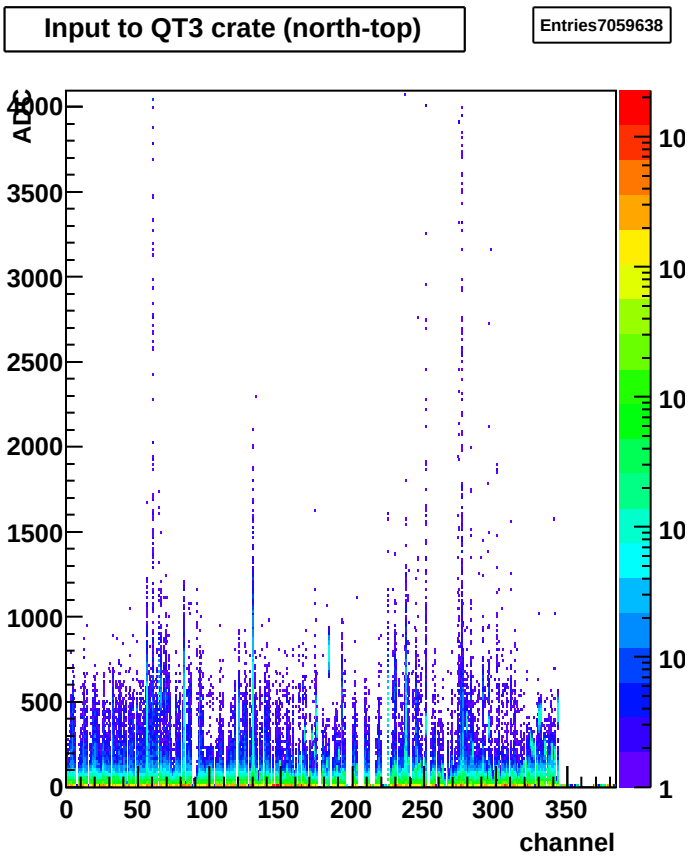

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 774516
Mean 4073
RMS 425.1

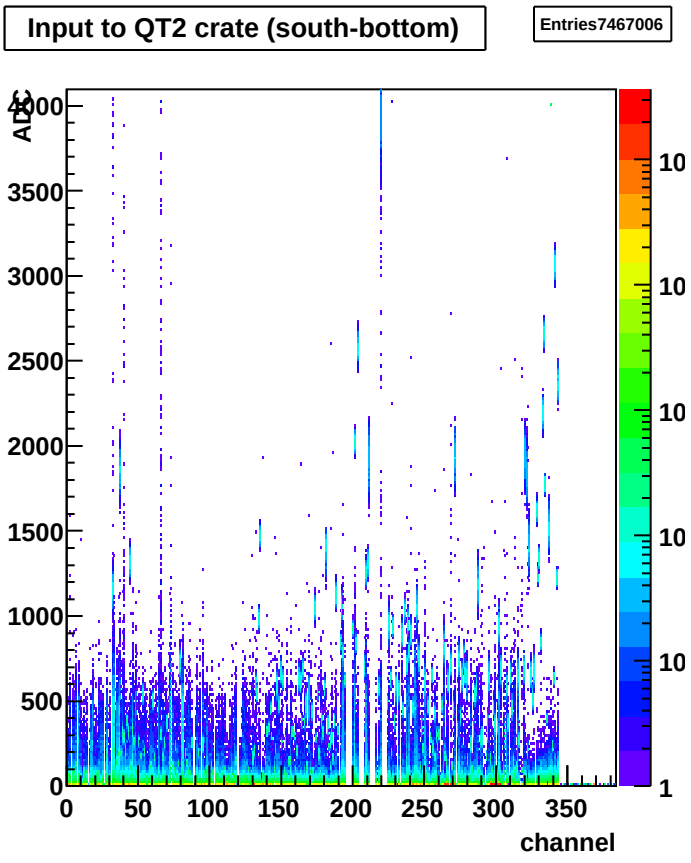

VPD-TAC-E
Entries 593974
Mean 1329
RMS 159.3

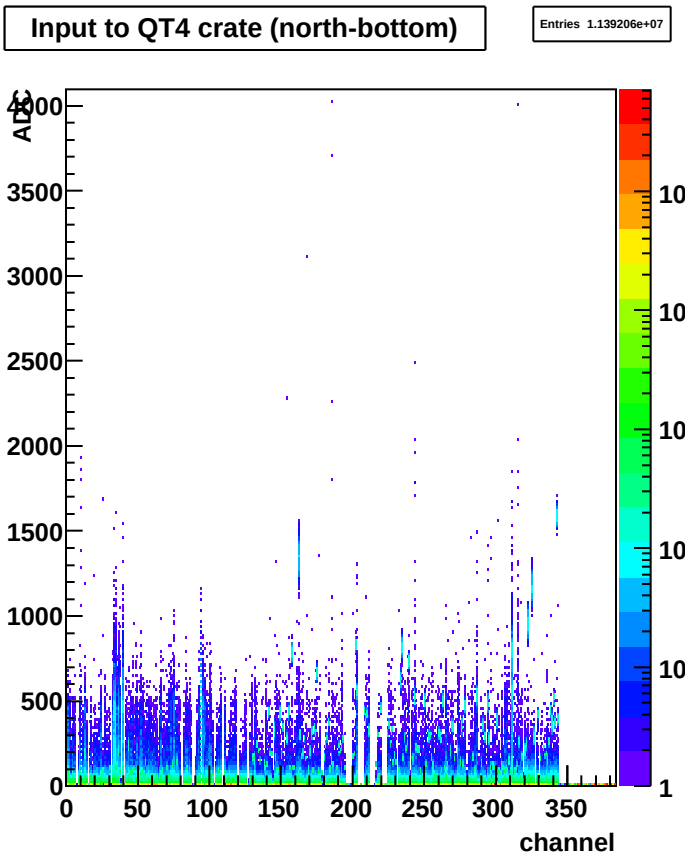

VPD-TAC-W
Entries 577332
Mean 1432
RMS 170.2

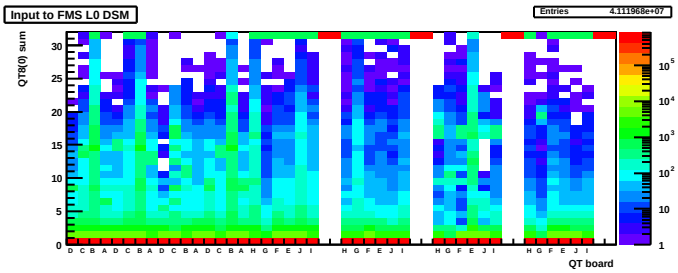

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 475088
Mean 3989
RMS 126.4

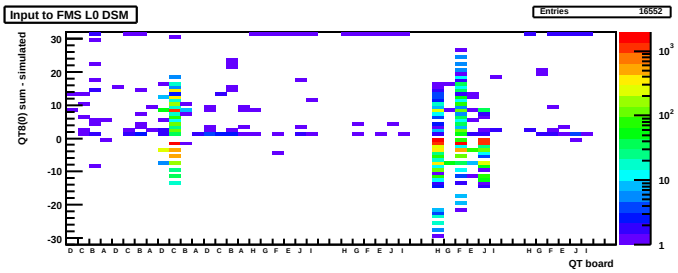

Input to QT1 crate (south-top)
Entries 1.232111e+07
ADC
channel

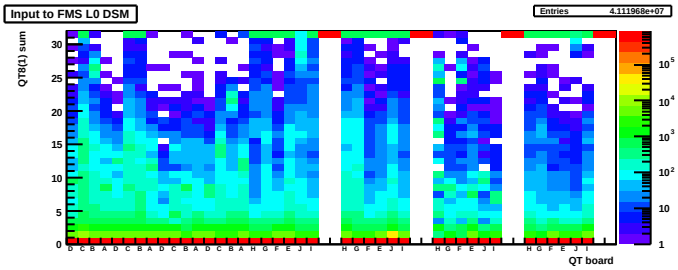

Input to QT3 crate (north-top)
Entries7059638
ADC
channel

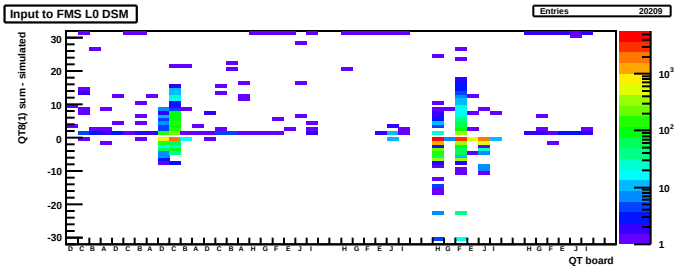

Input to QT2 crate (south-bottom)
Entries7467006
ADC
channel

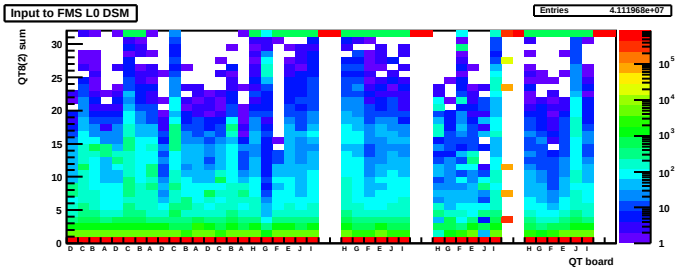

Input to QT4 crate (north-bottom)
Entries 1.139206e+07
ADC
channel

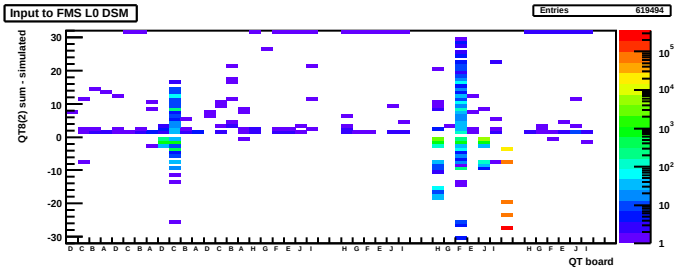

Input to FMS L0 DSM
Entries 4.111968e+07
QT8(0) sum
QT board

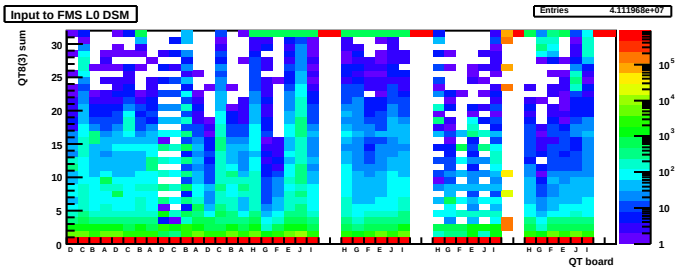

Input to FMS L0 DSM
Entries 16552
QT8(0) sum - simulated
QT board

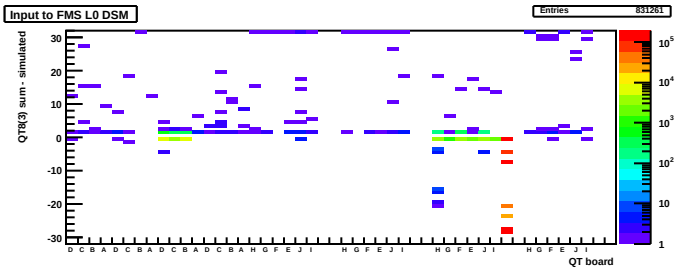

Input to FMS L0 DSM
Entries 4.111968e+07
QT8(1) sum
QT board

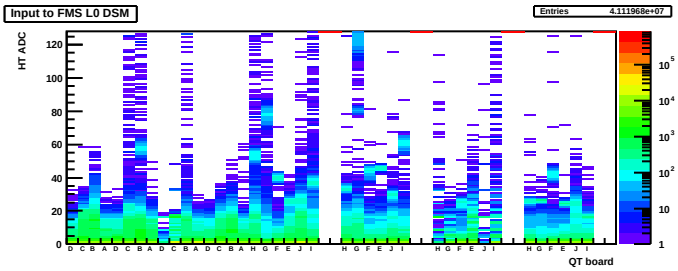

Input to FMS L0 DSM
Entries 20209
QT8(1) sum - simulated
QT board

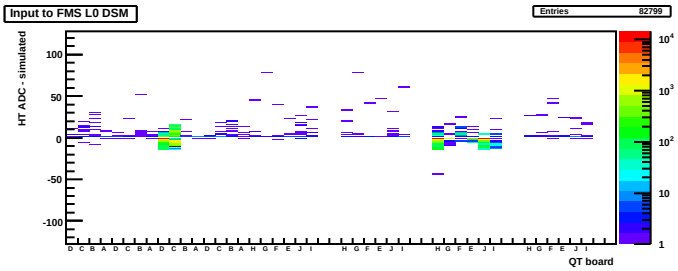

Input to FMS L0 DSM
Entries 4.111968e+07
QT8(2) sum
QT board

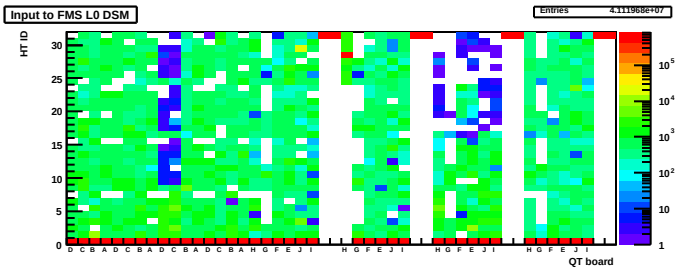

Input to FMS L0 DSM
Entries 619494
QT8(2) sum - simulated
QT board

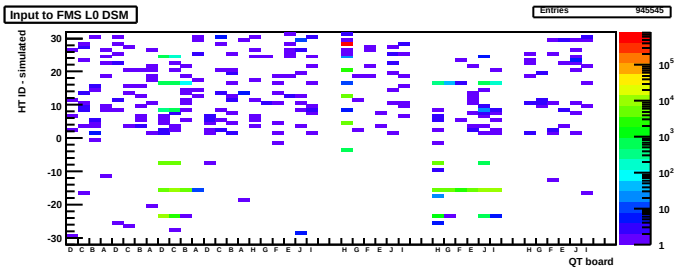

Input to FMS L0 DSM
Entries 4.111968e+07
QT8(3) sum
QT board

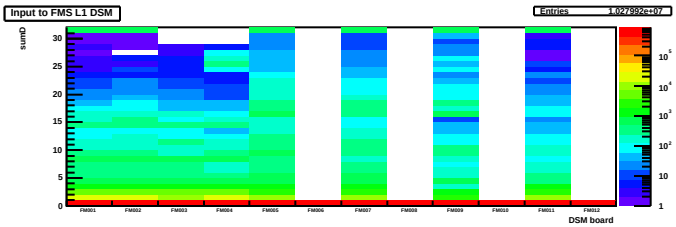

Input to FMS L0 DSM
Entries 831261
QT8(3) sum - simulated
QT board

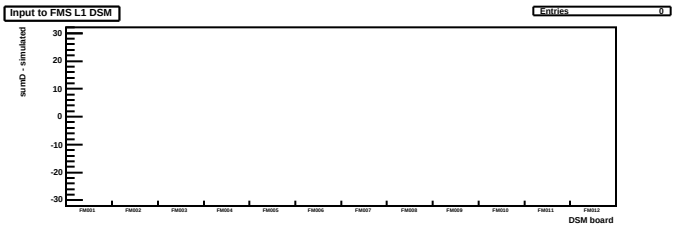

Input to FMS L0 DSM
Entries 4.111968e+07
HT ADC
QT board

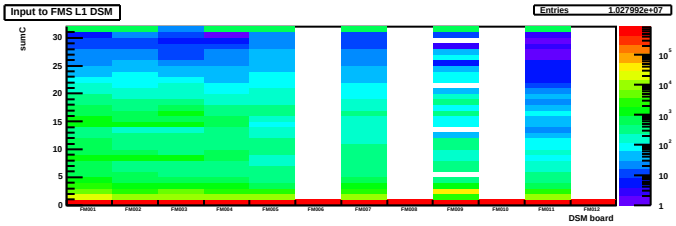

Input to FMS L0 DSM
Entries 82799
HT ADC - simulated
QT board

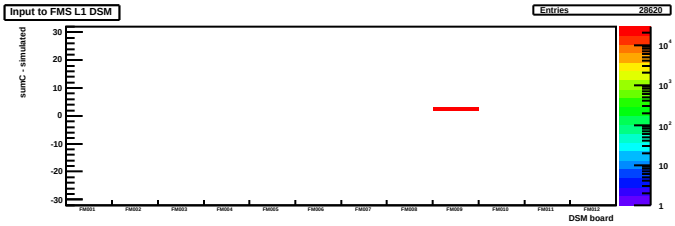

Input to FMS L0 DSM
Entries 4.111968e+07
HT ID
QT board

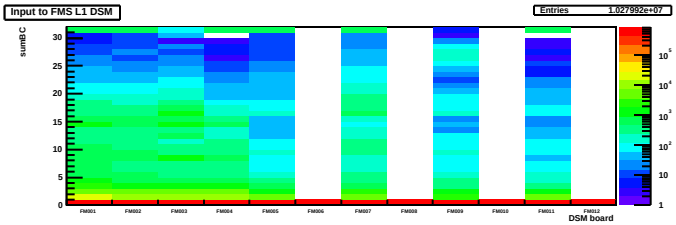

Input to FMS L0 DSM
Entries 945545
HT ID - simulated
QT board

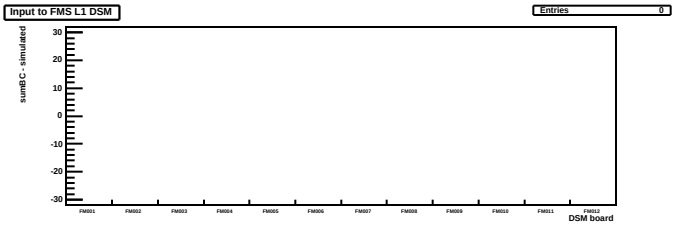

Input to FMS L1 DSM
Entries 1.027992e+07
sumD
DSM board

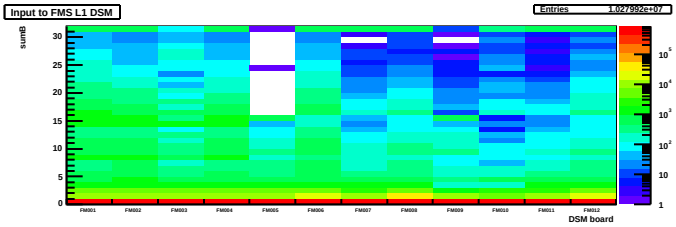

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

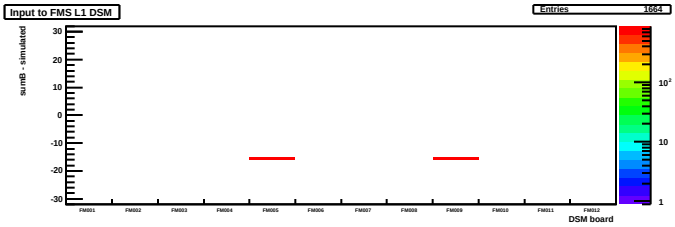

Input to FMS L1 DSM
Entries 1.027992e+07
sumC
DSM board

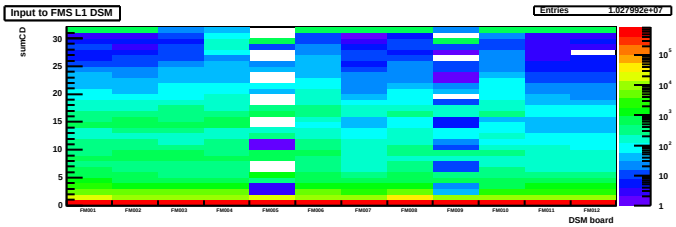

Input to FMS L1 DSM
Entries 28620
sumC - simulated
DSM board

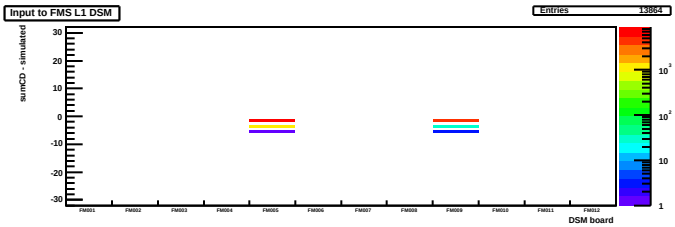

Input to FMS L1 DSM
Entries 1.027992e+07
sumBC
DSM board

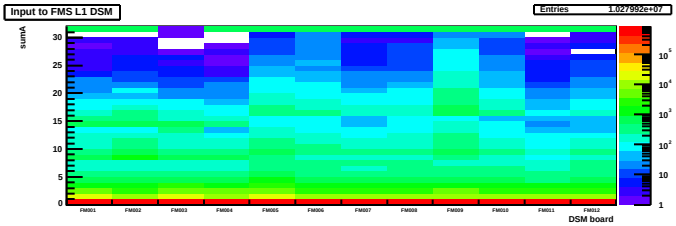

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

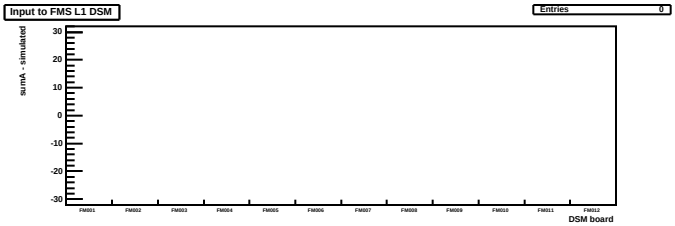

Input to FMS L1 DSM
Entries 1.027992e+07
sumB
DSM board

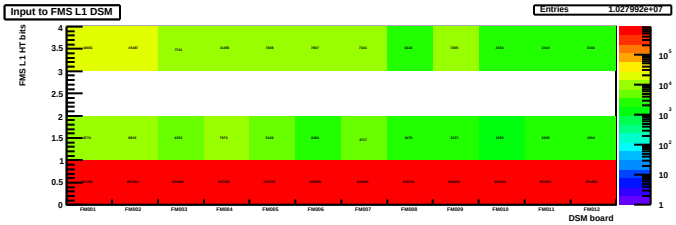

Input to FMS L1 DSM
Entries 1664
sumB - simulated
DSM board

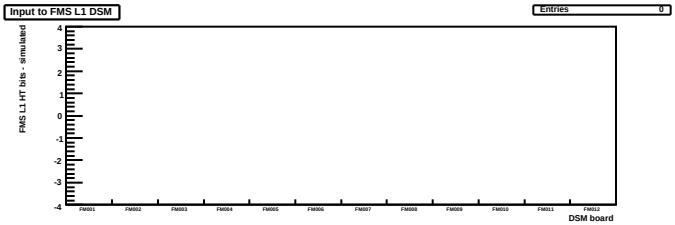

Input to FMS L1 DSM
Entries 1.027992e+07
sumCD
DSM board

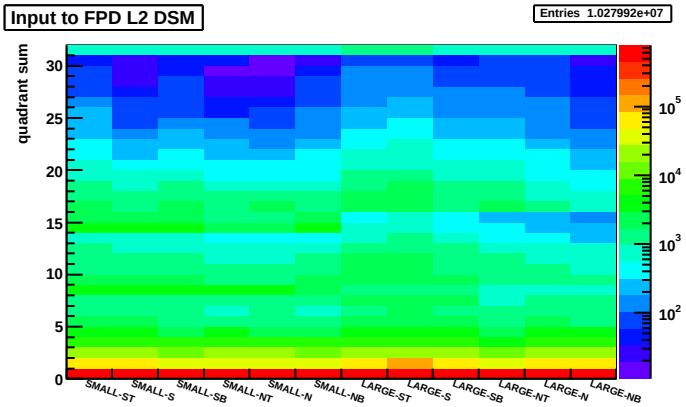

Input to FMS L1 DSM
Entries 13864
sumCD - simulated
DSM board

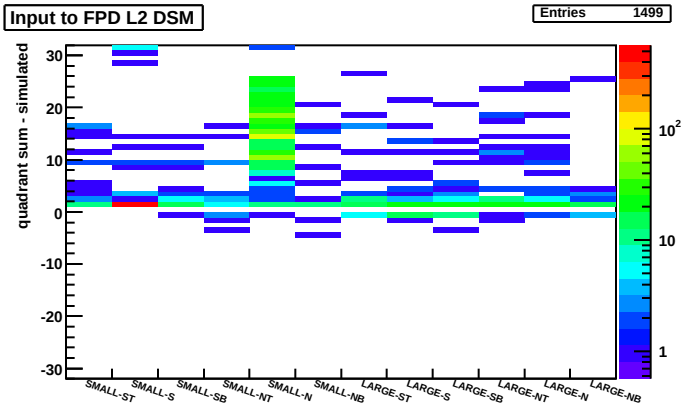

Input to FMS L1 DSM
Entries 1.027992e+07
sumA
DSM board

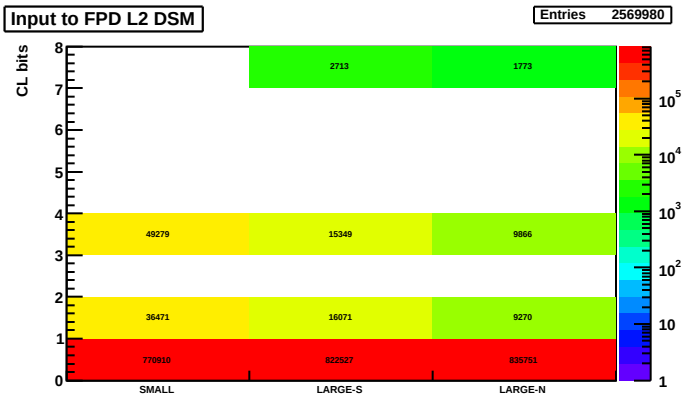

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

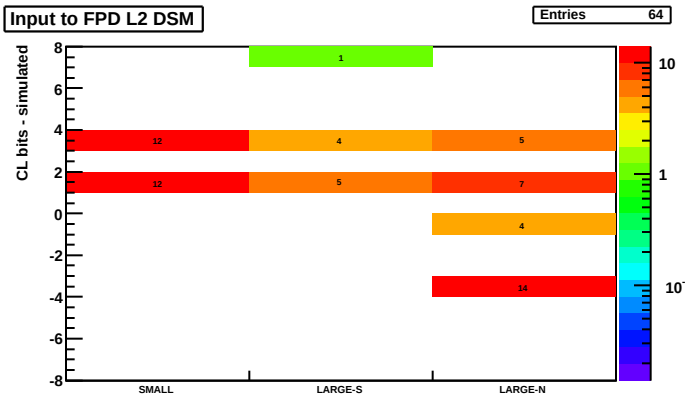

Input to FMS L1 DSM
Entries 1.027992e+07
FMS L1 HT bits
DSM board

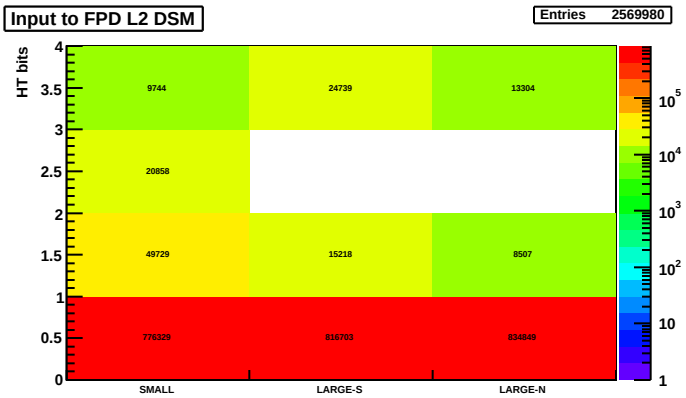

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

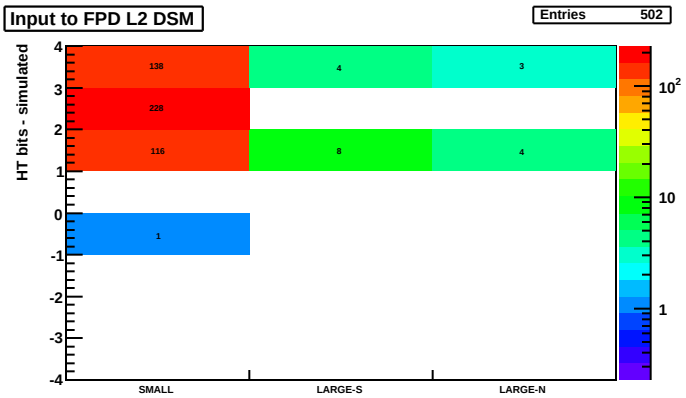

Input to FPD L2 DSM
Entries 1.027992e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

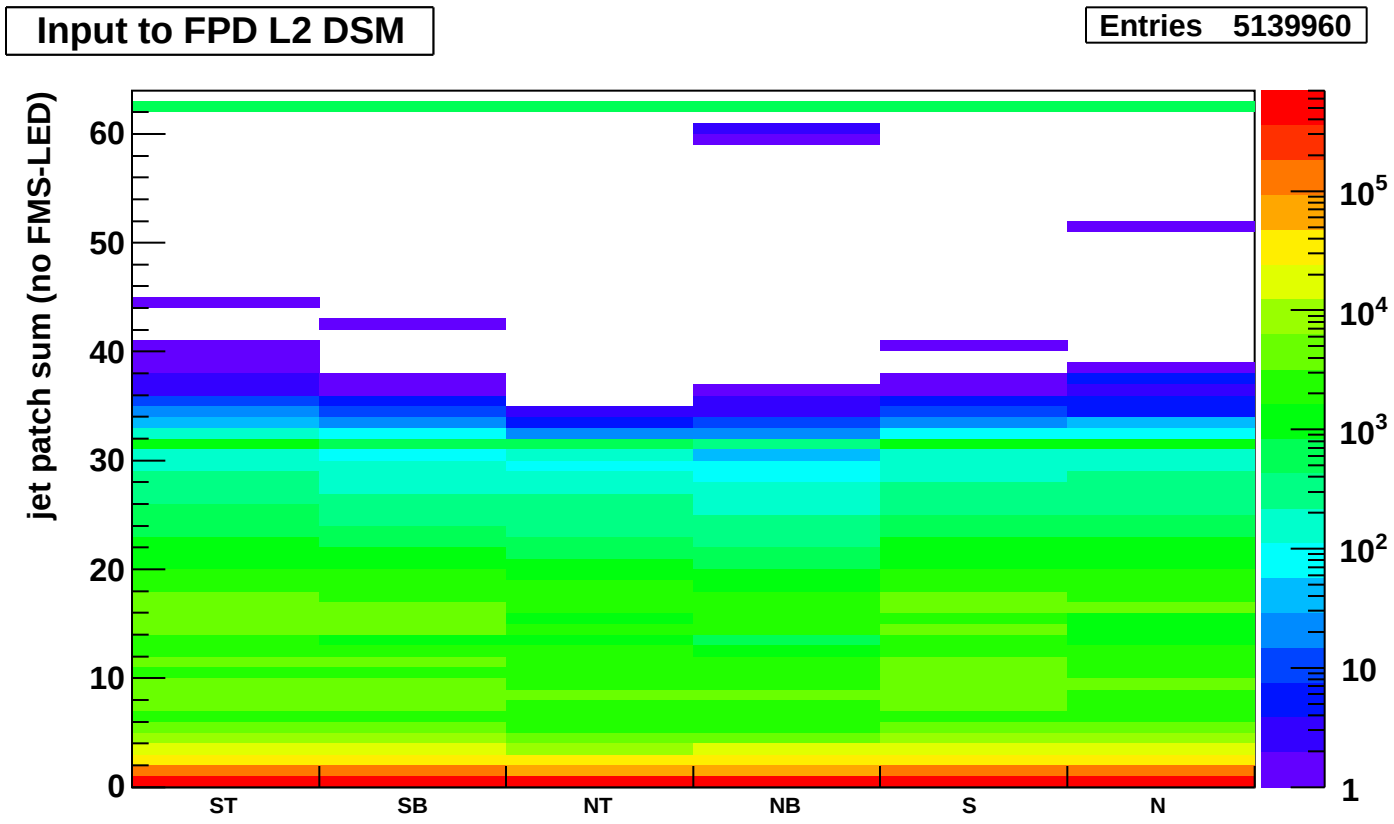

Input to FPD L2 DSM
Entries 1499
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

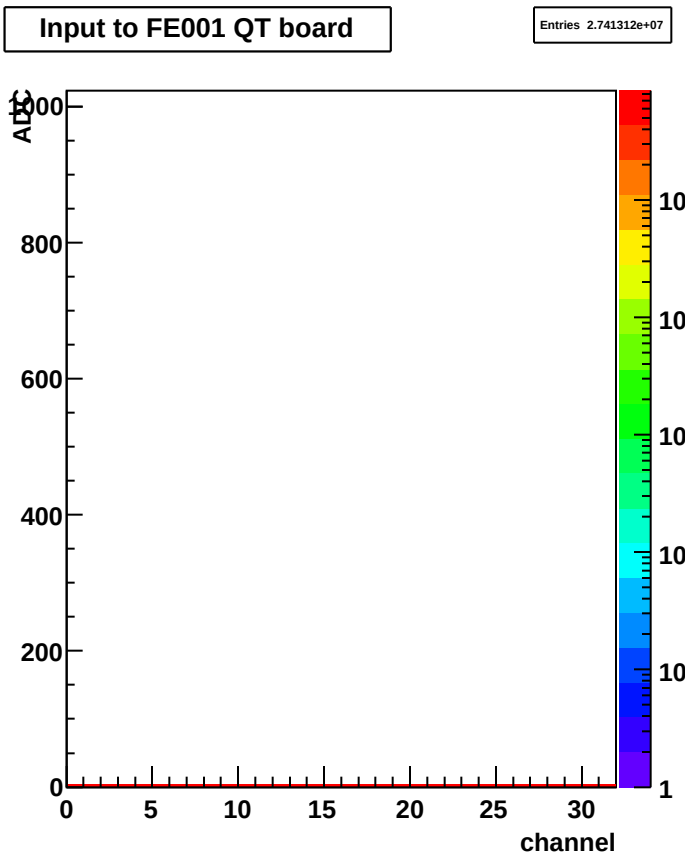

Input to FPD L2 DSM
Entries 2569980
CL bits
2713
1773
49279
15349
9866
36471
16071
9270
770910
822527
835751
SMALL
LARGE-S
LARGE-N

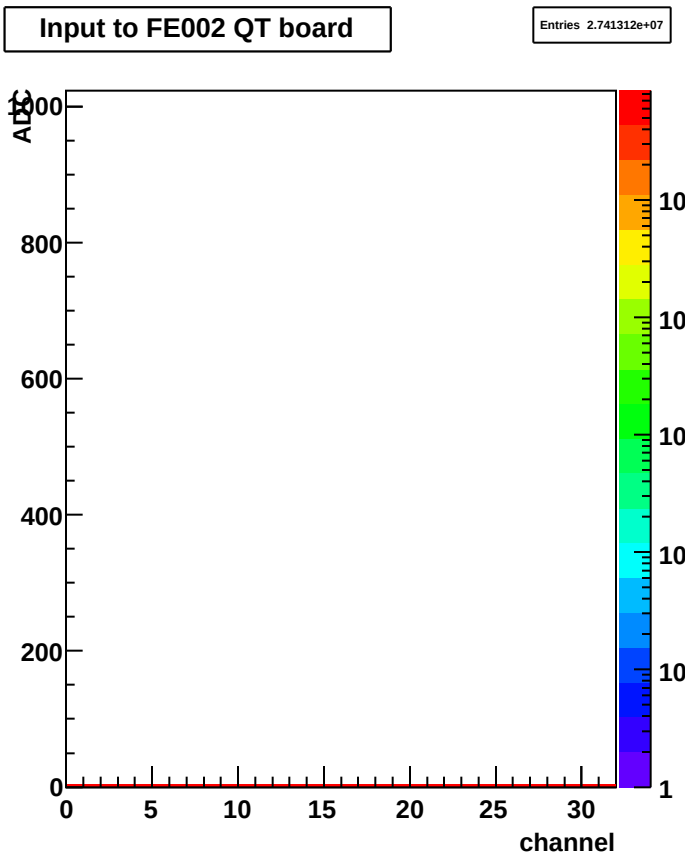

Input to FPD L2 DSM
Entries 64
CL bits - simulated
1
12
4
5
12
5
7
4
14
SMALL
LARGE-S
LARGE-N

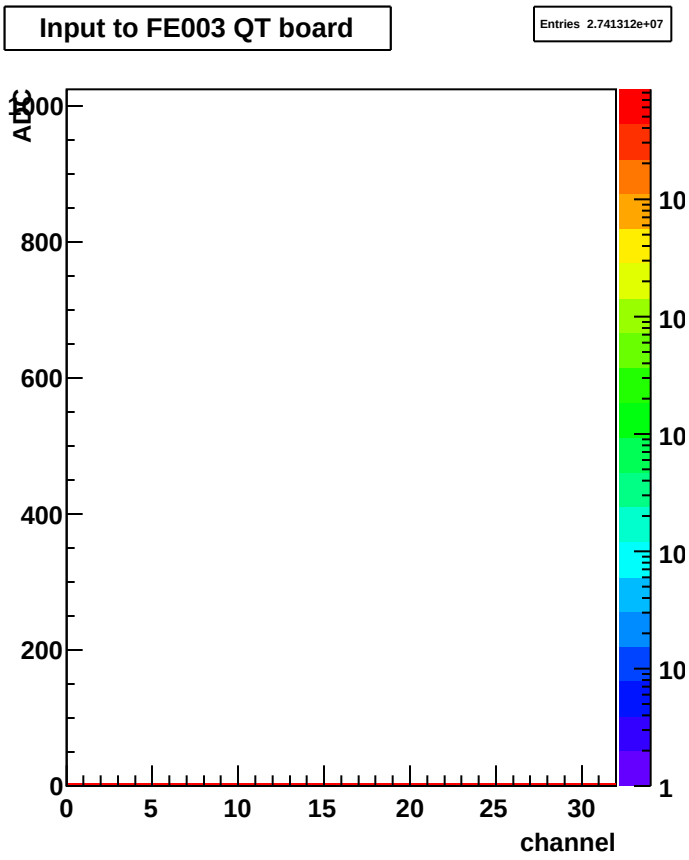

Input to FPD L2 DSM
Entries 2569980
HT bits
9744
24739
13304
20858
49729
15218
8507
776329
816703
834849
SMALL
LARGE-S
LARGE-N

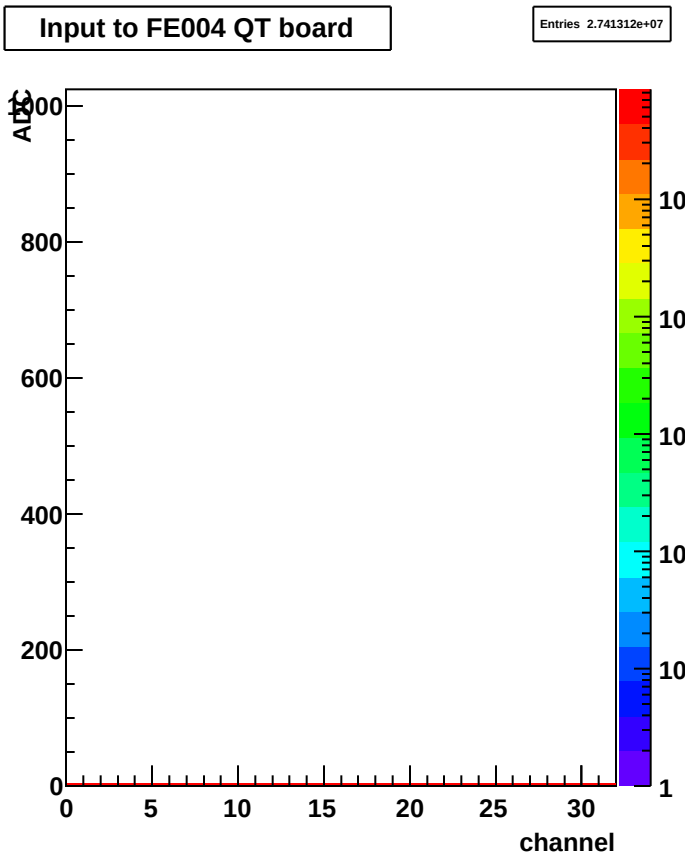

Input to FPD L2 DSM
Entries 502
HT bits - simulated
138
4
3
228
116
8
4
1
SMALL
LARGE-S
LARGE-N

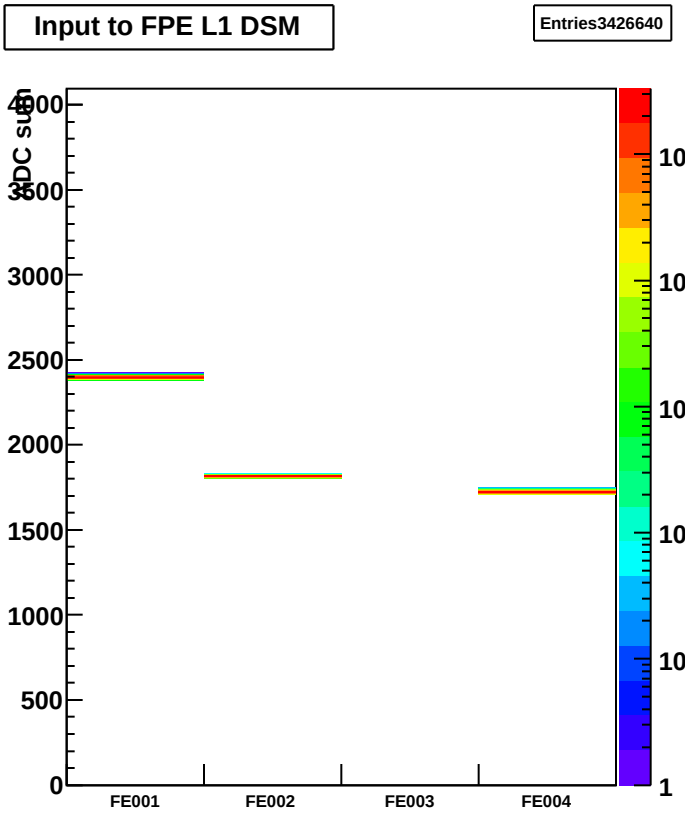

Input to FPD L2 DSM
Entries 5139960
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 2.741312e+07
ADC
channel

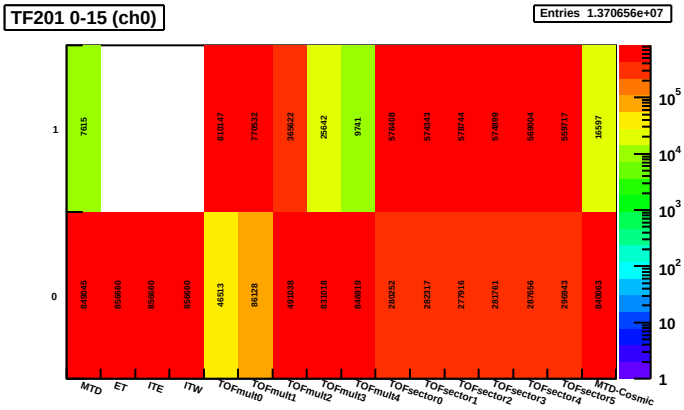

Input to FE002 QT board
Entries 2.741312e+07
ADC
channel


Input to FE003 QT board
Entries 2.741312e+07
ADC
channel

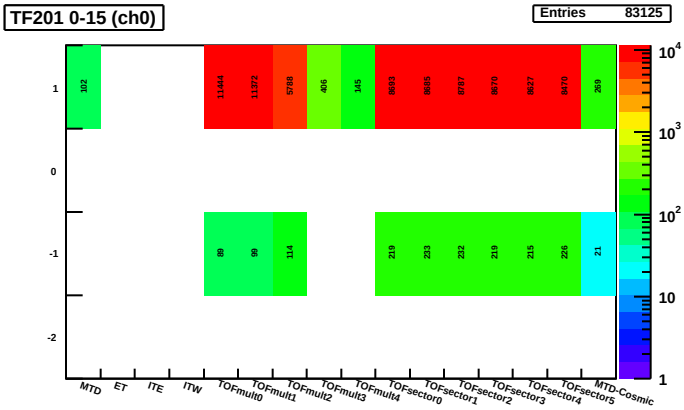

Input to FE004 QT board
Entries 2.741312e+07
ADC
channel


Input to FPE L1 DSM
Entries3426640
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

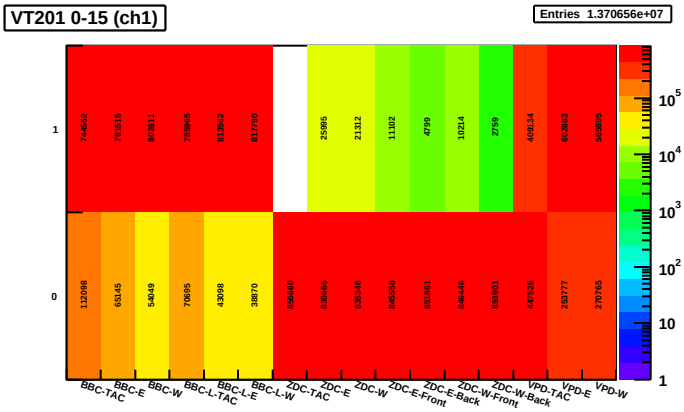
TF201 0-15 (ch0)



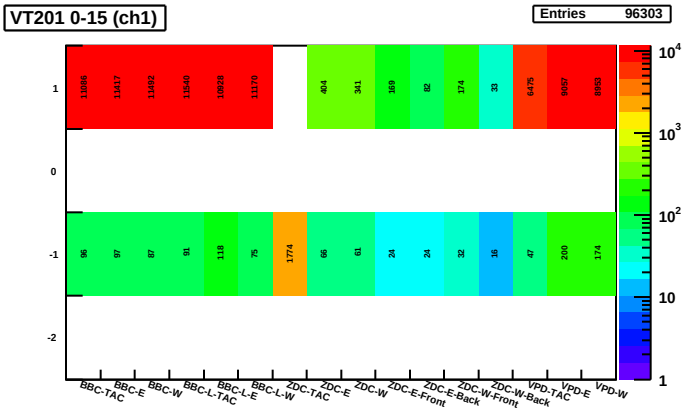
TF201 0-15 (ch0)



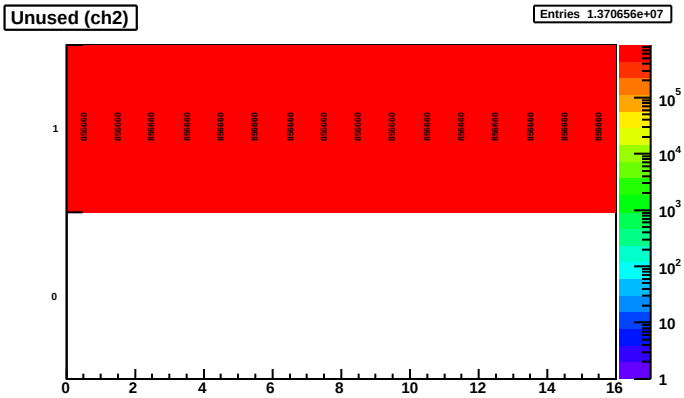
VT201 0-15 (ch1)

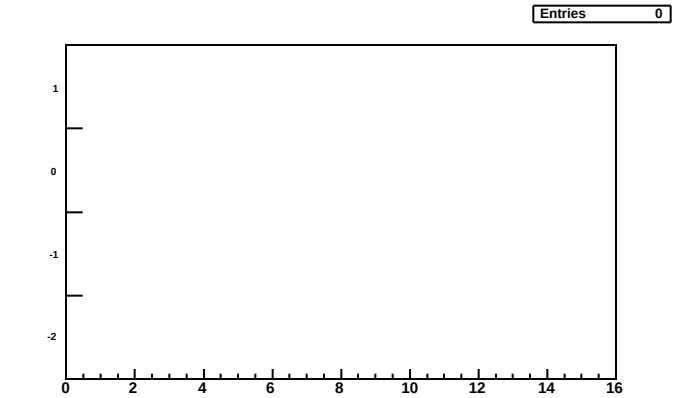


VT201 0-15 (ch1)

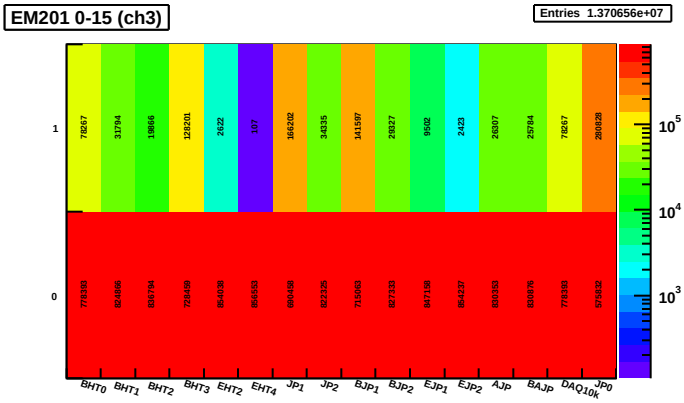


Unused (ch2)





EM201 0-15 (ch3)



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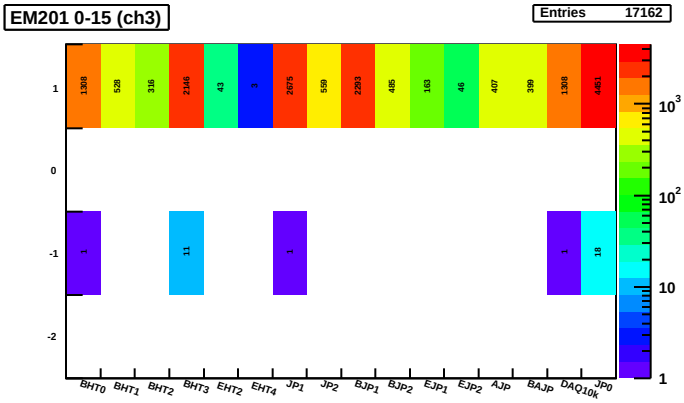
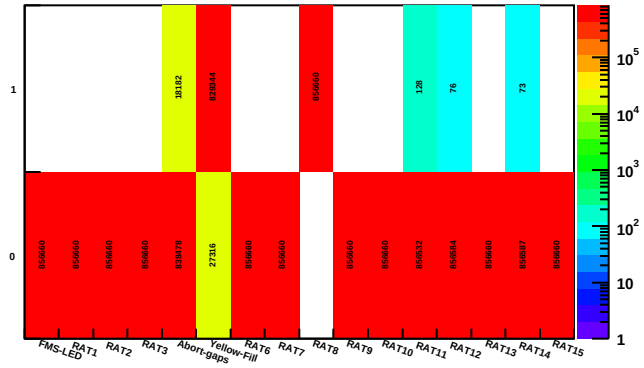
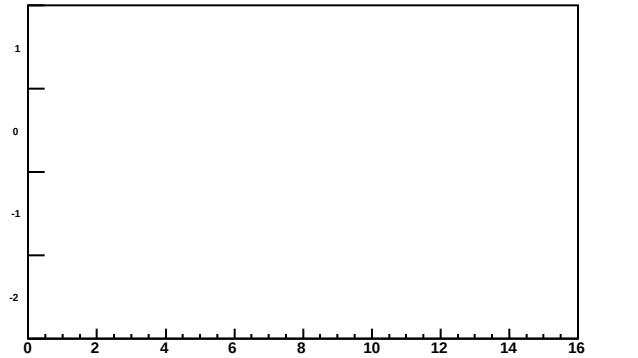


RAT board (ch4)





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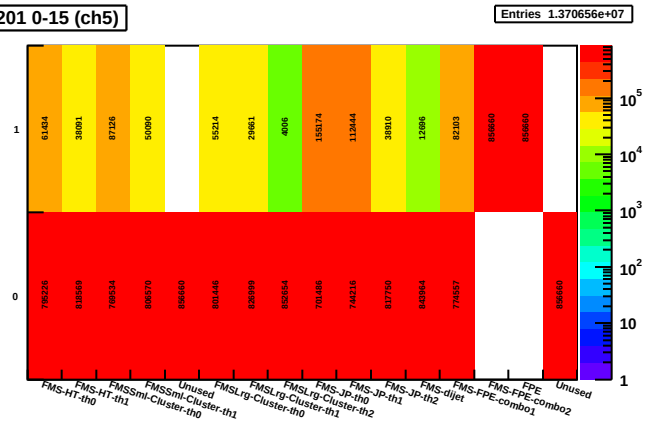

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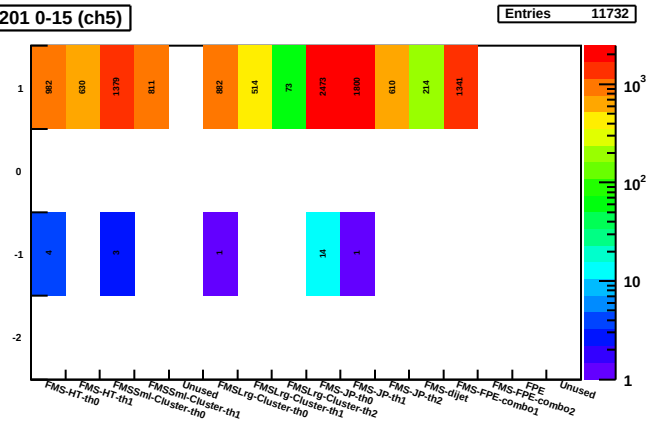

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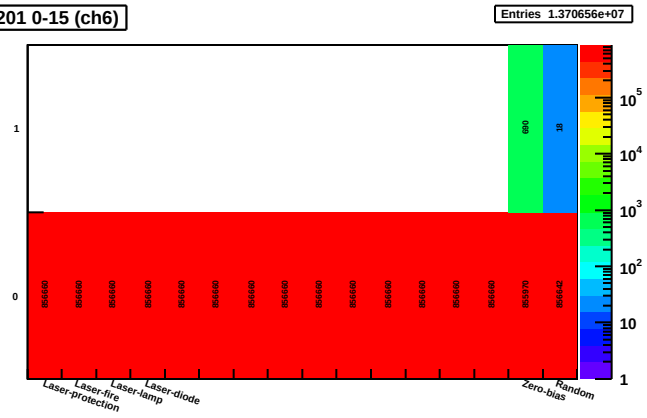

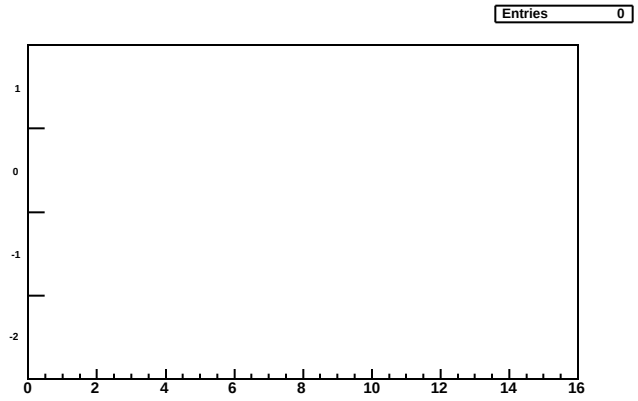
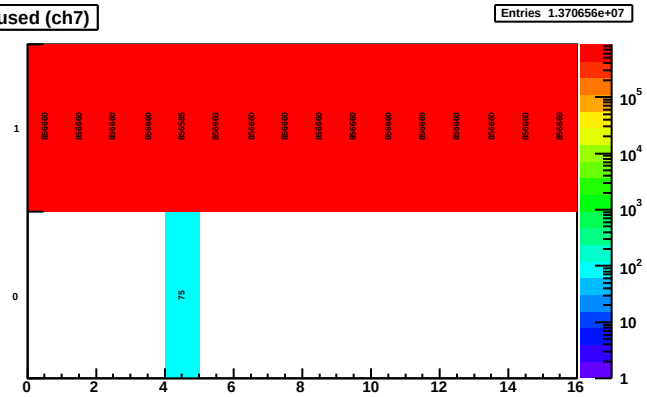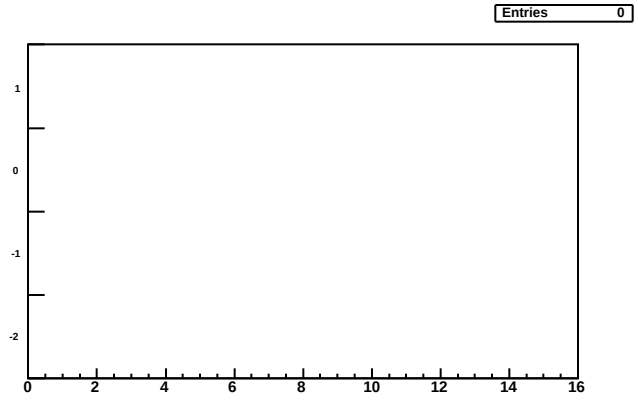
Unused (ch7)