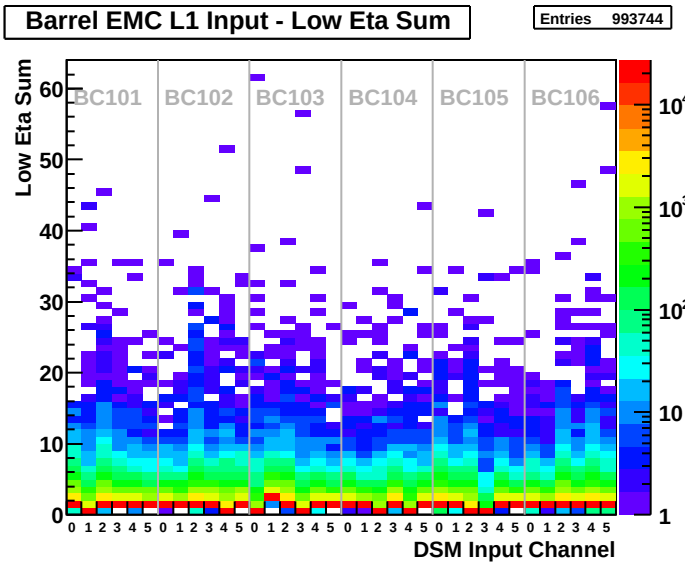
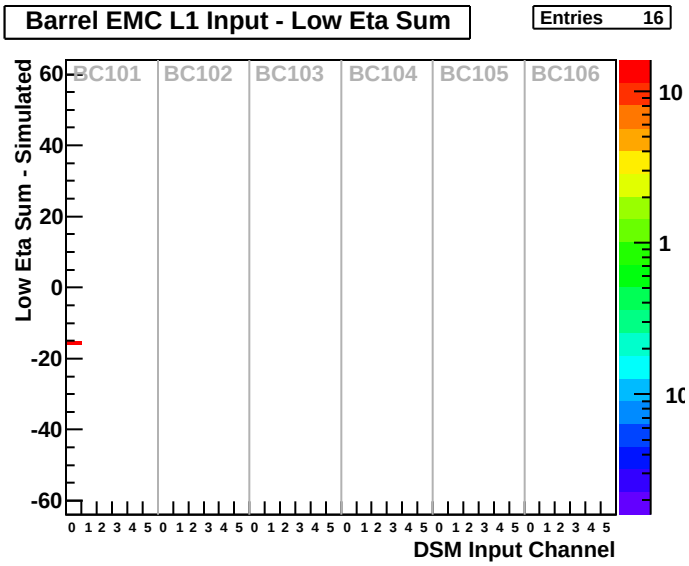
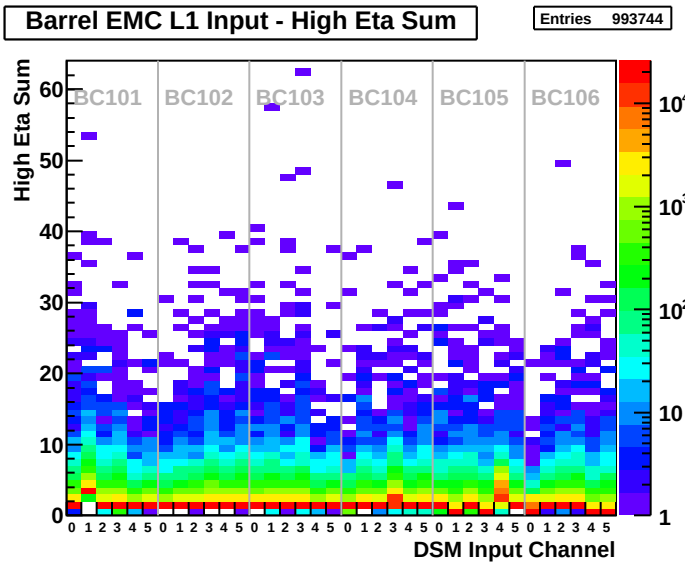
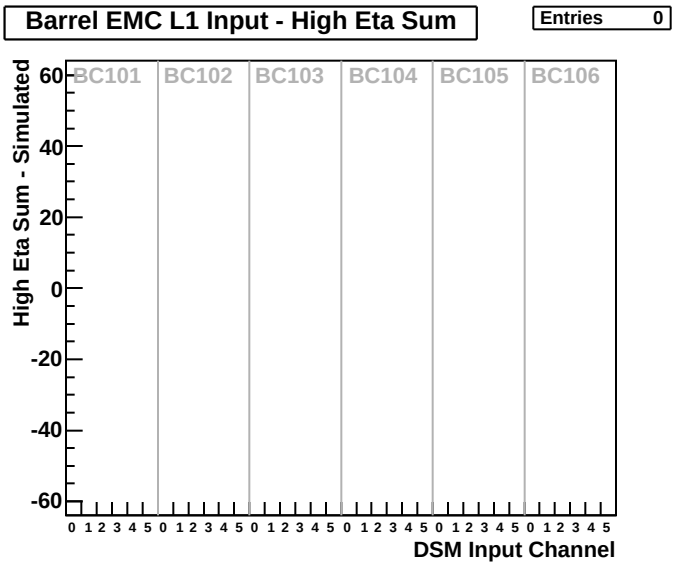
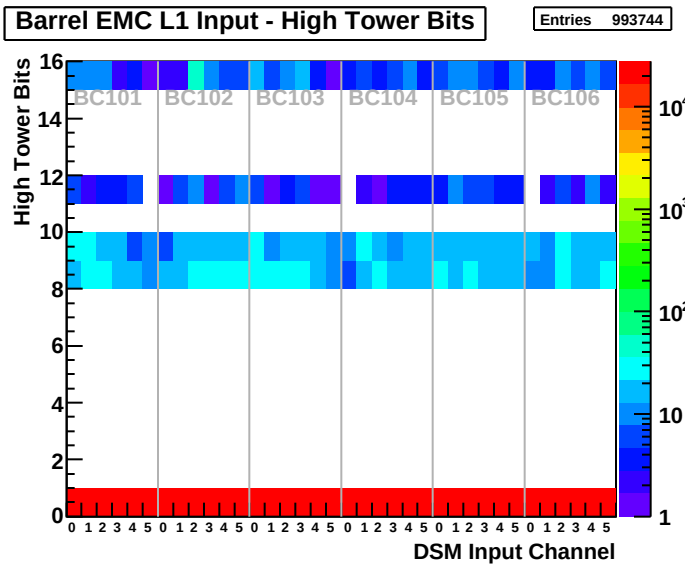
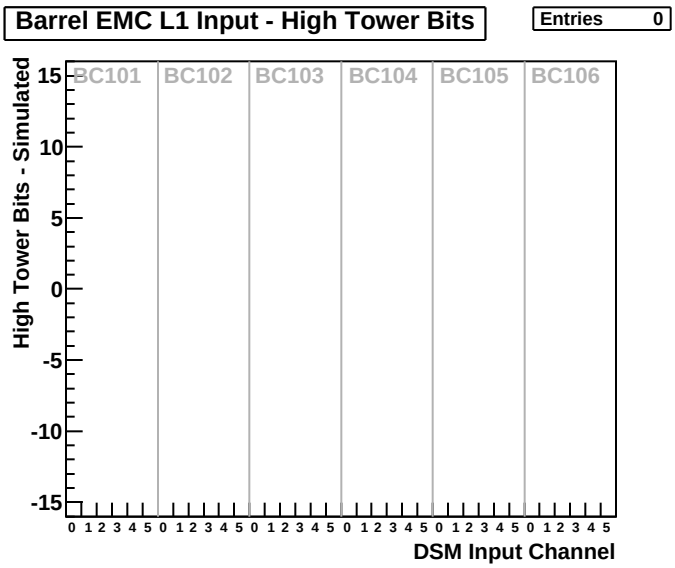

Barrel EMC L1 Input - Low Eta Sum
Entries 993744
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

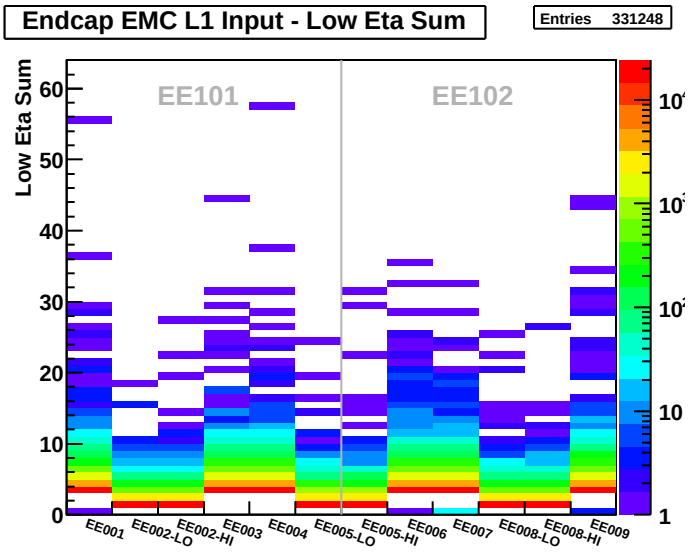

Barrel EMC L1 Input - Low Eta Sum
Entries 16
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

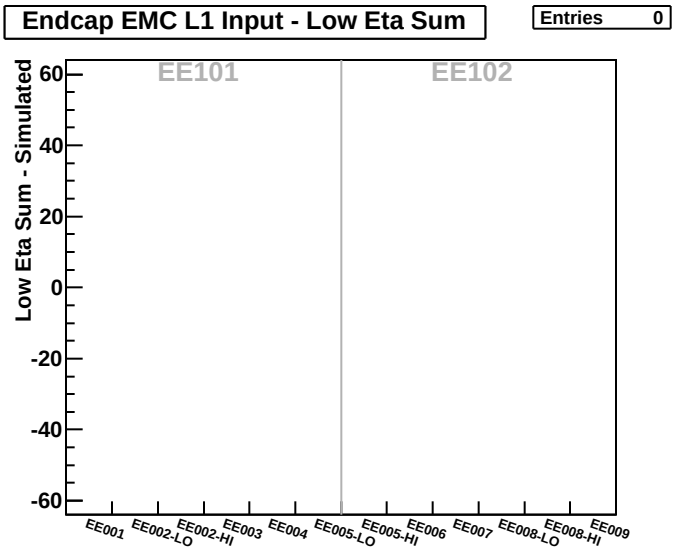

Barrel EMC L1 Input - High Eta Sum
Entries 993744
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

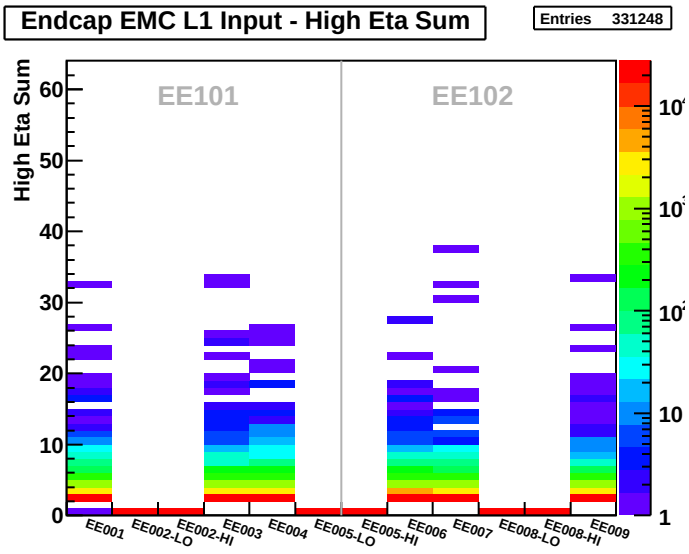

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

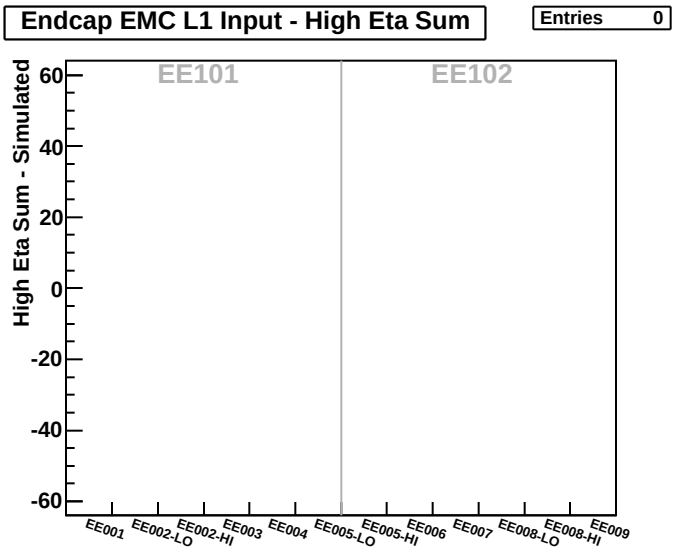

Barrel EMC L1 Input - High Tower Bits
Entries 993744
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

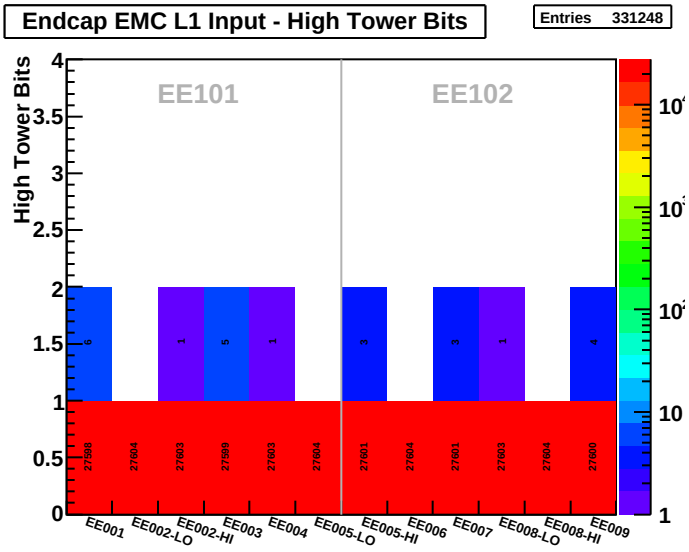

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

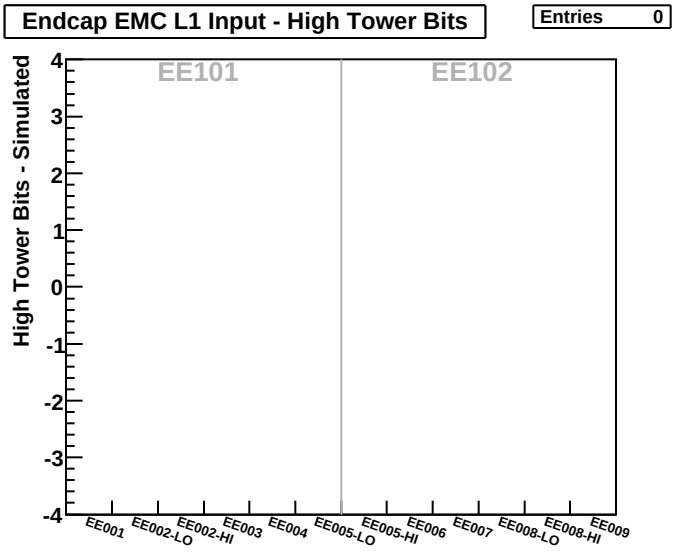

Endcap EMC L1 Input - Low Eta Sum
Entries 331248
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

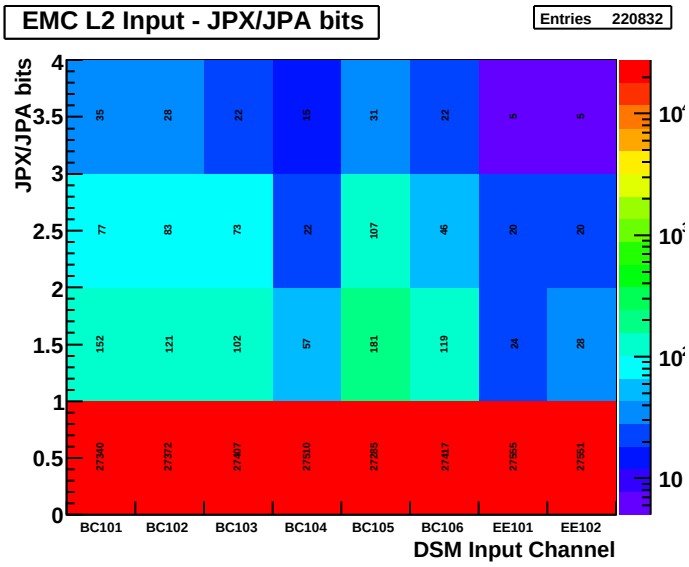

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

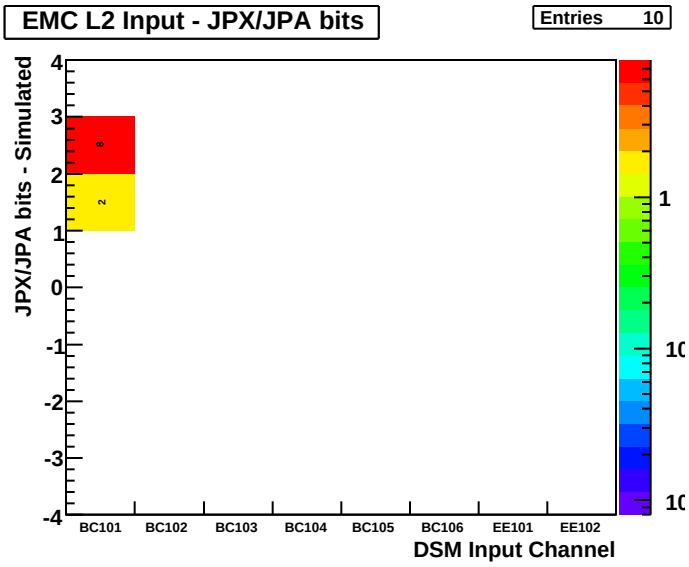

Endcap EMC L1 Input - High Eta Sum
Entries 331248
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

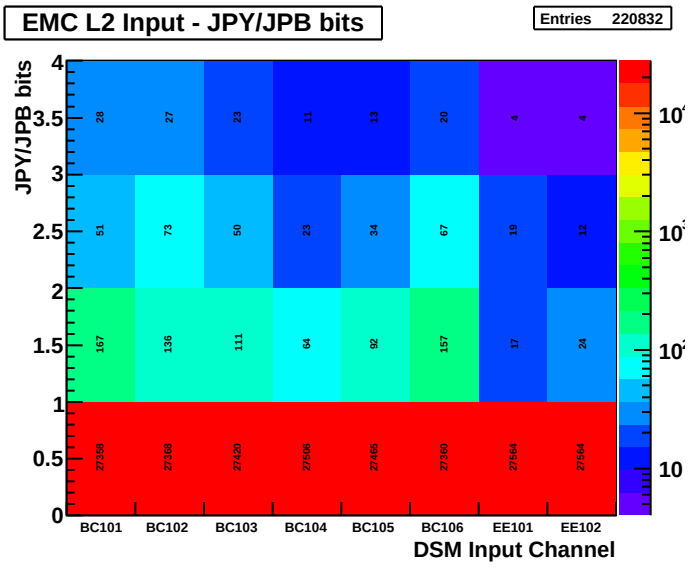

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

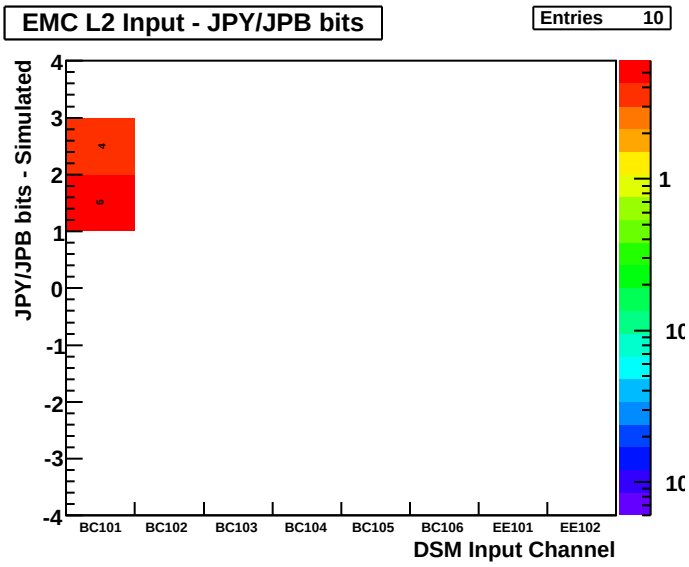

Endcap EMC L1 Input - High Tower Bits
Entries 331248
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

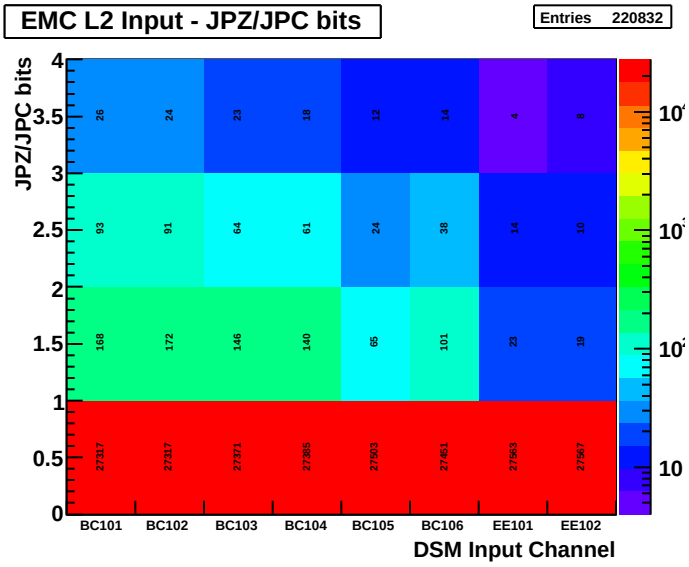

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

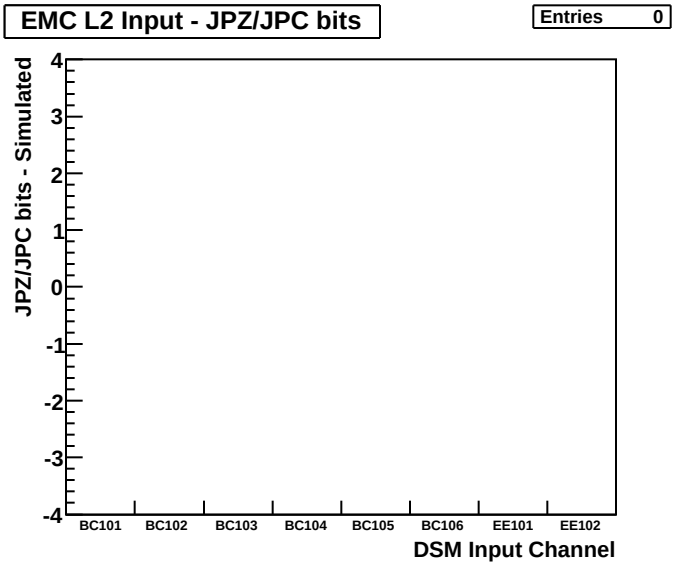

EMC L2 Input - JPX/JPA bits
Entries 220832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
35 28 22 15 31 22 5 5
77 83 73 22 107 46 20 20
152 121 102 57 181 119 24 28
27340 27372 27407 27510 27285 27417 27555 27551

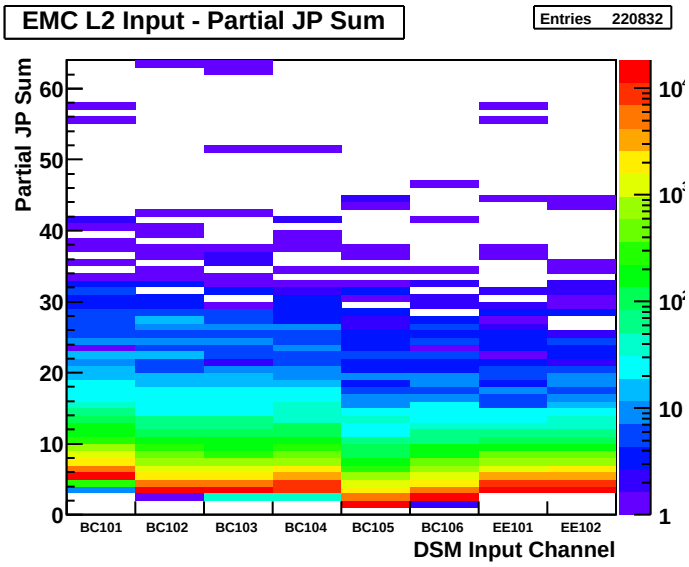

EMC L2 Input - JPX/JPA bits
Entries 10
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
8
2

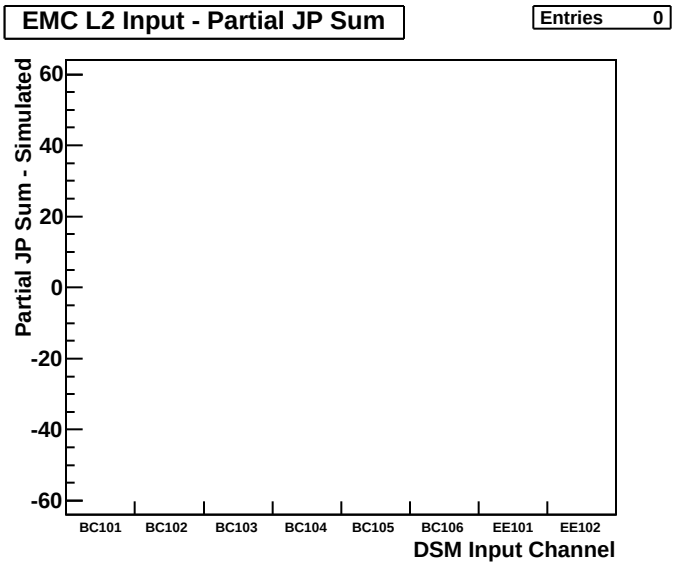

EMC L2 Input - JPY/JPB bits
Entries 220832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
28 27 23 11 13 20 4 4
51 73 50 23 34 67 19 12
167 136 111 64 92 157 17 24
27358 27368 27420 27506 27465 27360 27564 27564

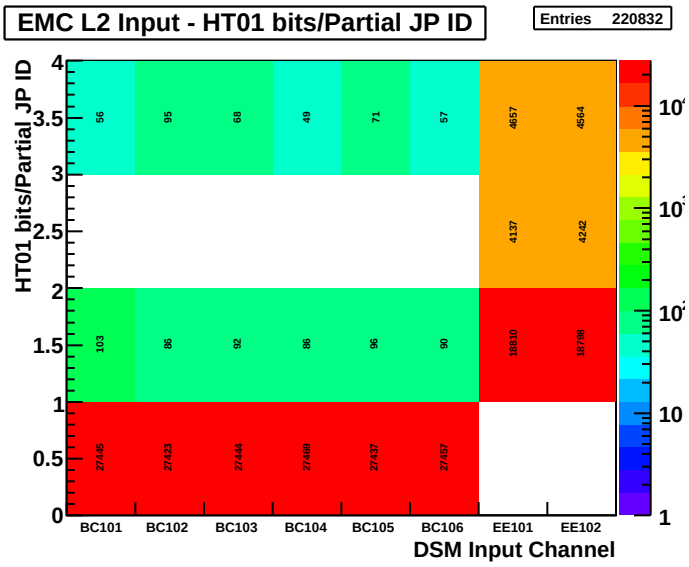

EMC L2 Input - JPY/JPB bits
Entries 10
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4
6

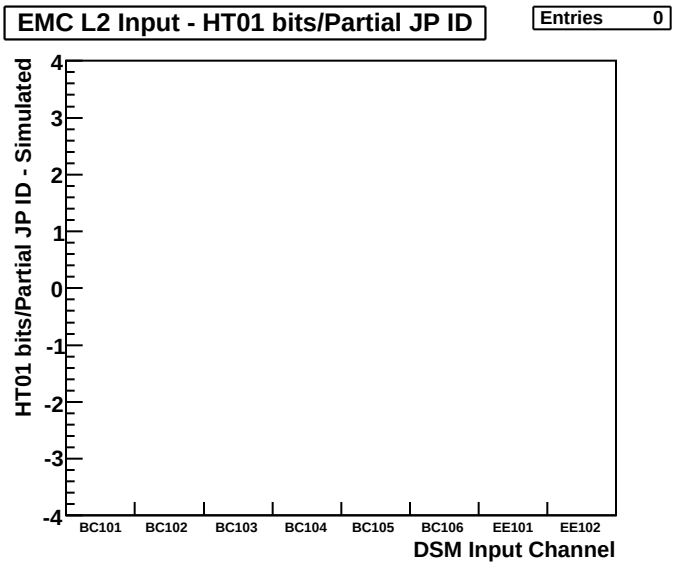

EMC L2 Input - JPZ/JPC bits
Entries 220832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
26 24 23 18 12 14 4 8
93 91 64 61 24 38 14 10
168 172 146 140 65 101 23 19
27317 27317 27371 27385 27503 27451 27563 27567

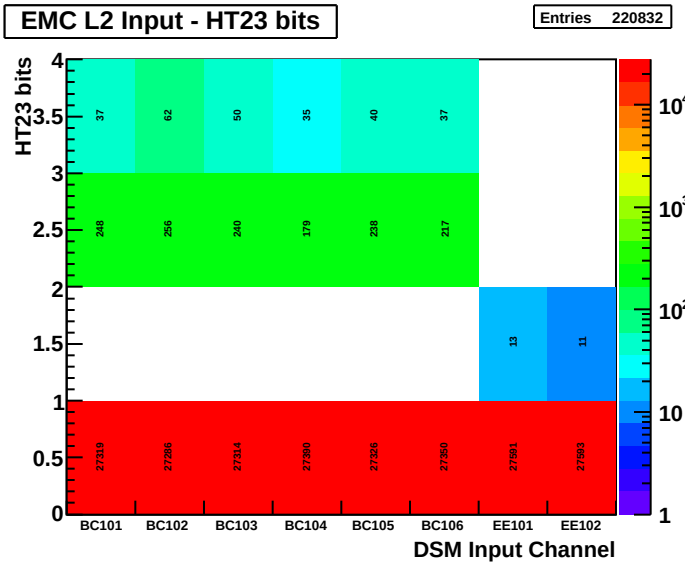

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

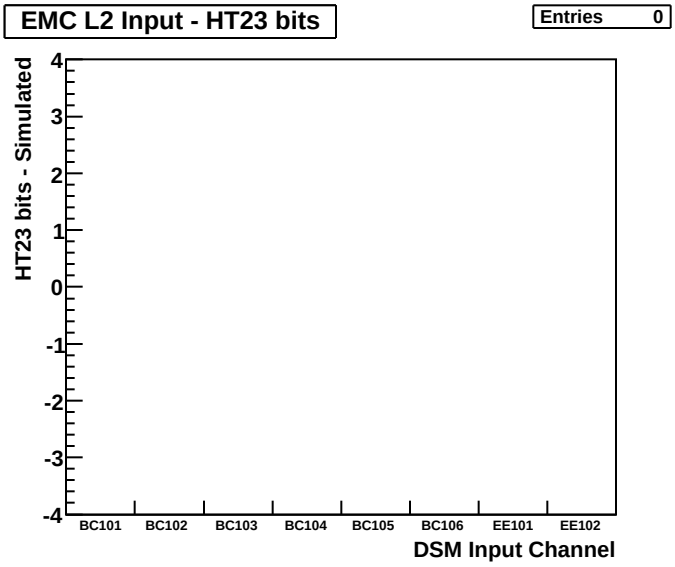

EMC L2 Input - Partial JP Sum
Entries 220832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

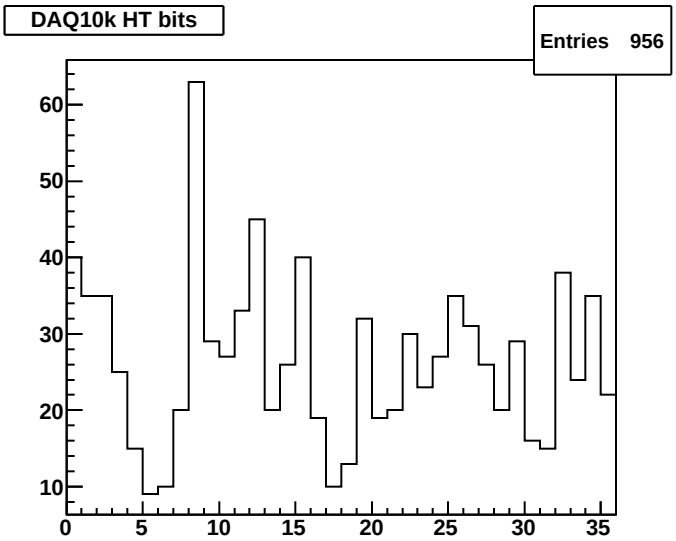

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

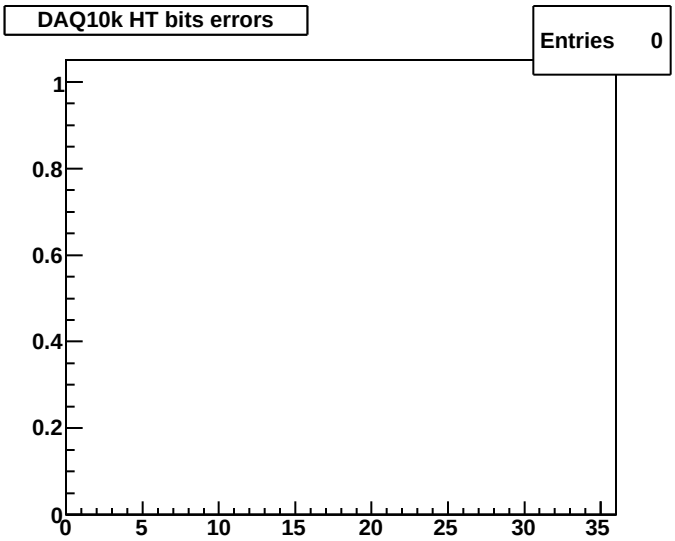

EMC L2 Input - HT01 bits/Partial JP ID
Entries 220832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

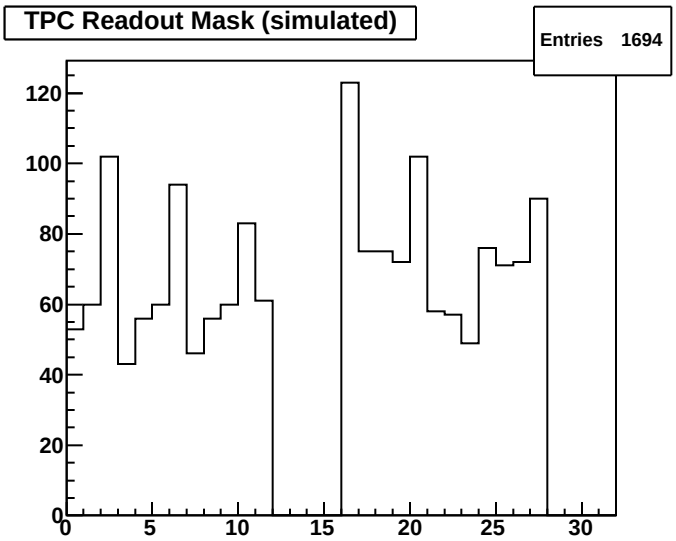

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

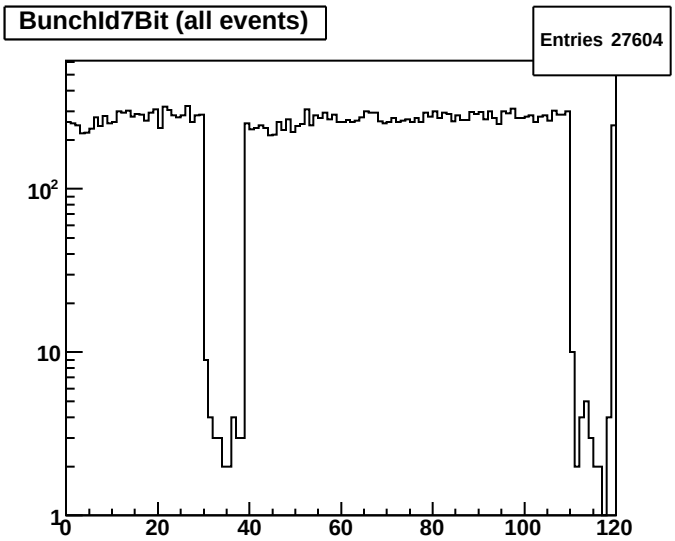

EMC L2 Input - HT23 bits
Entries 220832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

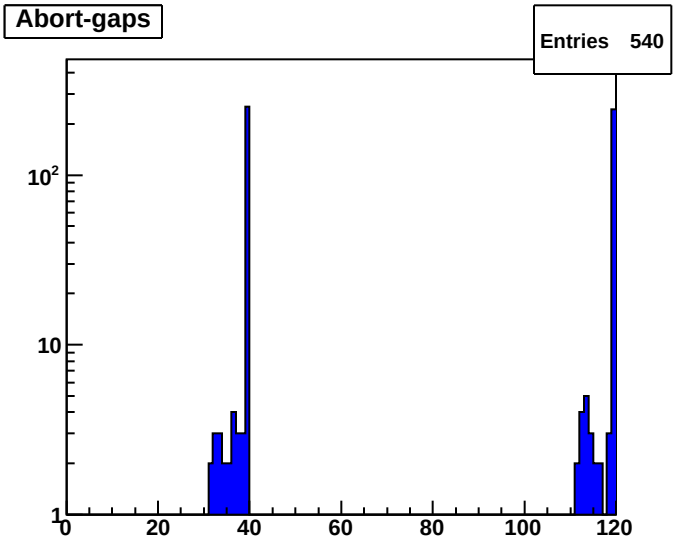

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

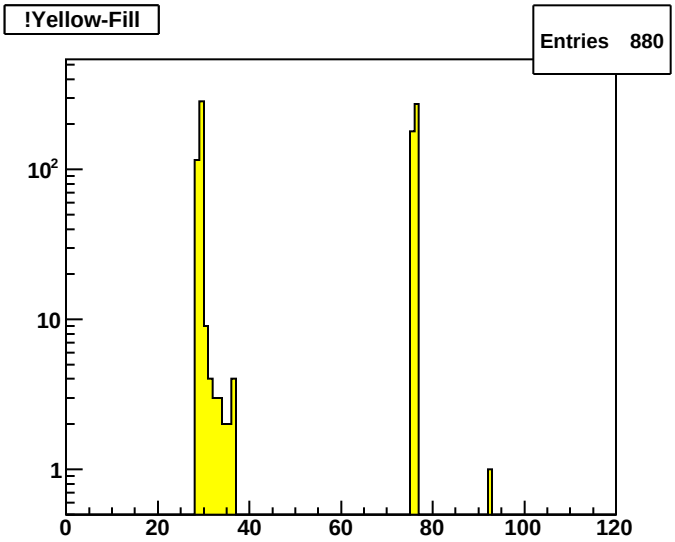

DAQ10k HT bits
Entries 956

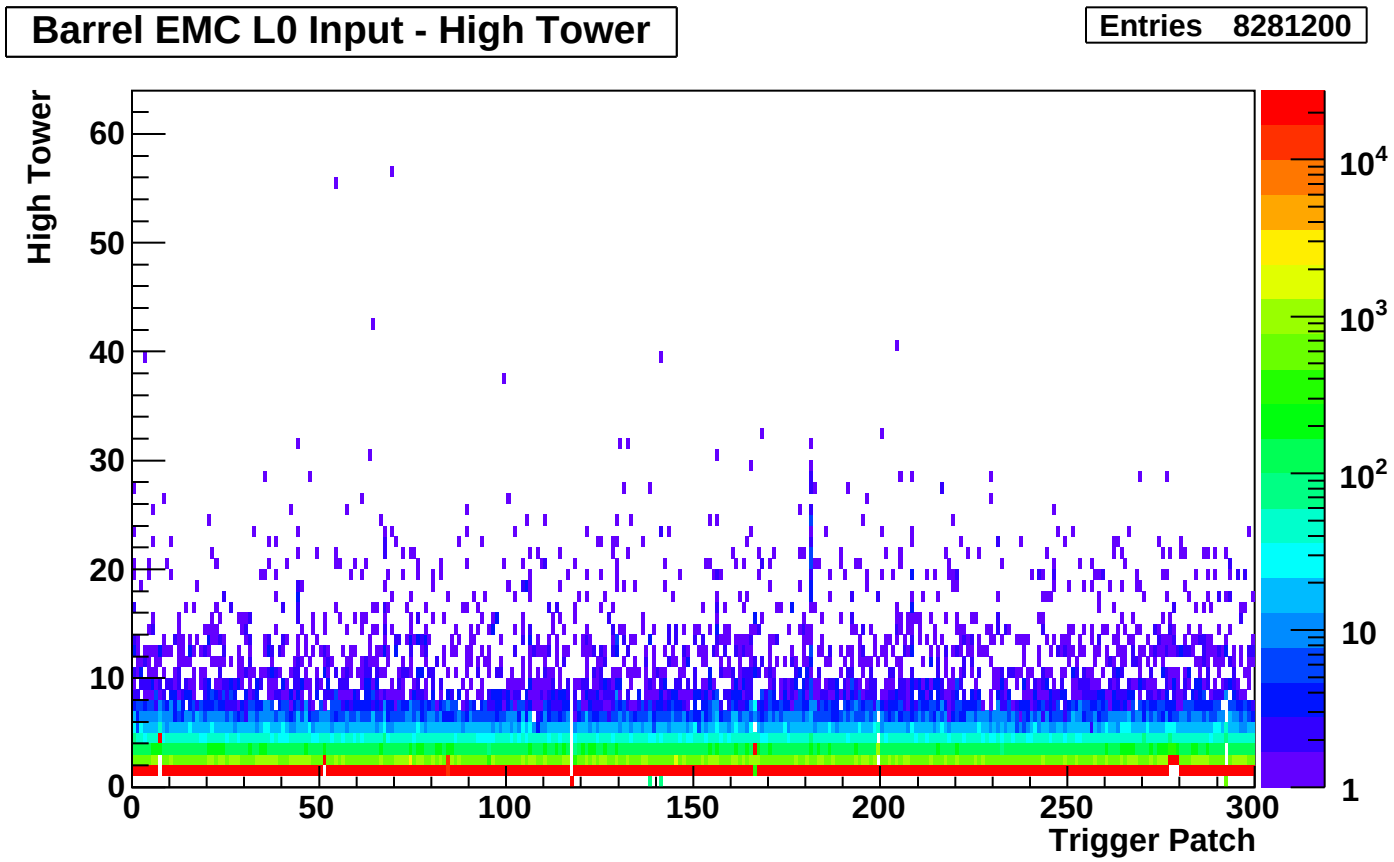

DAQ10k HT bits errors
Entries 0

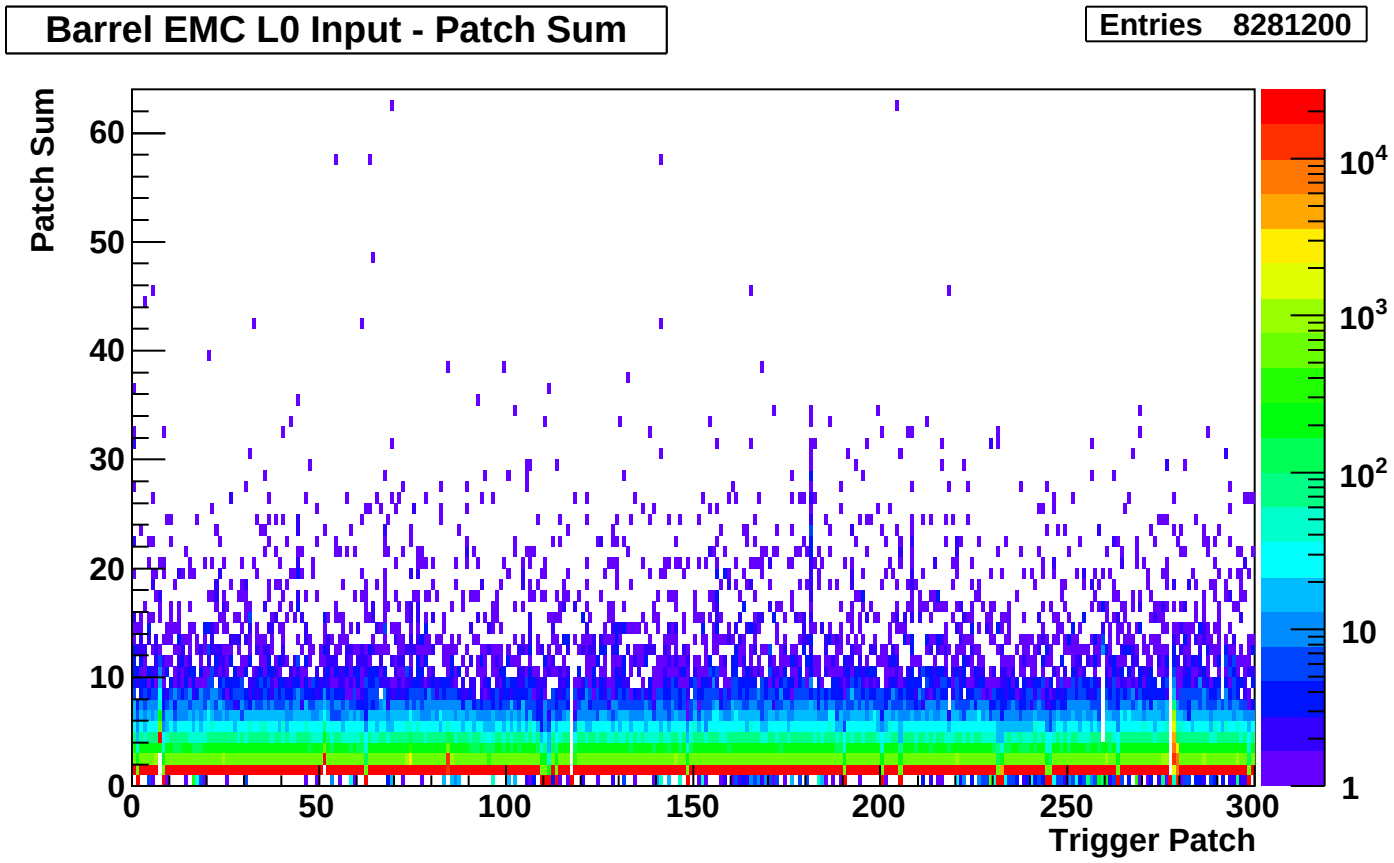

TPC Readout Mask (simulated)
Entries 1694

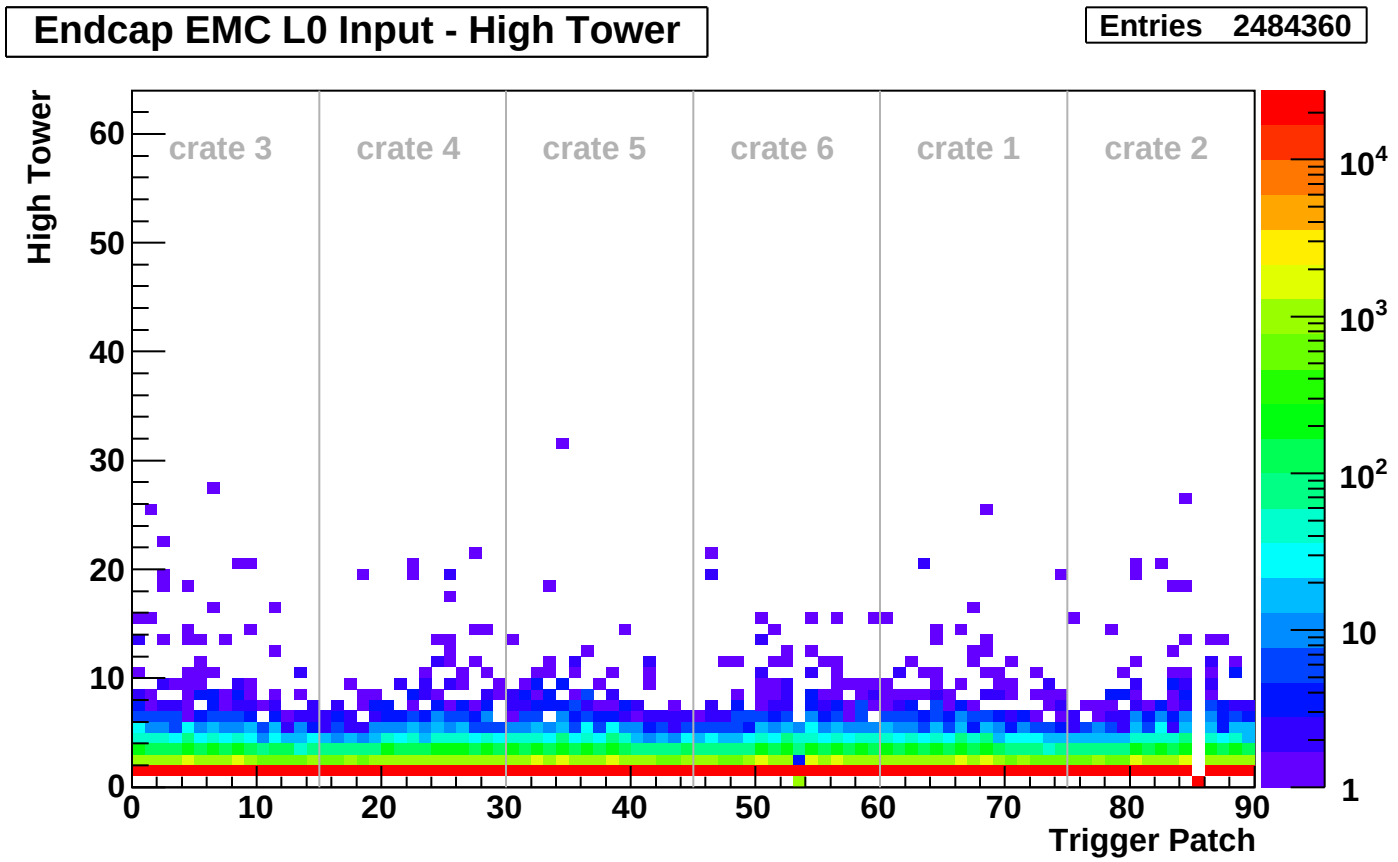

BunchId7Bit (all events)
Entries 27604

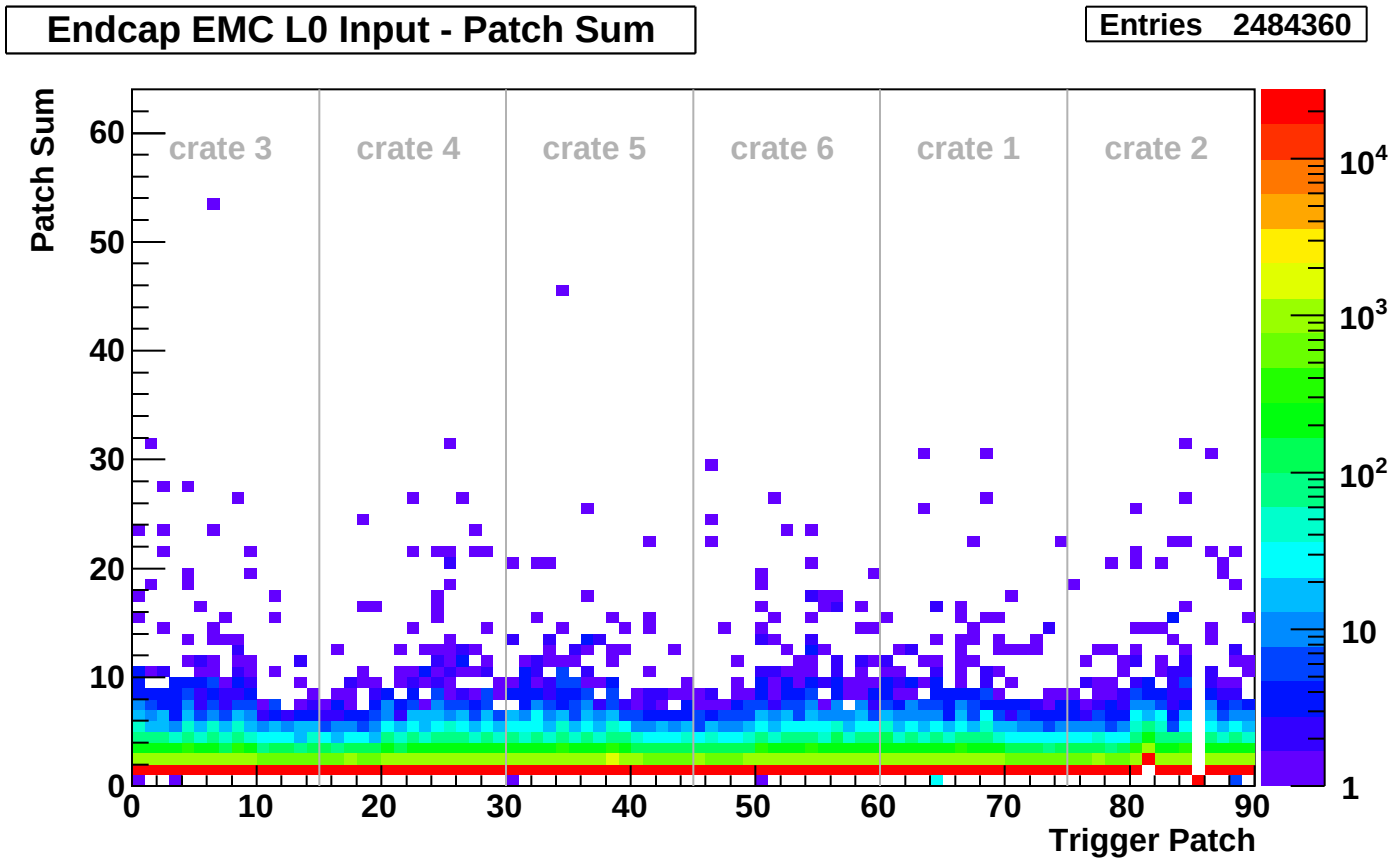

Abort-gaps
Entries 540

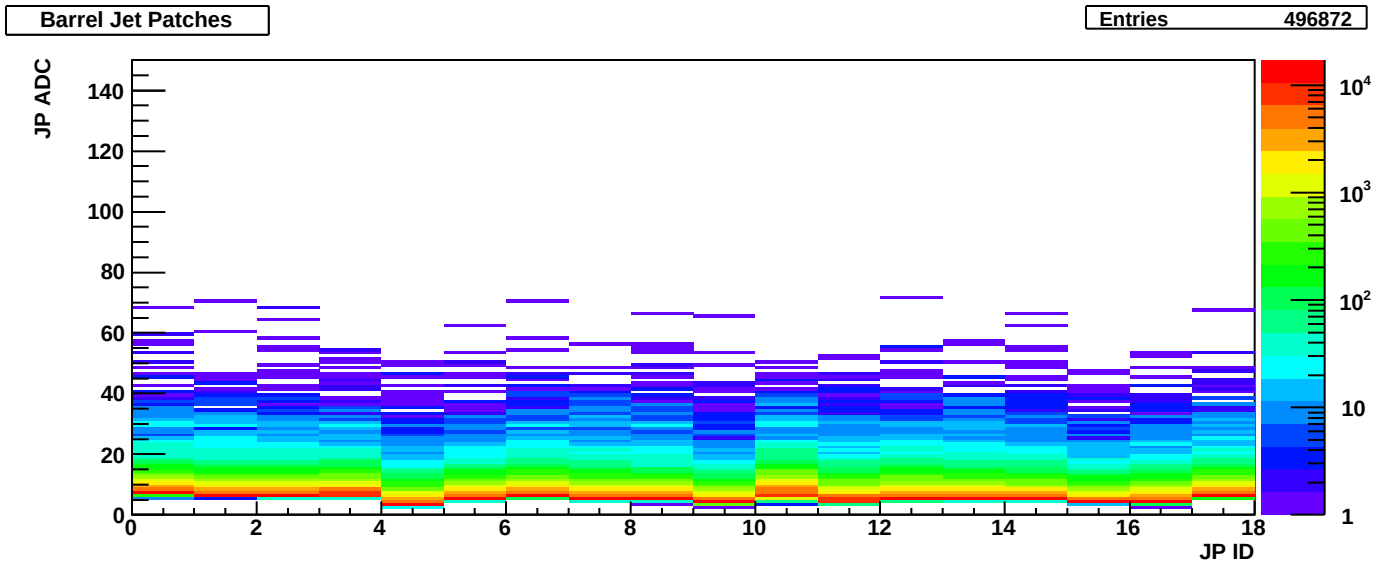

!Yellow-Fill
Entries 880

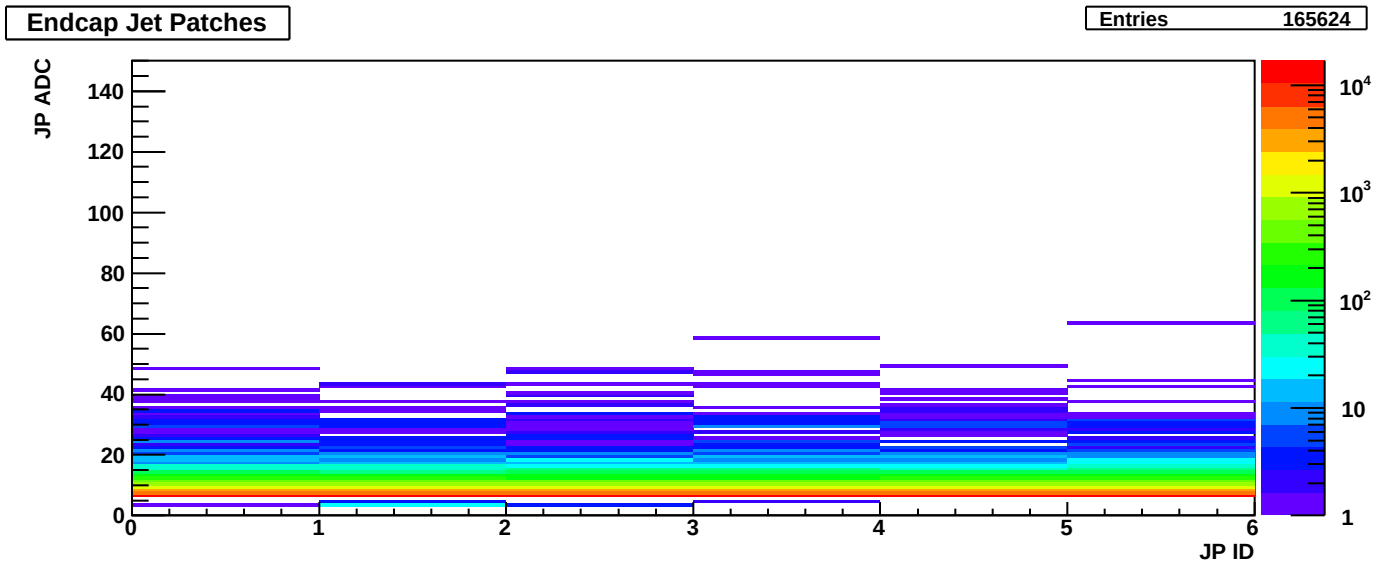

Barrel EMC L0 Input - High Tower
Entries 8281200
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

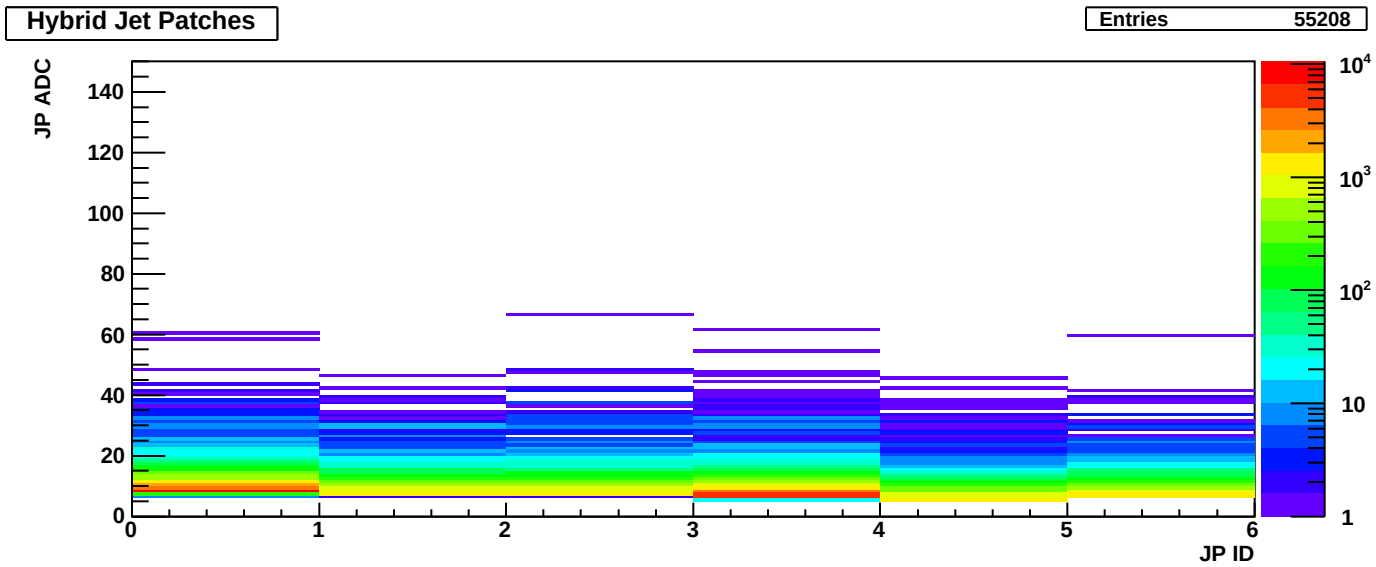

Barrel EMC L0 Input - Patch Sum
Entries 8281200
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

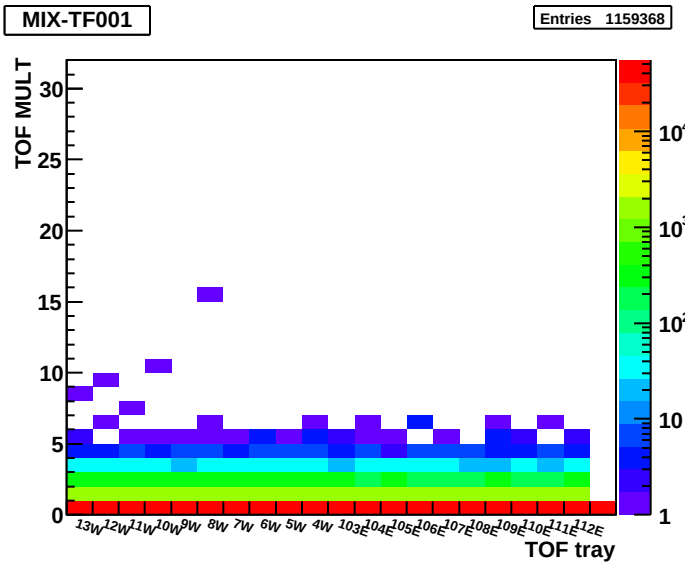

Endcap EMC L0 Input - High Tower
Entries 2484360
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

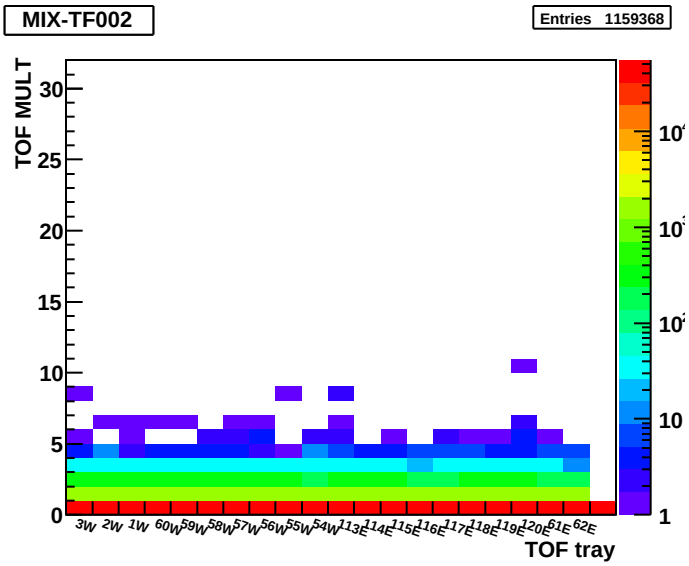

Endcap EMC L0 Input - Patch Sum
Entries 2484360
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

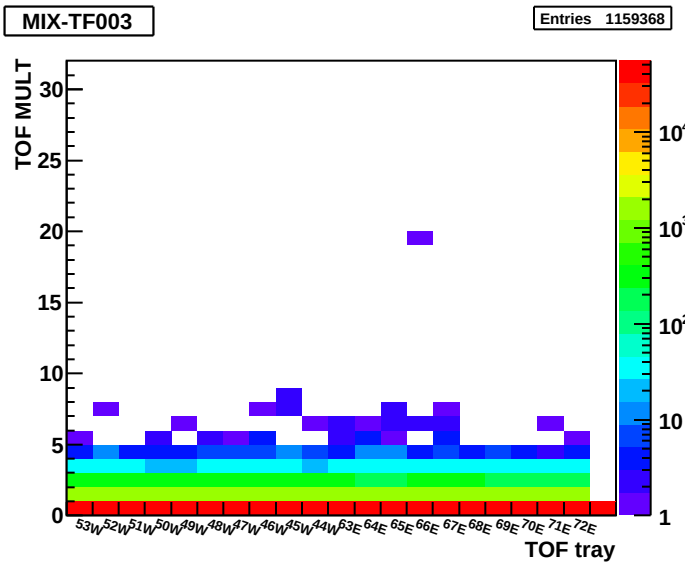

Barrel Jet Patches
Entries 496872
JP ADC
JP ID

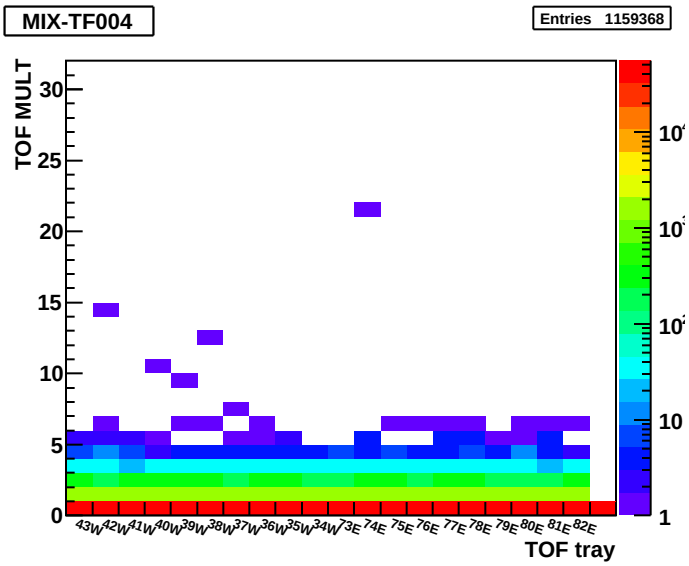

Endcap Jet Patches
Entries 165624
JP ADC
JP ID

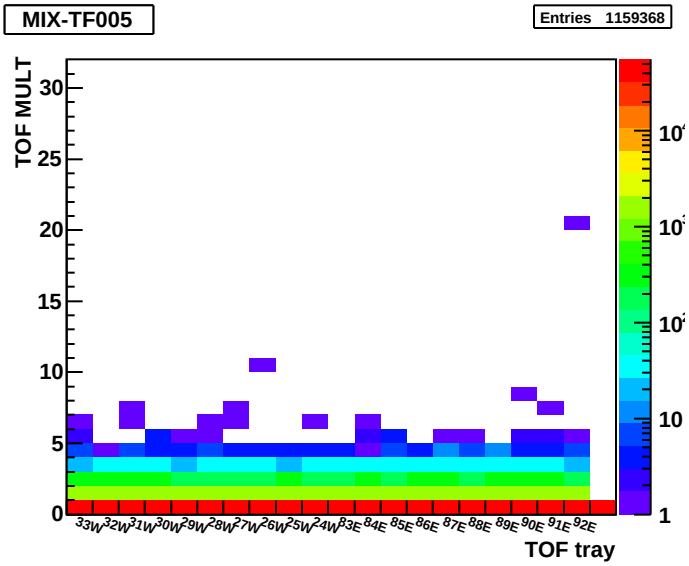

Hybrid Jet Patches
Entries 55208
JP ADC
JP ID

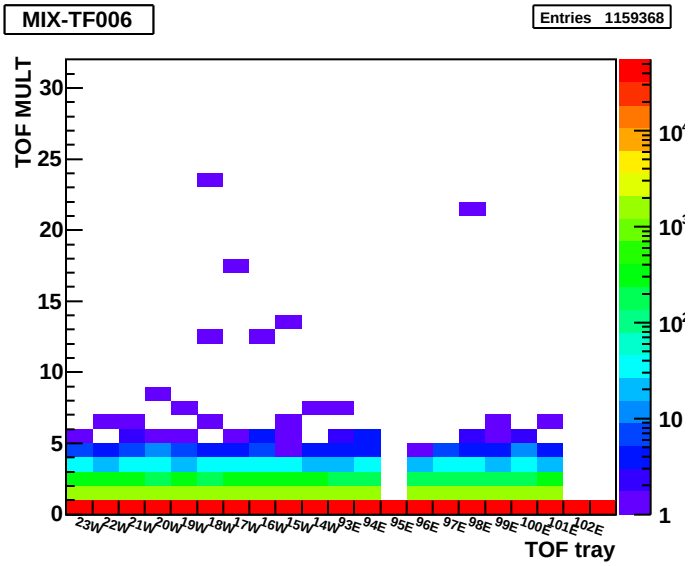

MIX-TF001
Entries 1159368
TOF MULT
TOF tray

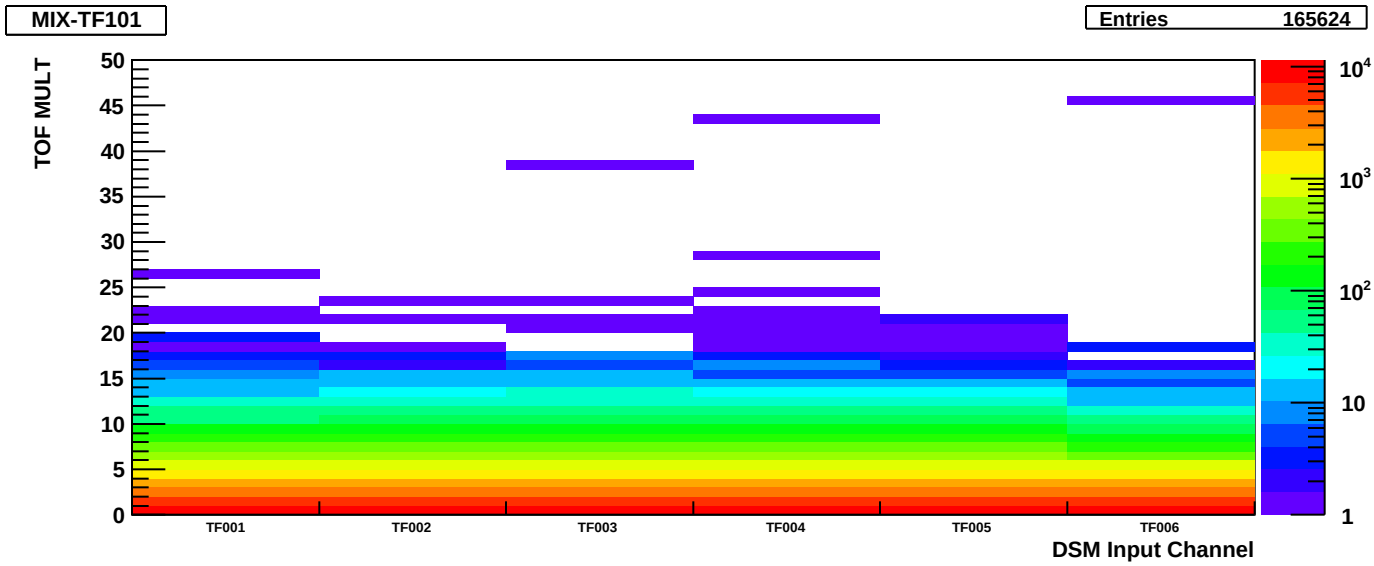

MIX-TF002
Entries 1159368
TOF MULT
TOF tray

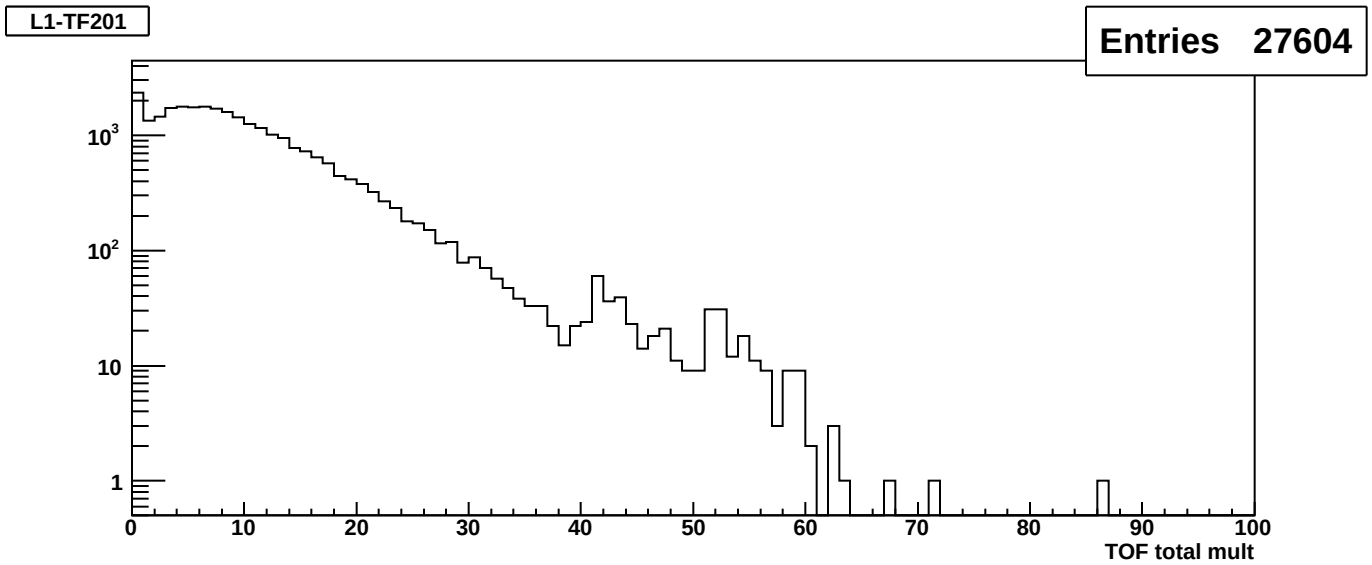

MIX-TF003
Entries 1159368
TOF MULT
TOF tray

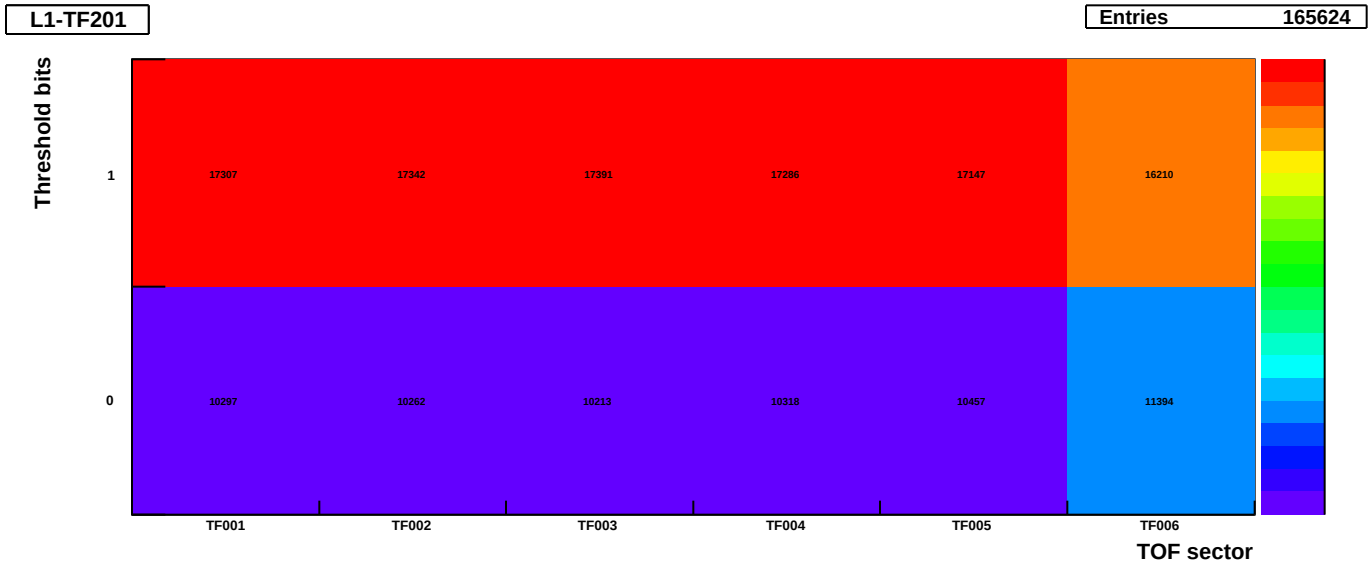

MIX-TF004
Entries 1159368
TOF MULT
TOF tray

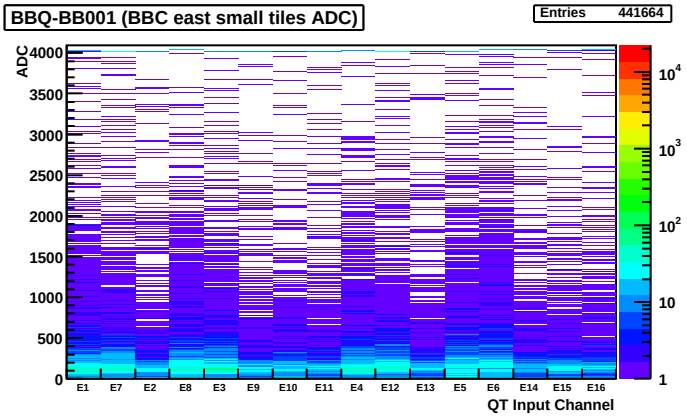

MIX-TF005
Entries 1159368
TOF MULT
TOF tray

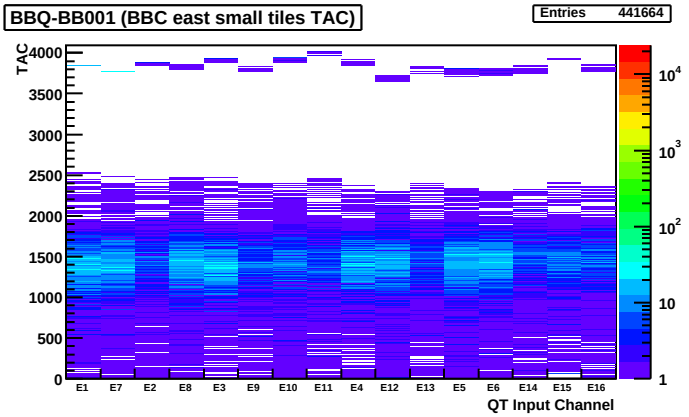

MIX-TF006
Entries 1159368
TOF MULT
TOF tray

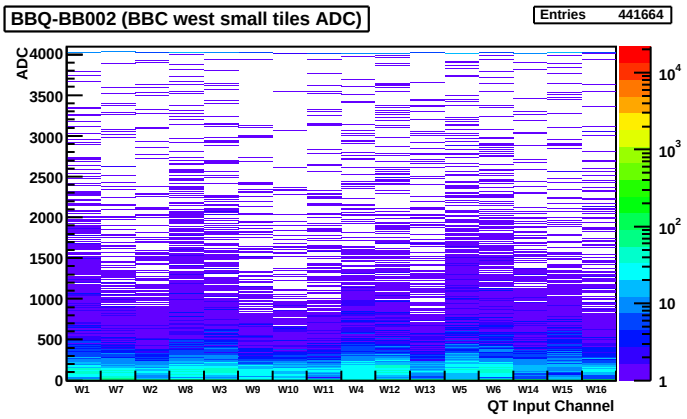

MIX-TF101
Entries 165624
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

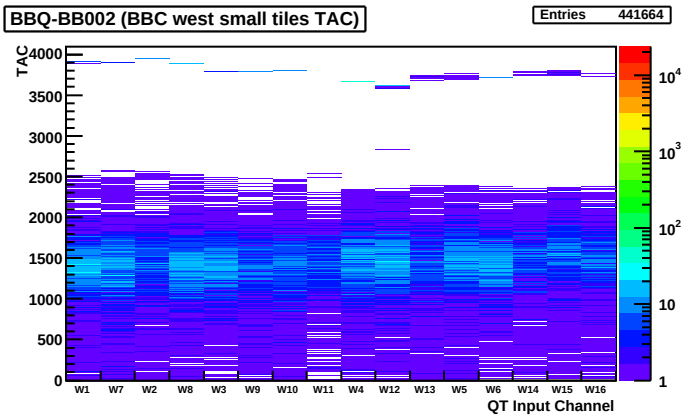

L1-TF201
Entries 27604
TOF total mult

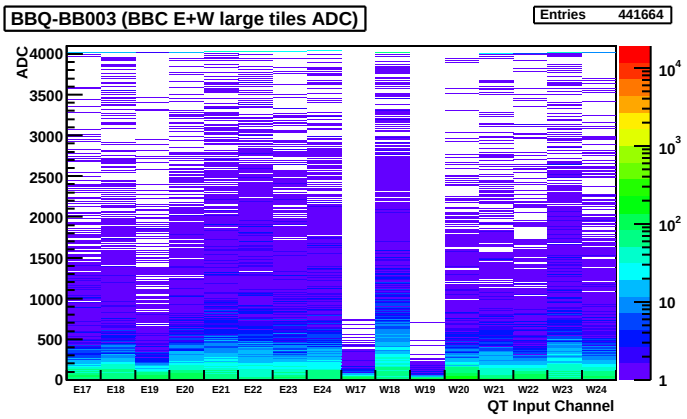

L1-TF201
Entries 165624
Threshold bits
17307
17342
17391
17286
17147
16210
10297
10262
10213
10318
10457
11394
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

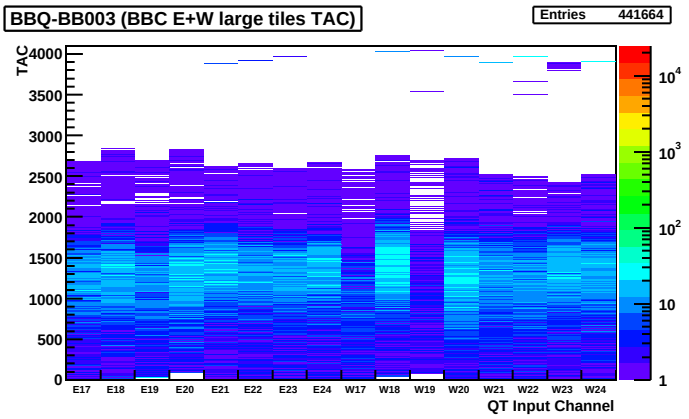

BBQ-BB001 (BBC east small tiles ADC)
Entries 441664
ADC
QT Input Channel

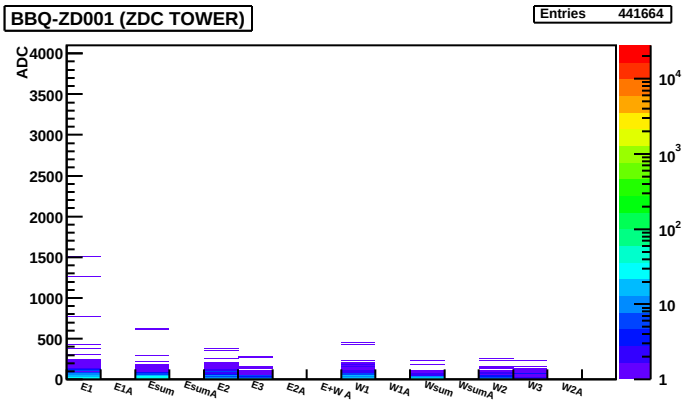

BBQ-BB001 (BBC east small tiles TAC)
Entries 441664
TAC
QT Input Channel

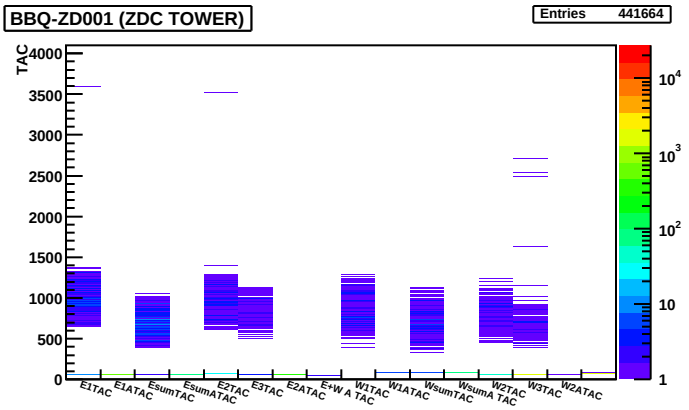

BBQ-BB002 (BBC west small tiles ADC)
Entries 441664
ADC
QT Input Channel

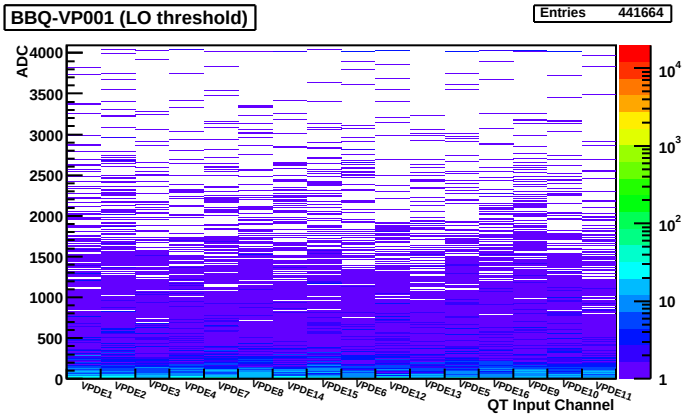

BBQ-BB002 (BBC west small tiles TAC)
Entries 441664
TAC
QT Input Channel

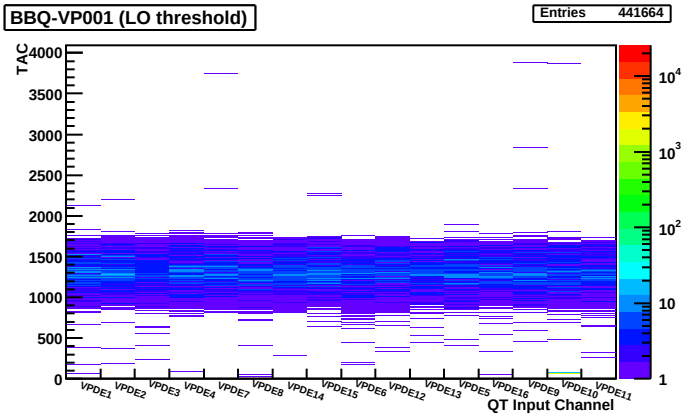

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 441664
ADC
QT Input Channel

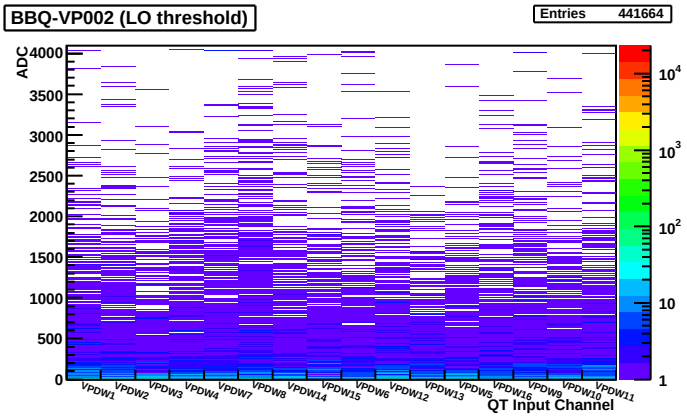

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 441664
TAC
QT Input Channel

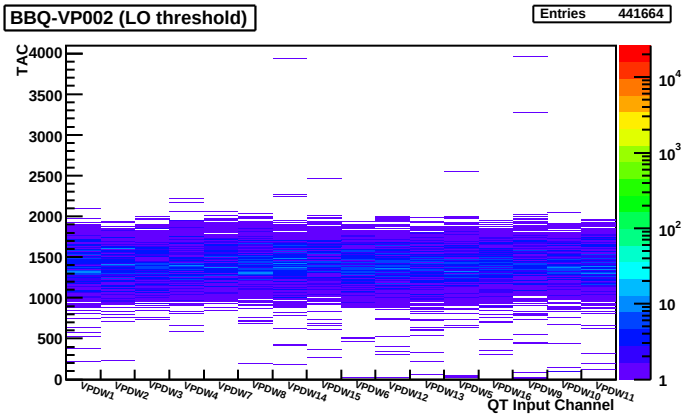

BBQ-ZD001 (ZDC TOWER)
Entries 441664
ADC

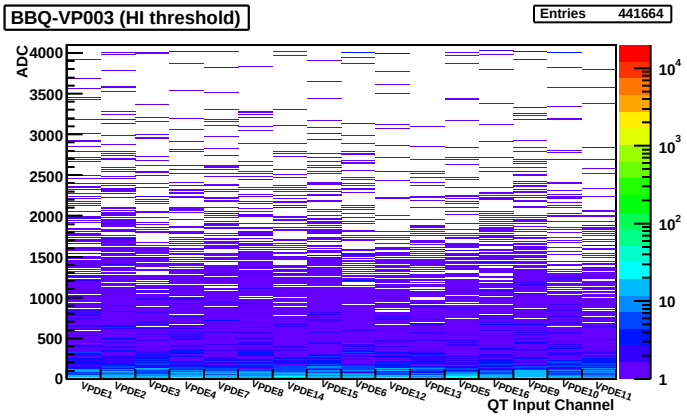

BBQ-ZD001 (ZDC TOWER)
Entries 441664
TAC

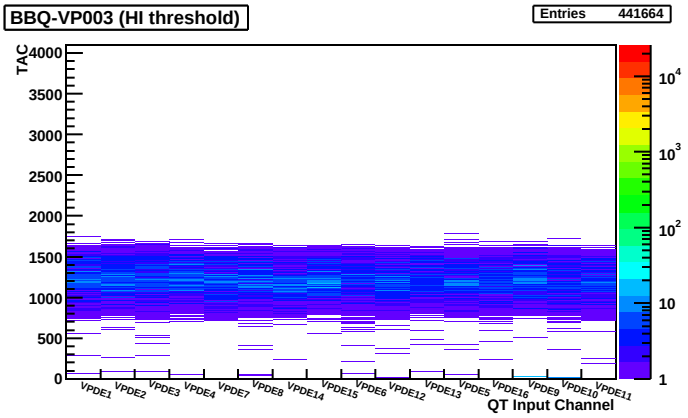

BBQ-VP001 (LO threshold)
Entries 441664
ADC
QT Input Channel

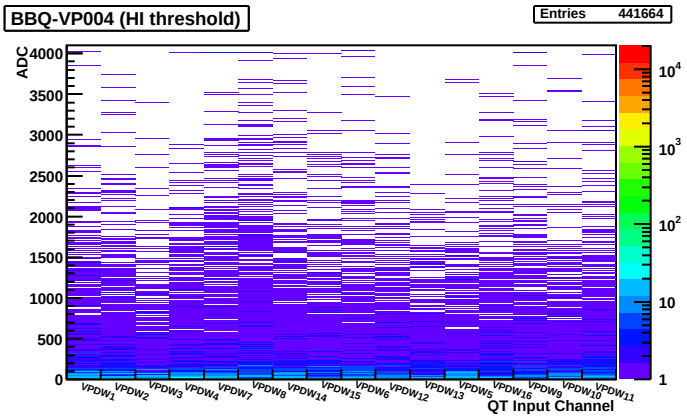

BBQ-VP001 (LO threshold)
Entries 441664
TAC
QT Input Channel

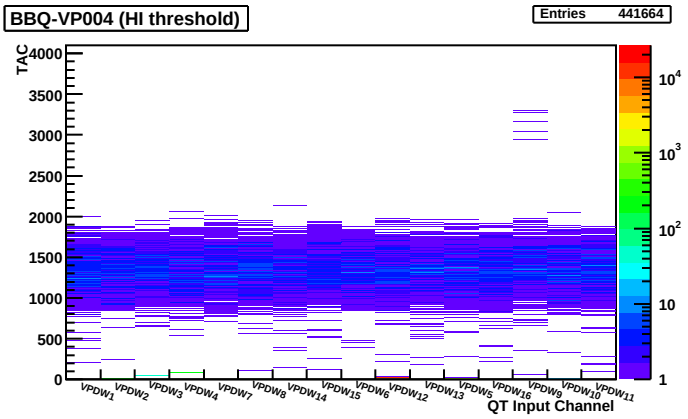

BBQ-VP002 (LO threshold)
Entries 441664
ADC
QT Input Channel

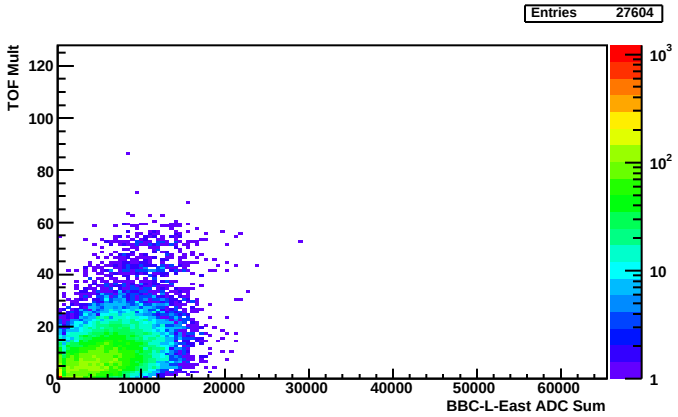

BBQ-VP002 (LO threshold)
Entries 441664
TAC
QT Input Channel

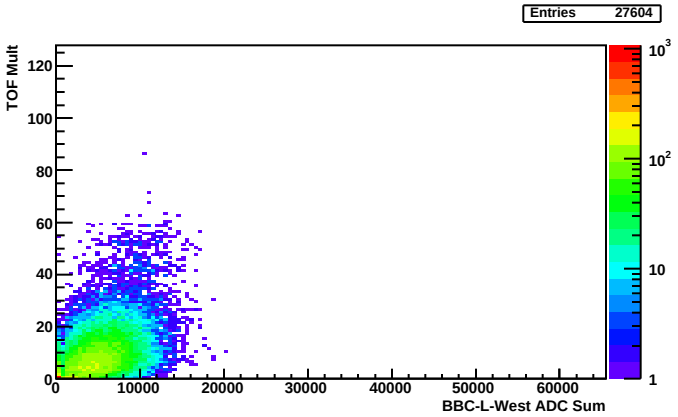

BBQ-VP003 (HI threshold)
Entries 441664
ADC
QT Input Channel

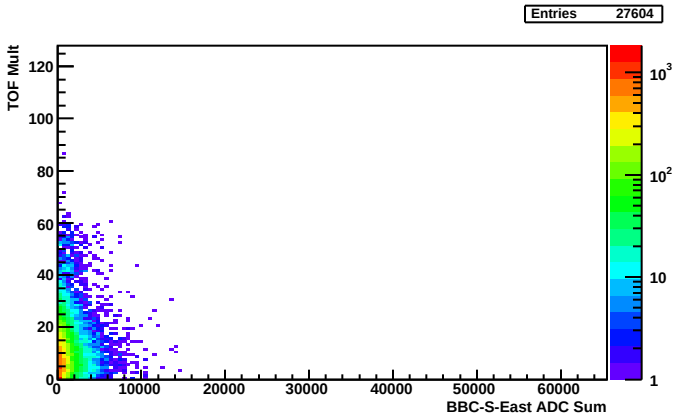

BBQ-VP003 (HI threshold)
Entries 441664
TAC
QT Input Channel

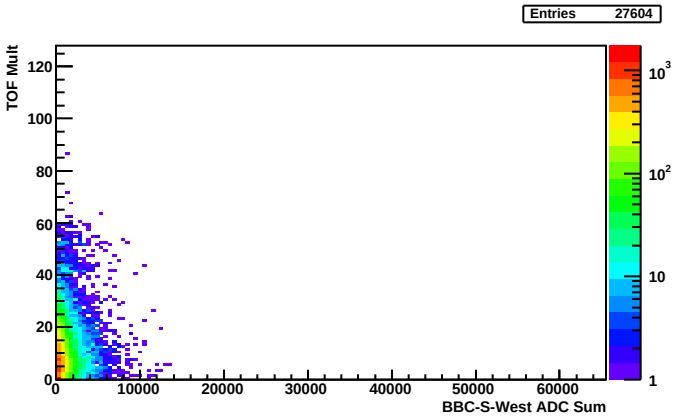

BBQ-VP004 (HI threshold)
Entries 441664
ADC
QT Input Channel

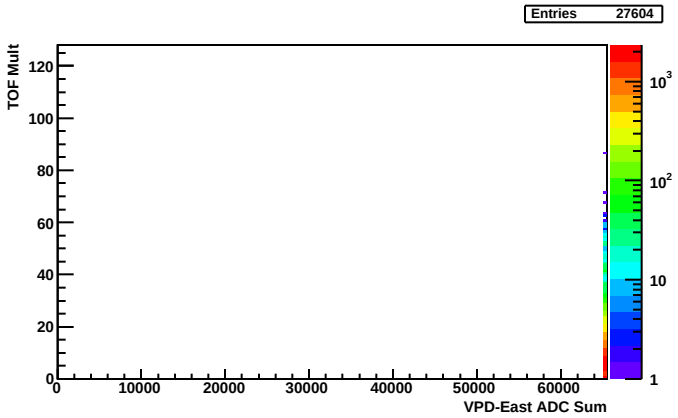

BBQ-VP004 (HI threshold)
Entries 441664
TAC
QT Input Channel

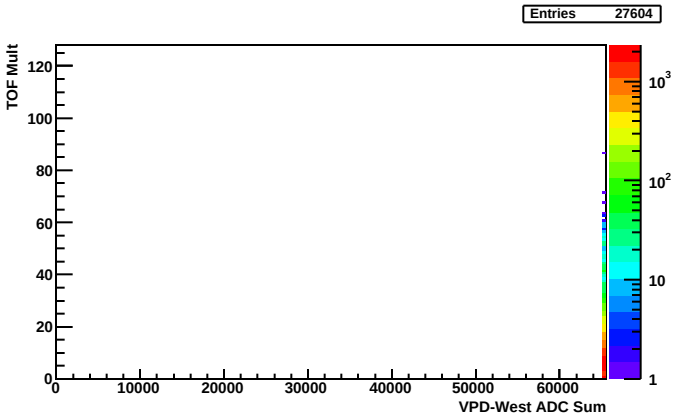

Entries 27604
TOF Mult
BBC-L-East ADC Sum

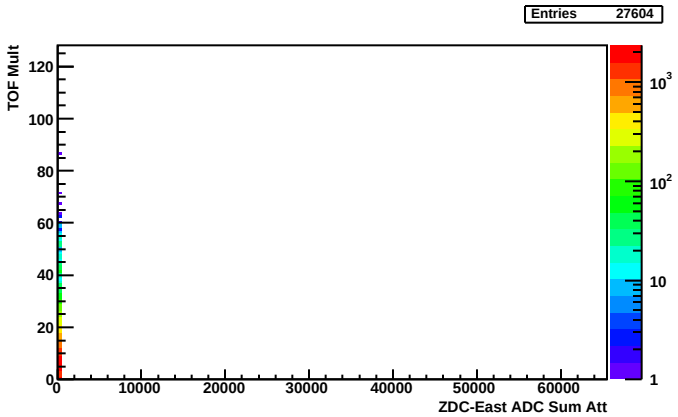

Entries 27604
TOF Mult
BBC-L-West ADC Sum

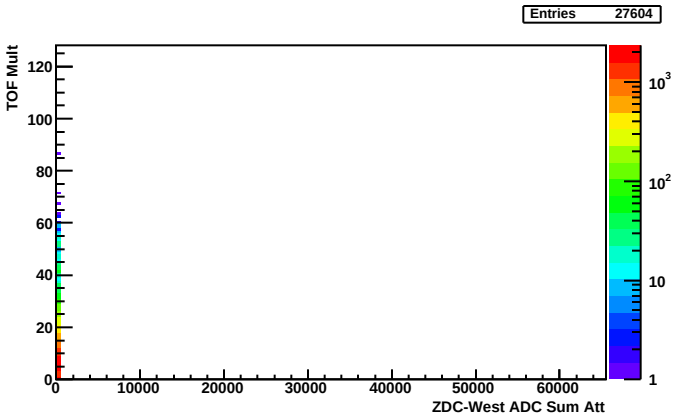

Entries 27604
TOF Mult
BBC-S-East ADC Sum

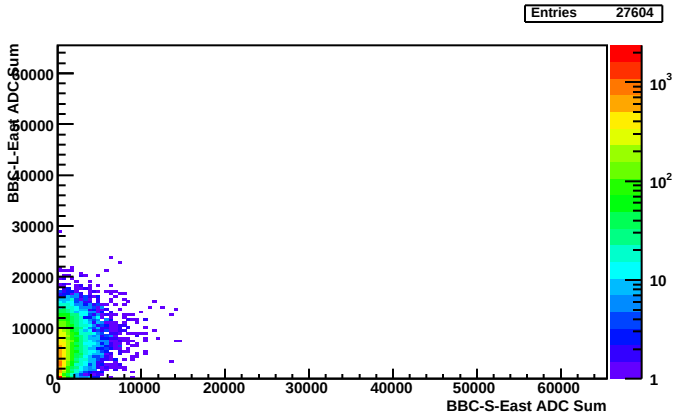

Entries 27604
TOF Mult
BBC-S-West ADC Sum

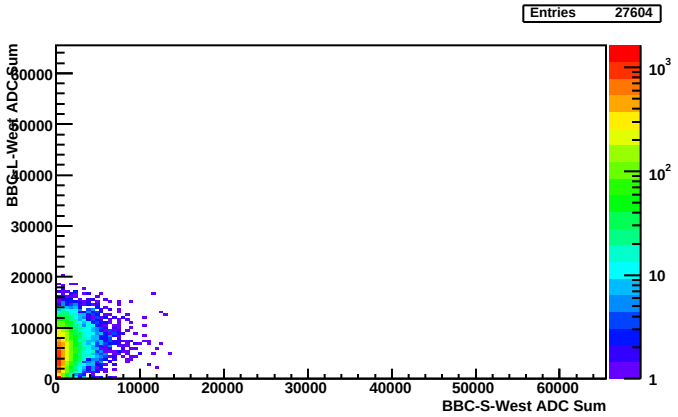

Entries 27604
TOF Mult
VPD-East ADC Sum

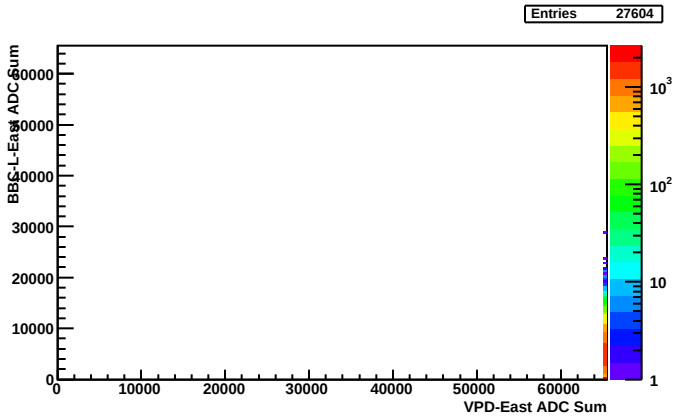

Entries 27604
TOF Mult
VPD-West ADC Sum

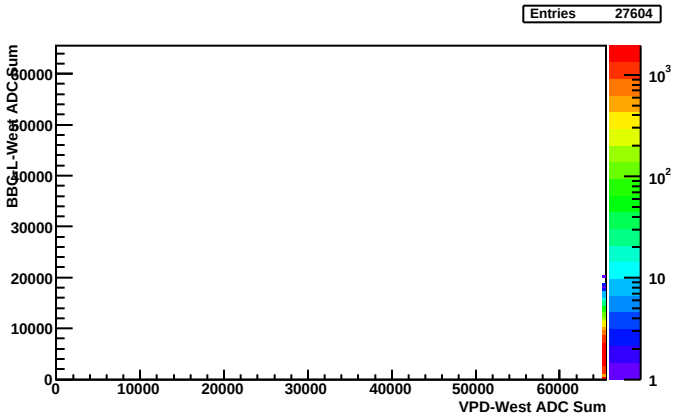

Entries 27604
TOF Mult
ZDC-East ADC Sum Att


Entries 27604
TOF Mult
ZDC-West ADC Sum Att


Entries 27604
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 27604
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 27604
BBC-L-East ADC Sum
VPD-East ADC Sum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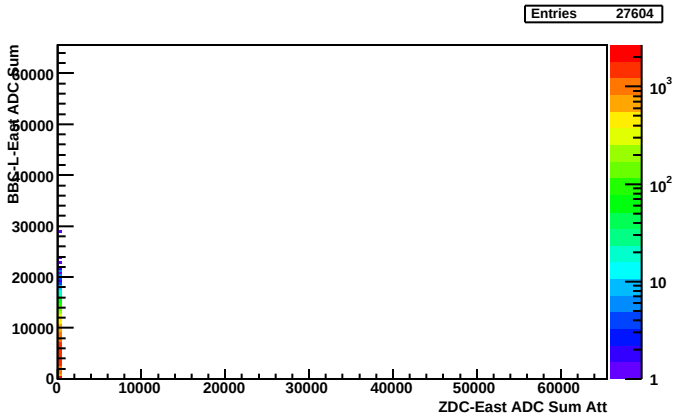
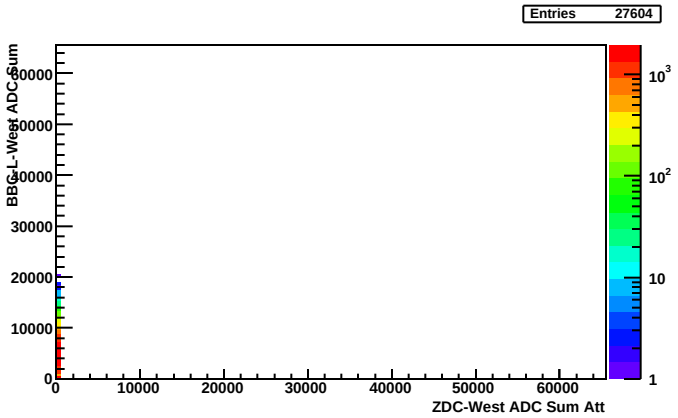
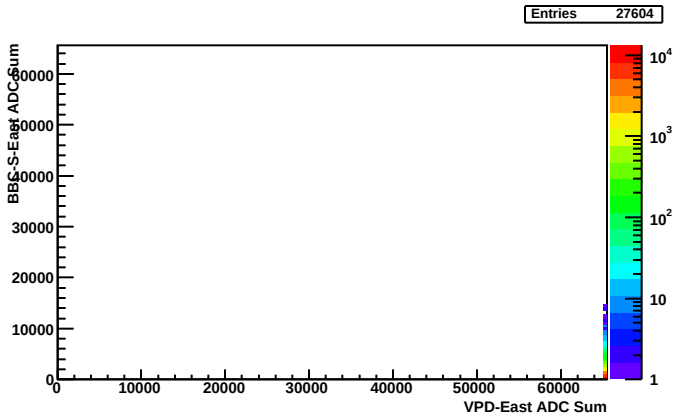
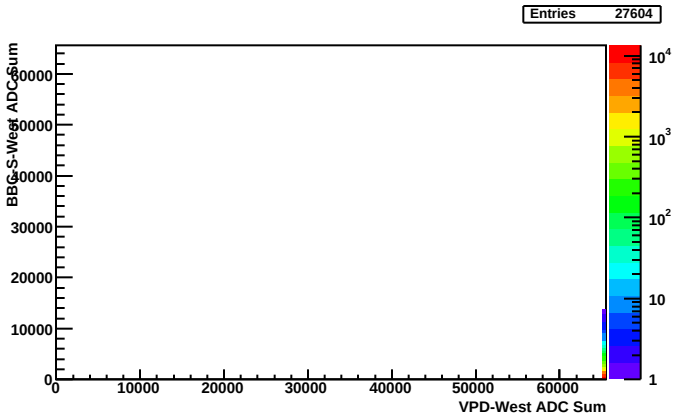
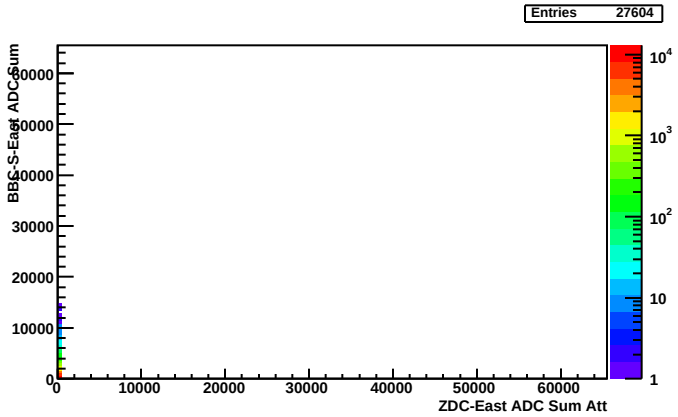
BBC-L-West ADC Sum
VPD-West ADC Sum

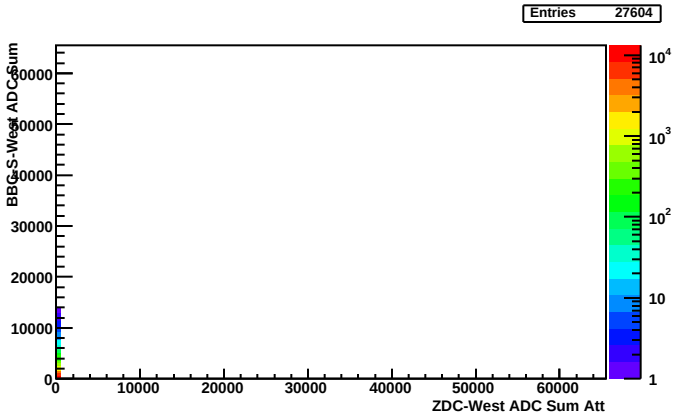

Entries 27604
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

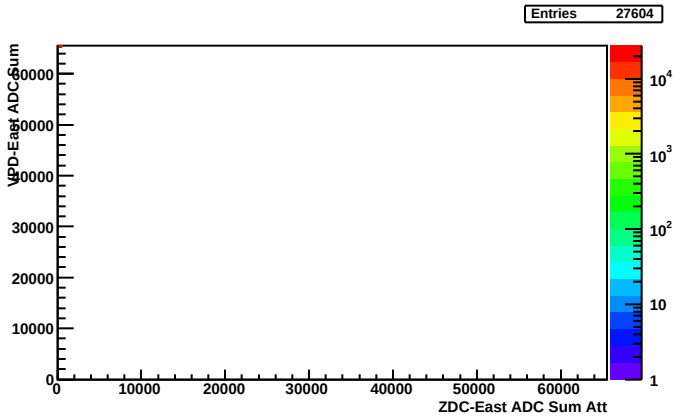

Entries 27604
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

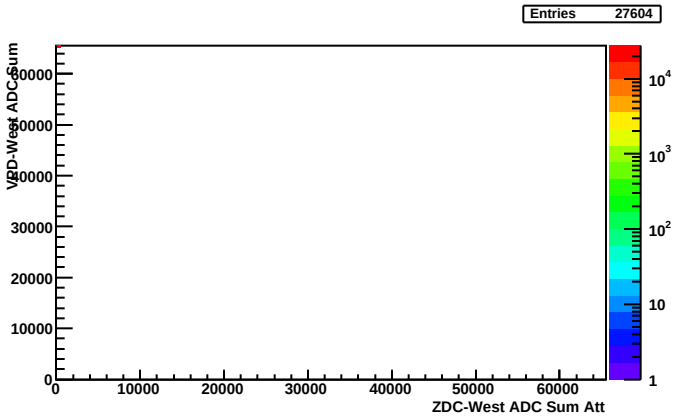

Entries 27604
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 27604
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 27604
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 27604
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 27604
VPD-East ADC Sum
ZDC-East ADC Sum Att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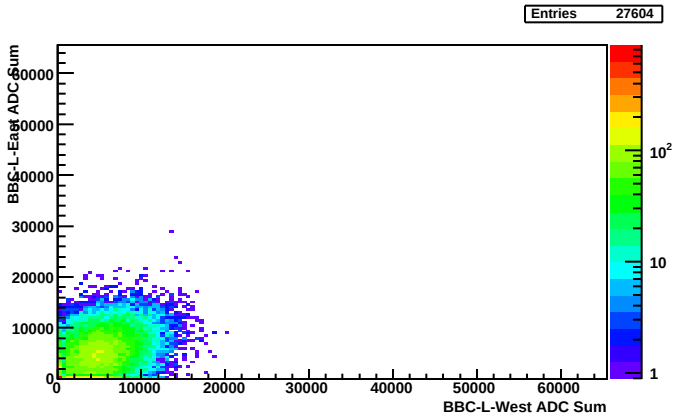
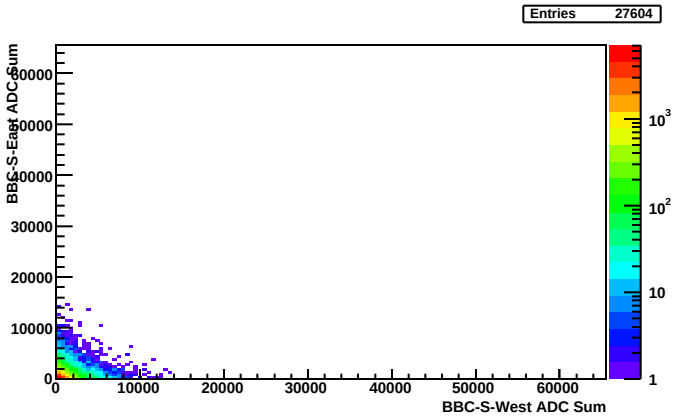
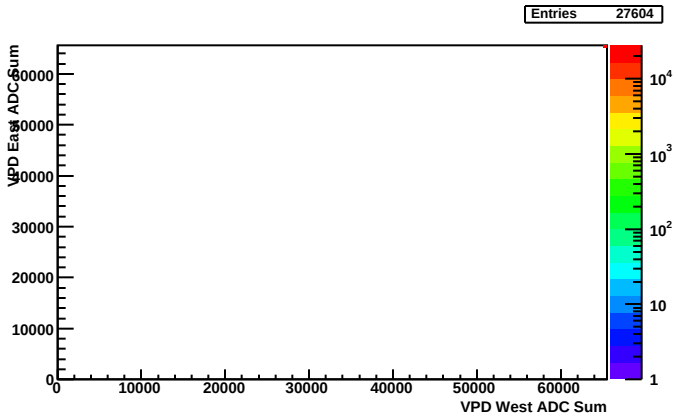
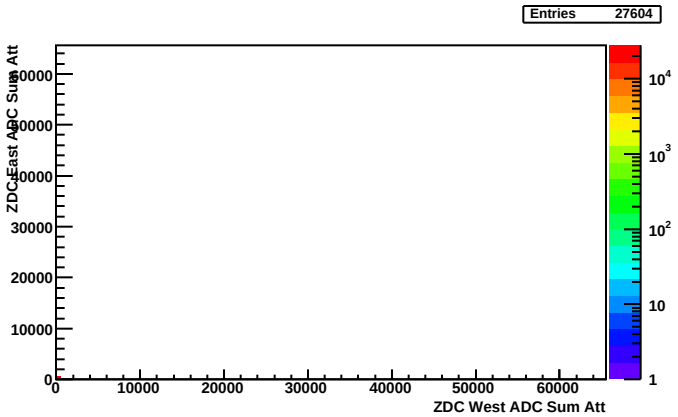
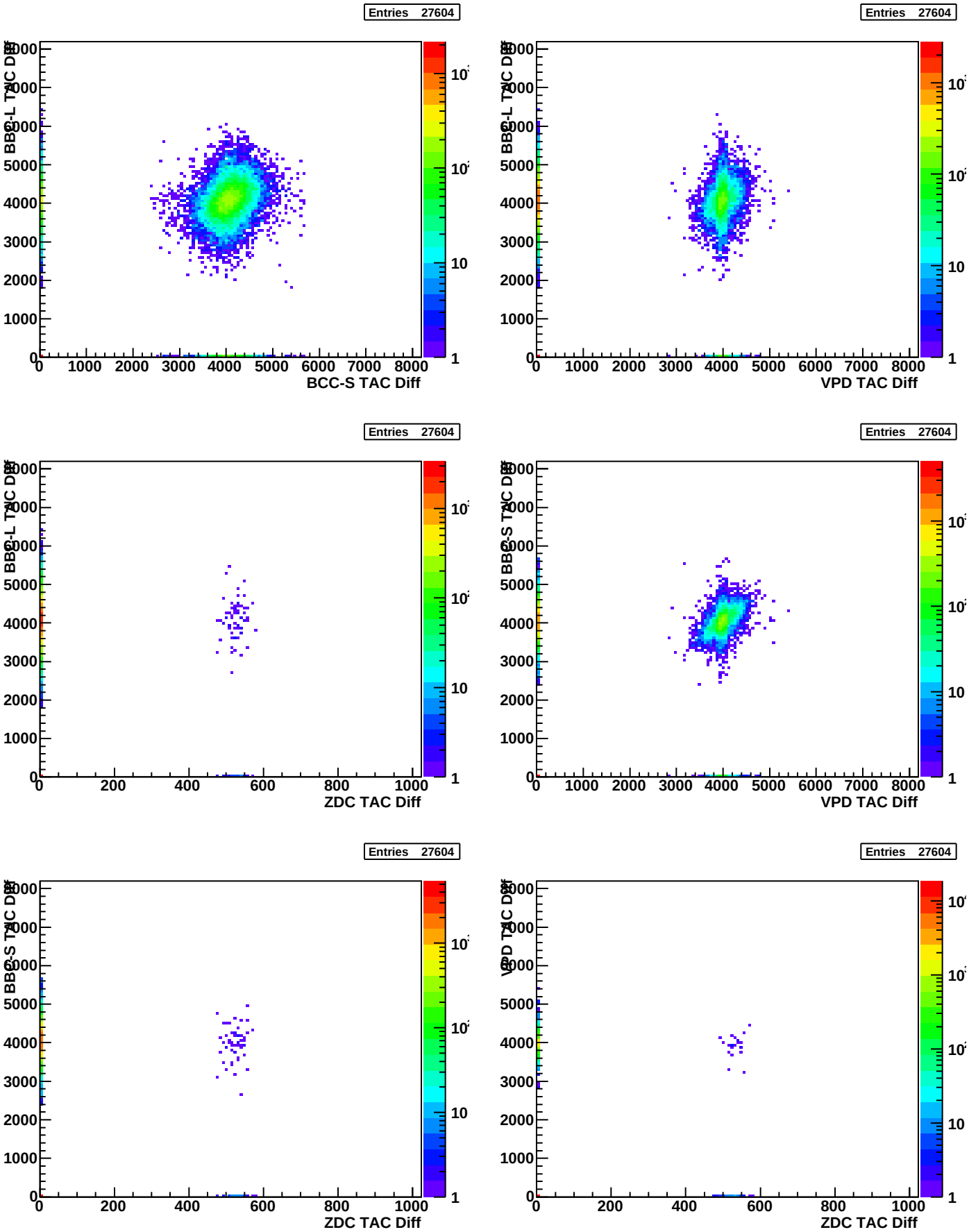
VPD-West ADC Sum
ZDC-West ADC Sum Att

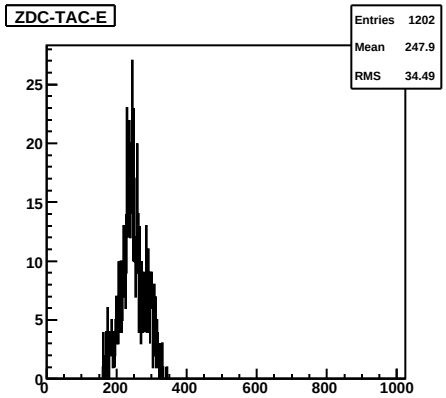

Entries 27604
BBC-L-East ADC Sum
BBC-L-West ADC Sum

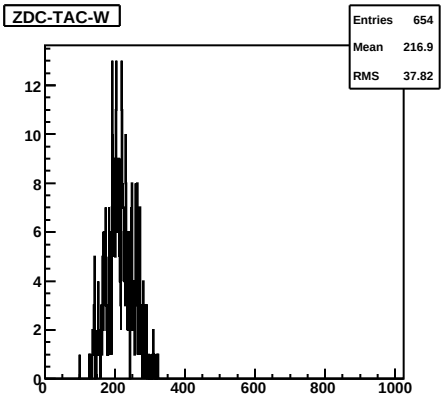

Entries 27604
BBC-S-East ADC Sum
BBC-S-West ADC Sum

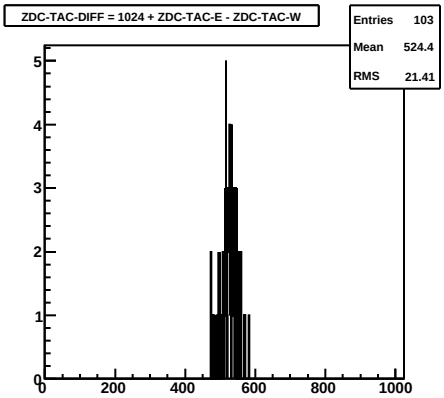

Entries 27604
VPD East ADC Sum
VPD West ADC Sum

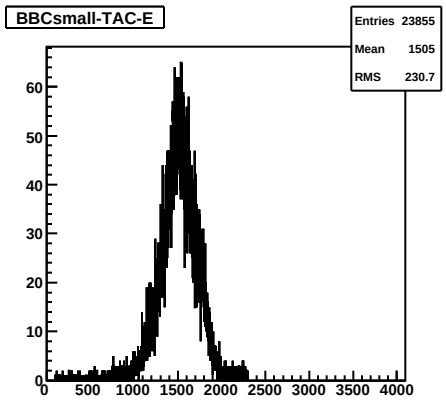

Entries 27604
ZDC East ADC Sum Att
ZDC West ADC Sum Att

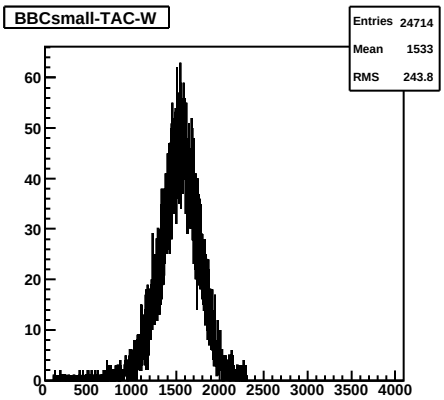

Entries 27604
BBC-L TAC Diff
BCC-S TAC Diff
Entries 27604
BBC-L TAC Diff
VPD TAC Diff
Entries 27604
BBC-L TAC Diff
ZDC TAC Diff
Entries 27604
BBC-S TAC Diff
VPD TAC Diff
Entries 27604
BBC-S TAC Diff
ZDC TAC Diff
Entries 27604
VPD TAC Diff
ZDC TAC Diff

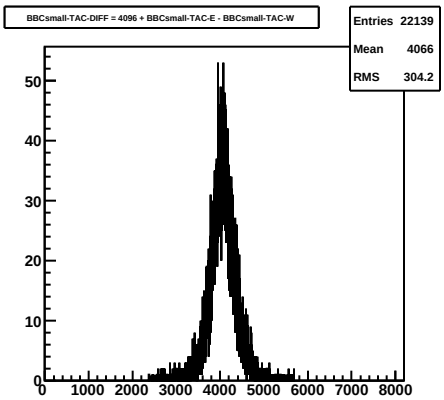

ZDC-TAC-E
Entries 1202
Mean 247.9
RMS 34.49

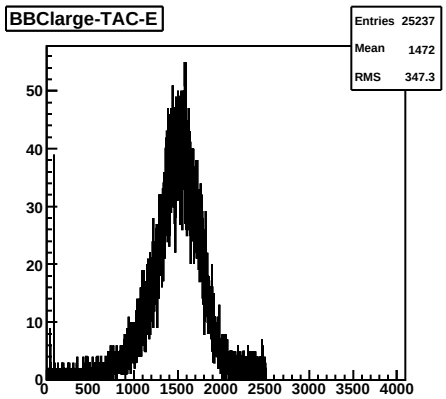

ZDC-TAC-W
Entries 654
Mean 216.9
RMS 37.82

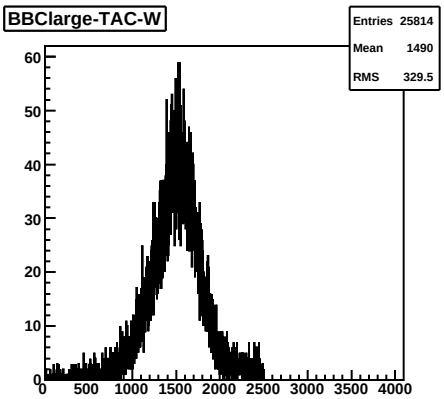

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 103
Mean 524.4
RMS 21.41

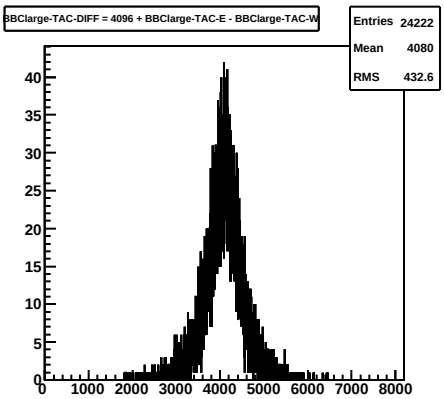

BBCsmall-TAC-E
Entries 23855
Mean 1505
RMS 230.7

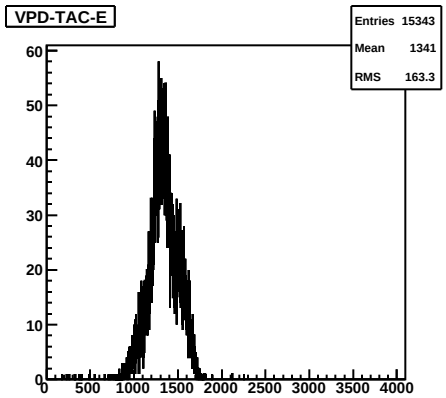

BBCsmall-TAC-W
Entries 24714
Mean 1533
RMS 243.8

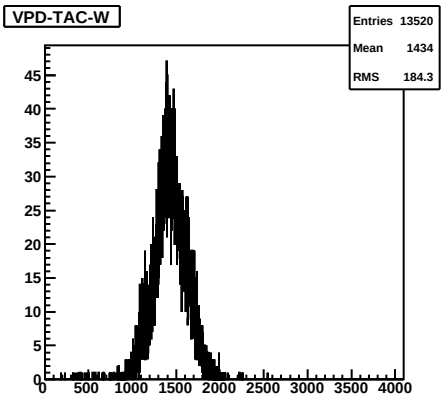

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 22139
Mean 4066
RMS 304.2

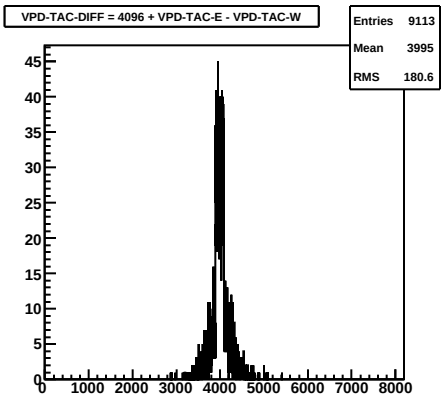

BBClarge-TAC-E
Entries 25237
Mean 1472
RMS 347.3

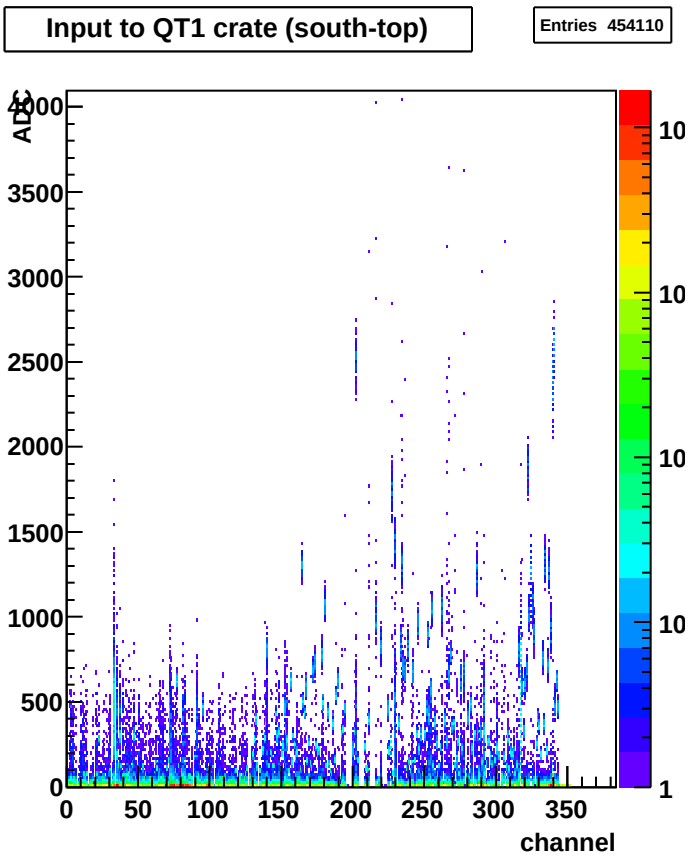

BBClarge-TAC-W
Entries 25814
Mean 1490
RMS 329.5

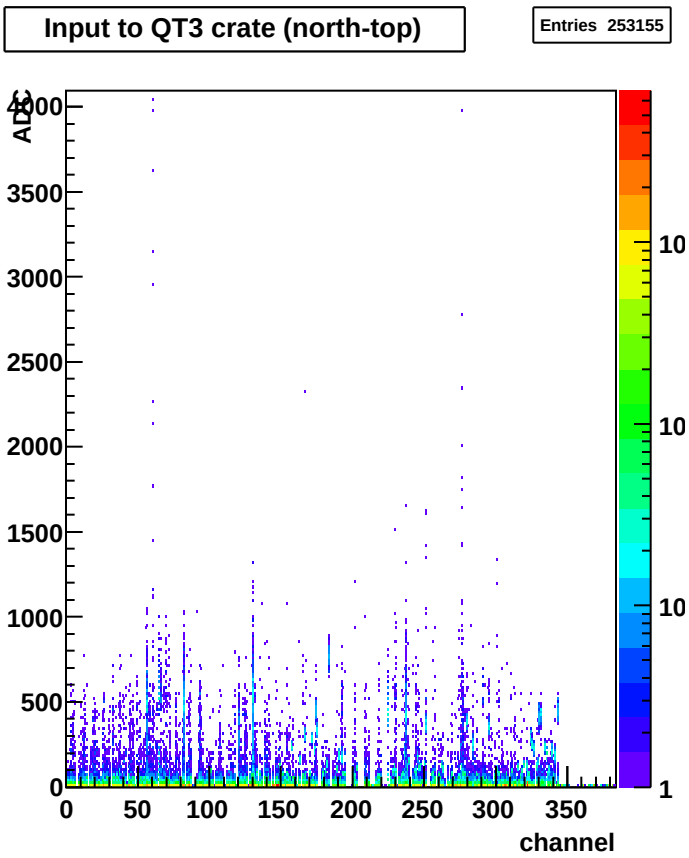

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 24222
Mean 4080
RMS 432.6

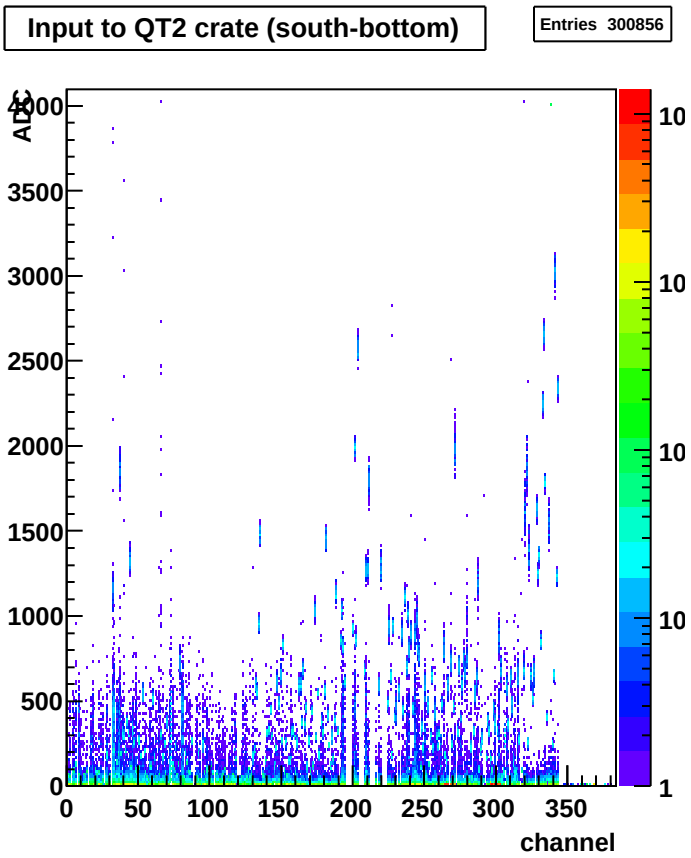

VPD-TAC-E
Entries 15343
Mean 1341
RMS 163.3

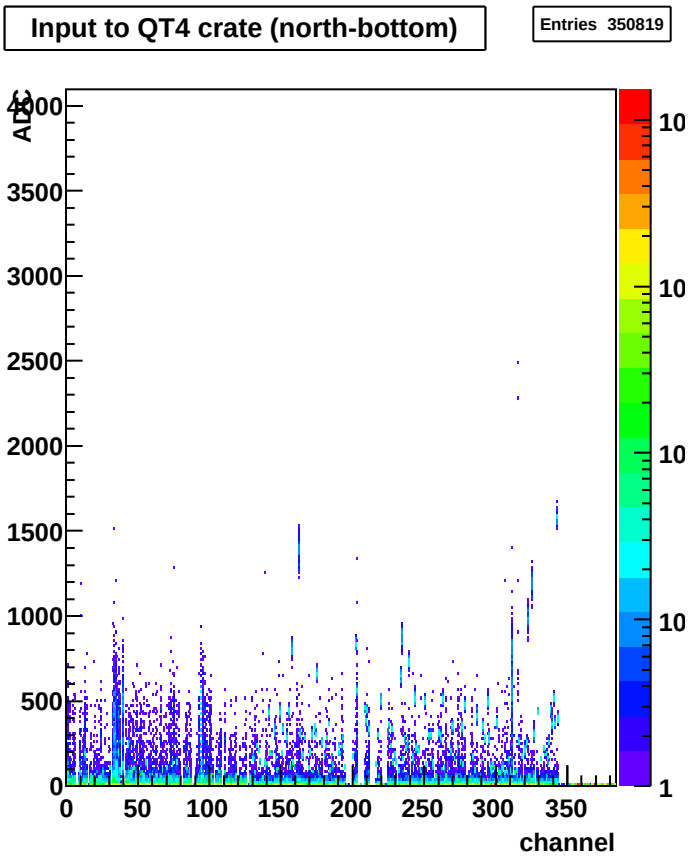

VPD-TAC-W
Entries 13520
Mean 1434
RMS 184.3

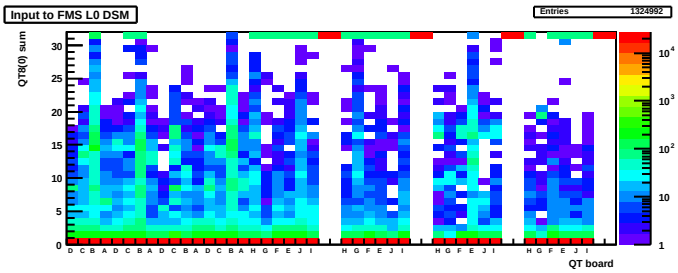

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 9113
Mean 3995
RMS 180.6

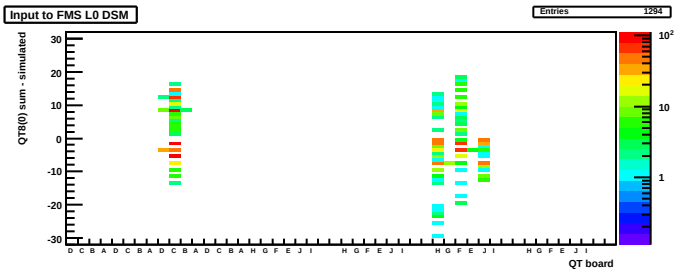

Input to QT1 crate (south-top)
Entries 454110
ADC
channel

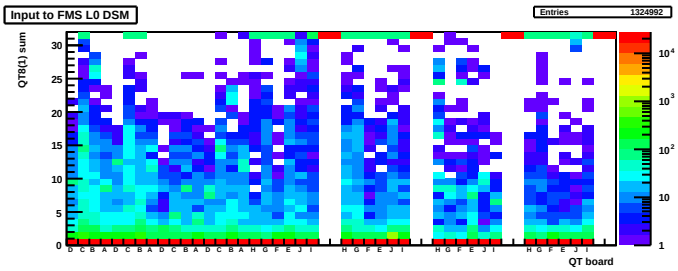

Input to QT3 crate (north-top)
Entries 253155
ADC
channel

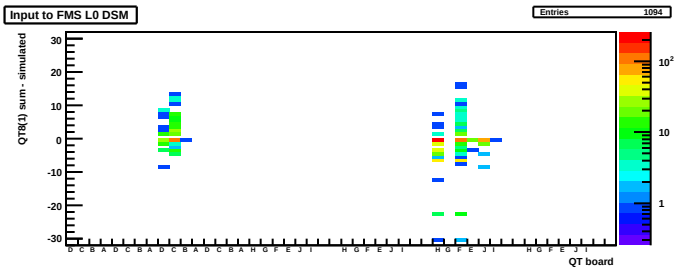

Input to QT2 crate (south-bottom)
Entries 300856
ADC
channel

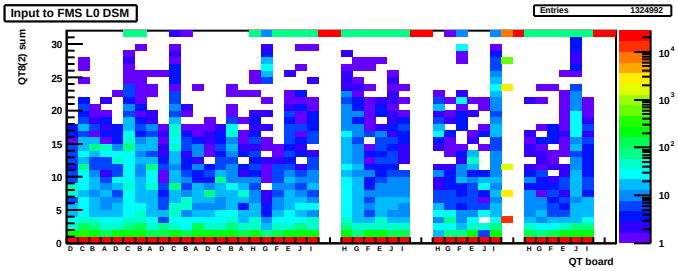

Input to QT4 crate (north-bottom)
Entries 350819
ADC
channel

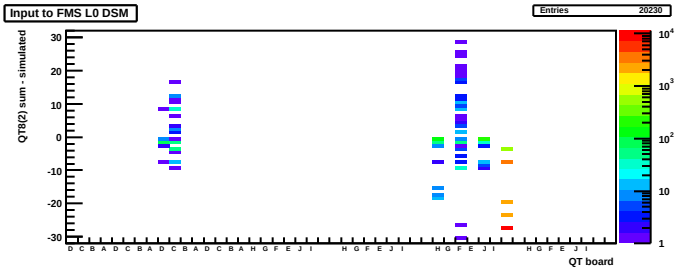

Input to FMS L0 DSM
Entries 1324992
QT8(0) sum
QT board

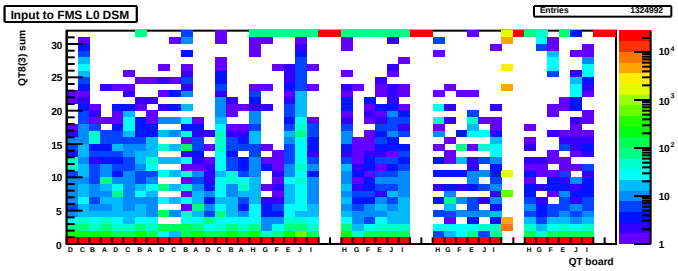

Input to FMS L0 DSM
Entries 1294
QT8(0) sum - simulated
QT board

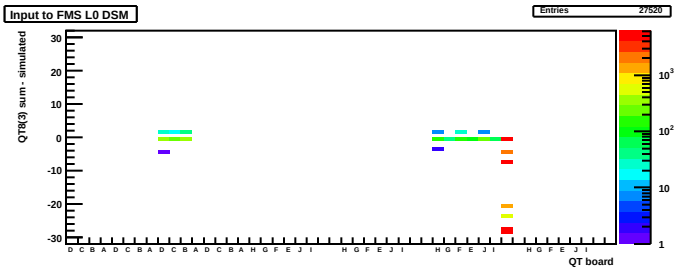

Input to FMS L0 DSM
Entries 1324992
QT8(1) sum
QT board

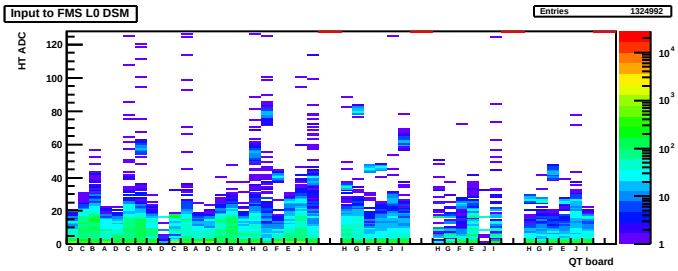

Input to FMS L0 DSM
Entries 1094
QT8(1) sum - simulated
QT board

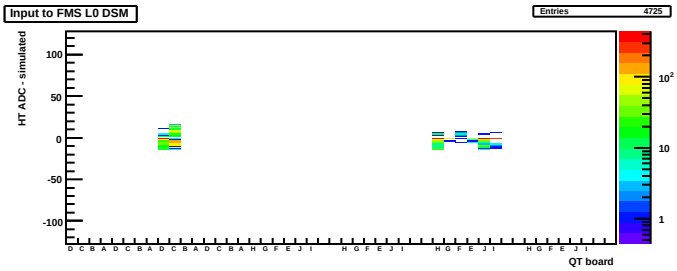

Input to FMS L0 DSM
Entries 1324992
QT8(2) sum
QT board

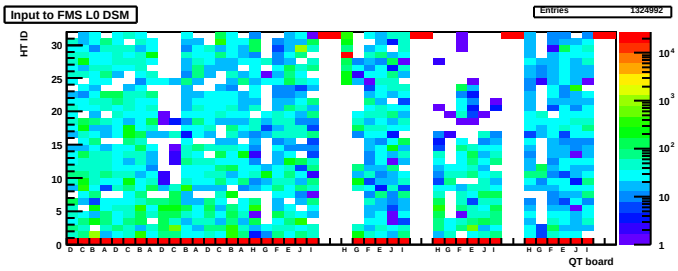

Input to FMS L0 DSM
Entries 20230
QT8(2) sum - simulated
QT board

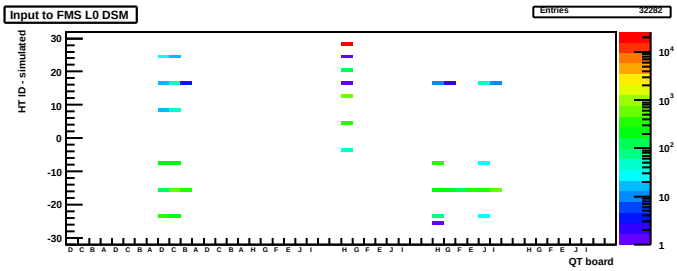

Input to FMS L0 DSM
Entries 1324992
QT8(3) sum
QT board

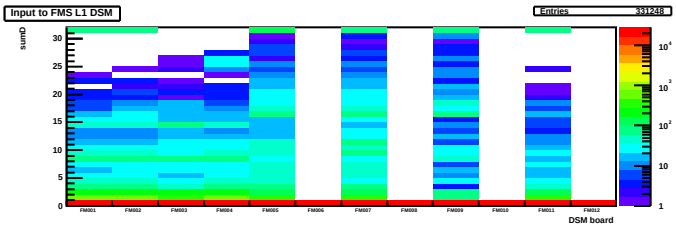

Input to FMS L0 DSM
Entries 27520
QT8(3) sum - simulated
QT board

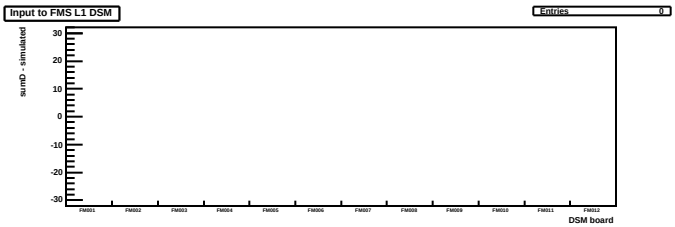

Input to FMS L0 DSM
Entries 1324992
HT ADC
QT board

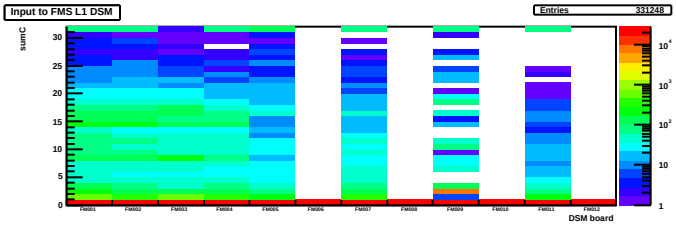

Input to FMS L0 DSM
Entries 4725
HT ADC - simulated
QT board

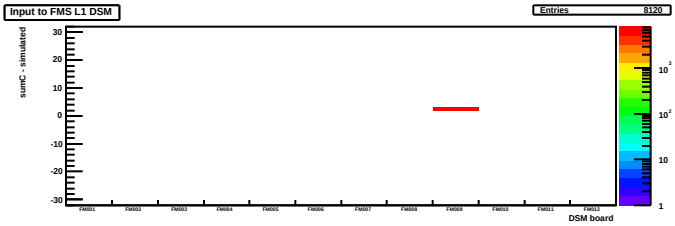

Input to FMS L0 DSM
Entries 1324992
HT ID
QT board

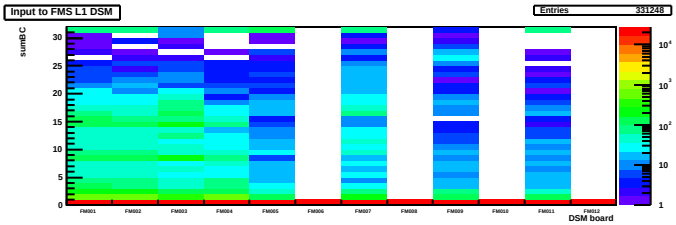

Input to FMS L0 DSM
Entries 32282
HT ID - simulated
QT board

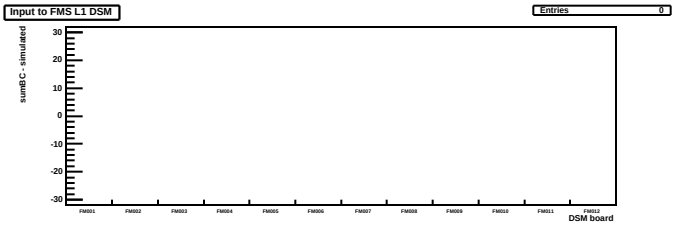

Input to FMS L1 DSM
Entries 331248
sumD
DSM board

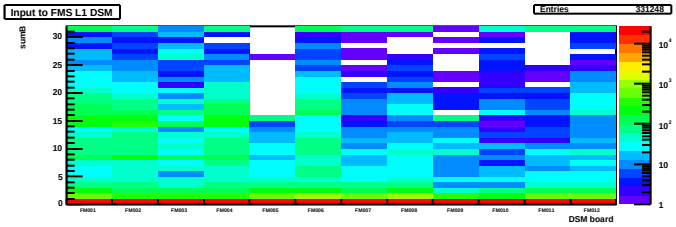

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

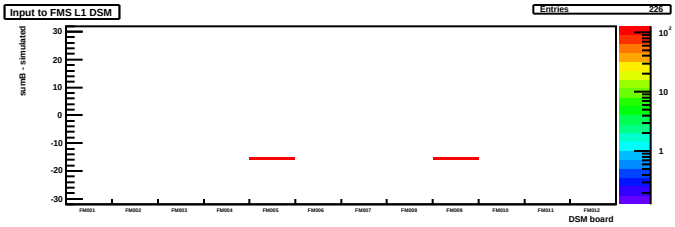

Input to FMS L1 DSM
Entries 331248
sumC
DSM board

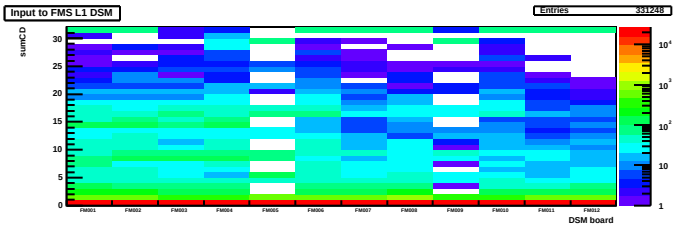

Input to FMS L1 DSM
Entries 8120
sumC - simulated
DSM board

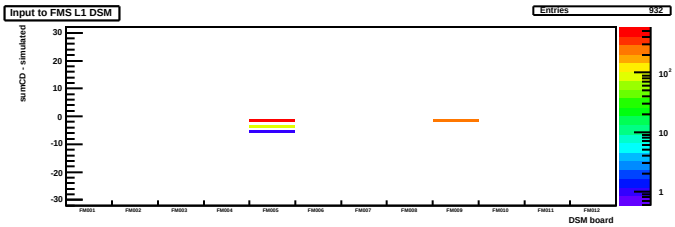

Input to FMS L1 DSM
Entries 331248
sumBC
DSM board

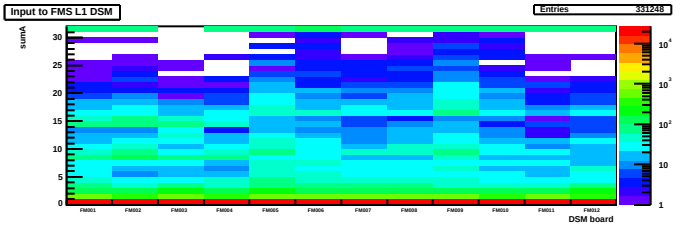

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

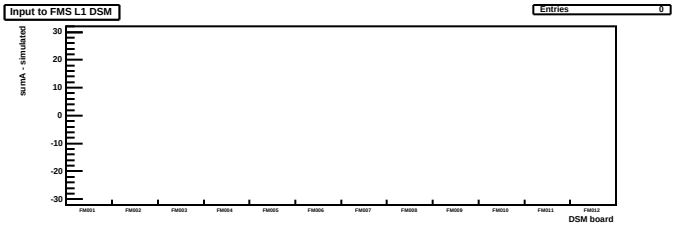

Input to FMS L1 DSM
Entries 331248
sumB
DSM board

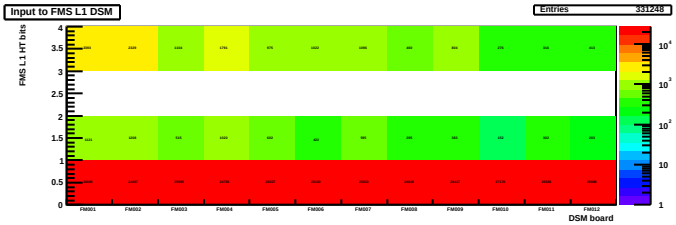

Input to FMS L1 DSM
Entries 226
sumB - simulated
DSM board

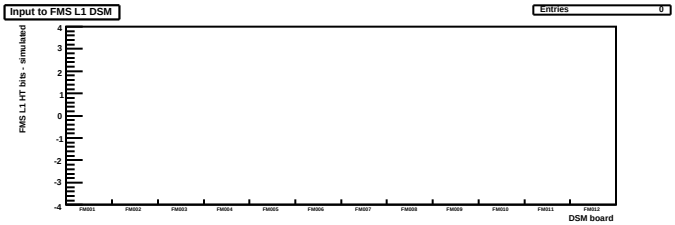

Input to FMS L1 DSM
Entries 331248
sumCD
DSM board

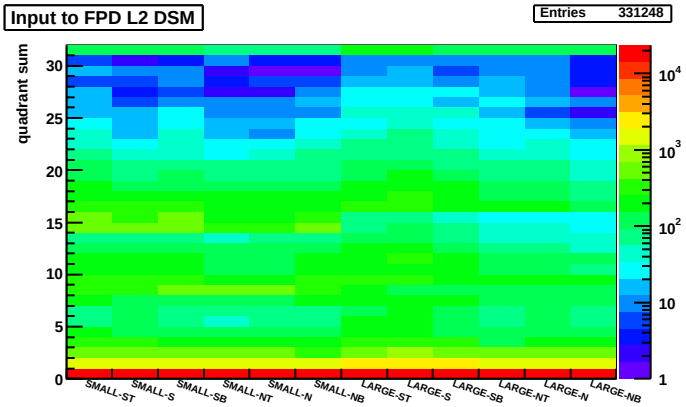

Input to FMS L1 DSM
Entries 932
sumCD - simulated
DSM board

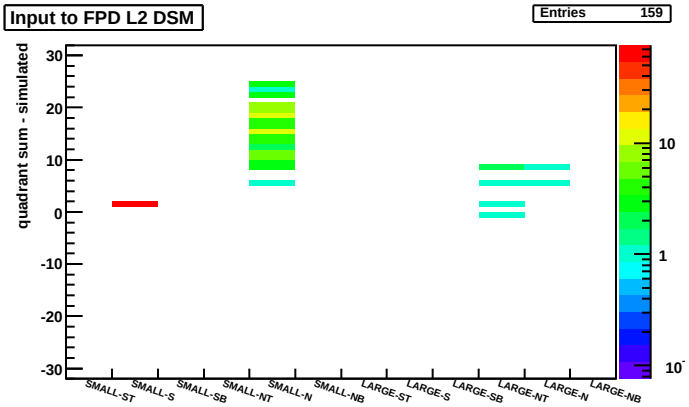

Input to FMS L1 DSM
Entries 331248
sumA
DSM board

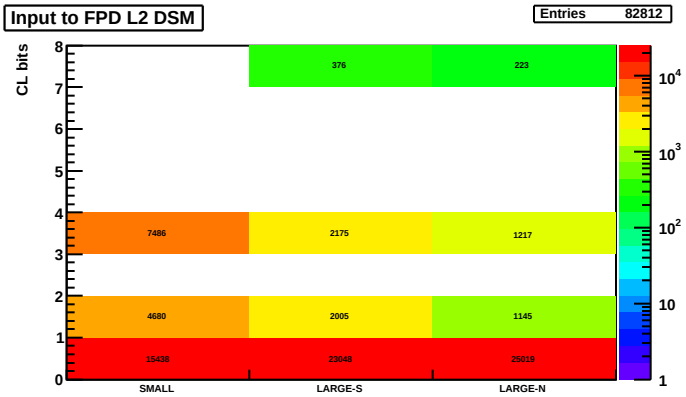

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

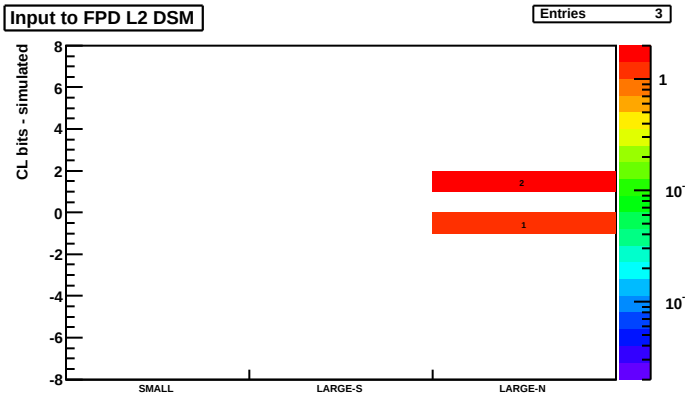

Input to FMS L1 DSM
Entries 331248
FMS L1 HT bits
DSM board

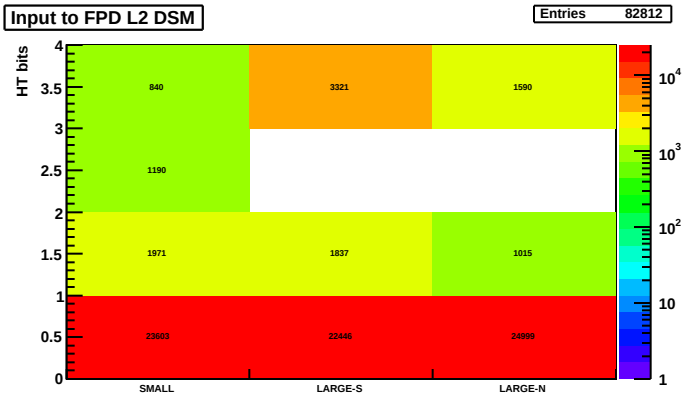

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

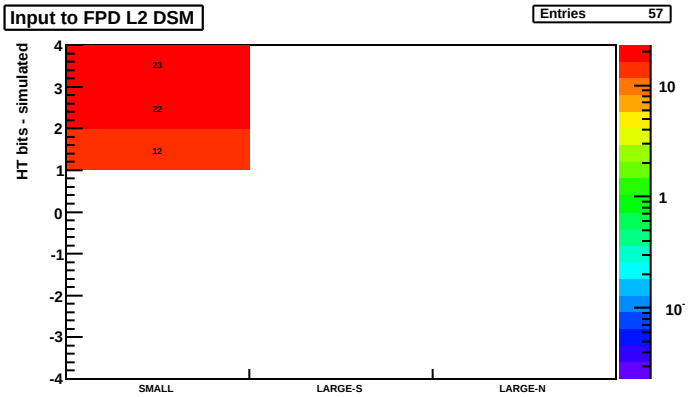

Input to FPD L2 DSM
Entries 331248
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

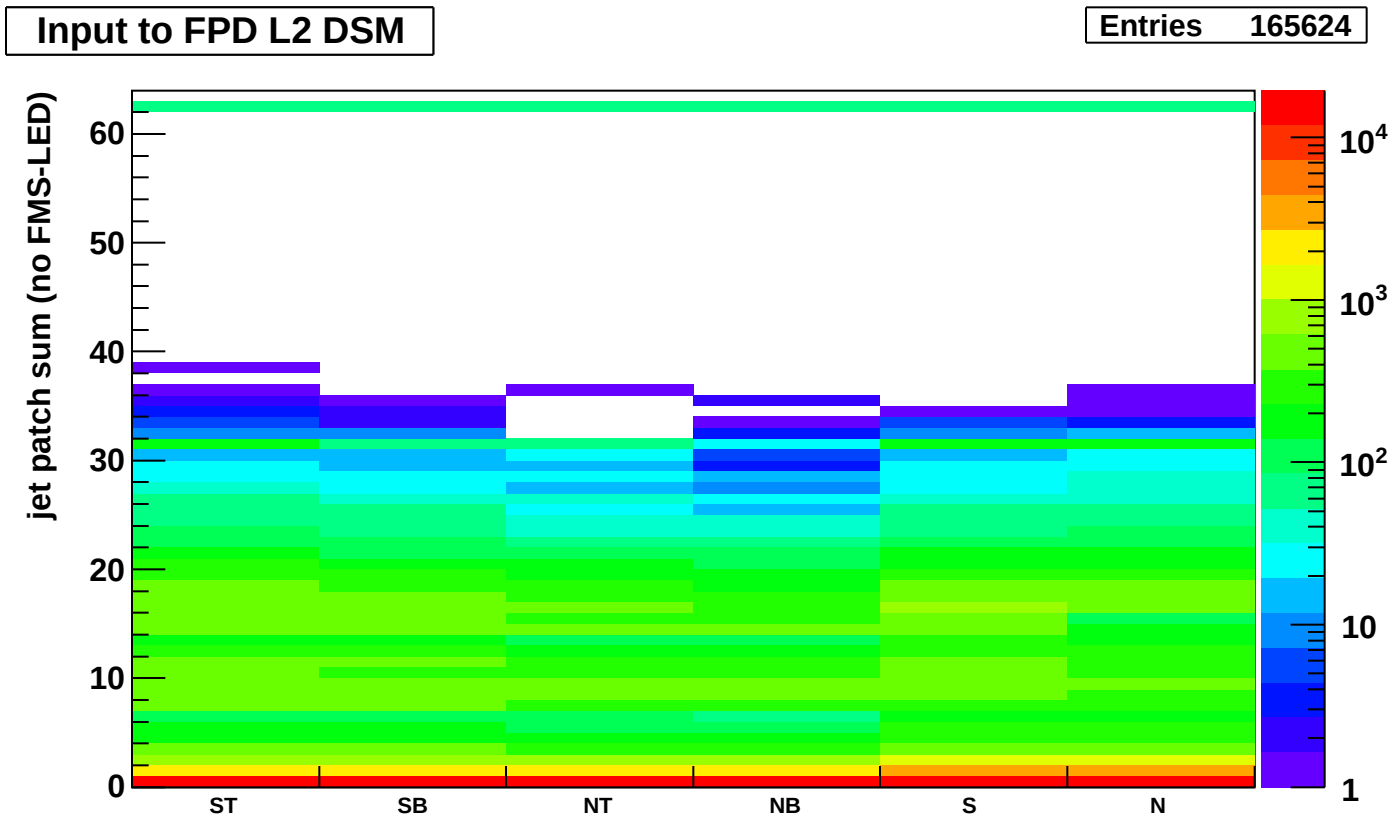

Input to FPD L2 DSM
Entries 159
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

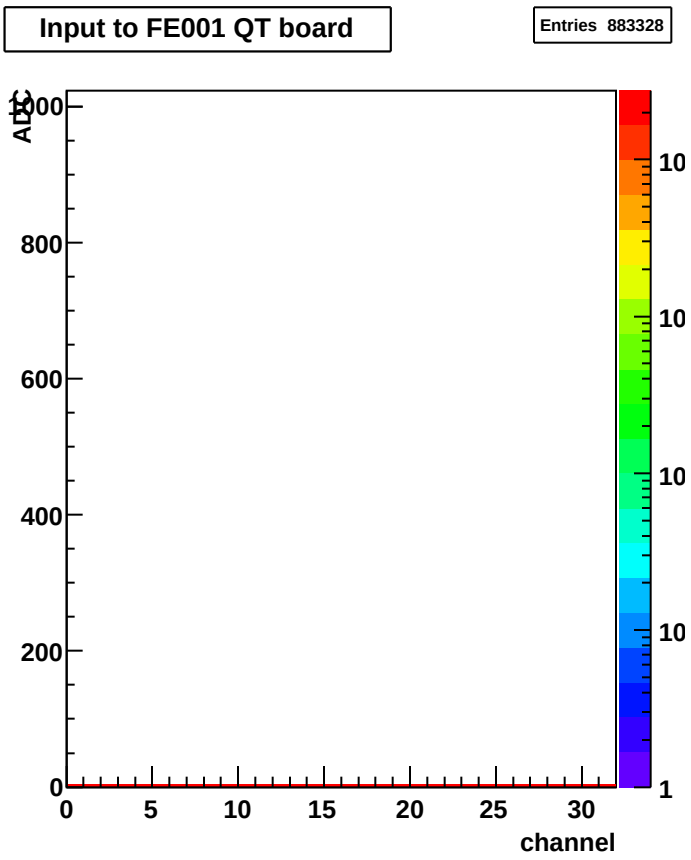

Input to FPD L2 DSM
Entries 82812
CL bits
376
223
7486
2175
1217
4680
2005
1145
15438
23048
25019
SMALL
LARGE-S
LARGE-N

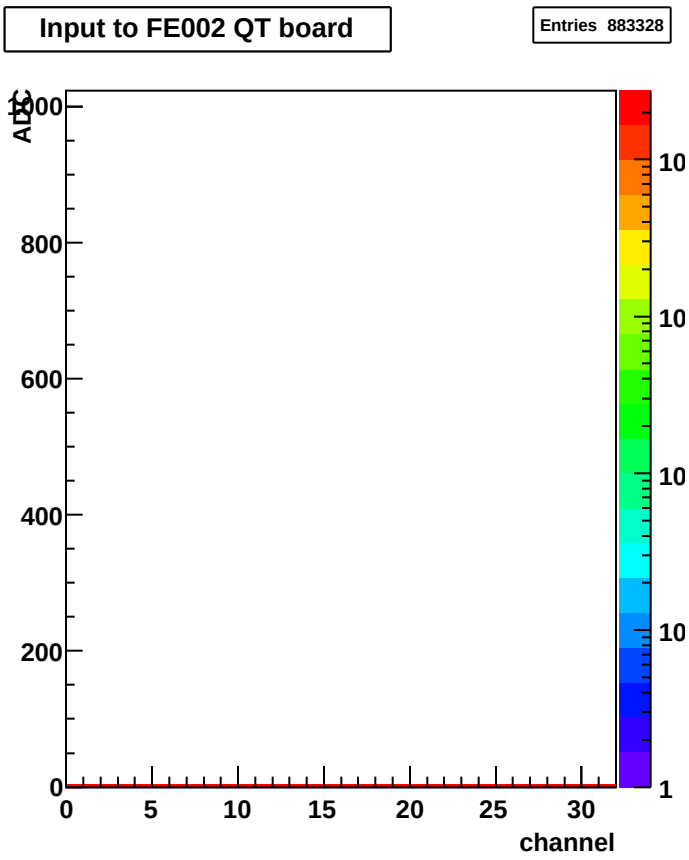

Input to FPD L2 DSM
Entries 3
CL bits - simulated
2
1
SMALL
LARGE-S
LARGE-N

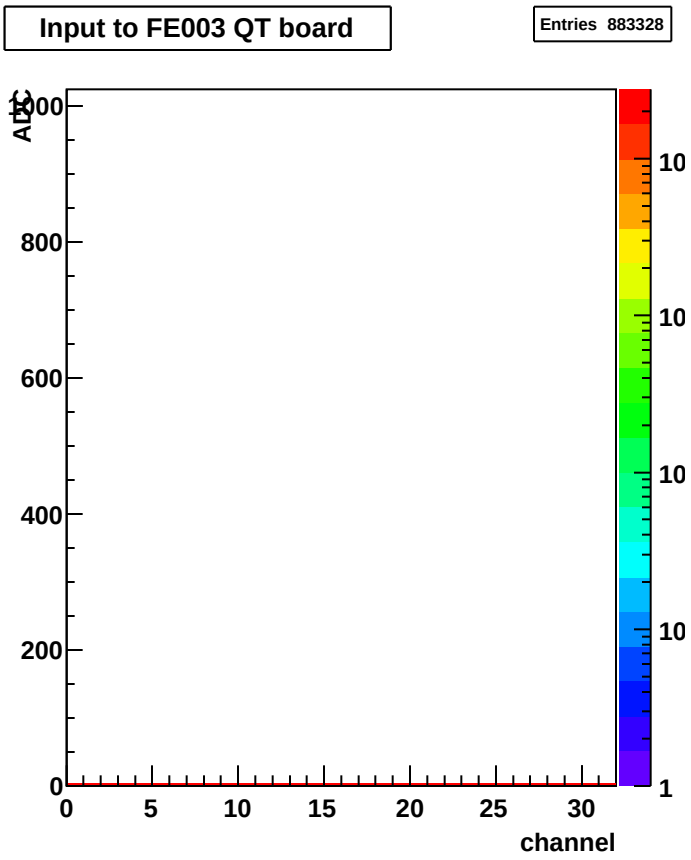

Input to FPD L2 DSM
Entries 82812
HT bits
840
3321
1590
1190
1971
1837
1015
23603
22446
24999
SMALL
LARGE-S
LARGE-N

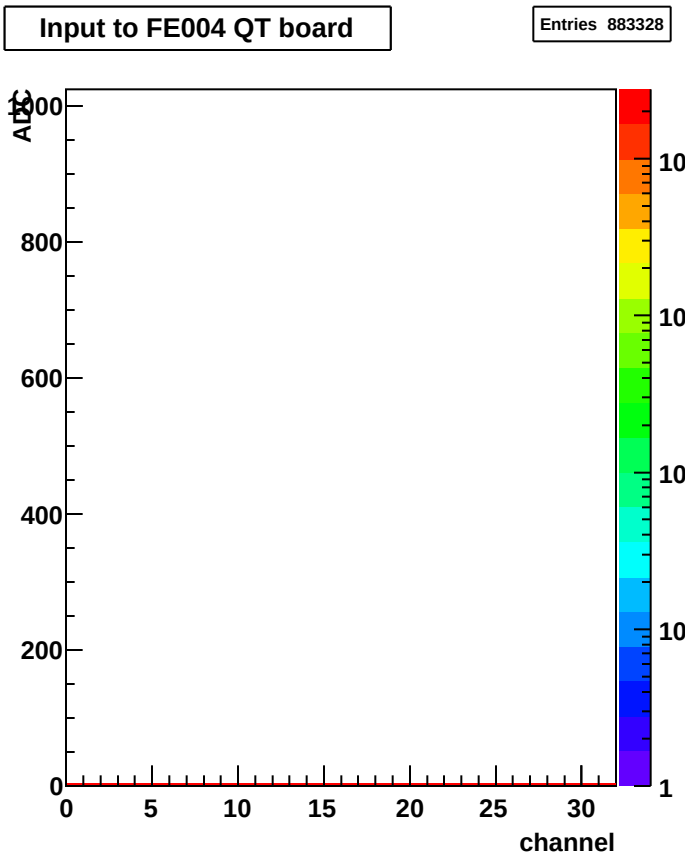

Input to FPD L2 DSM
Entries 57
HT bits - simulated
23
22
12
SMALL
LARGE-S
LARGE-N

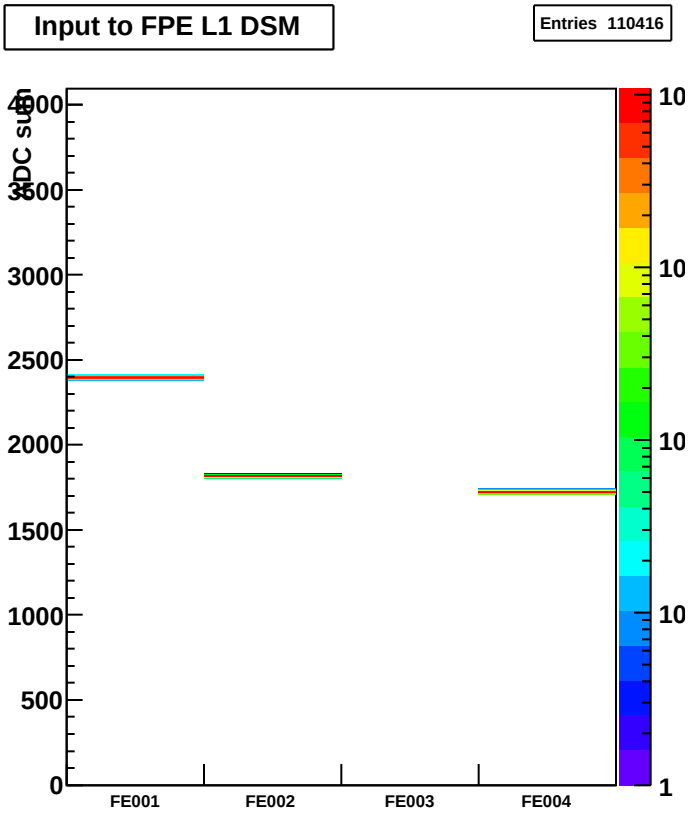

Input to FPD L2 DSM
Entries 165624
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

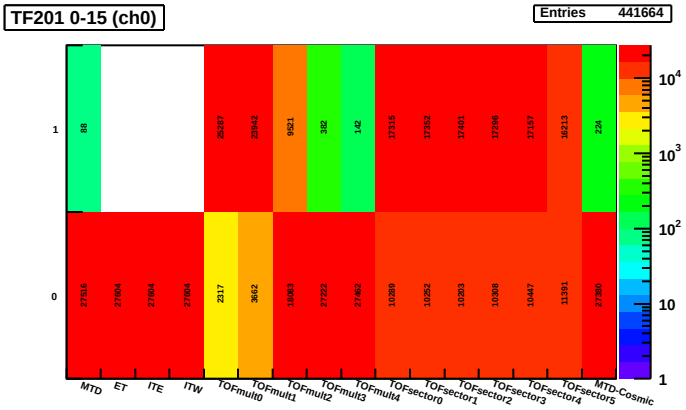

Input to FE001 QT board
Entries 883328
ADC
channel

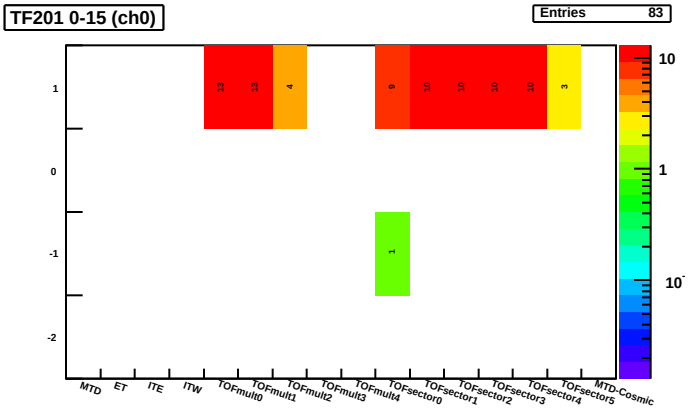

Input to FE002 QT board
Entries 883328
ADC
channel

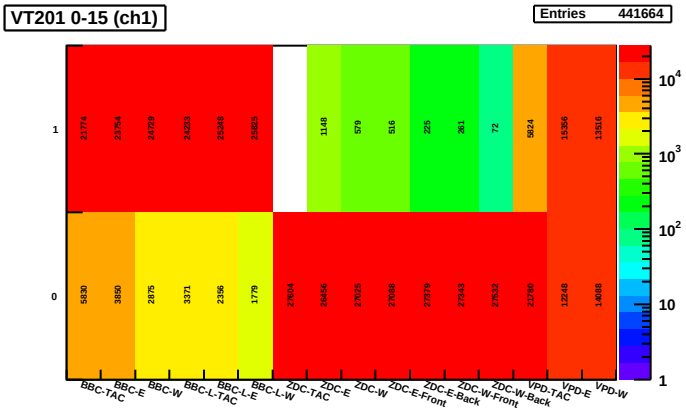

Input to FE003 QT board
Entries 883328
ADC
channel

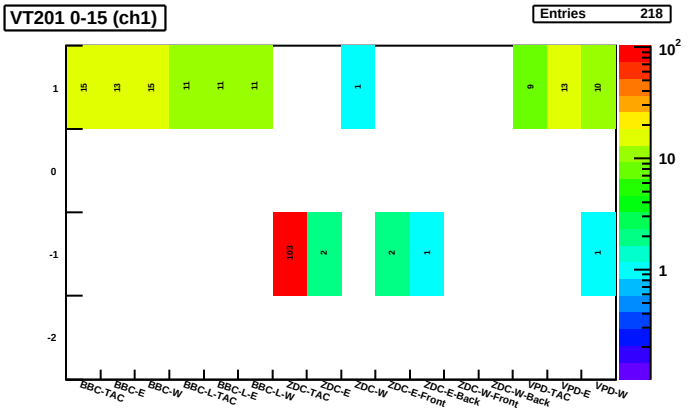

Input to FE004 QT board
Entries 883328
ADC
channel

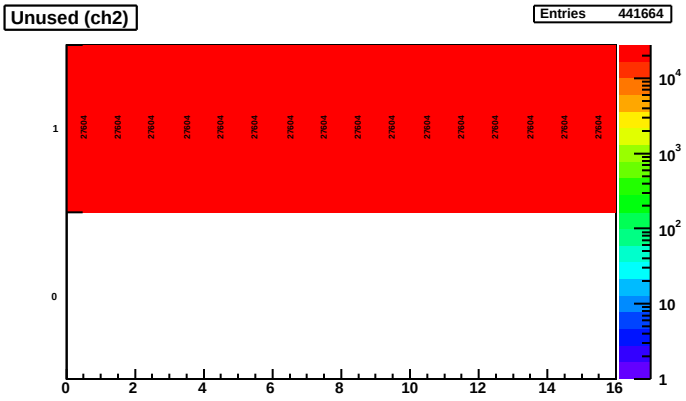

Input to FPE L1 DSM
Entries 110416
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

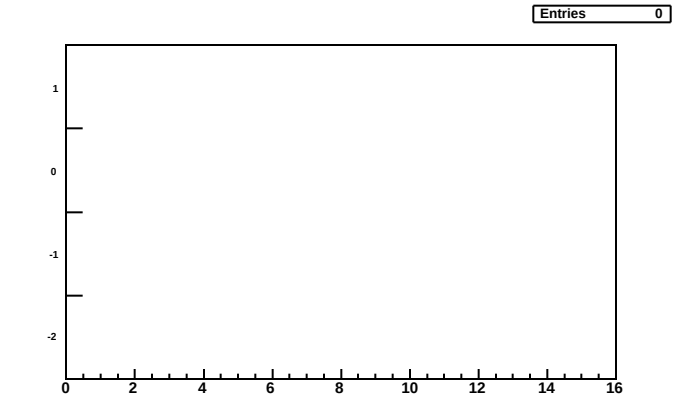

TF201 0-15 (ch0)
Entries 441664
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

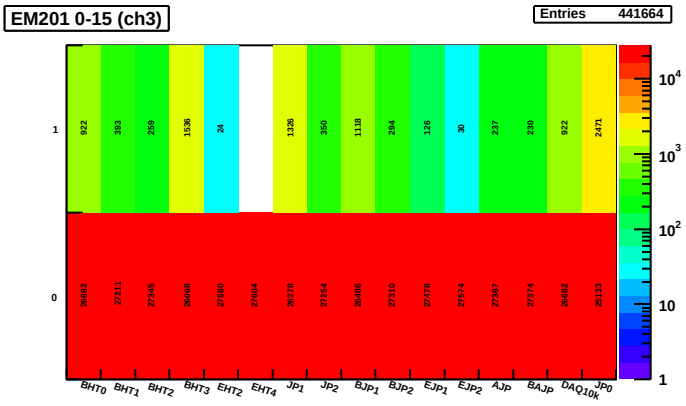

TF201 0-15 (ch0)
Entries 83
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 441664
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 218
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 441664


Entries 0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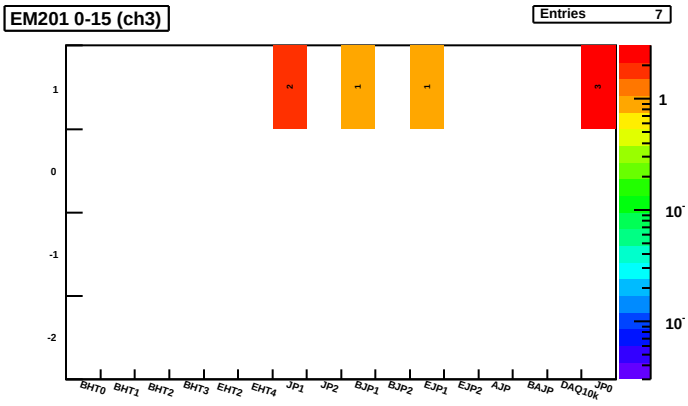
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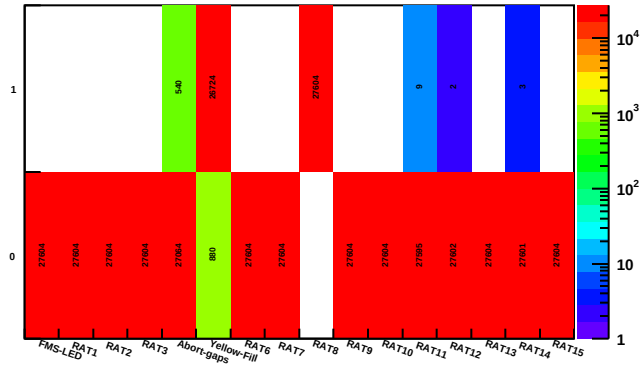
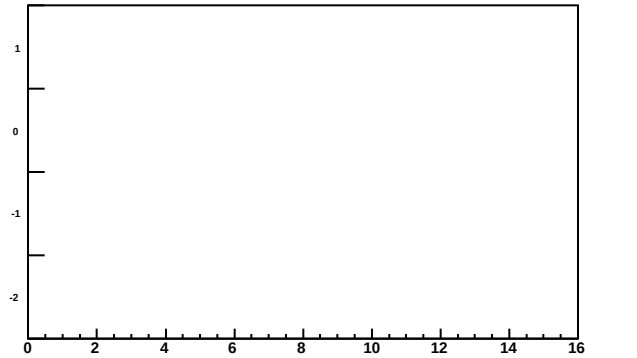
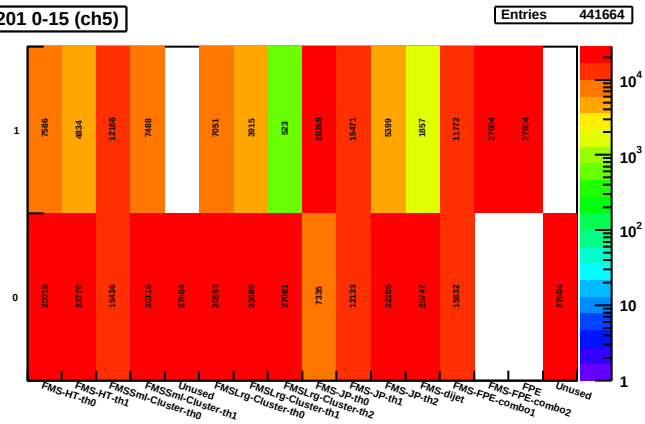
RAT board (ch4)





FP201 0-15 (ch5)



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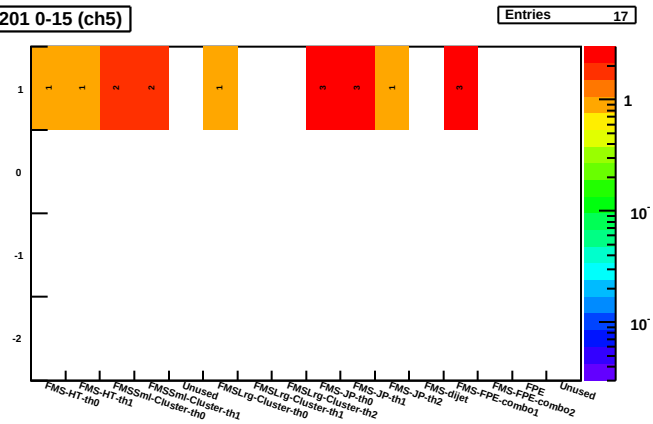

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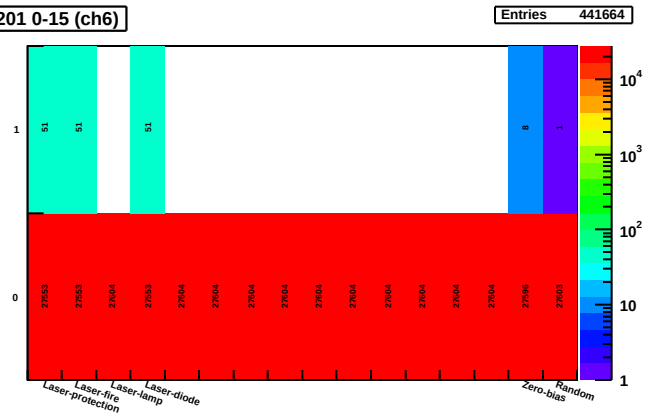

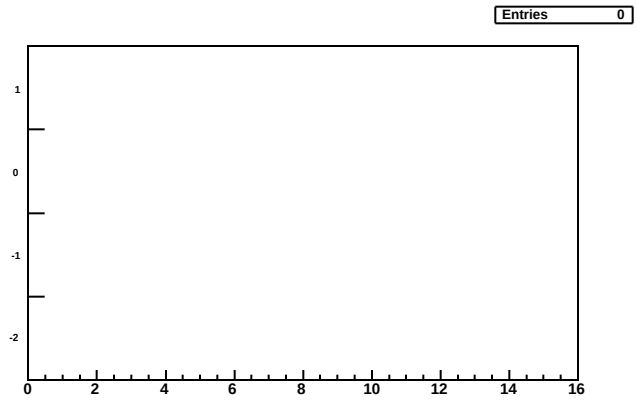
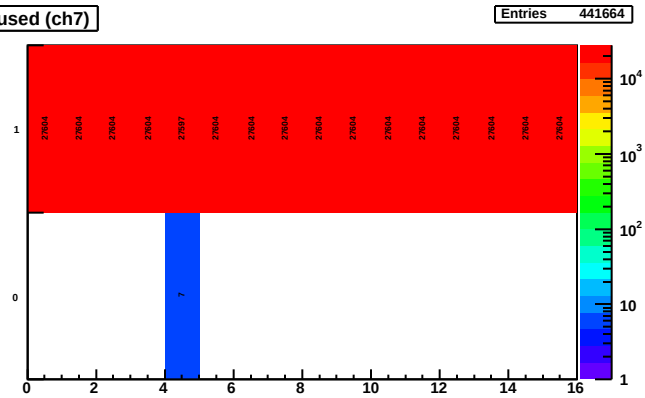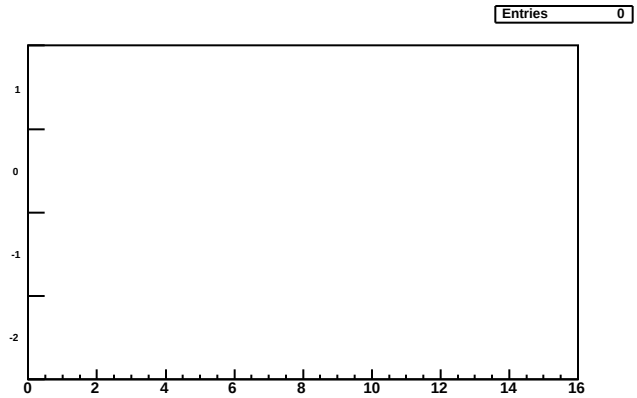
Unused (ch7)