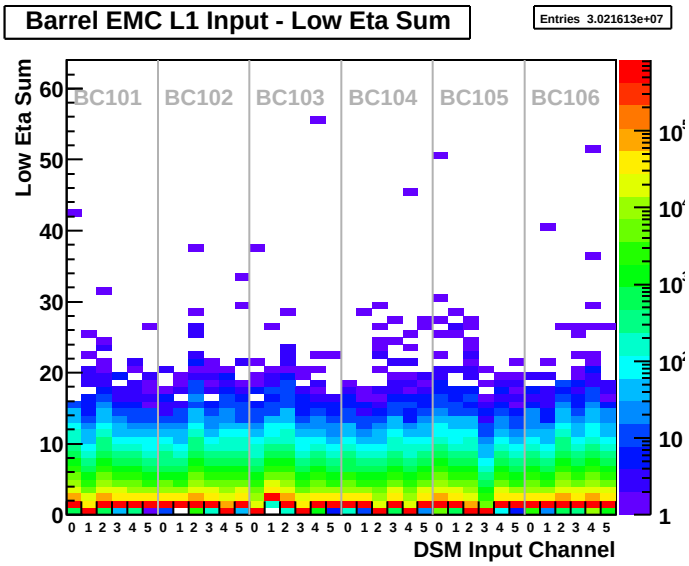
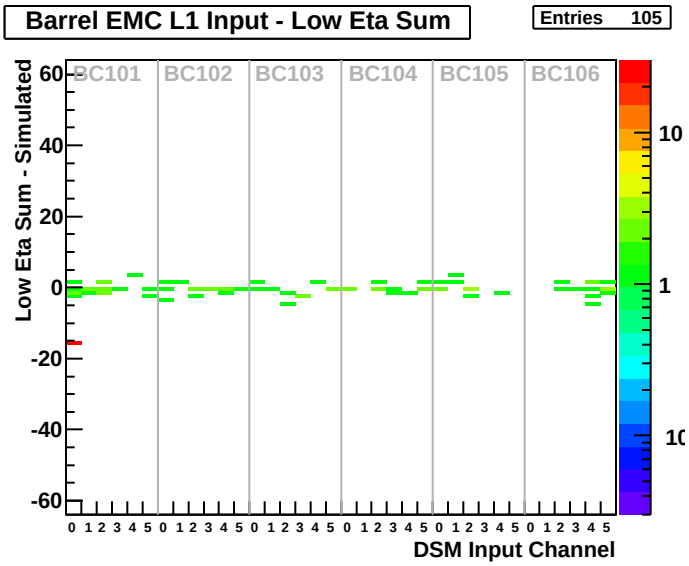
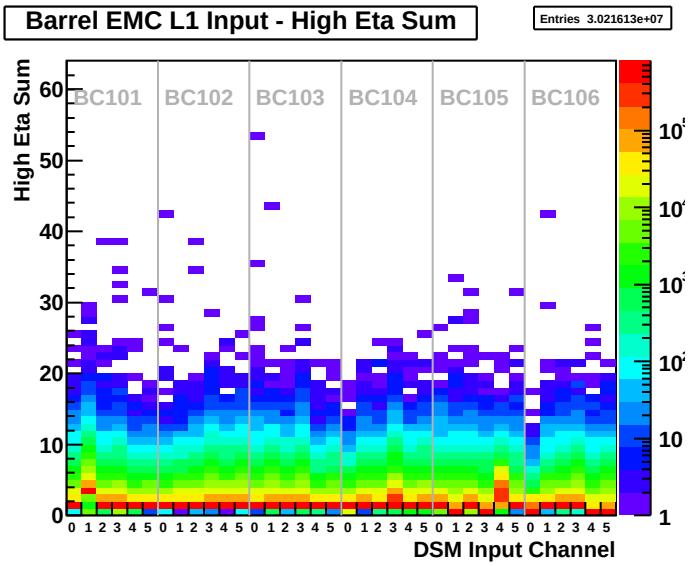
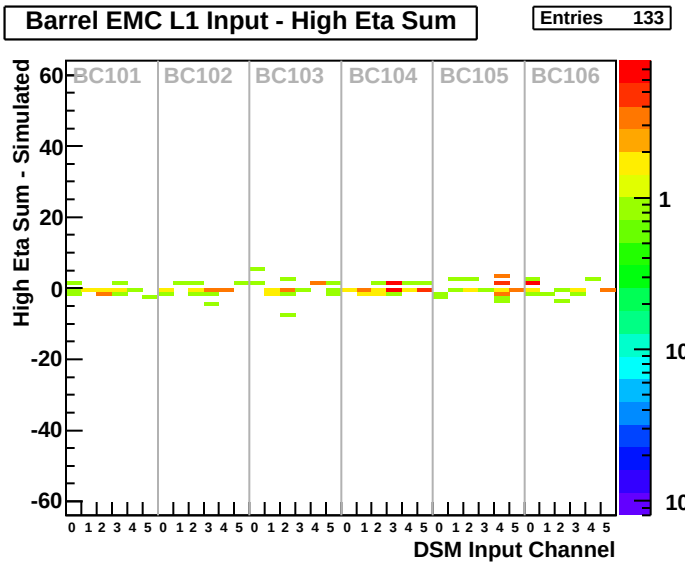
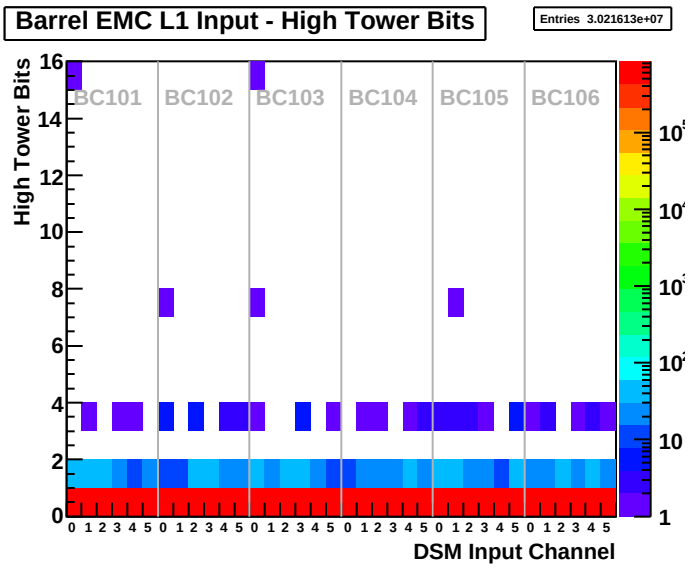
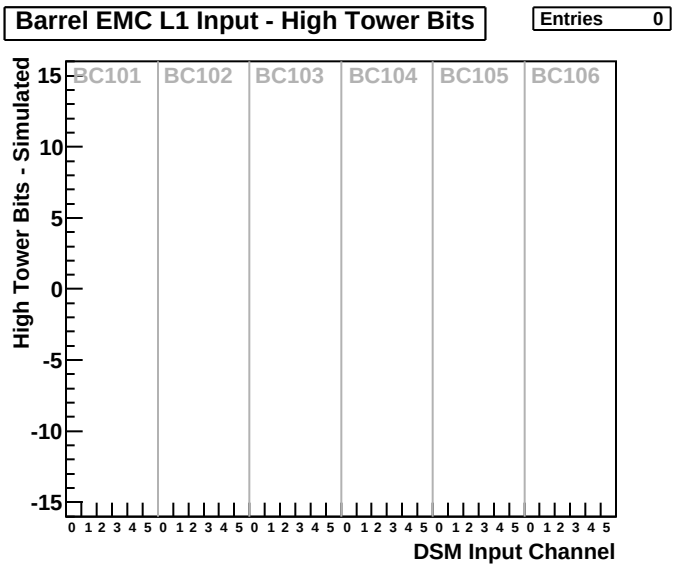

Barrel EMC L1 Input - Low Eta Sum
Entries 3.021613e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

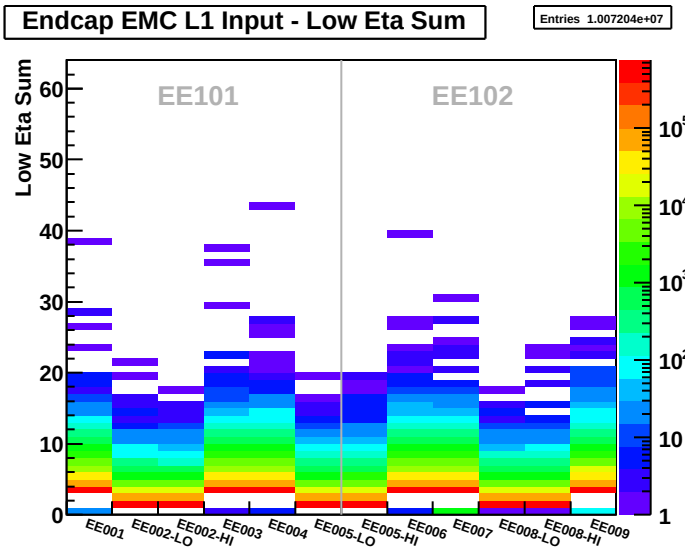

Barrel EMC L1 Input - Low Eta Sum
Entries 105
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

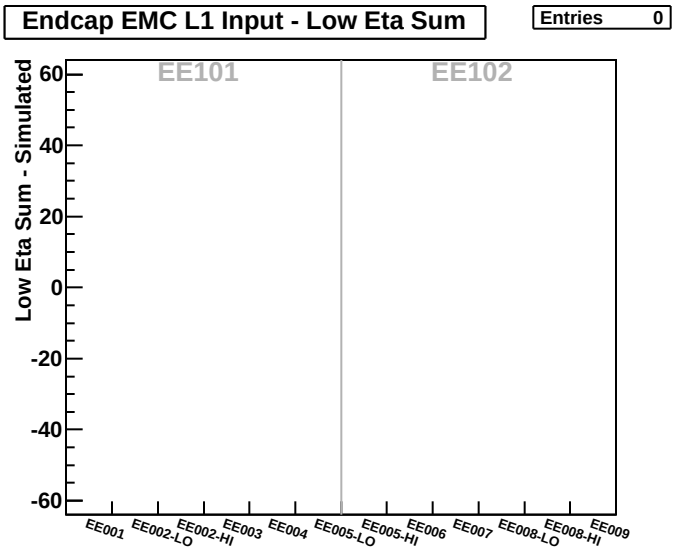

Barrel EMC L1 Input - High Eta Sum
Entries 3.021613e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

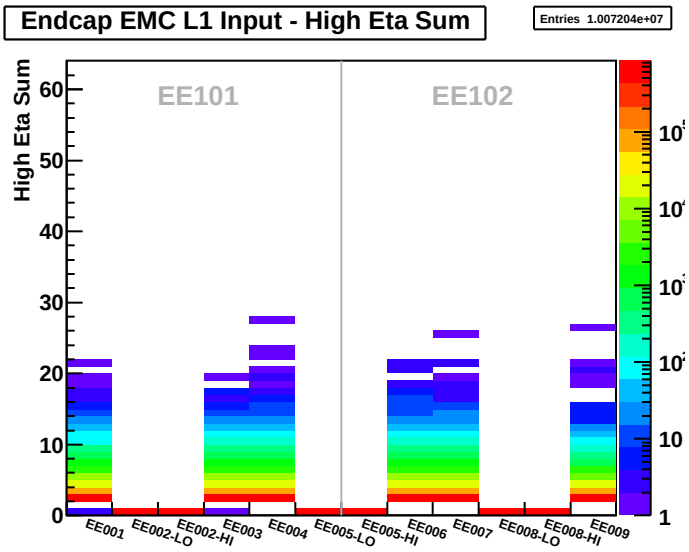

Barrel EMC L1 Input - High Eta Sum
Entries 133
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

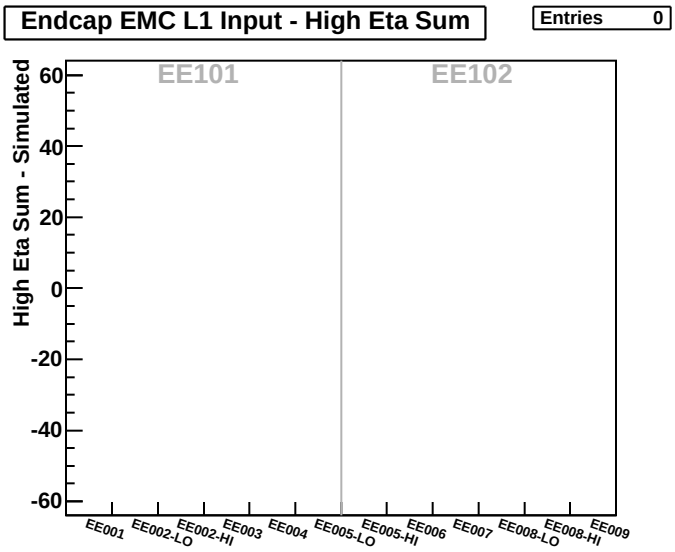

Barrel EMC L1 Input - High Tower Bits
Entries 3.021613e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

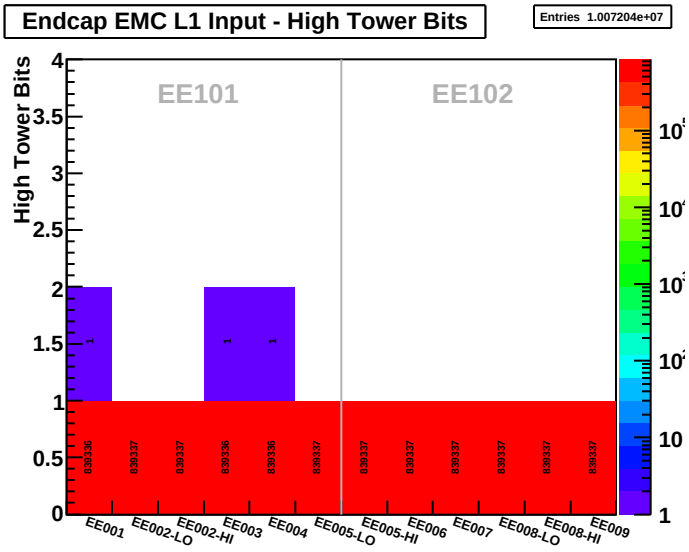

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

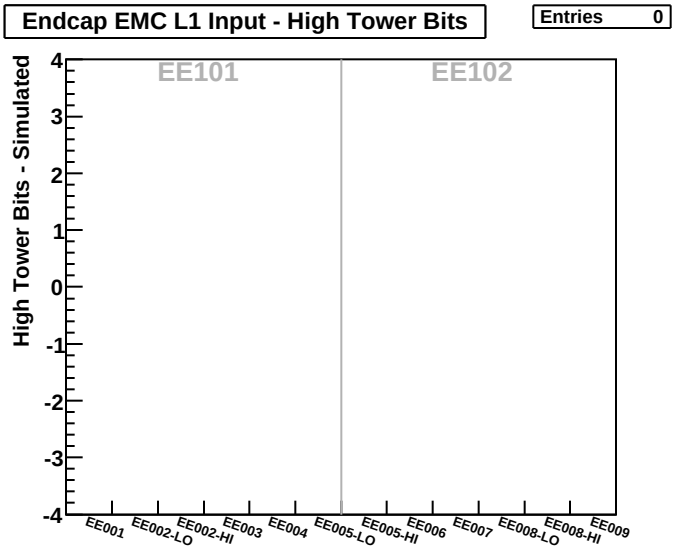

Endcap EMC L1 Input - Low Eta Sum
Entries 1.007204e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

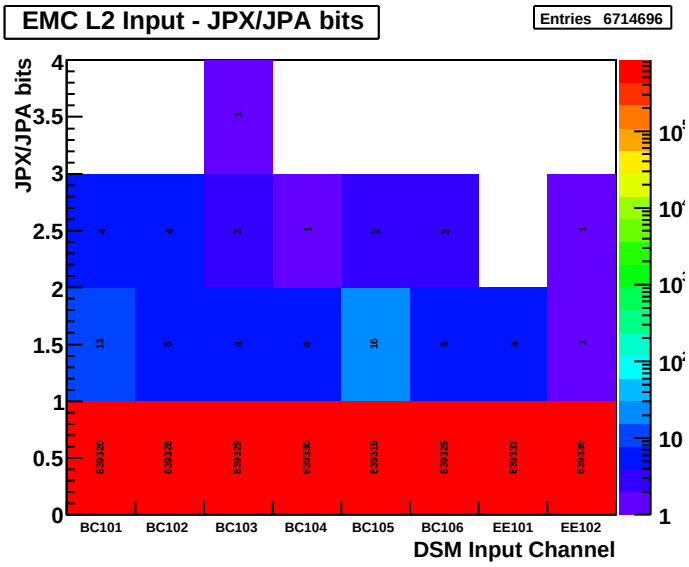

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

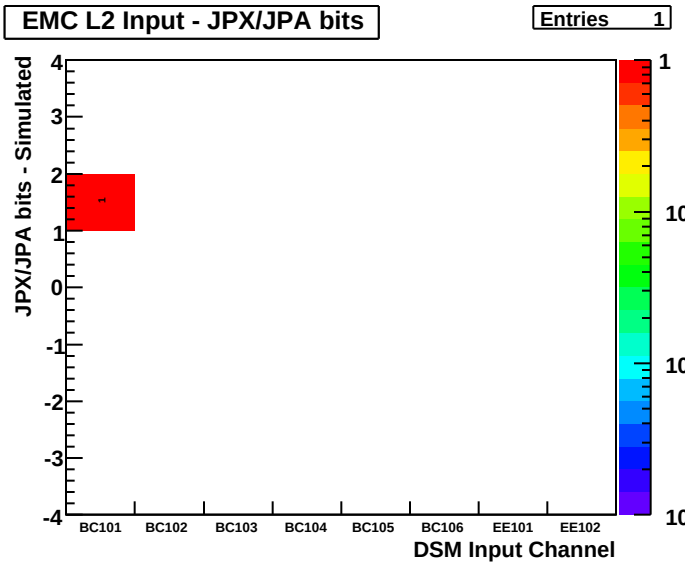

Endcap EMC L1 Input - High Eta Sum
Entries 1.007204e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

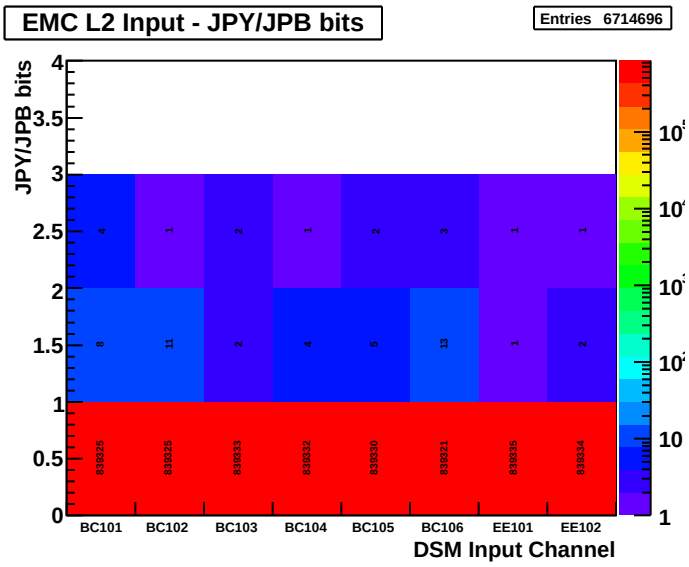

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

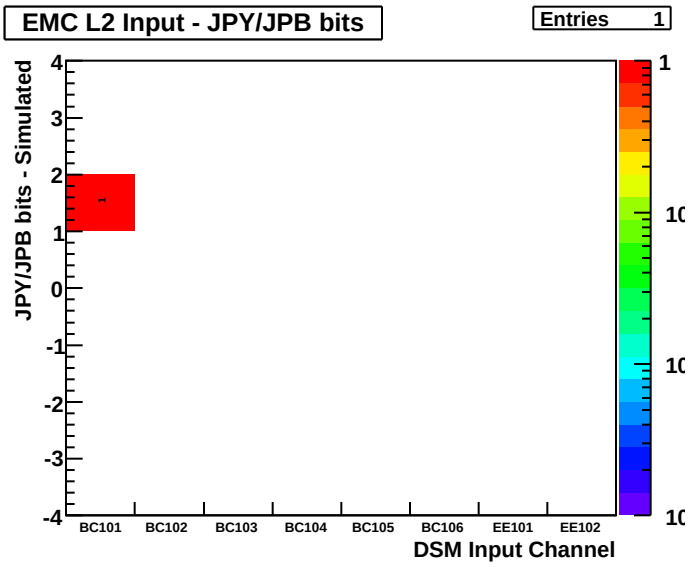

Endcap EMC L1 Input - High Tower Bits
Entries 1.007204e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

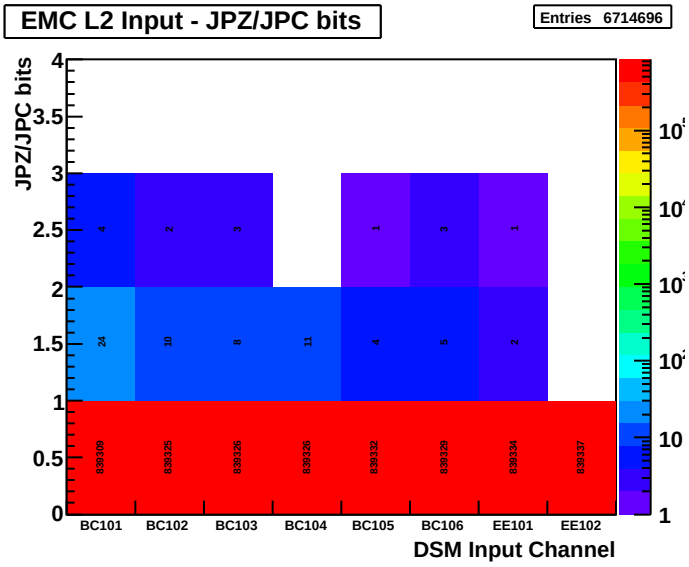

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

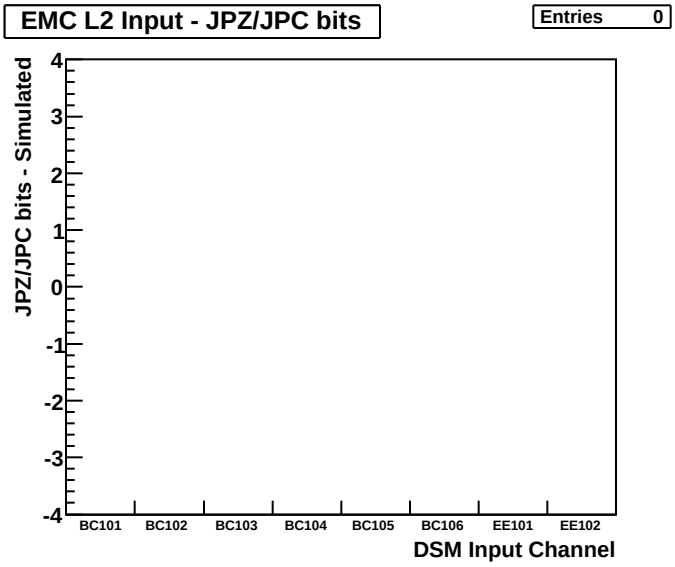

EMC L2 Input - JPX/JPA bits
Entries 6714696
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

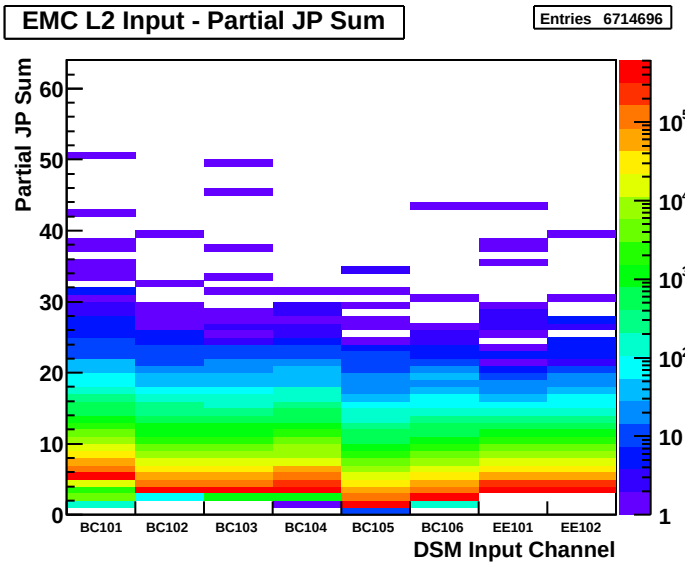

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

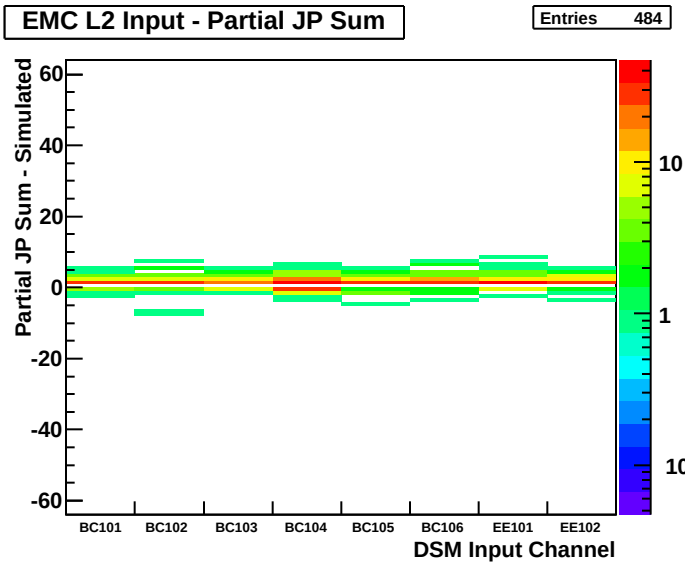

EMC L2 Input - JPY/JPB bits
Entries 6714696
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

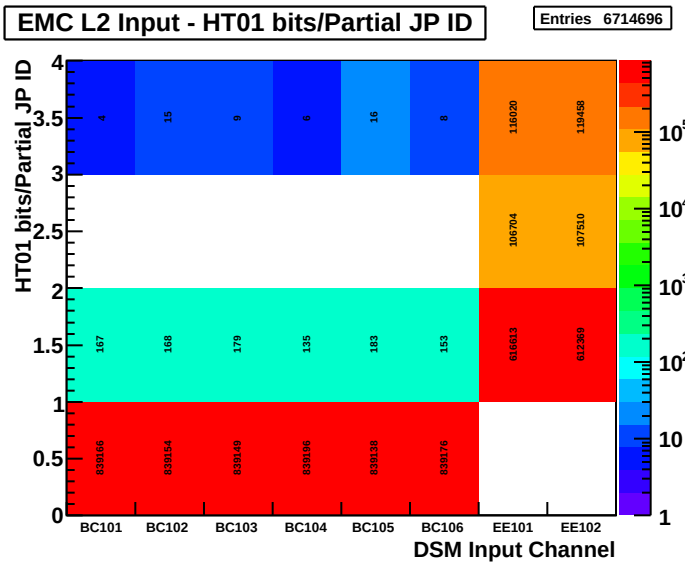

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

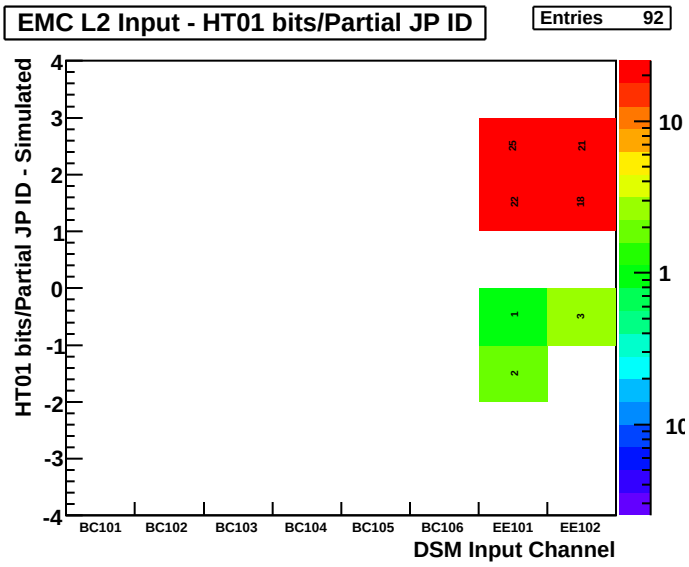

EMC L2 Input - JPZ/JPC bits
Entries 6714696
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

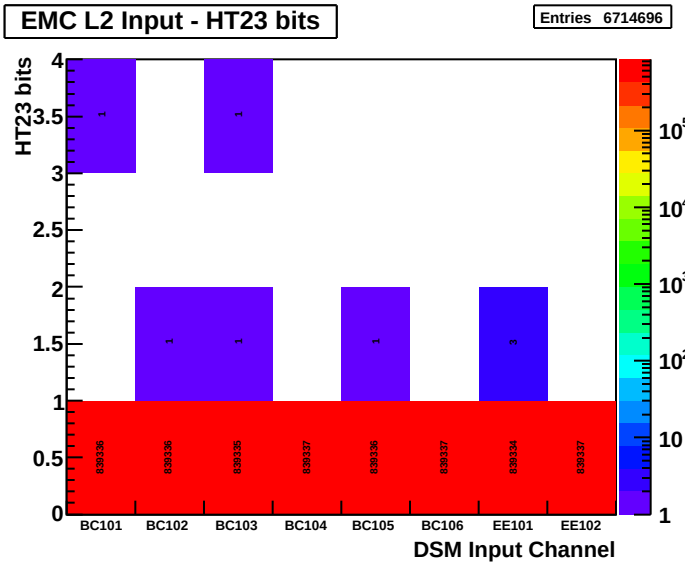

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

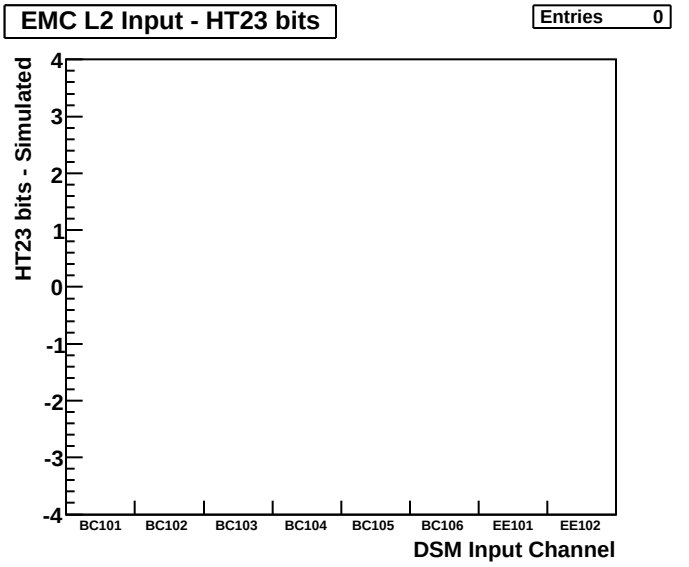

EMC L2 Input - Partial JP Sum
Entries 6714696
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

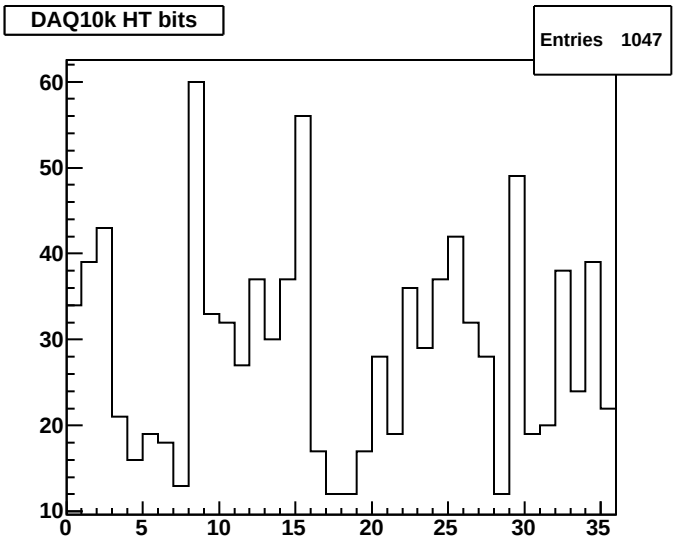

EMC L2 Input - Partial JP Sum
Entries 484
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

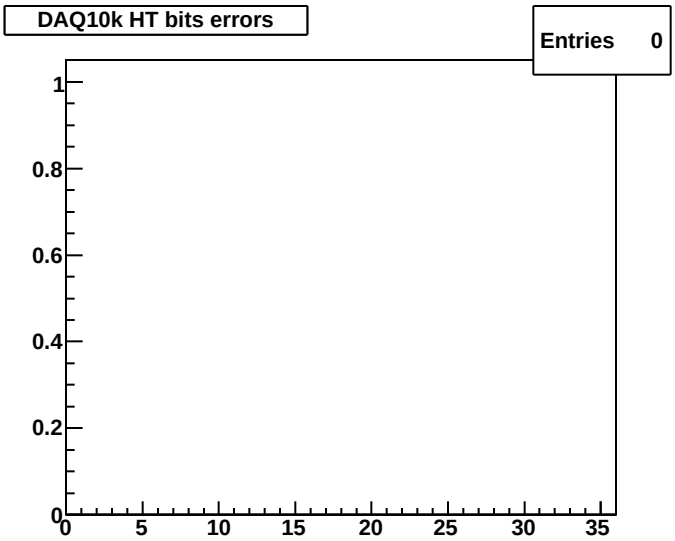

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6714696
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

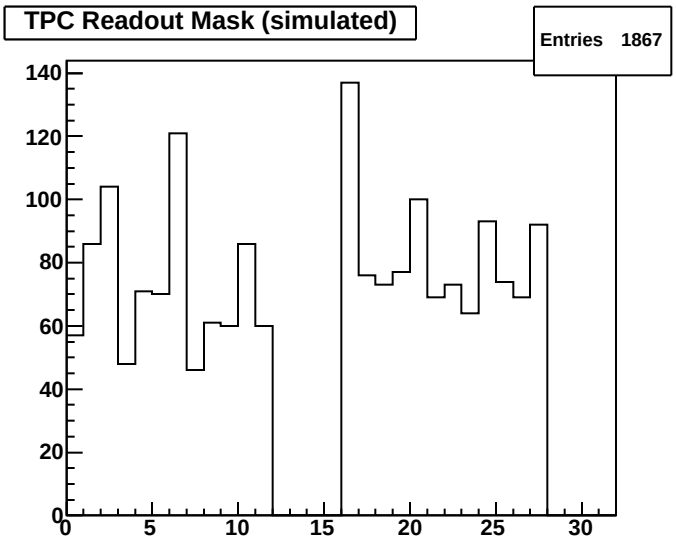

EMC L2 Input - HT01 bits/Partial JP ID
Entries 92
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

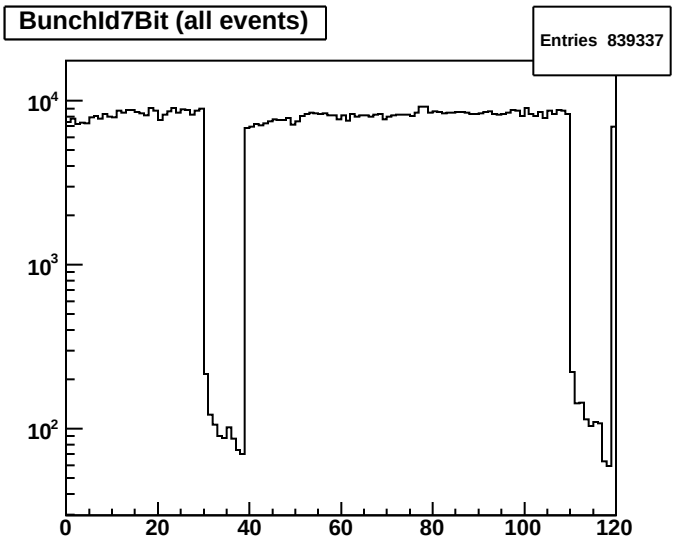

EMC L2 Input - HT23 bits
Entries 6714696
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

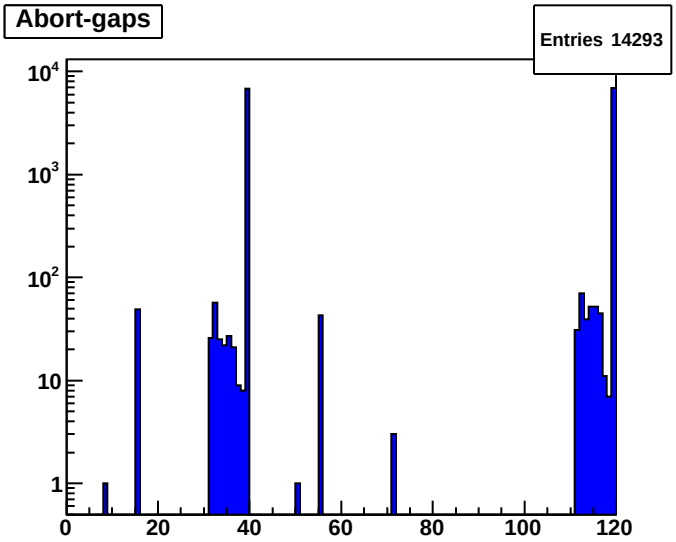

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

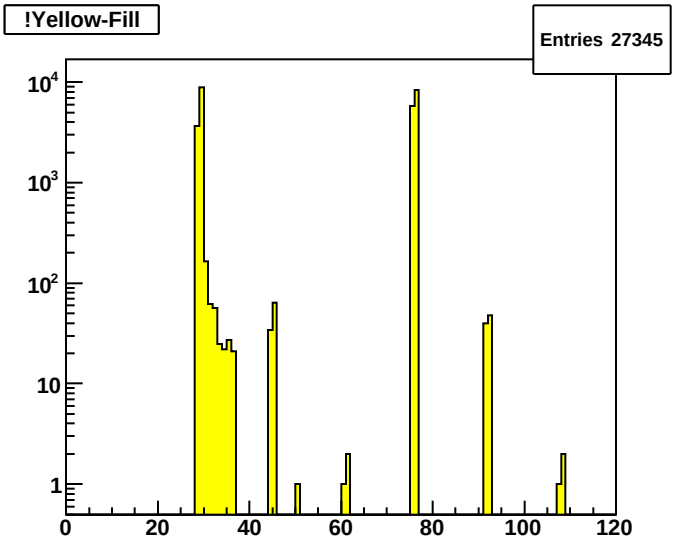

DAQ10k HT bits
Entries 1047

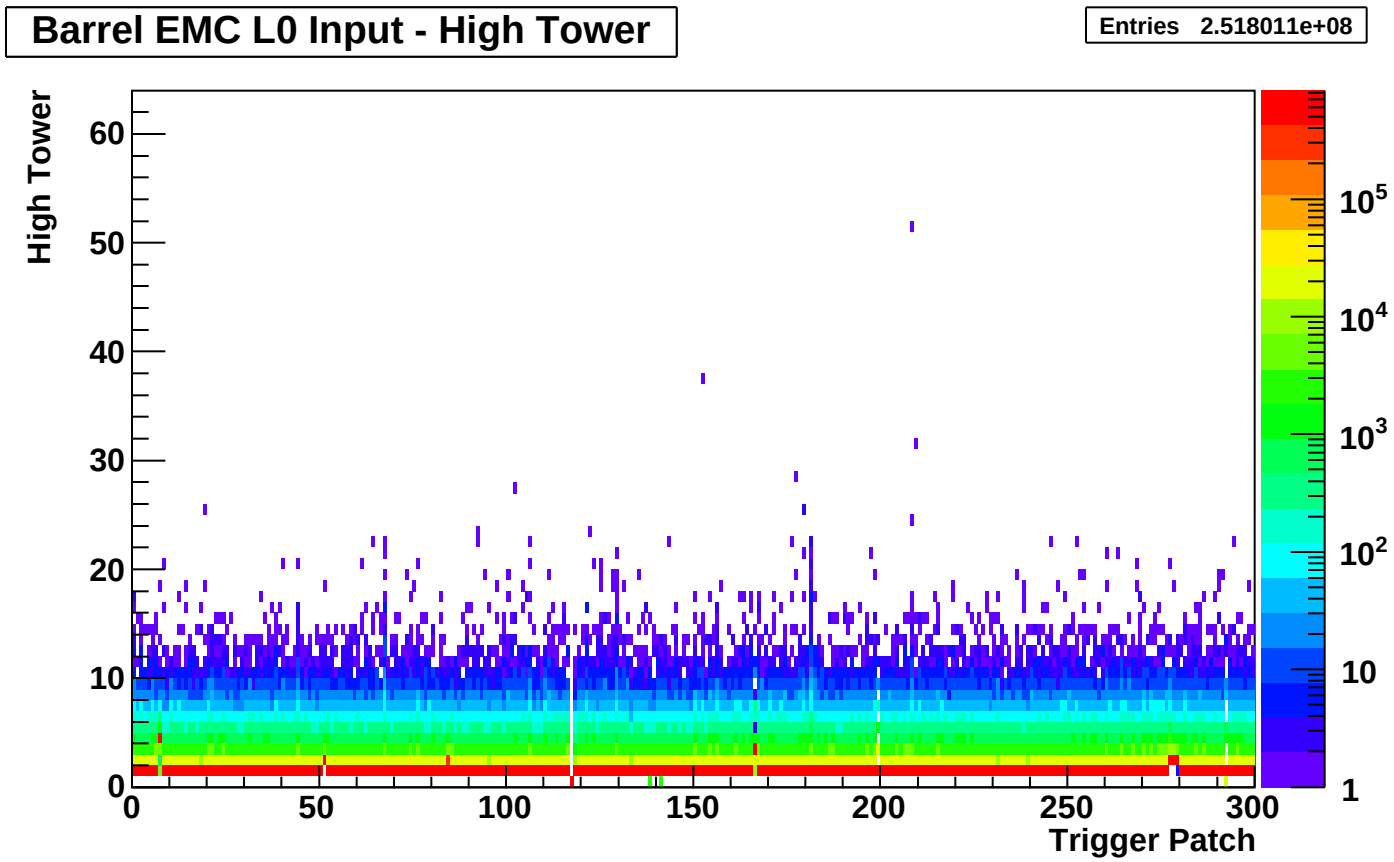

DAQ10k HT bits errors
Entries 0

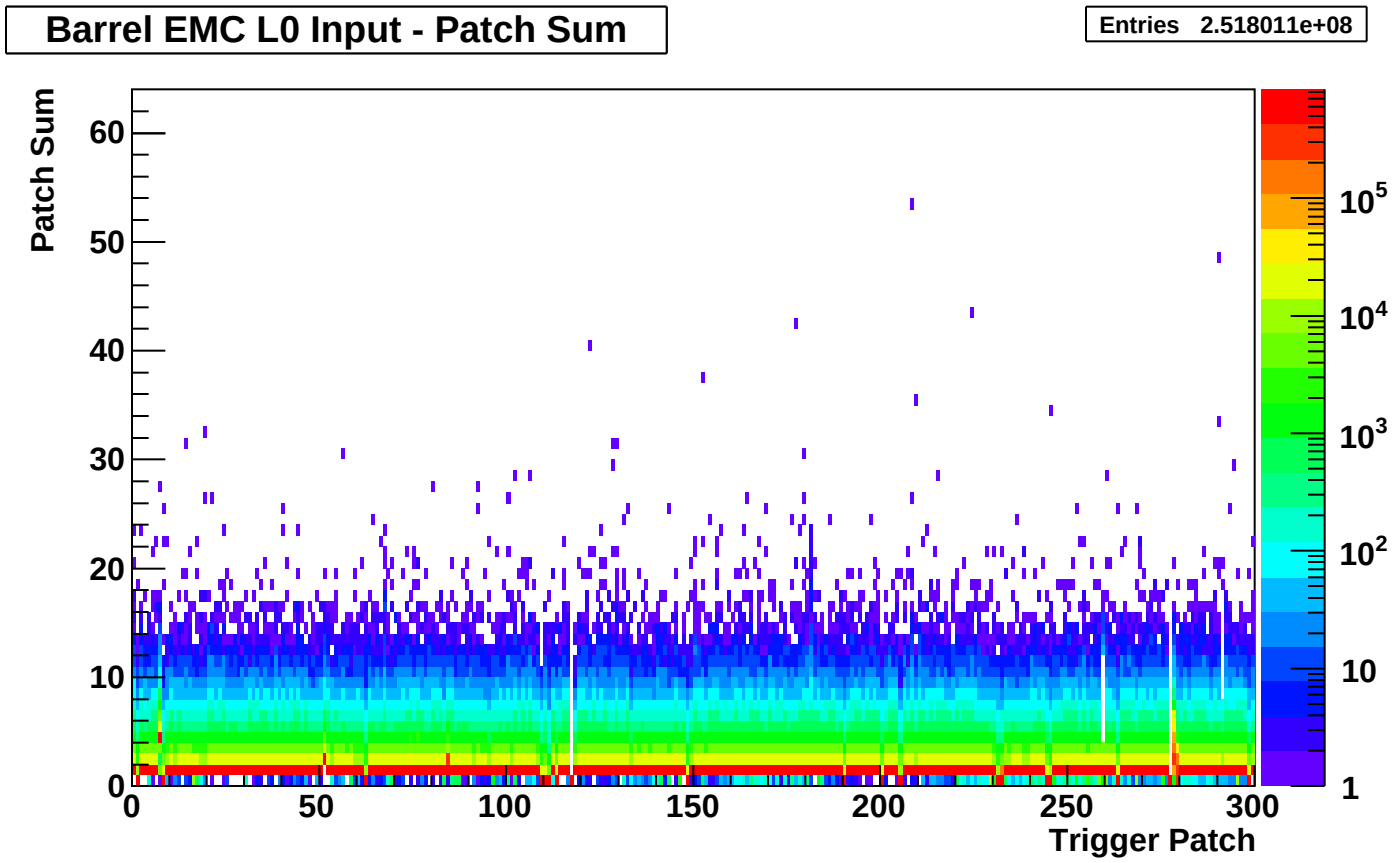

TPC Readout Mask (simulated)
Entries 1867

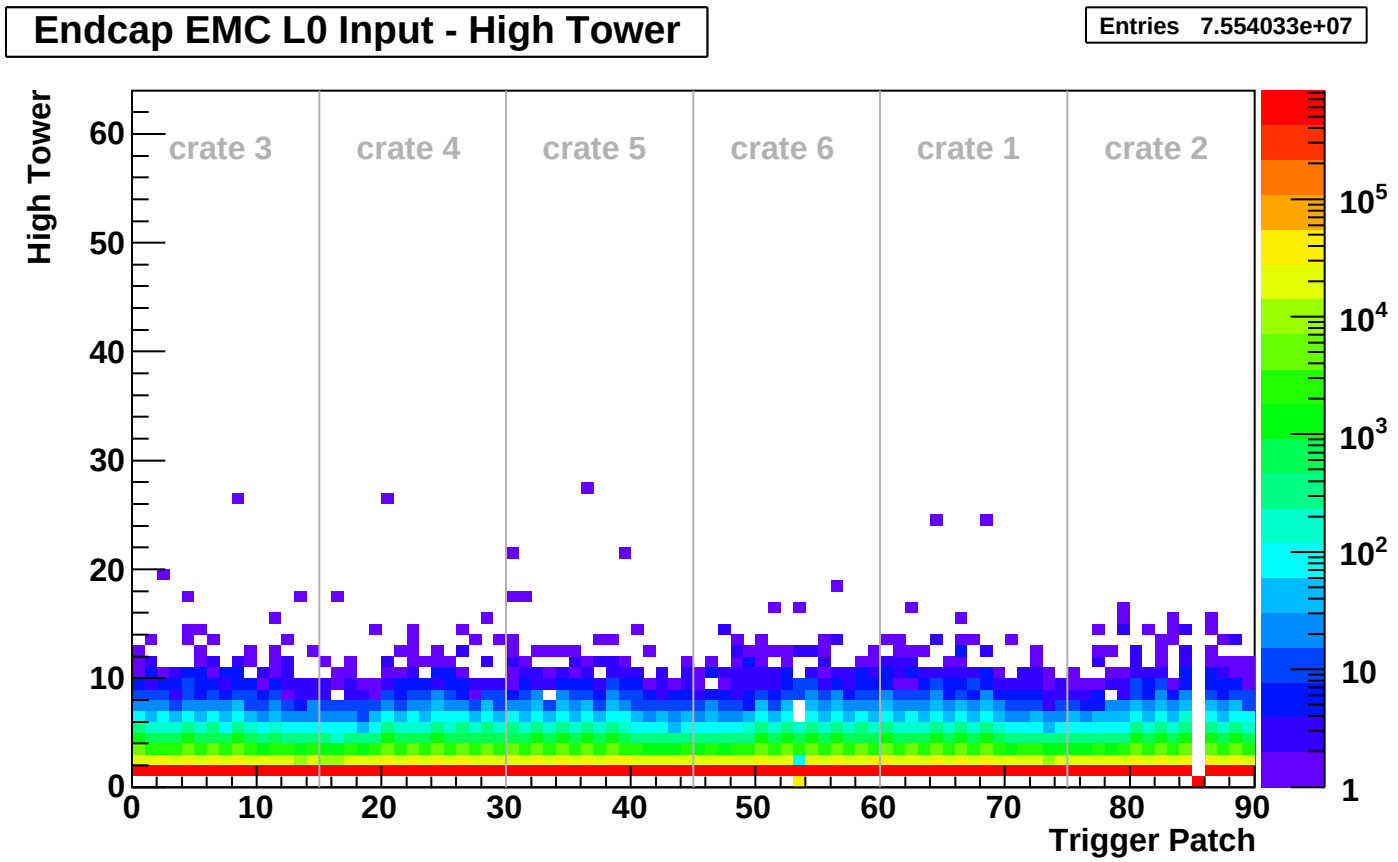

BunchId7Bit (all events)
Entries 839337

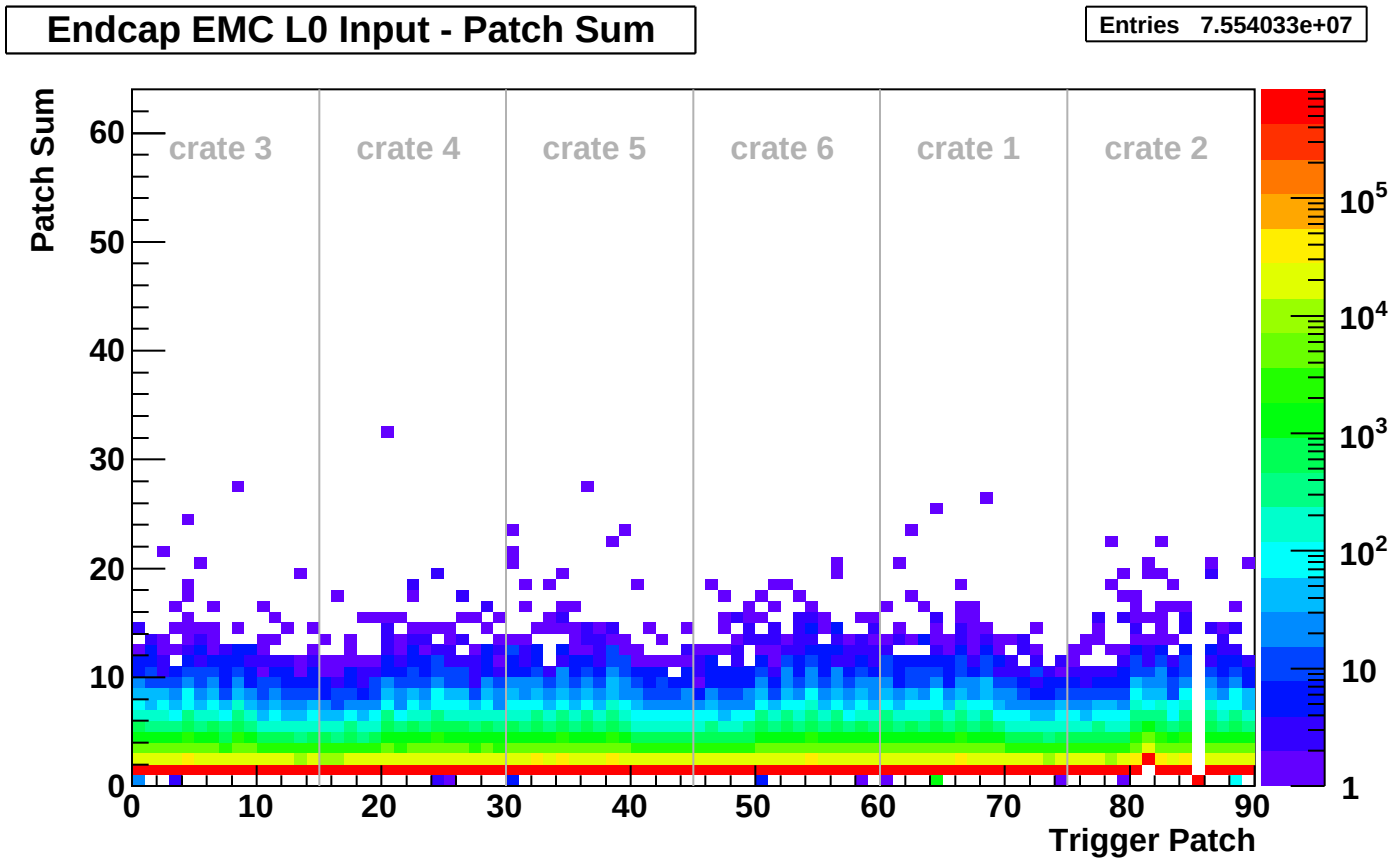

Abort-gaps
Entries 14293

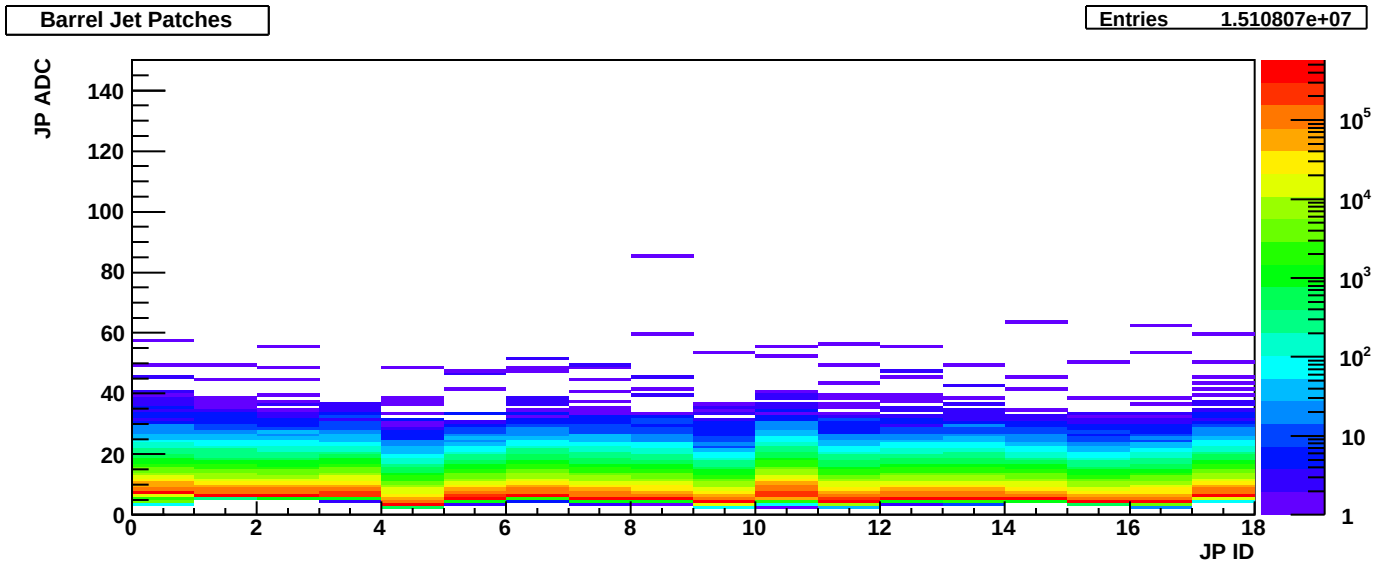

!Yellow-Fill
Entries 27345

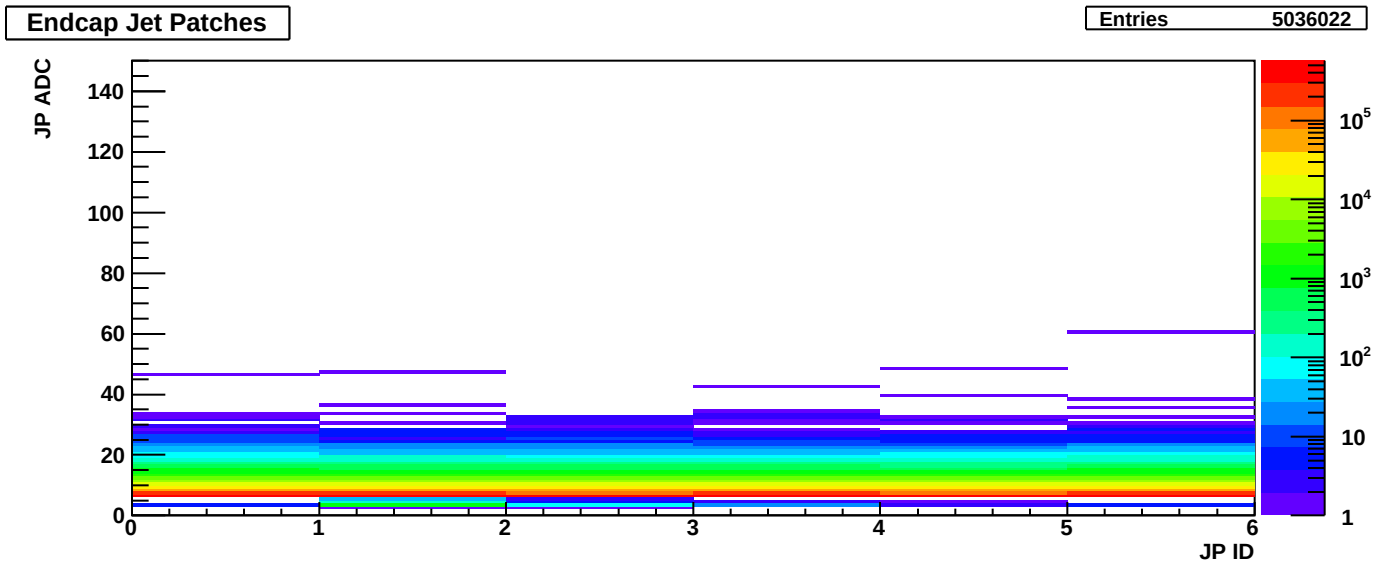

Barrel EMC L0 Input - High Tower
Entries 2.518011e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

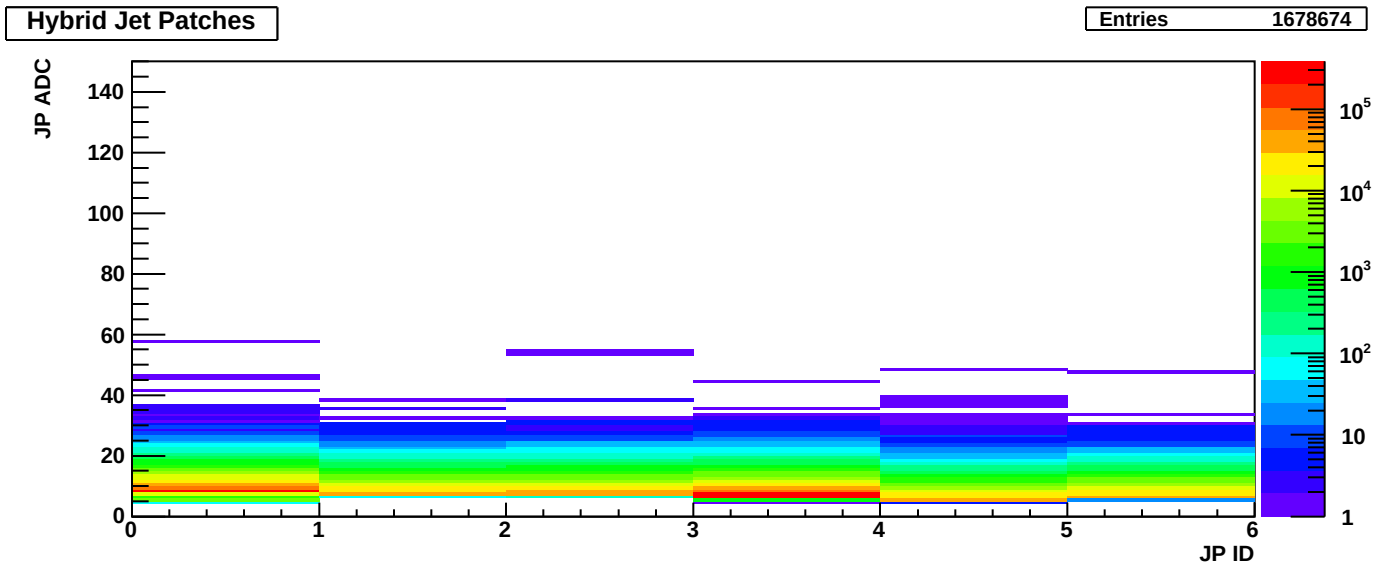

Barrel EMC L0 Input - Patch Sum
Entries 2.518011e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

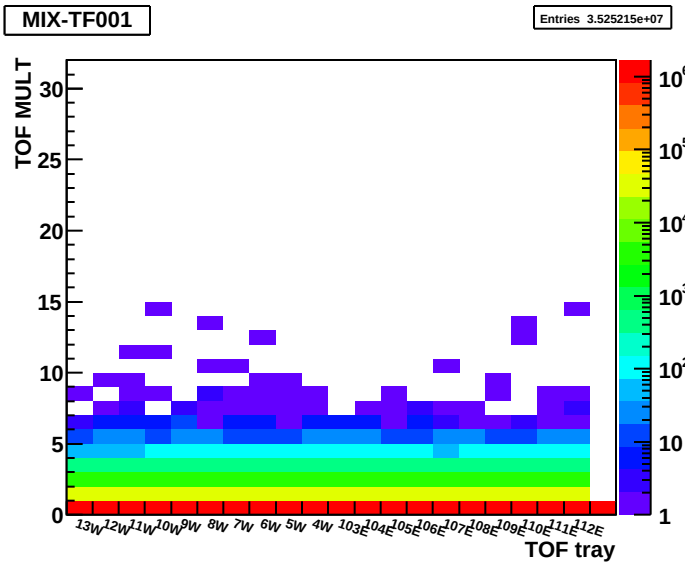

Endcap EMC L0 Input - High Tower
Entries 7.554033e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

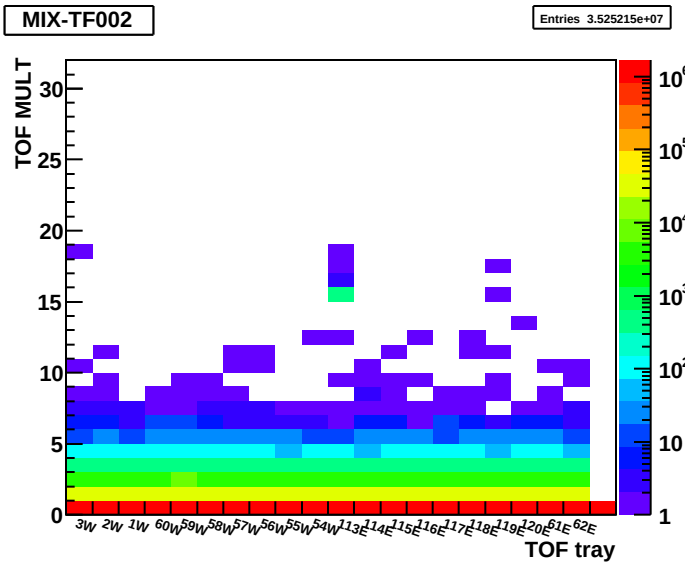

Endcap EMC L0 Input - Patch Sum
Entries 7.554033e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

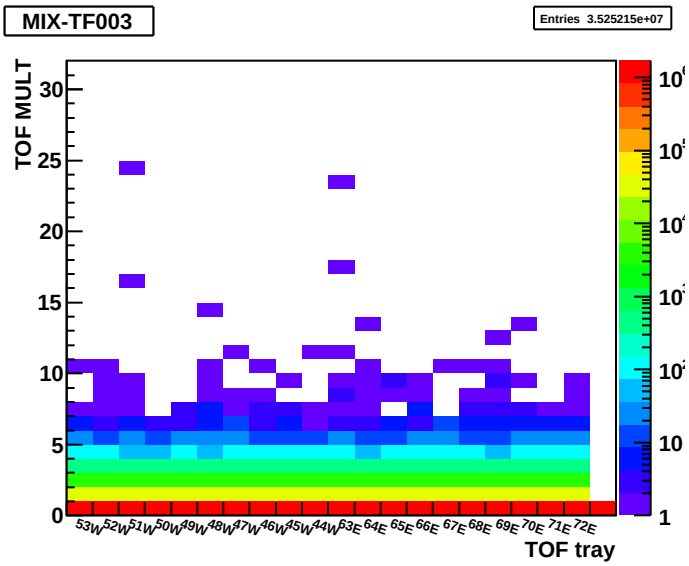

Barrel Jet Patches
Entries 1.510807e+07
JP ADC
JP ID

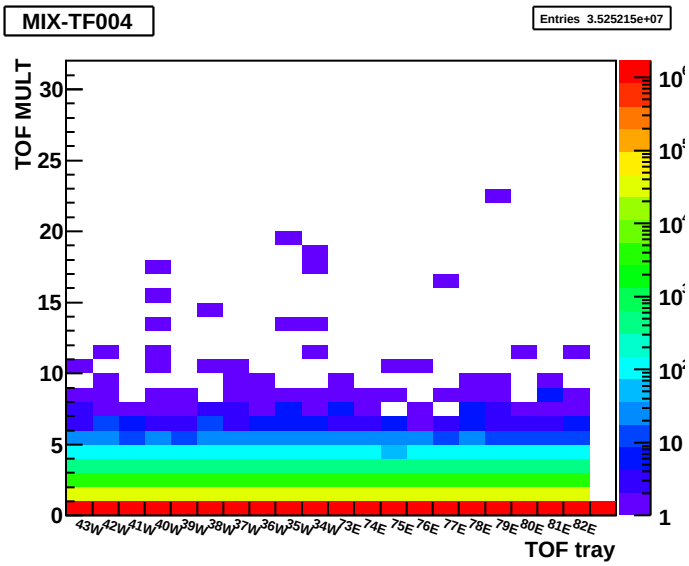

Endcap Jet Patches
Entries 5036022
JP ADC
JP ID

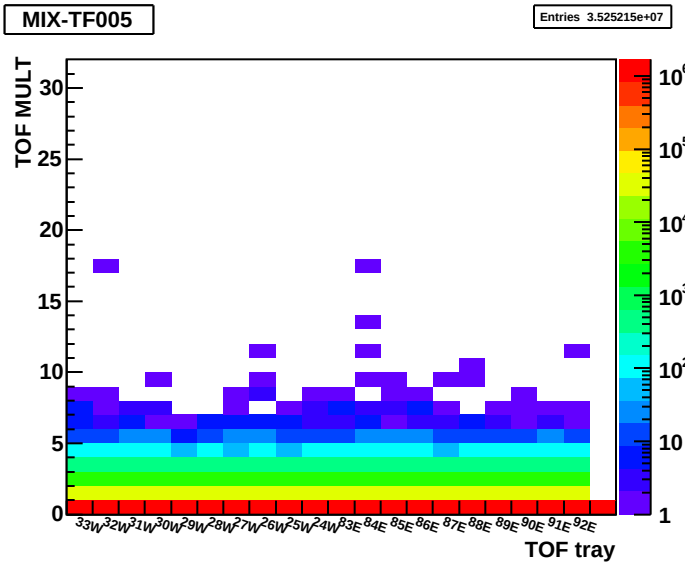

Hybrid Jet Patches
Entries 1678674
JP ADC
JP ID

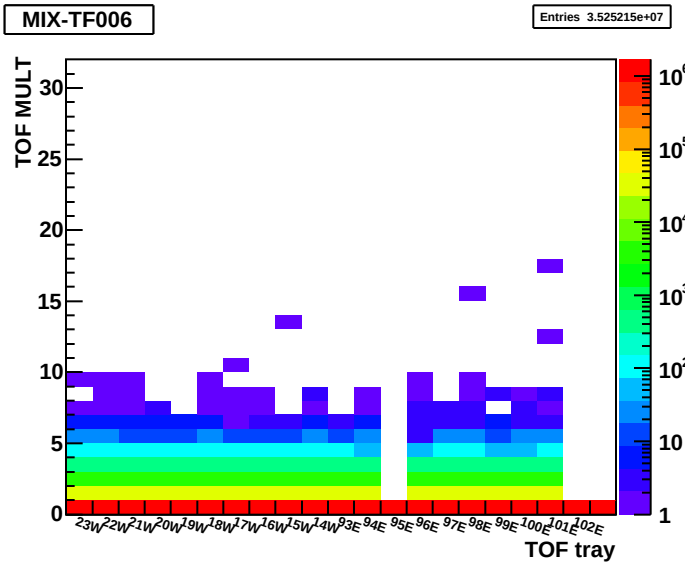

MIX-TF001
Entries 3.525215e+07
TOF MULT
TOF tray

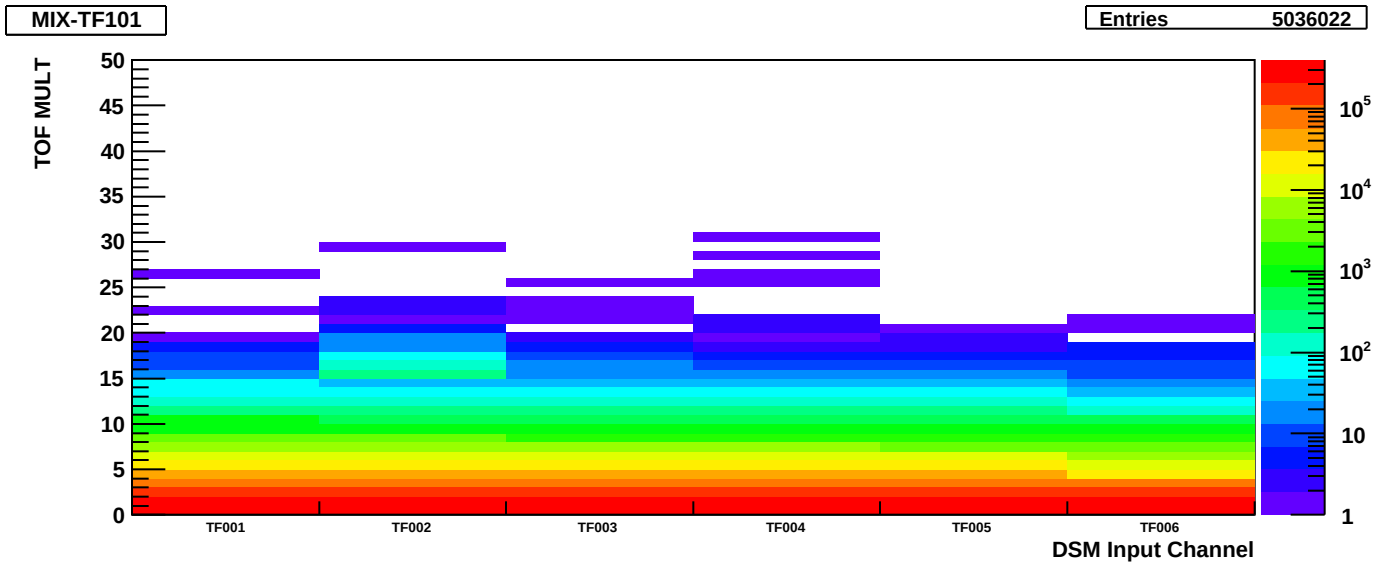

MIX-TF002
Entries 3.525215e+07
TOF MULT
TOF tray

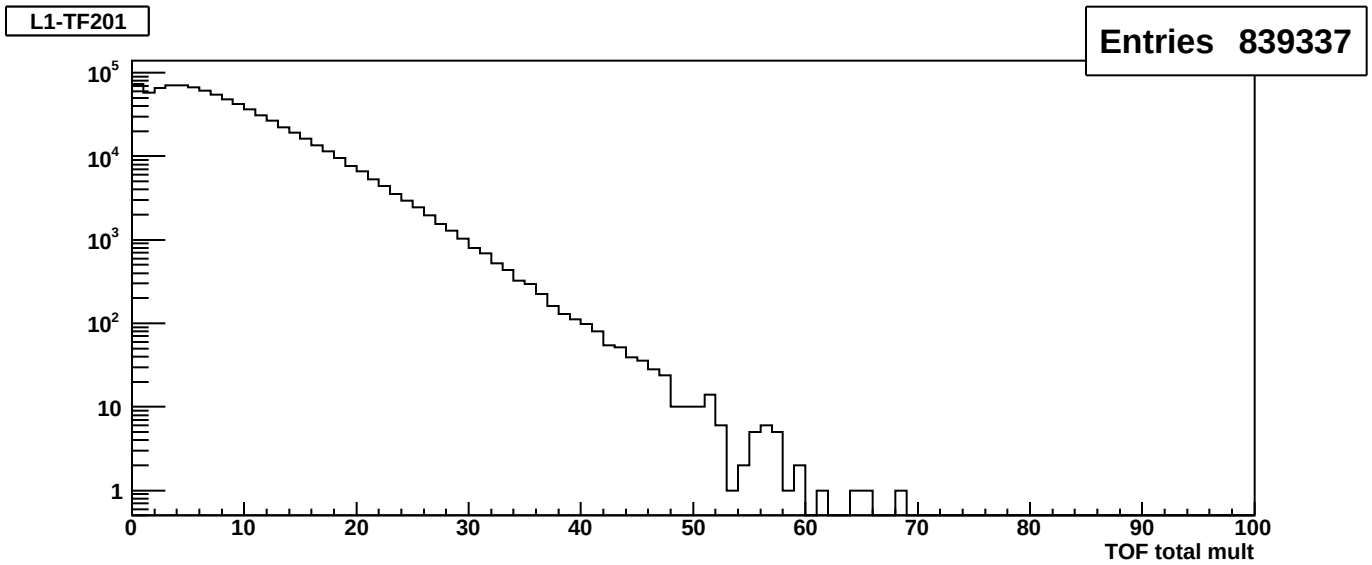

MIX-TF003
Entries 3.525215e+07
TOF MULT
TOF tray

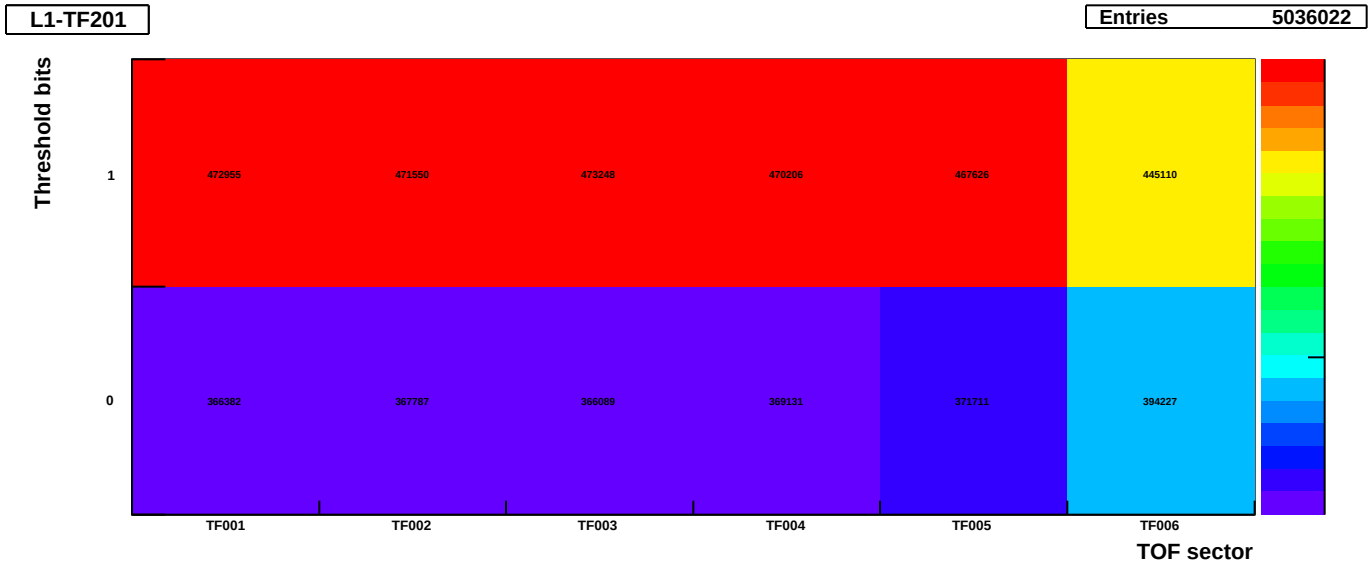

MIX-TF004
Entries 3.525215e+07
TOF MULT
TOF tray

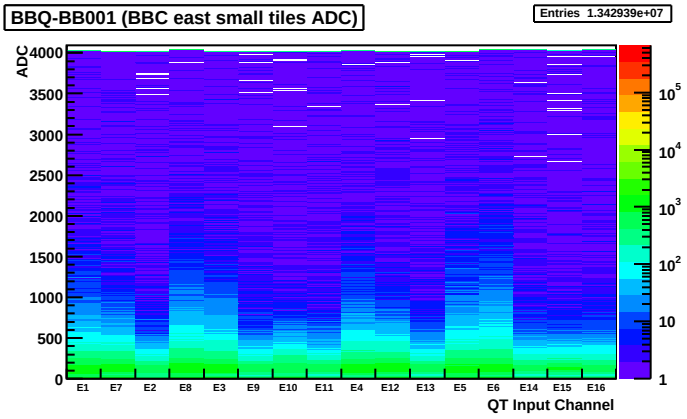

MIX-TF005
Entries 3.525215e+07
TOF MULT
TOF tray

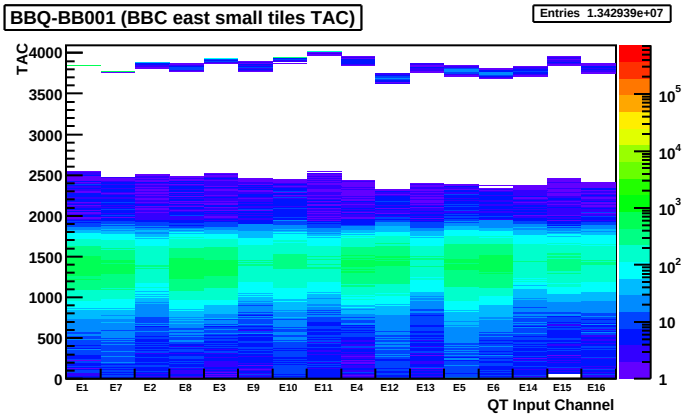

MIX-TF006
Entries 3.525215e+07
TOF MULT
TOF tray

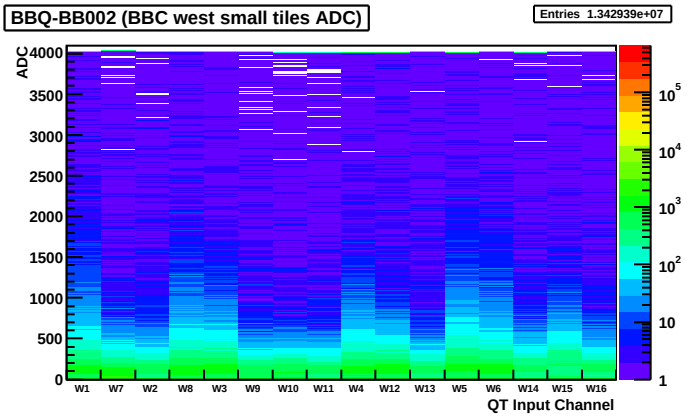

MIX-TF101
Entries 5036022
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

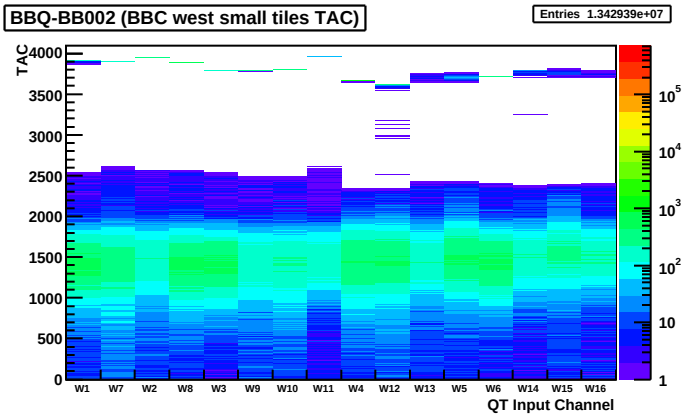

L1-TF201
Entries 839337
TOF total mult

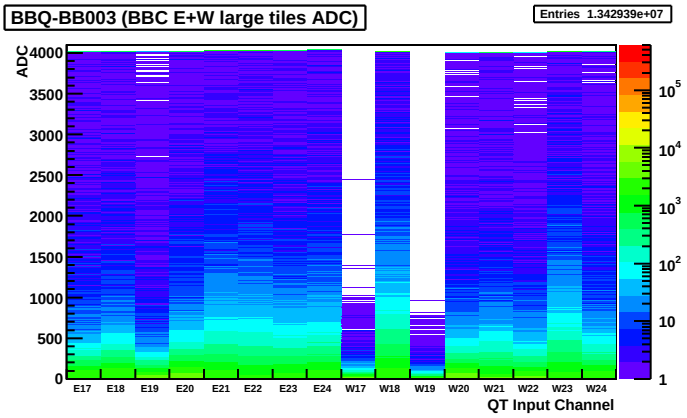

L1-TF201
Entries 5036022
Threshold bits
472955
471550
473248
470206
467626
445110
366382
367787
366089
369131
371711
394227
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

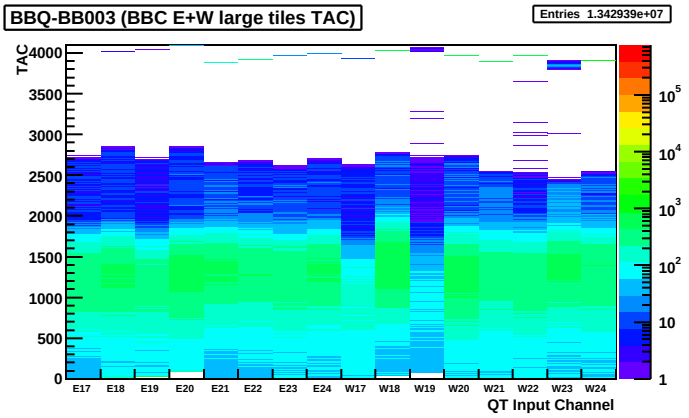

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.342939e+07
ADC
QT Input Channel

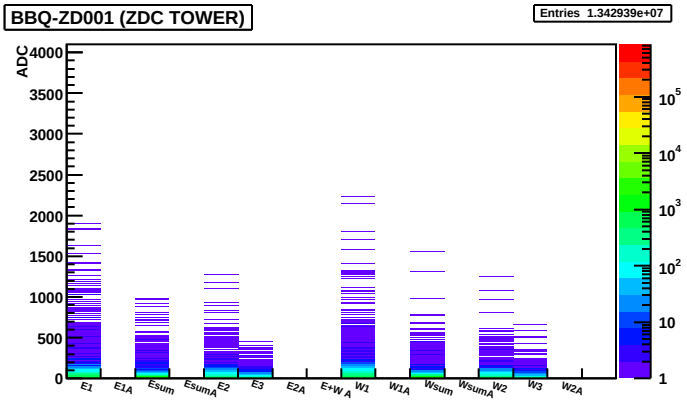

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.342939e+07
TAC
QT Input Channel

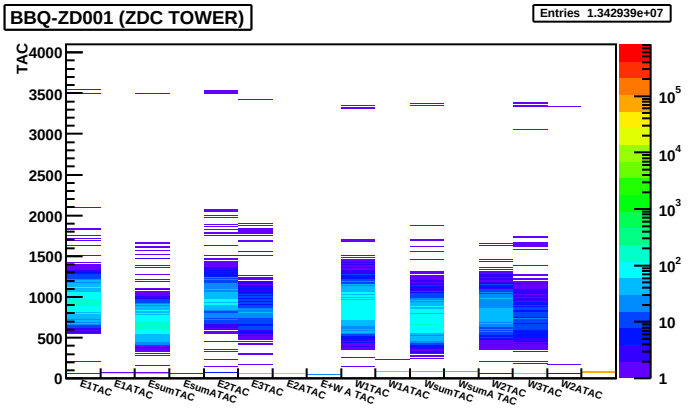

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.342939e+07
ADC
QT Input Channel

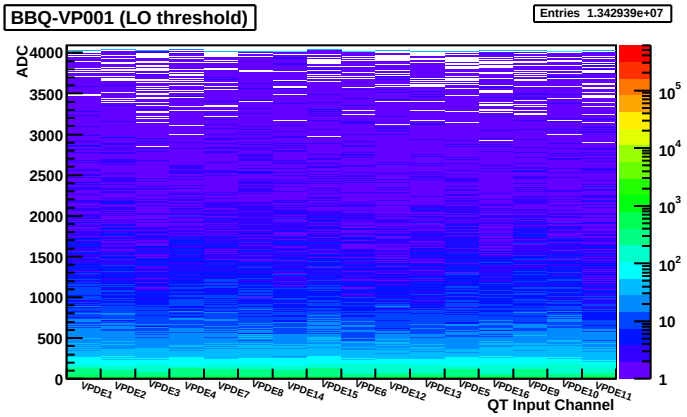

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.342939e+07
TAC
QT Input Channel

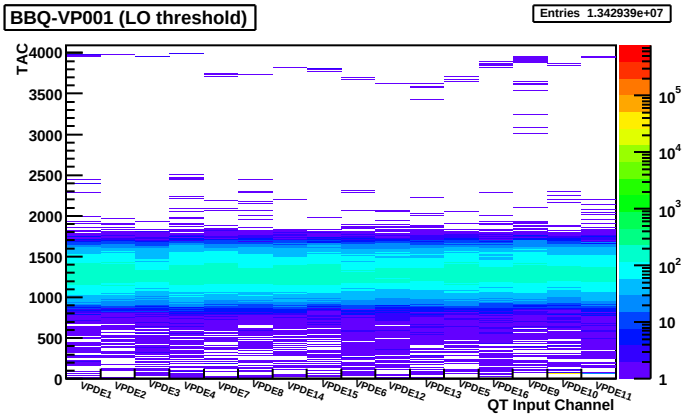

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.342939e+07
ADC
QT Input Channel

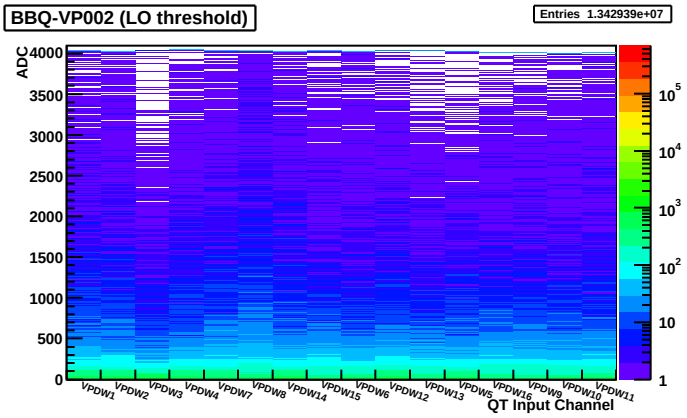

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.342939e+07
TAC
QT Input Channel

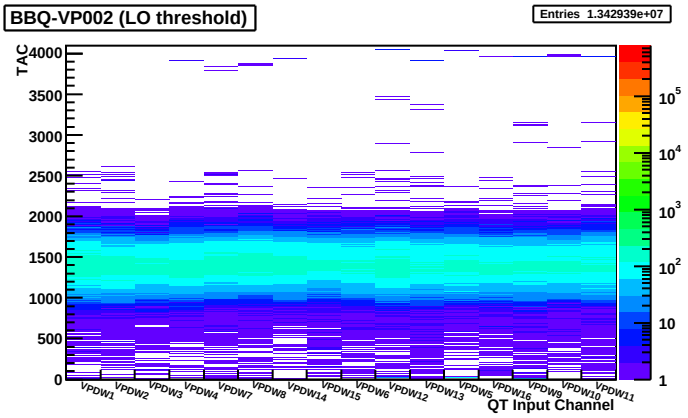

BBQ-ZD001 (ZDC TOWER)
Entries 1.342939e+07
ADC

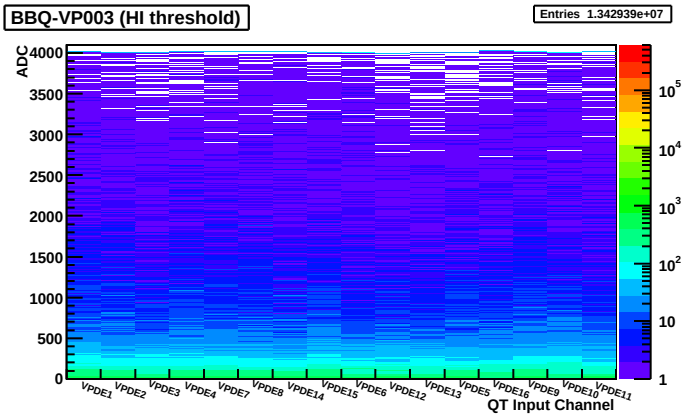

BBQ-ZD001 (ZDC TOWER)
Entries 1.342939e+07
TAC

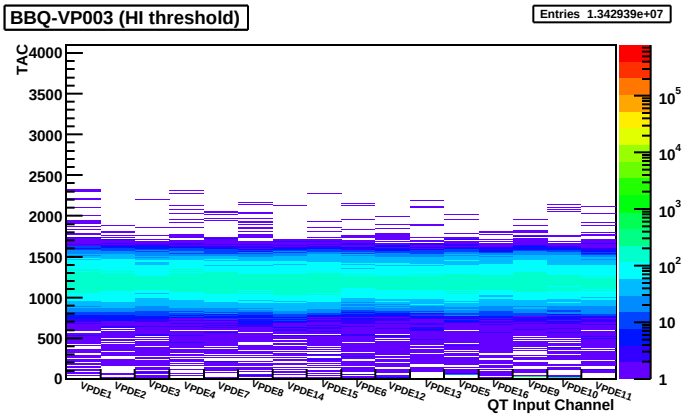

BBQ-VP001 (LO threshold)
Entries 1.342939e+07
ADC
QT Input Channel

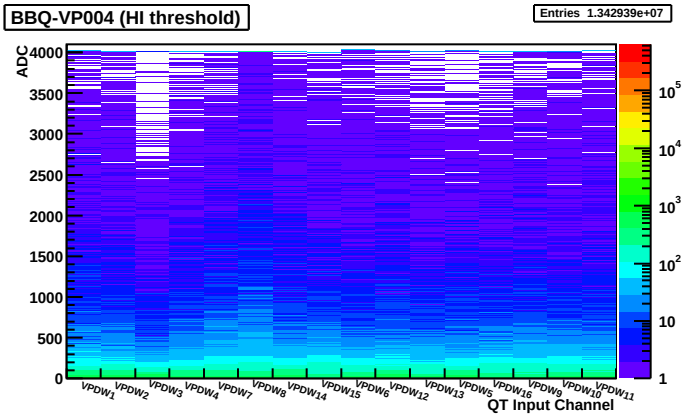

BBQ-VP001 (LO threshold)
Entries 1.342939e+07
TAC
QT Input Channel

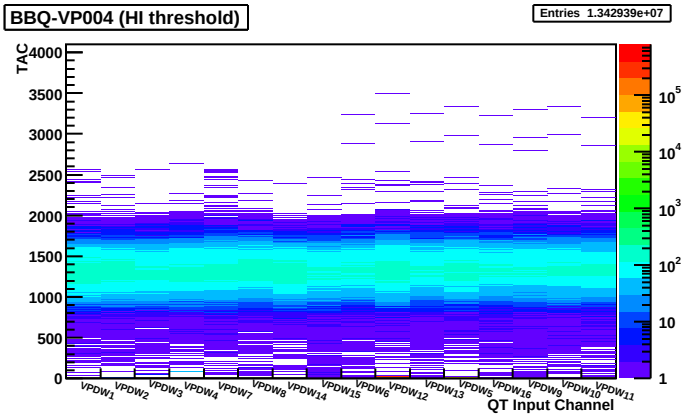

BBQ-VP002 (LO threshold)
Entries 1.342939e+07
ADC
QT Input Channel

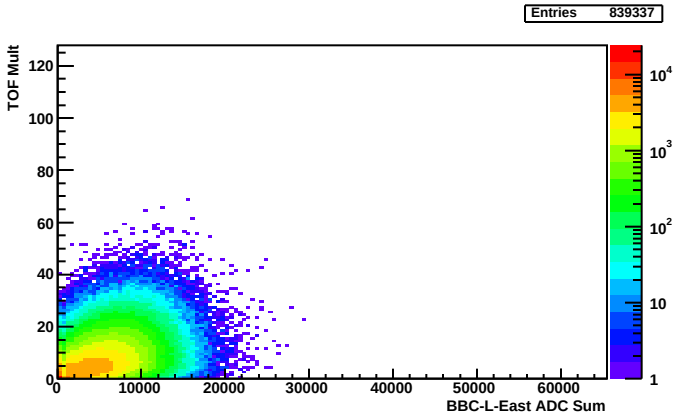

BBQ-VP002 (LO threshold)
Entries 1.342939e+07
TAC
QT Input Channel

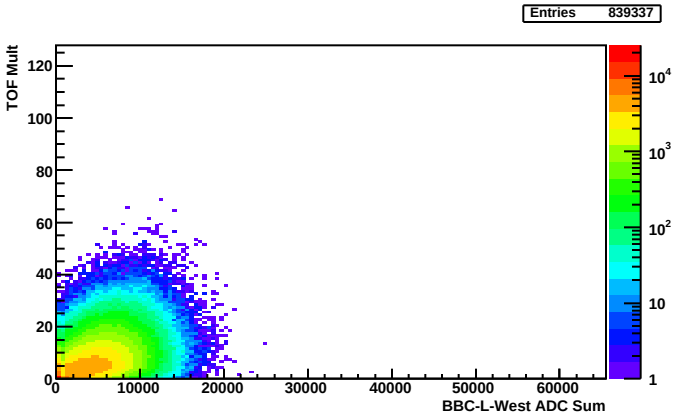

BBQ-VP003 (HI threshold)
Entries 1.342939e+07
ADC
QT Input Channel

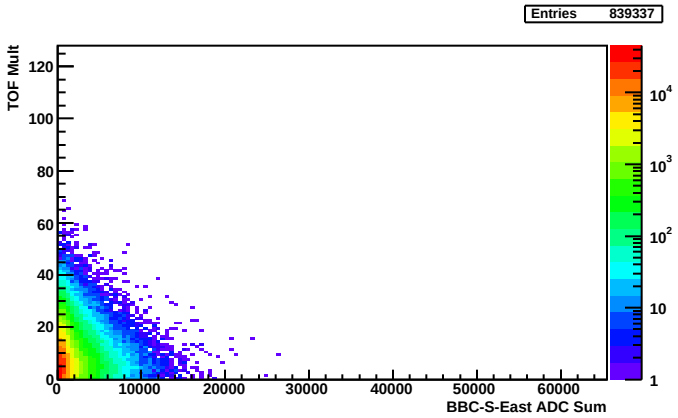

BBQ-VP003 (HI threshold)
Entries 1.342939e+07
TAC
QT Input Channel

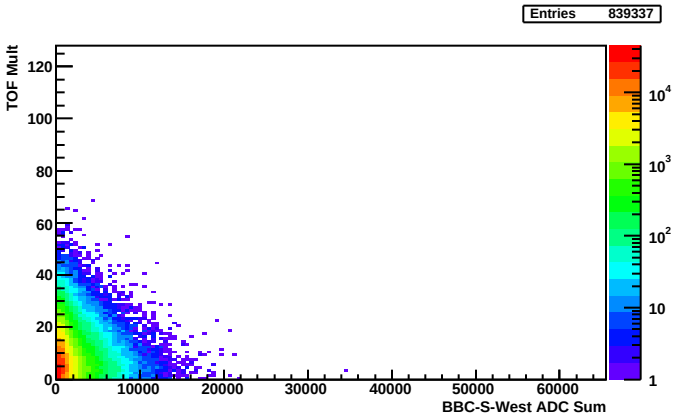

BBQ-VP004 (HI threshold)
Entries 1.342939e+07
ADC
QT Input Channel

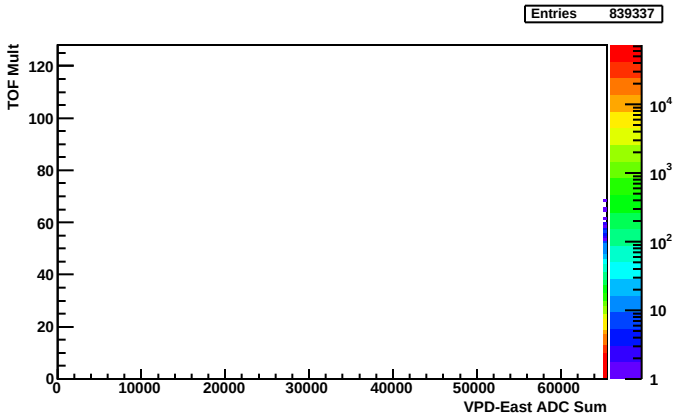

BBQ-VP004 (HI threshold)
Entries 1.342939e+07
TAC
QT Input Channel

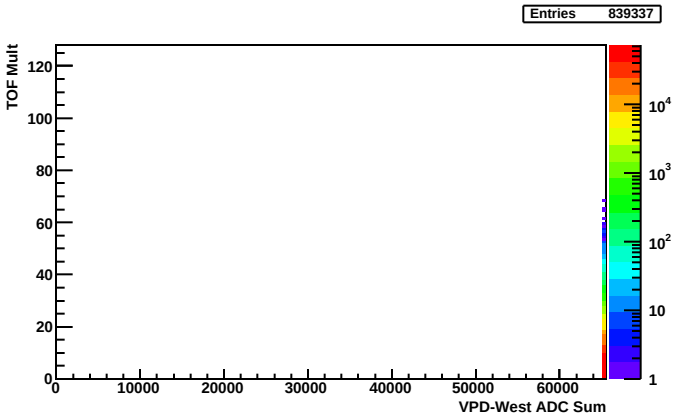

Entries 839337
TOF Mult
BBC-L-East ADC Sum

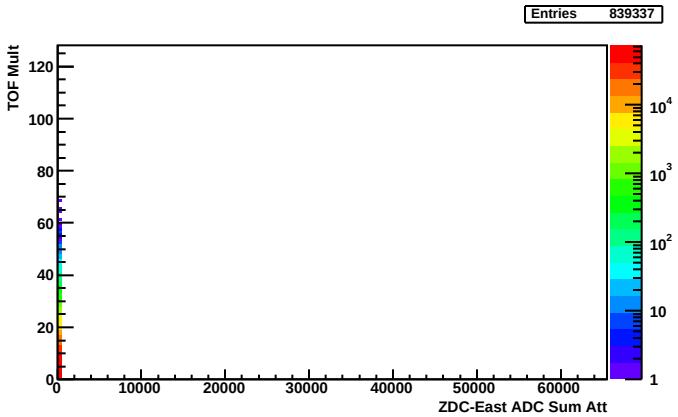

Entries 839337
TOF Mult
BBC-L-West ADC Sum

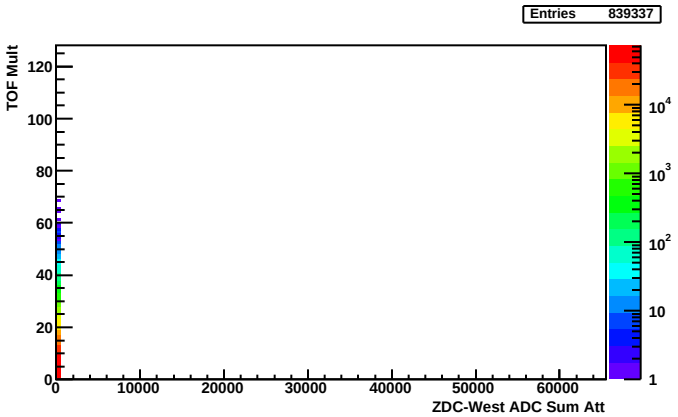

Entries 839337
TOF Mult
BBC-S-East ADC Sum

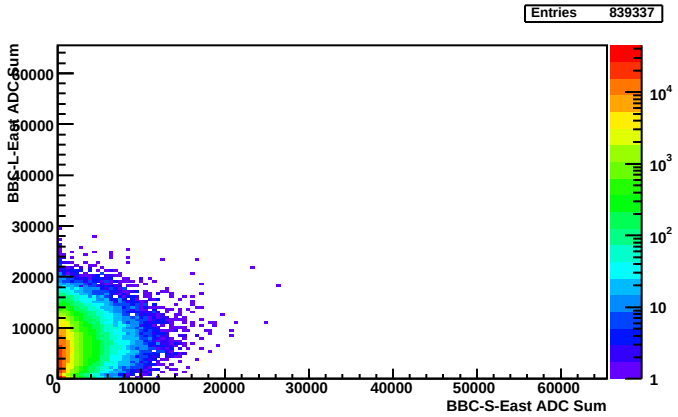

Entries 839337
TOF Mult
BBC-S-West ADC Sum

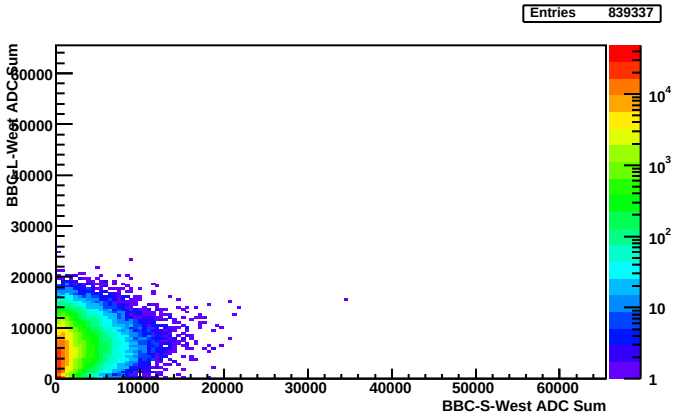

Entries 839337
TOF Mult
VPD-East ADC Sum

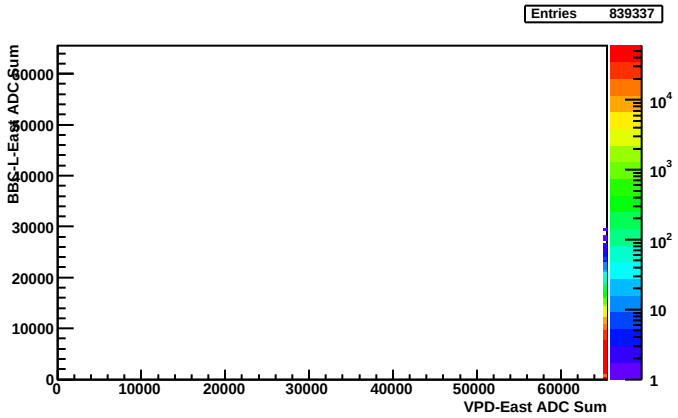

Entries 839337
TOF Mult
VPD-West ADC Sum

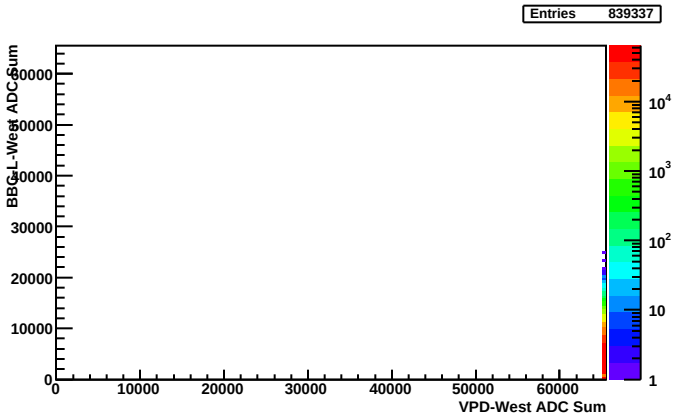

Entries 839337
TOF Mult
ZDC-East ADC Sum Att

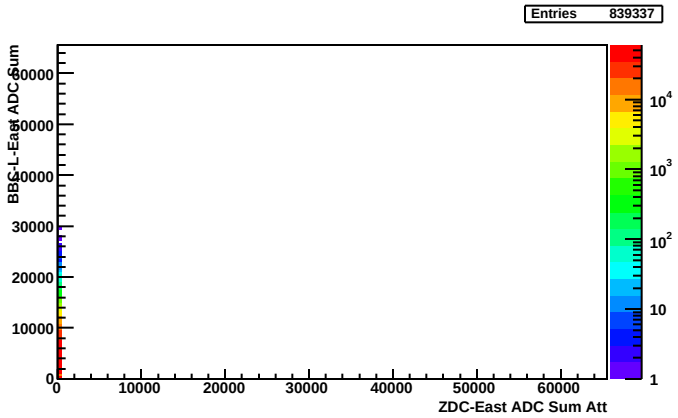

Entries 839337
TOF Mult
ZDC-West ADC Sum Att


Entries 839337
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 839337
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 839337
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 839337
BBC-L-West ADC Sum
VPD-West ADC Sum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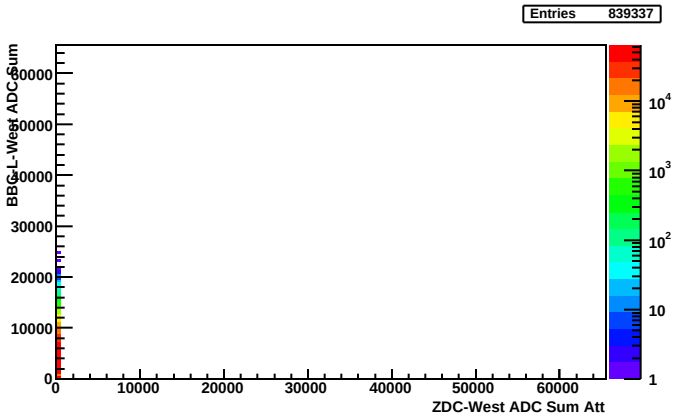
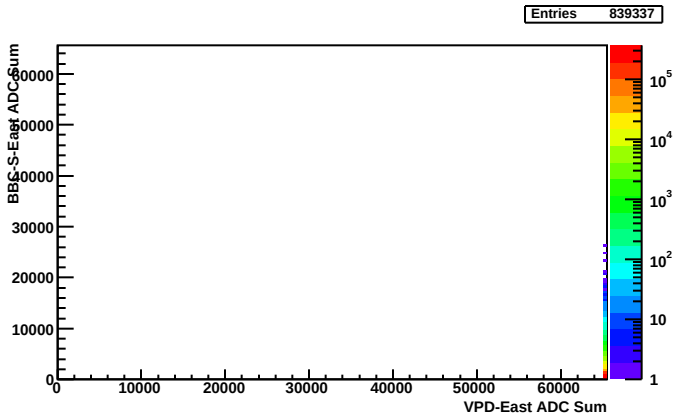
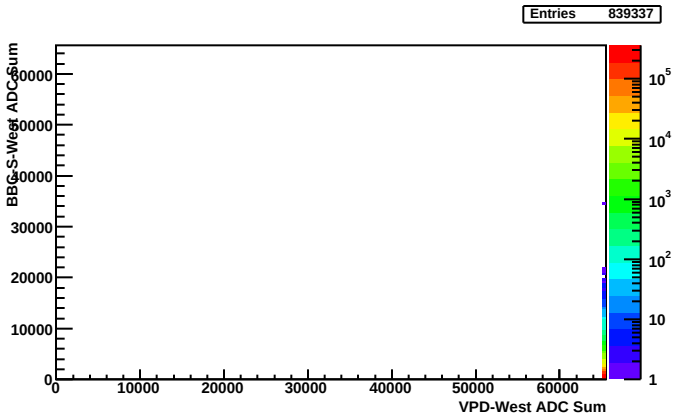
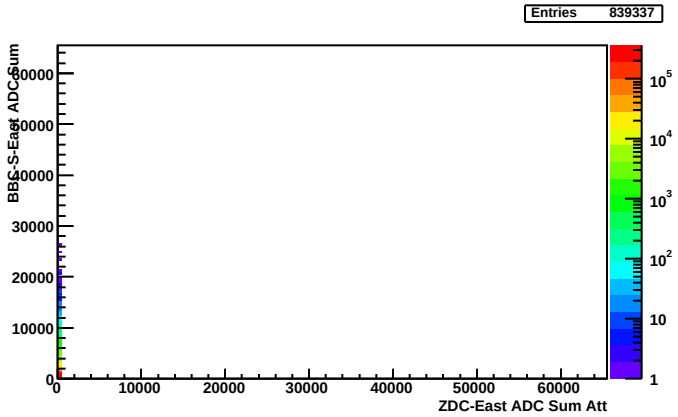
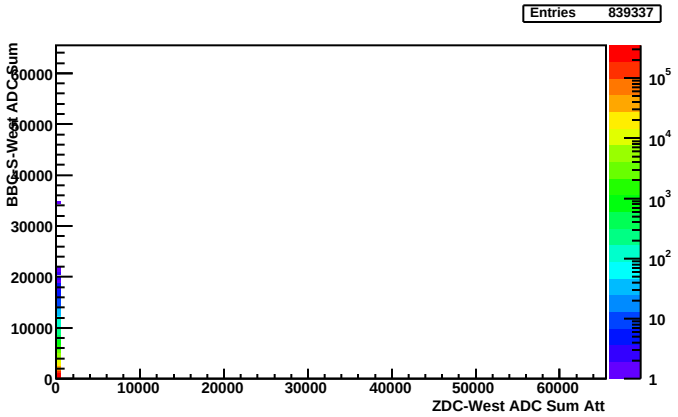
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

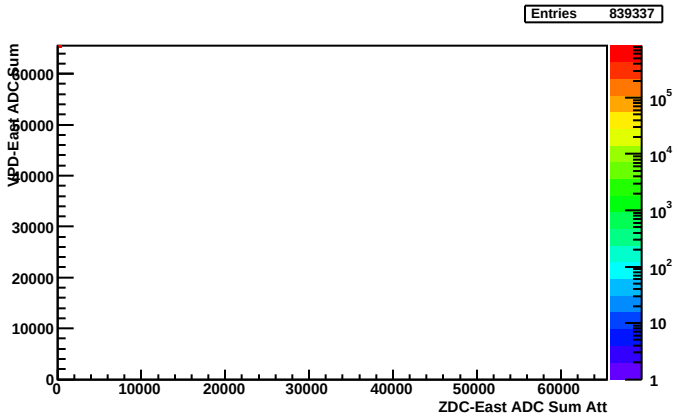

Entries 839337
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

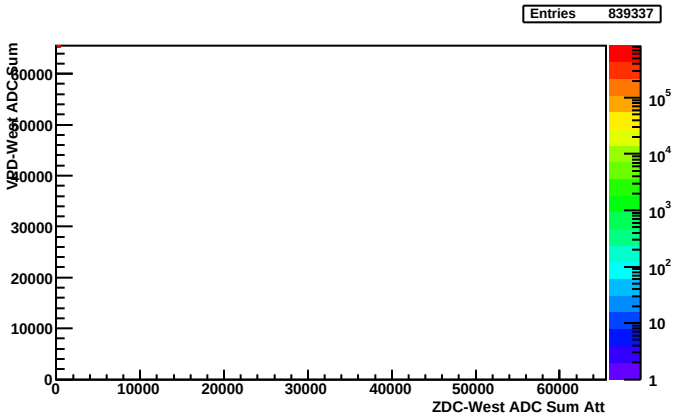

Entries 839337
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 839337
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 839337
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 839337
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 839337
VPD-East ADC Sum
ZDC-East ADC Sum Att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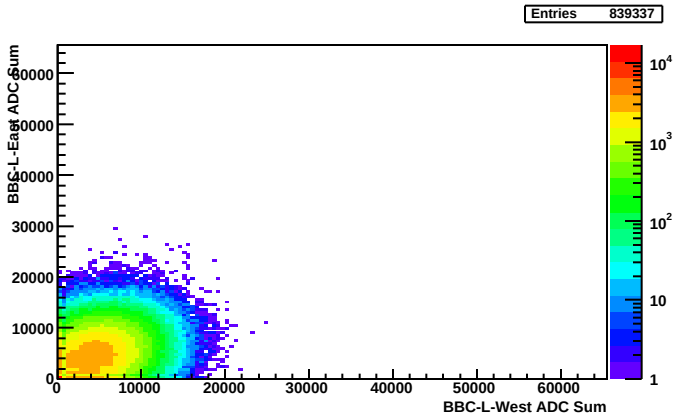
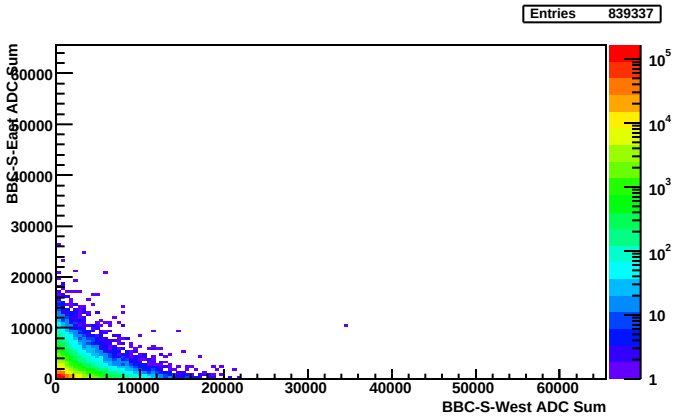
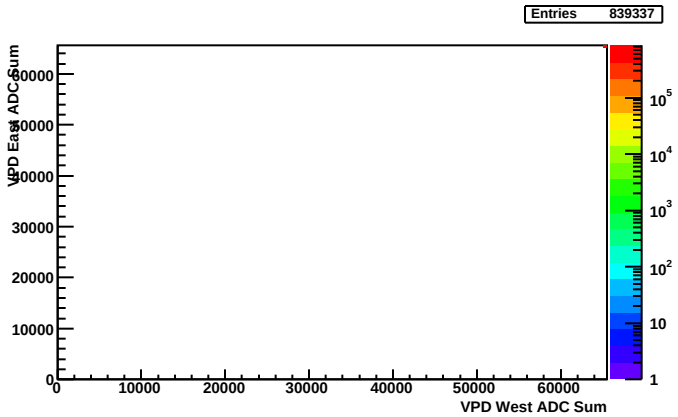
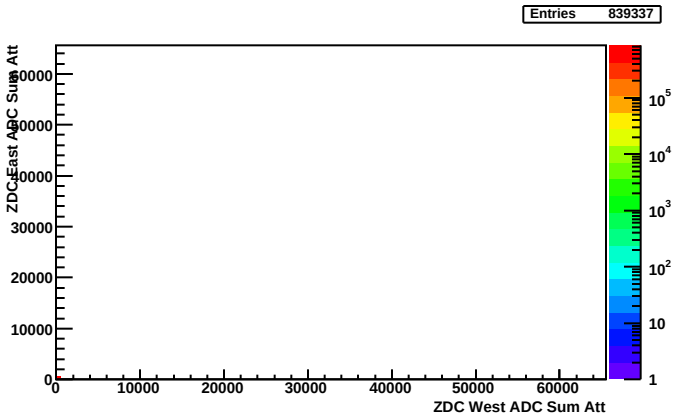
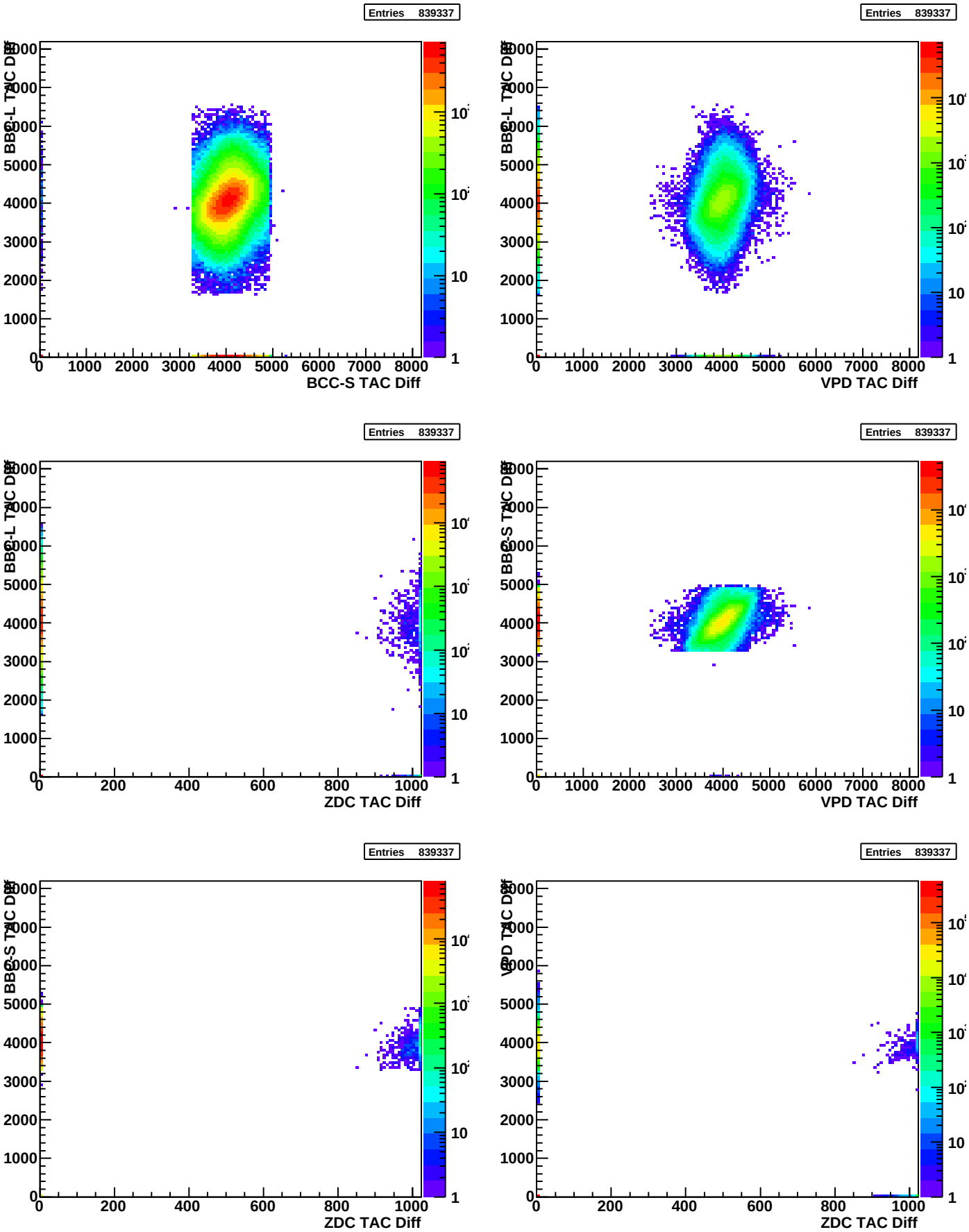
VPD-West ADC Sum
ZDC-West ADC Sum Att

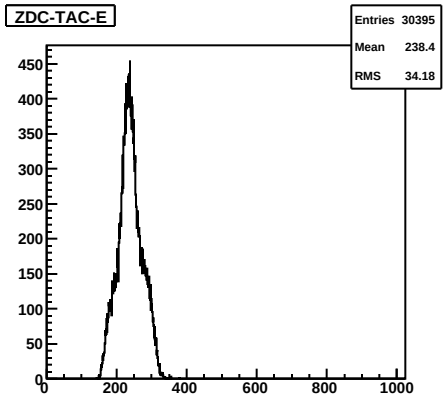

Entries 839337
BBC-L-East ADC Sum
BBC-L-West ADC Sum

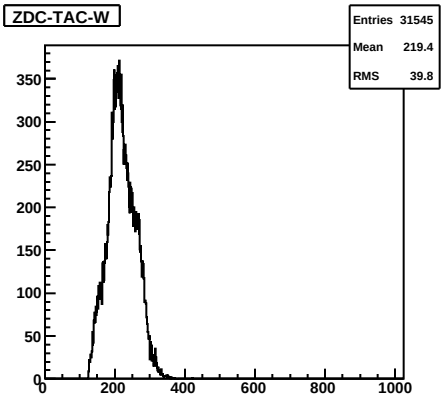

Entries 839337
BBC-S-East ADC Sum
BBC-S-West ADC Sum

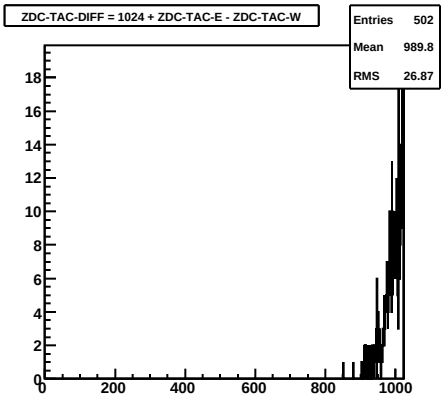

Entries 839337
VPD East ADC Sum
VPD West ADC Sum

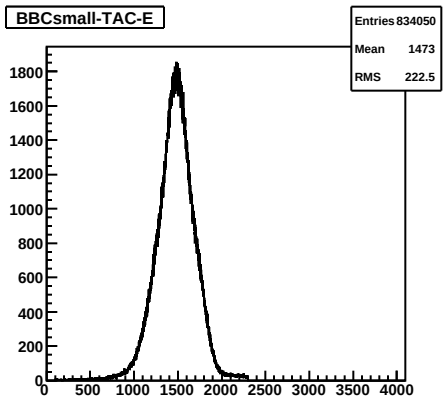

Entries 839337
ZDC East ADC Sum Att
ZDC West ADC Sum Att

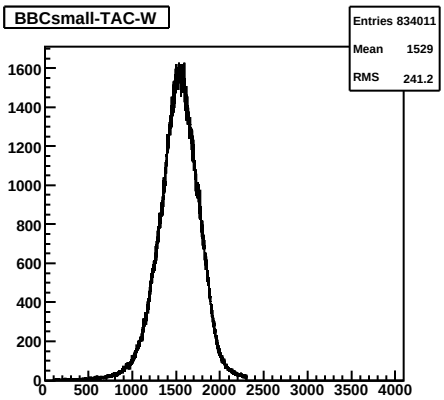

Entries 839337
BBC-L TAC Diff
BCC-S TAC Diff
Entries 839337
BBC-L TAC Diff
VPD TAC Diff
Entries 839337
BBC-L TAC Diff
ZDC TAC Diff
Entries 839337
BBC-S TAC Diff
VPD TAC Diff
Entries 839337
BBC-S TAC Diff
ZDC TAC Diff
Entries 839337
VPD TAC Diff
ZDC TAC Diff

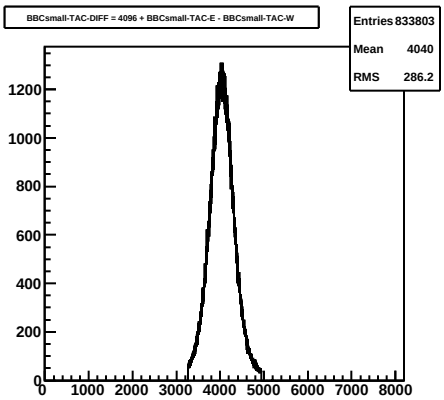

ZDC-TAC-E
Entries 30395
Mean 238.4
RMS 34.18

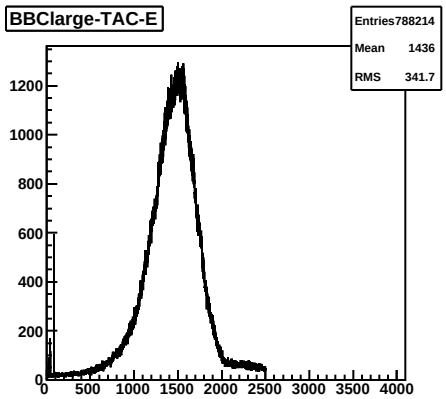

ZDC-TAC-W
Entries 31545
Mean 219.4
RMS 39.8

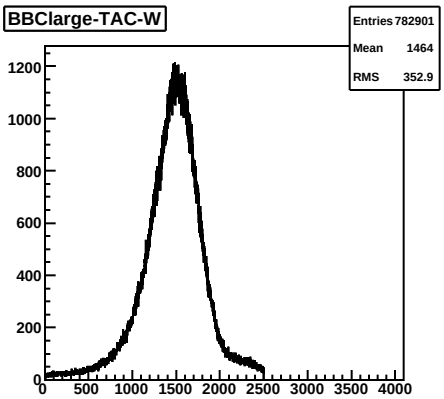

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 502
Mean 989.8
RMS 26.87

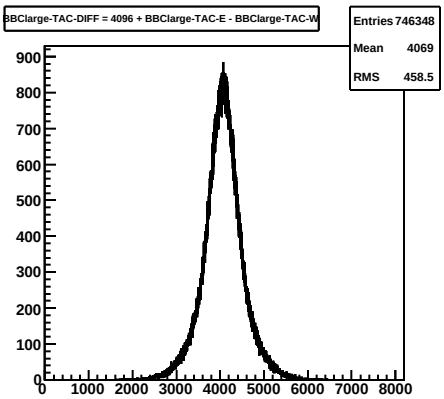

BBCsmall-TAC-E
Entries 834050
Mean 1473
RMS 222.5

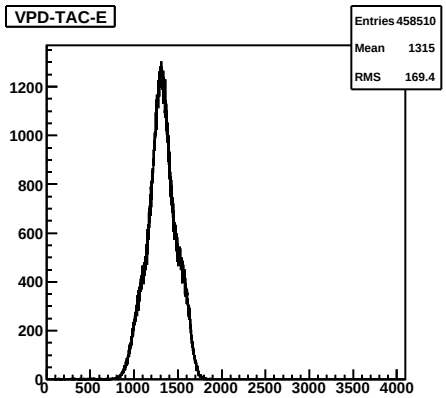

BBCsmall-TAC-W
Entries 834011
Mean 1529
RMS 241.2

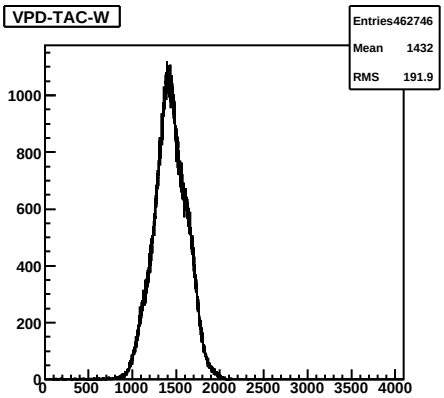

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 833803
Mean 4040
RMS 286.2

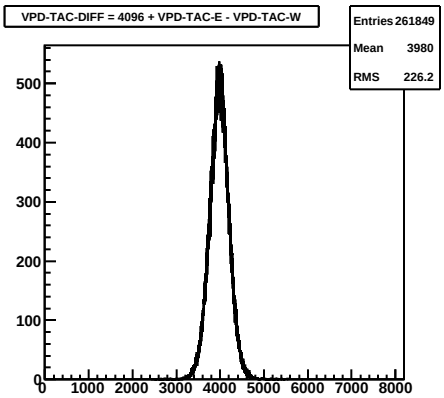

BBClarge-TAC-E
Entries 788214
Mean 1436
RMS 341.7

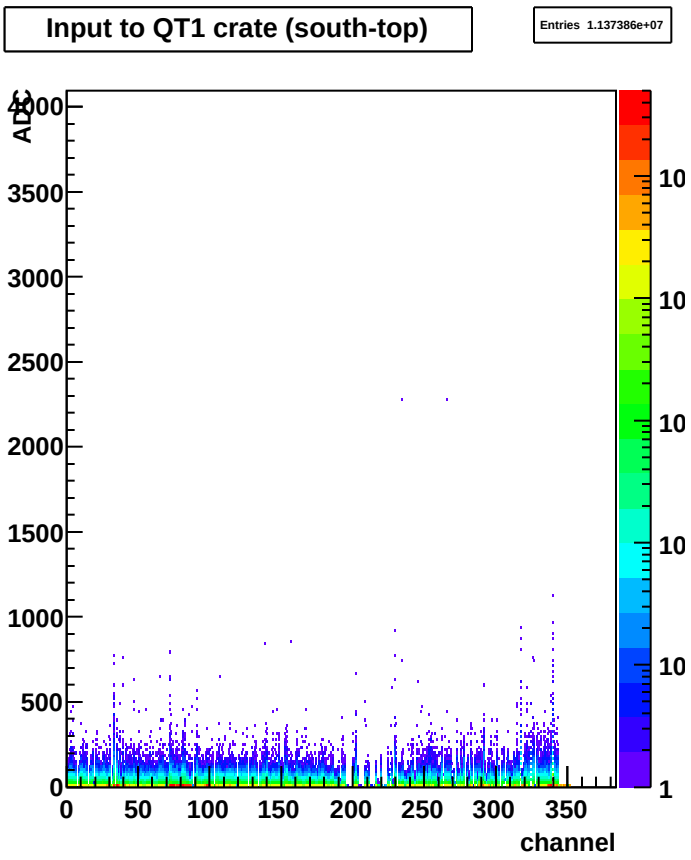

BBClarge-TAC-W
Entries 782901
Mean 1464
RMS 352.9

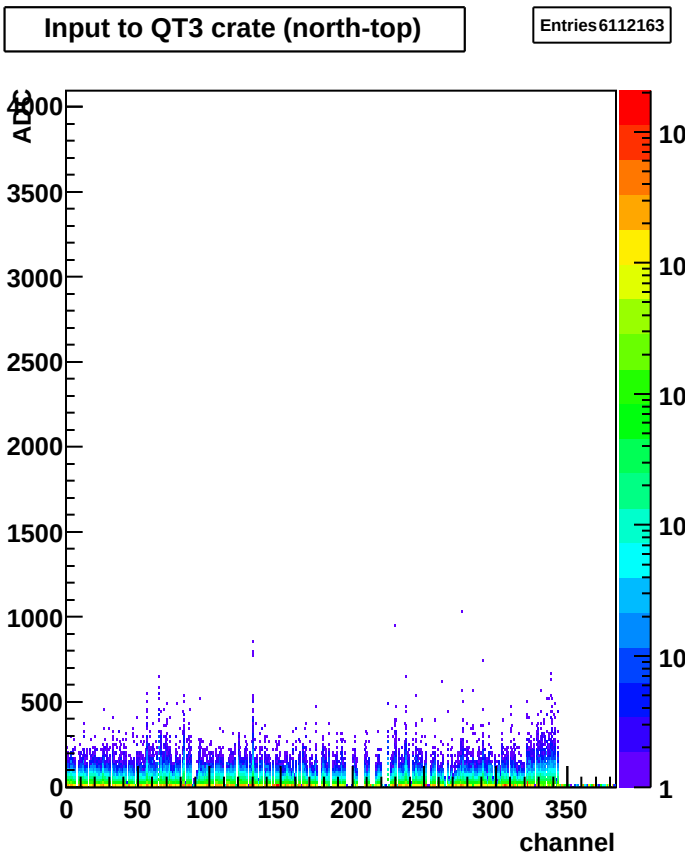

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 746348
Mean 4069
RMS 458.5

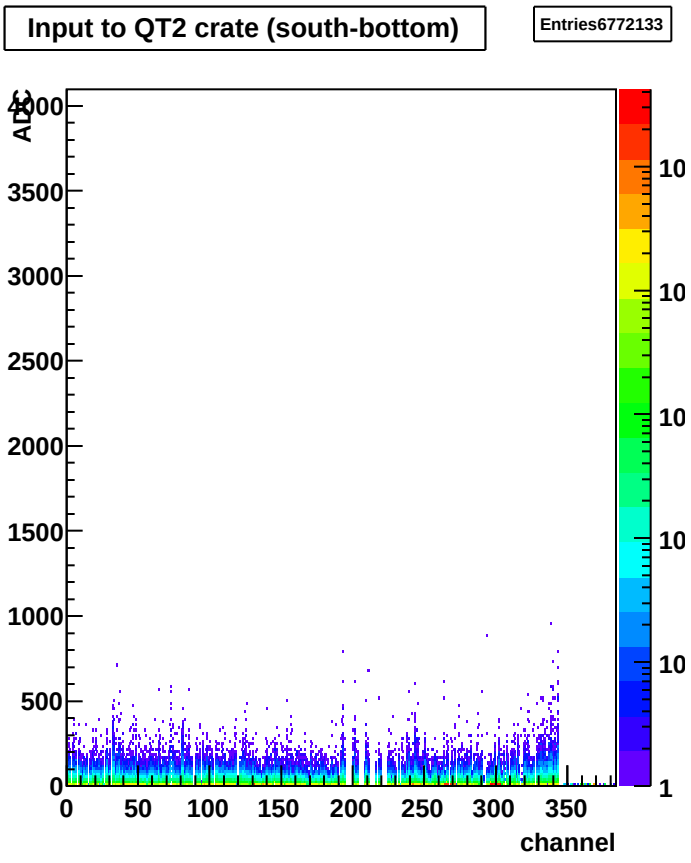

VPD-TAC-E
Entries 458510
Mean 1315
RMS 169.4

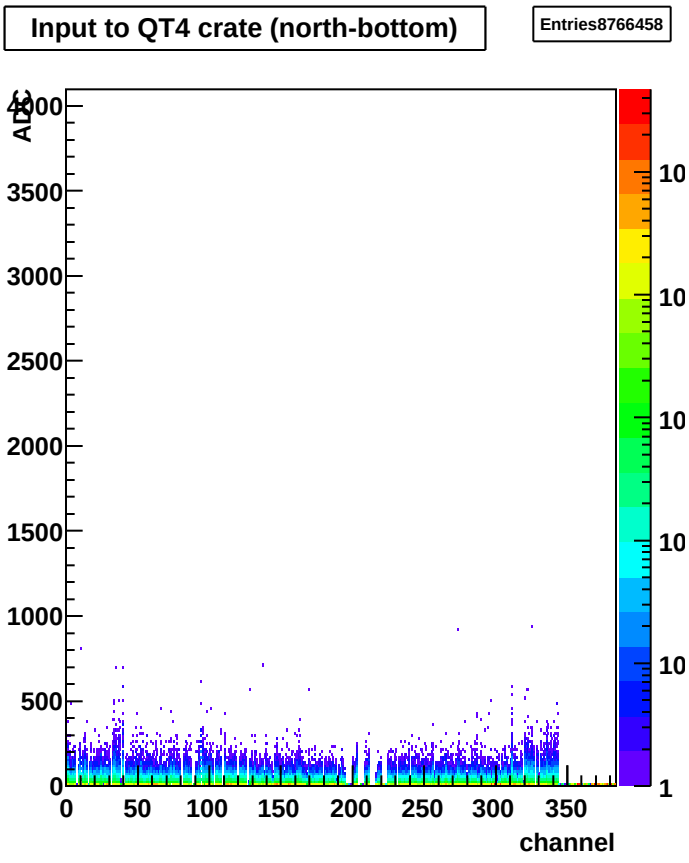

VPD-TAC-W
Entries 462746
Mean 1432
RMS 191.9

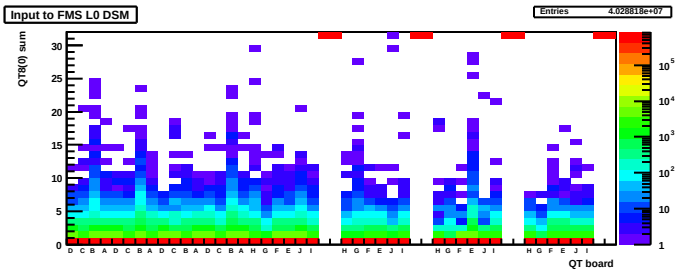

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 261849
Mean 3980
RMS 226.2

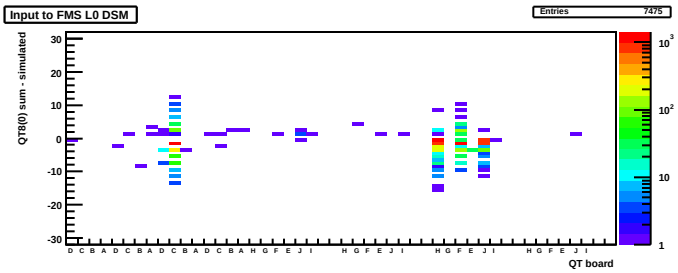

Input to QT1 crate (south-top)
Entries 1.137386e+07
ADC
channel

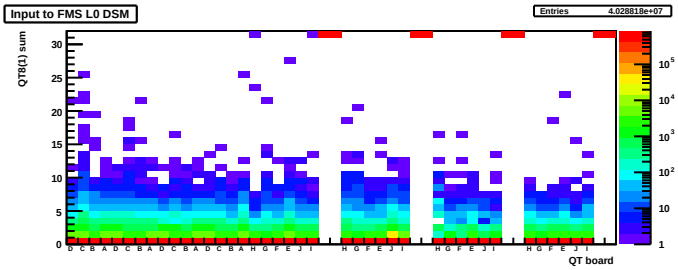

Input to QT3 crate (north-top)
Entries 6112163
ADC
channel

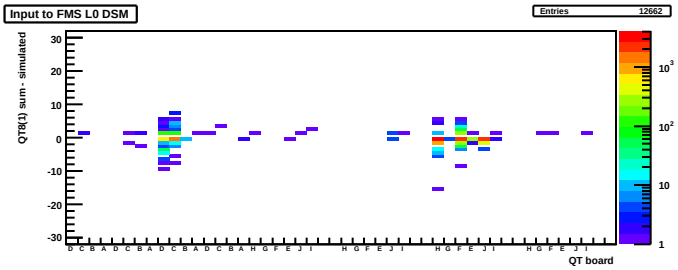

Input to QT2 crate (south-bottom)
Entries 6772133
ADC
channel

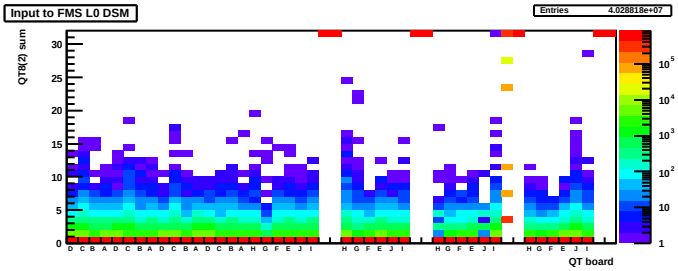

Input to QT4 crate (north-bottom)
Entries 8766458
ADC
channel

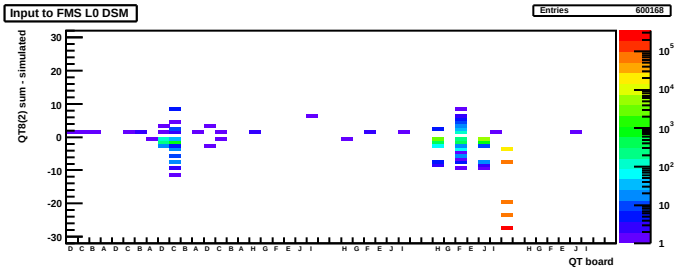

Input to FMS L0 DSM
Entries 4.028818e+07
QT8(0) sum
QT board

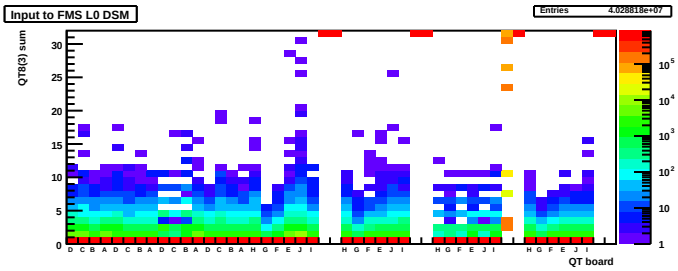

Input to FMS L0 DSM
Entries 7475
QT8(0) sum - simulated
QT board

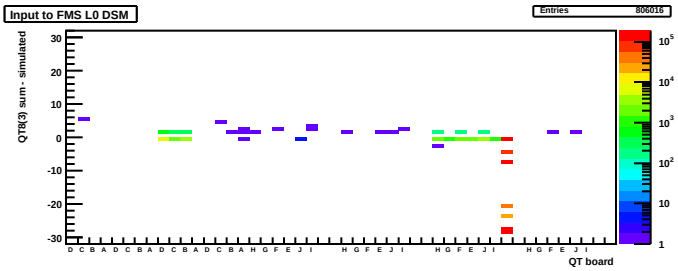

Input to FMS L0 DSM
Entries 4.028818e+07
QT8(1) sum
QT board

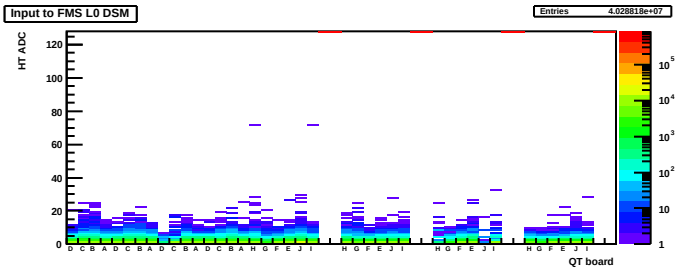

Input to FMS L0 DSM
Entries 12662
QT8(1) sum - simulated
QT board

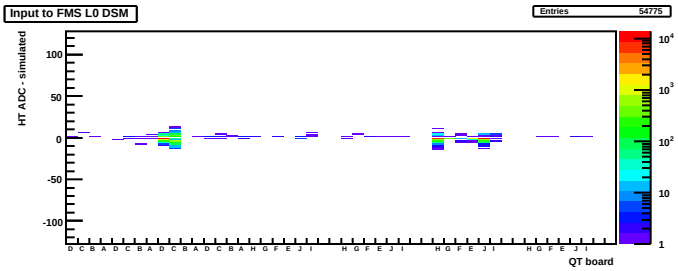

Input to FMS L0 DSM
Entries 4.028818e+07
QT8(2) sum
QT board

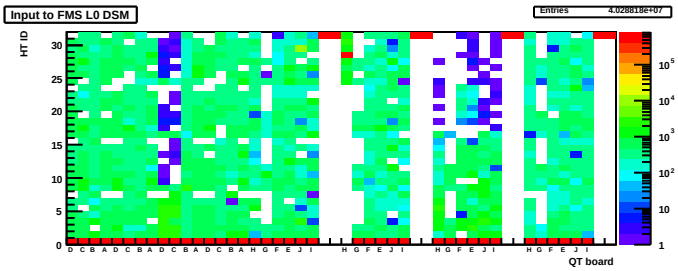

Input to FMS L0 DSM
Entries 600168
QT8(2) sum - simulated
QT board

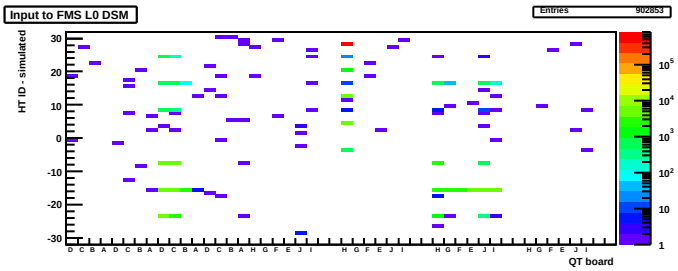

Input to FMS L0 DSM
Entries 4.028818e+07
QT8(3) sum
QT board

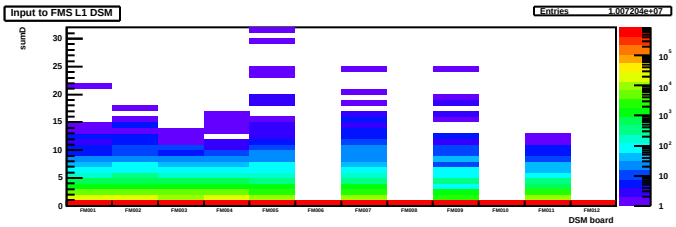

Input to FMS L0 DSM
Entries 806016
QT8(3) sum - simulated
QT board

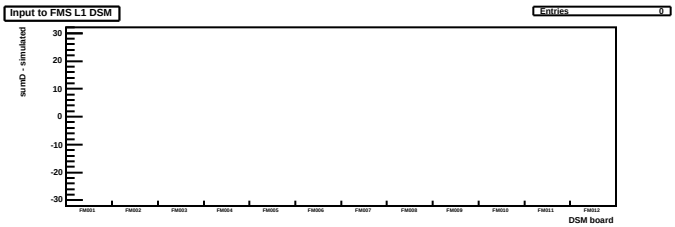

Input to FMS L0 DSM
Entries 4.028818e+07
HT ADC
QT board

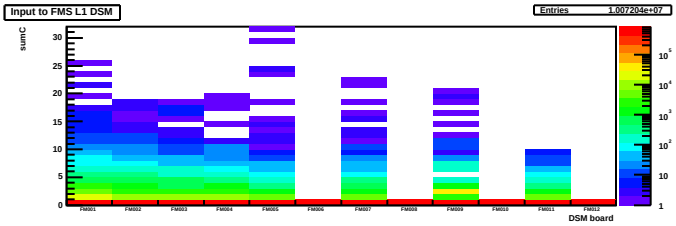

Input to FMS L0 DSM
Entries 54775
HT ADC - simulated
QT board

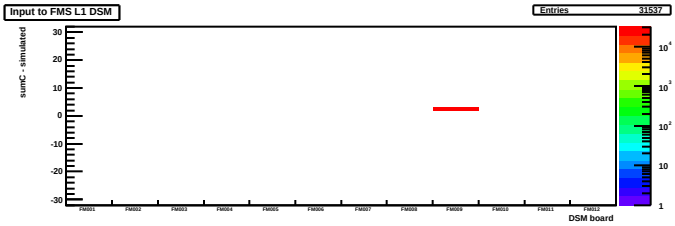

Input to FMS L0 DSM
Entries 4.028818e+07
HT ID
QT board

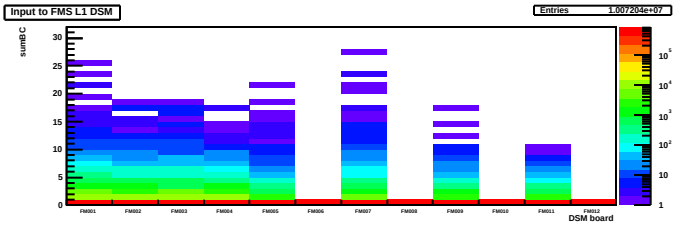

Input to FMS L0 DSM
Entries 902853
HT ID - simulated
QT board

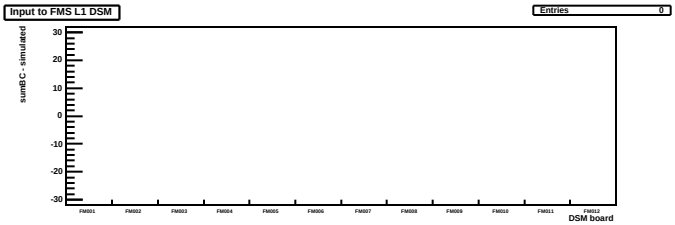

Input to FMS L1 DSM
Entries 1.007204e+07
sumD
DSM board

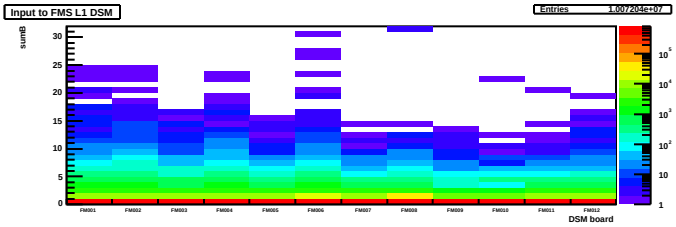

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

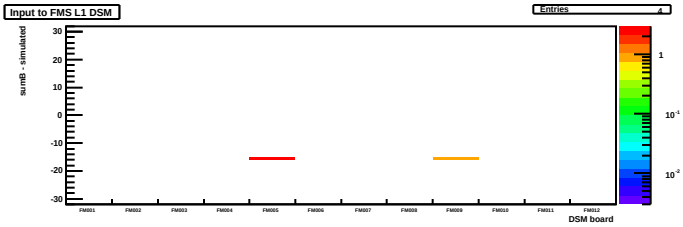

Input to FMS L1 DSM
Entries 1.007204e+07
sumC
DSM board

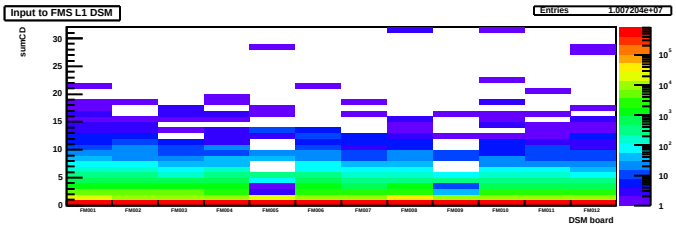

Input to FMS L1 DSM
Entries 31537
sumC - simulated
DSM board

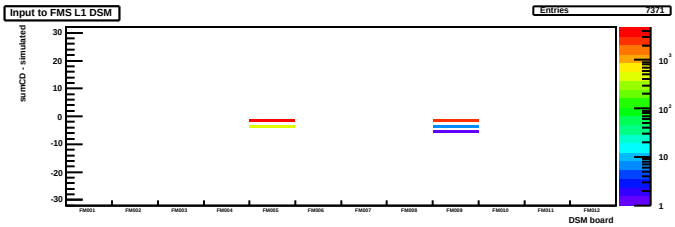

Input to FMS L1 DSM
Entries 1.007204e+07
sumBC
DSM board

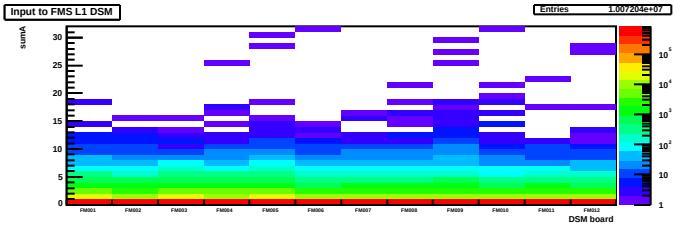

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

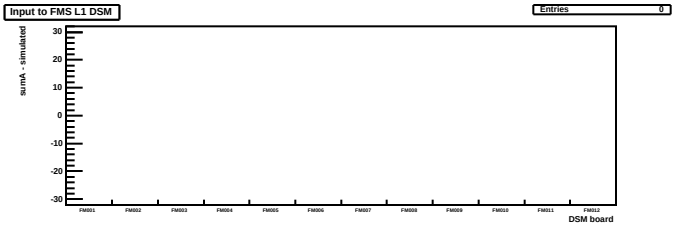

Input to FMS L1 DSM
Entries 1.007204e+07
sumB
DSM board

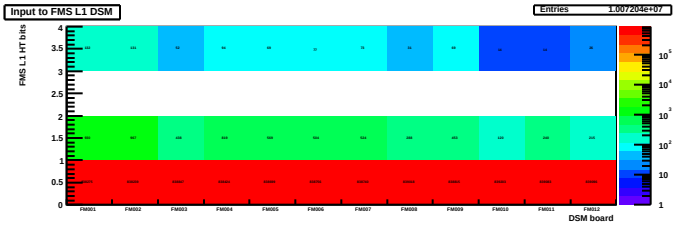

Input to FMS L1 DSM
Entries 4
sumB - simulated
DSM board

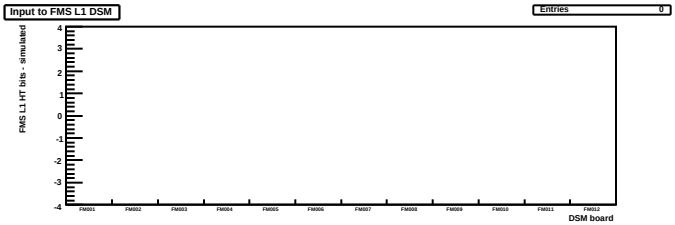

Input to FMS L1 DSM
Entries 1.007204e+07
sumCD
DSM board

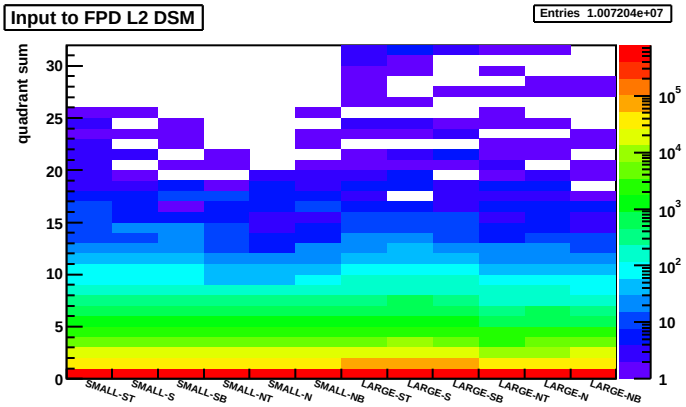

Input to FMS L1 DSM
Entries 7371
sumCD - simulated
DSM board

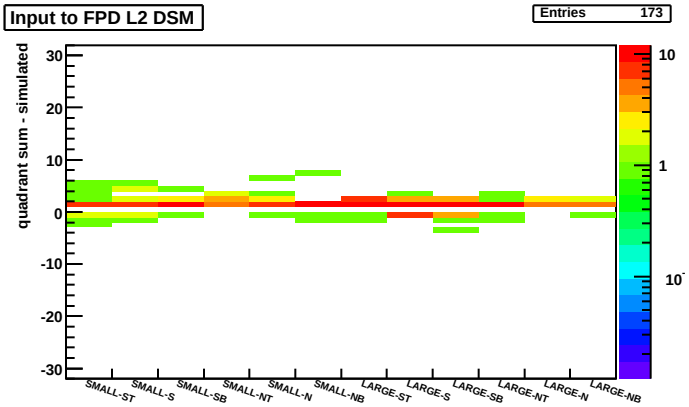

Input to FMS L1 DSM
Entries 1.007204e+07
sumA
DSM board

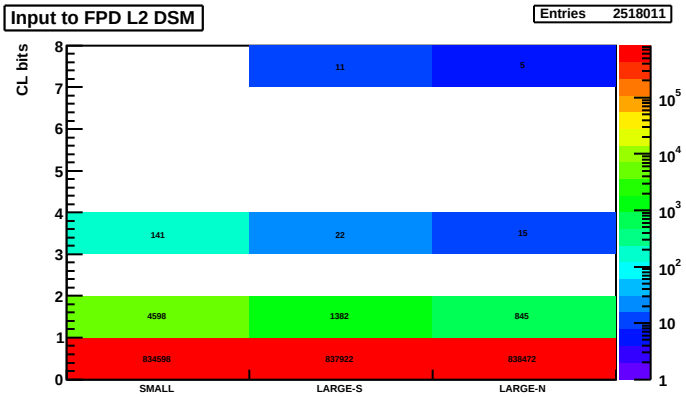

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

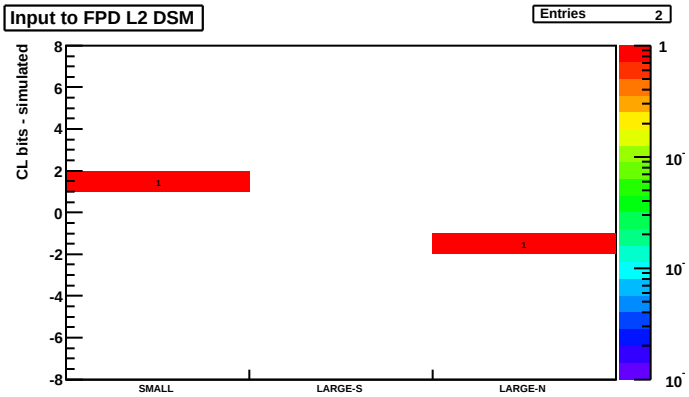

Input to FMS L1 DSM
Entries 1.007204e+07
FMS L1 HT bits
DSM board

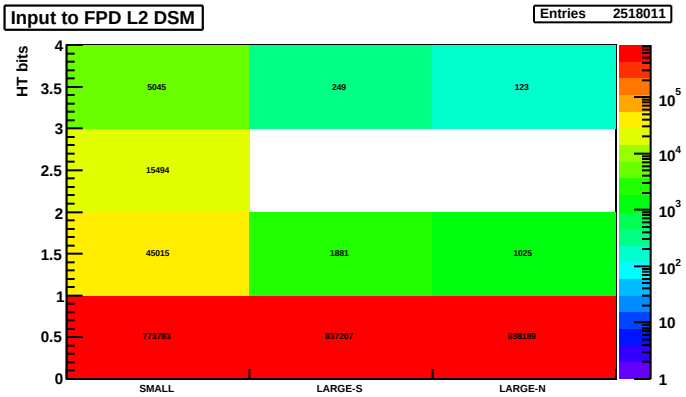

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

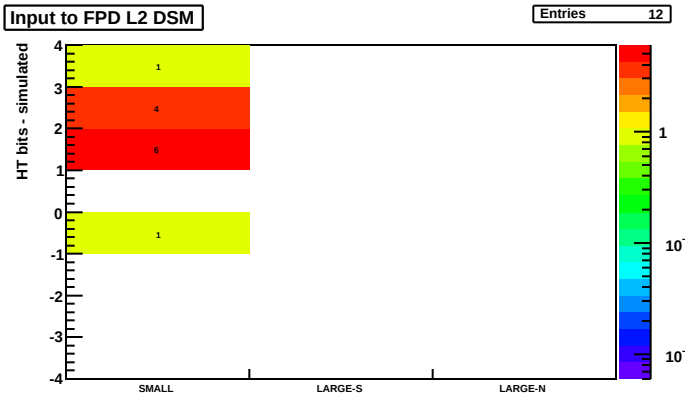

Input to FPD L2 DSM
Entries 1.007204e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

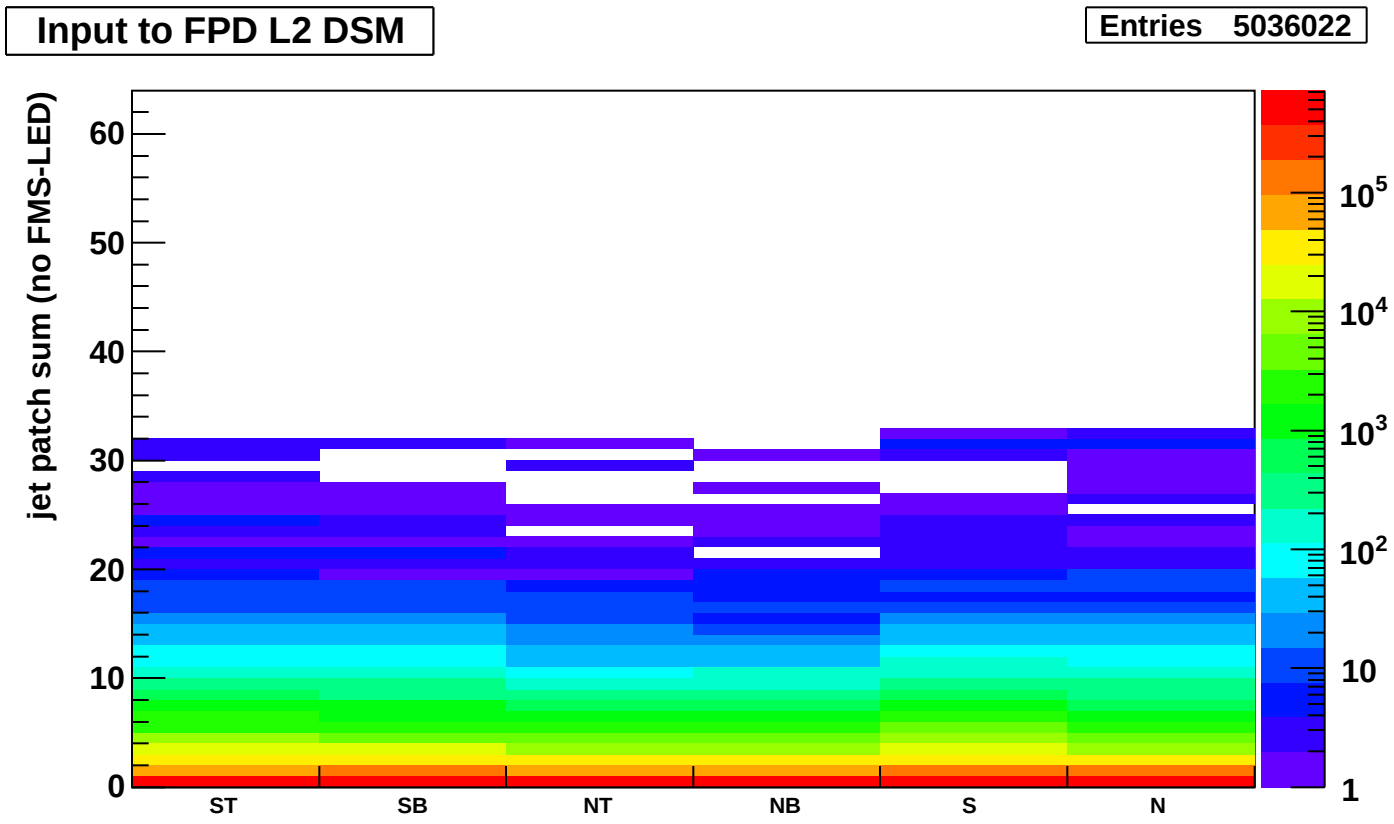

Input to FPD L2 DSM
Entries 173
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

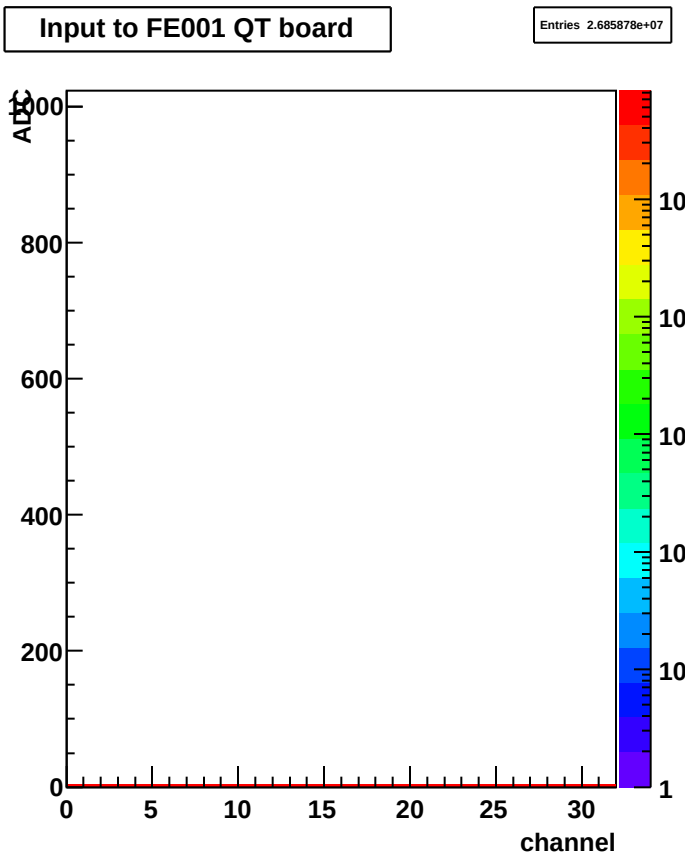

Input to FPD L2 DSM
Entries 2518011
CL bits
11
5
141
22
15
4598
1382
845
834598
837922
838472
SMALL
LARGE-S
LARGE-N

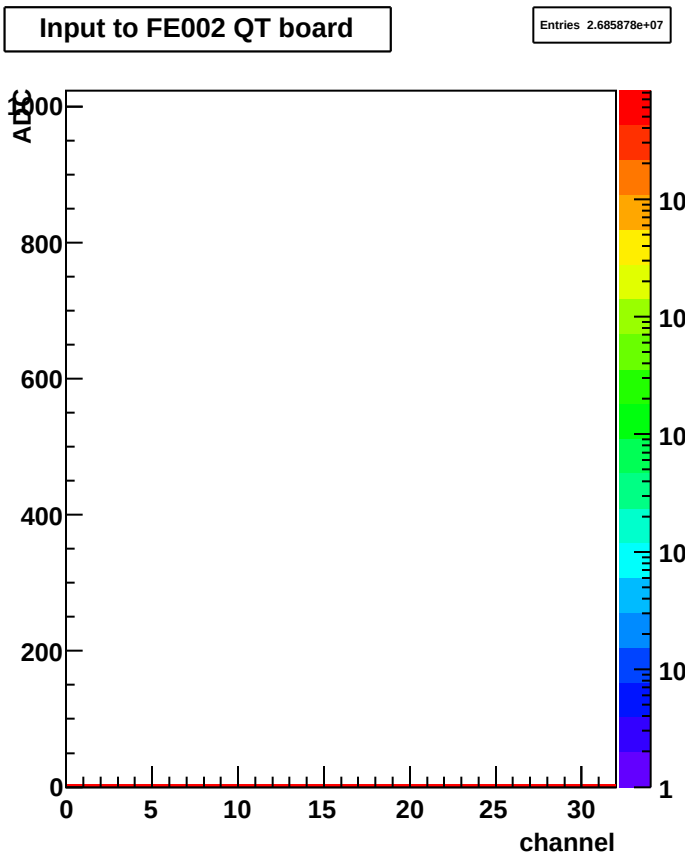

Input to FPD L2 DSM
Entries 2
CL bits - simulated
1
1
SMALL
LARGE-S
LARGE-N

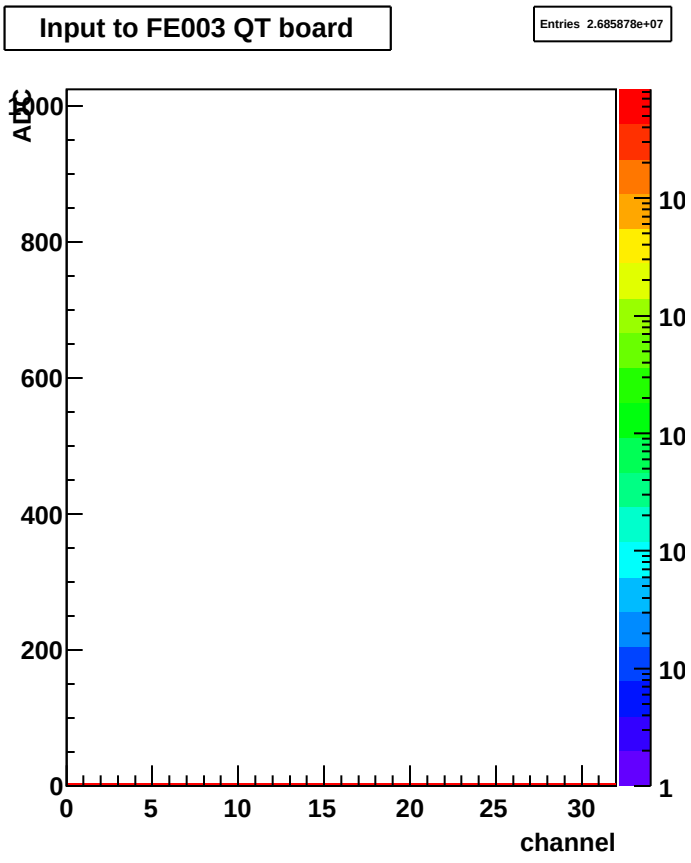

Input to FPD L2 DSM
Entries 2518011
HT bits
5045
249
123
15494
45015
1881
1025
773783
837207
838189
SMALL
LARGE-S
LARGE-N

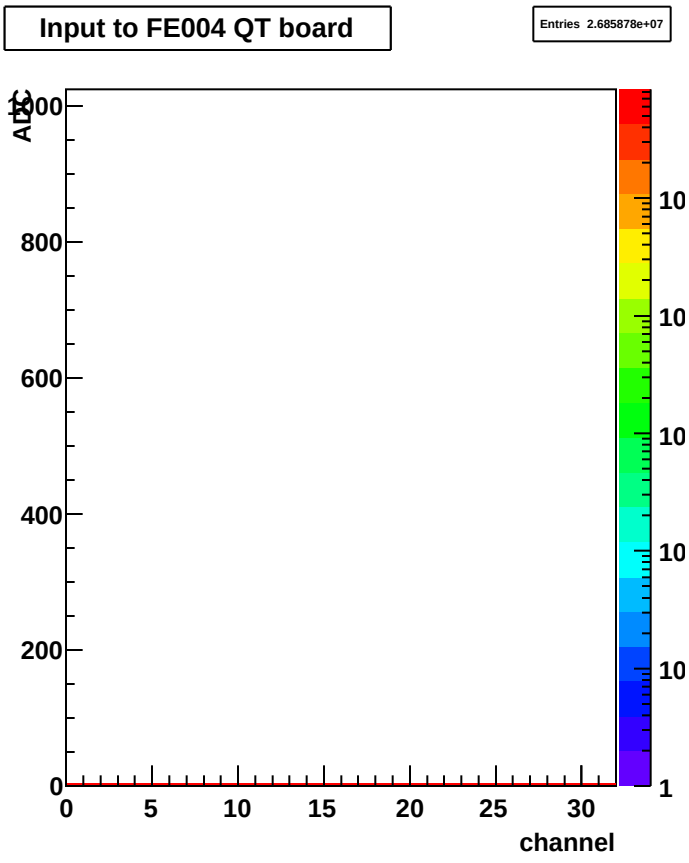

Input to FPD L2 DSM
Entries 12
HT bits - simulated
1
4
6
1
SMALL
LARGE-S
LARGE-N

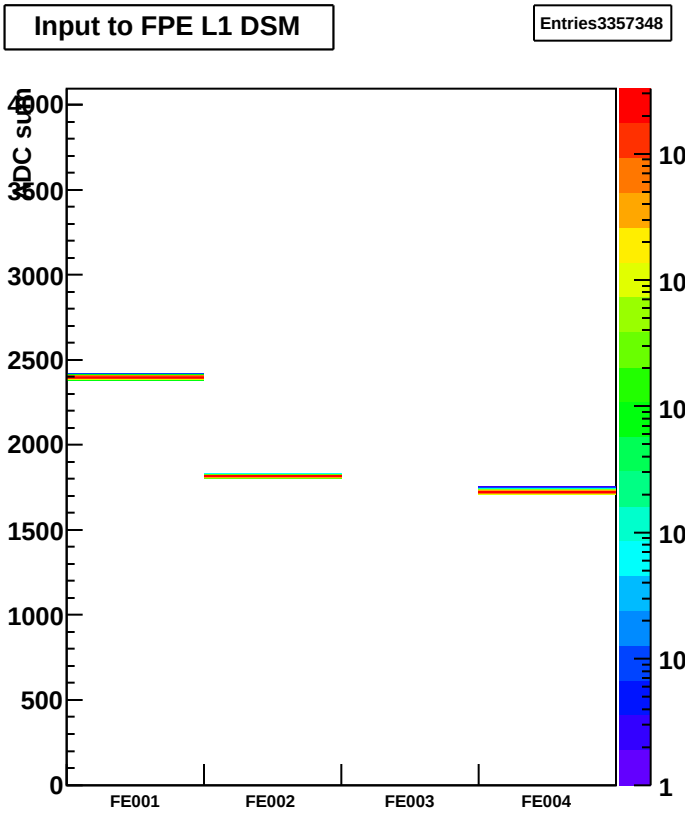

Input to FPD L2 DSM
Entries 5036022
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

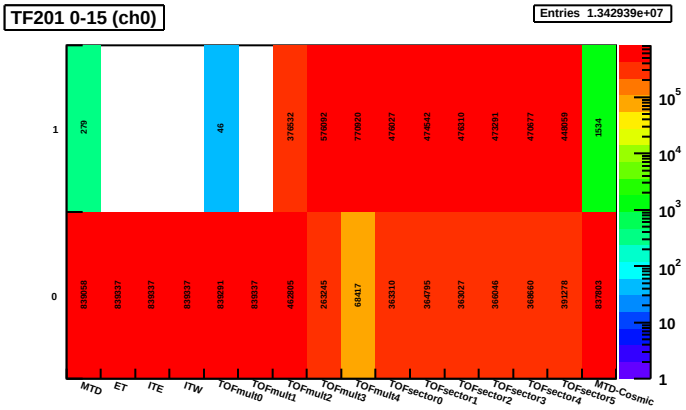

Input to FE001 QT board
Entries 2.685878e+07
ADC
channel

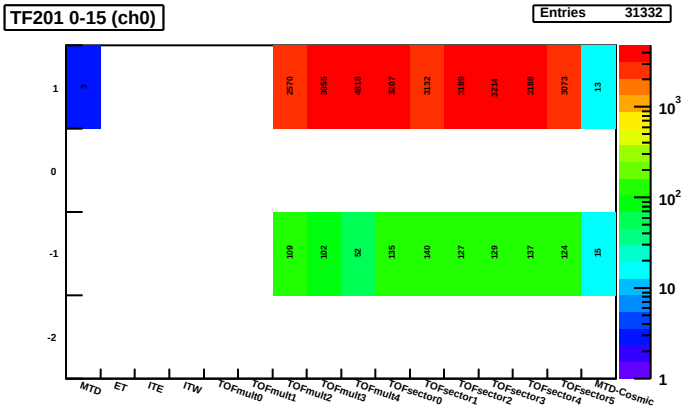

Input to FE002 QT board
Entries 2.685878e+07
ADC
channel

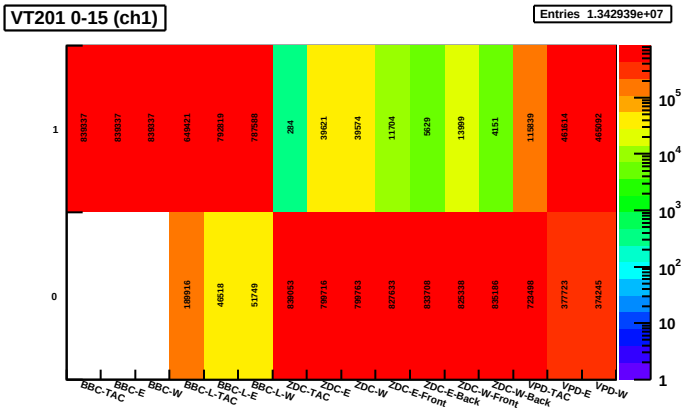

Input to FE003 QT board
Entries 2.685878e+07
ADC
channel

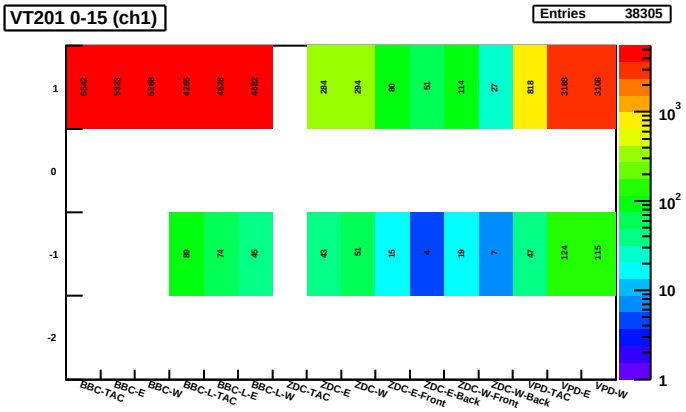

Input to FE004 QT board
Entries 2.685878e+07
ADC
channel

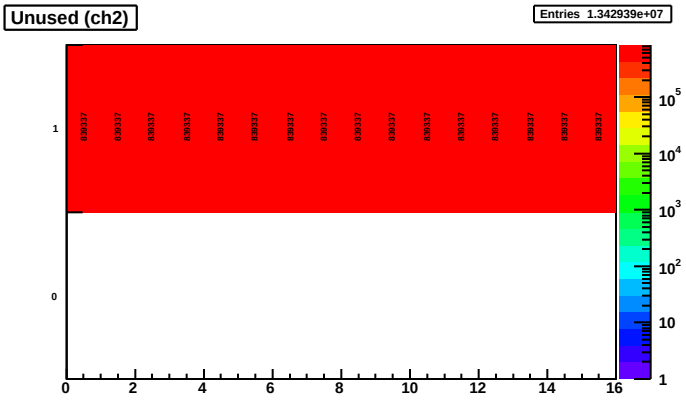

Input to FPE L1 DSM
Entries3357348
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.342939e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 31332
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.342939e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 38305
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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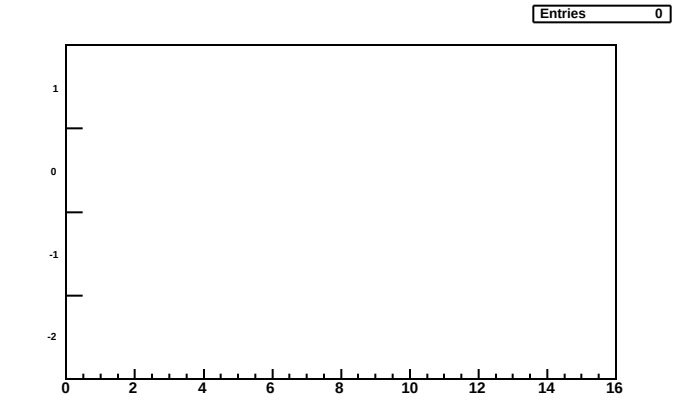
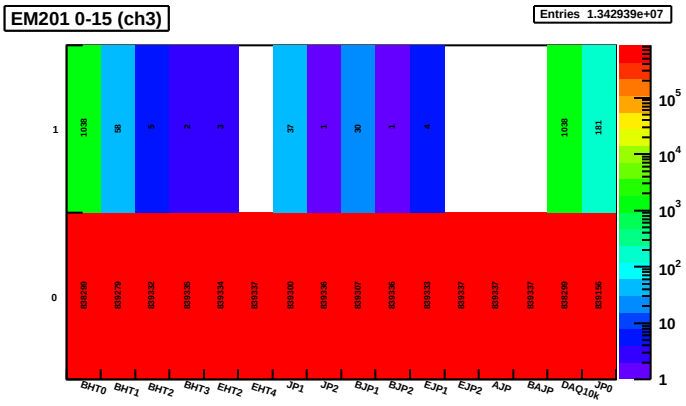

Entries 0


EM201 0-15 (ch3)
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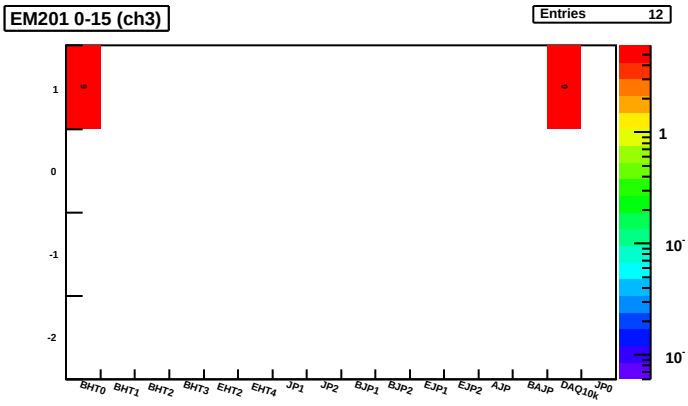
EM201 0-15 (ch3)
Entries 12
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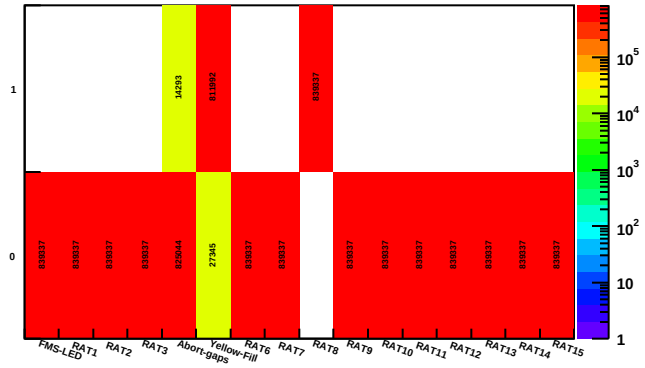
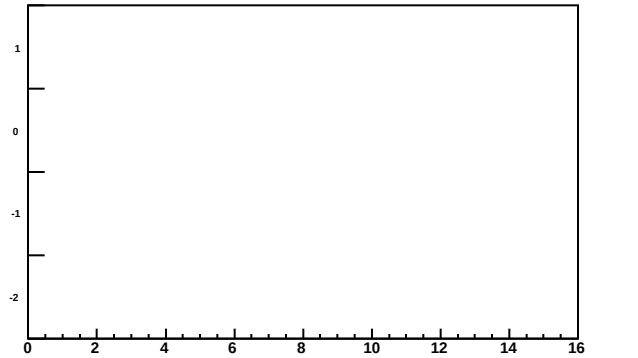
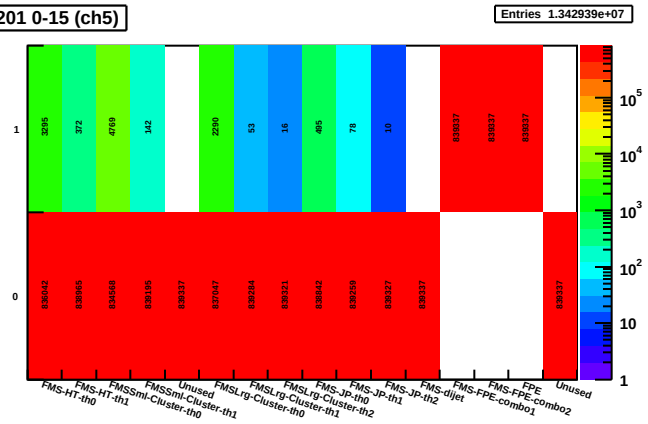
RAT board (ch4)





FP201 0-15 (ch5)



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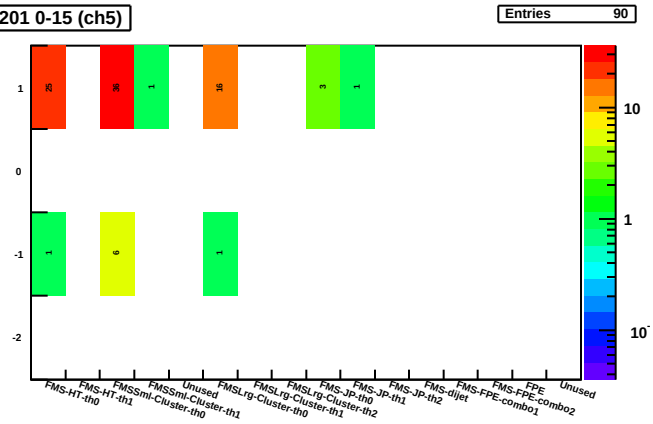
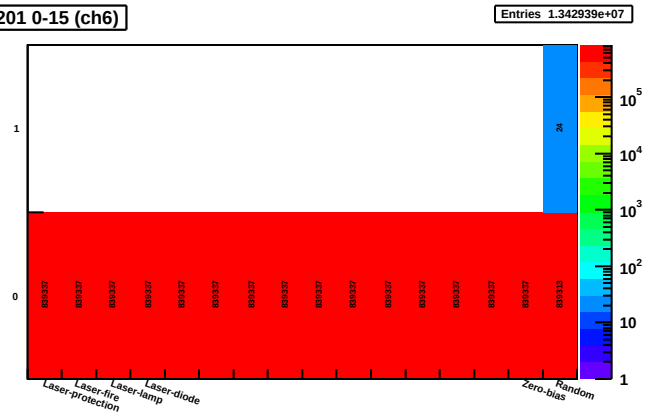
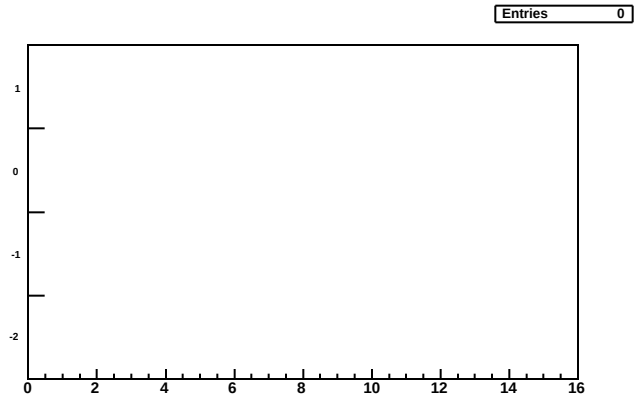
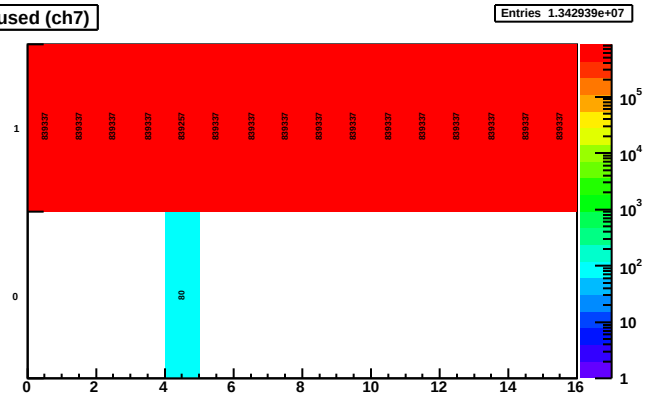
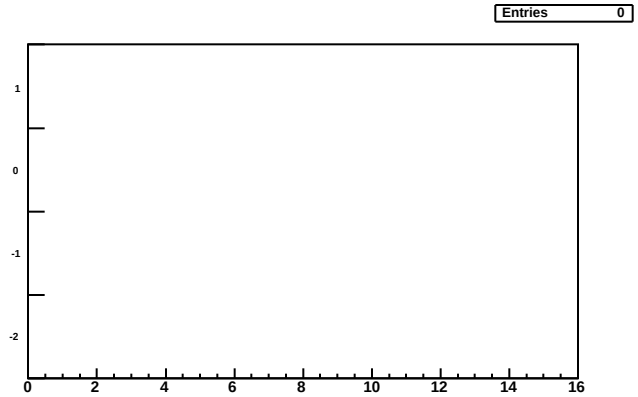


ST201 0-15 (ch6)





Unused (ch7)