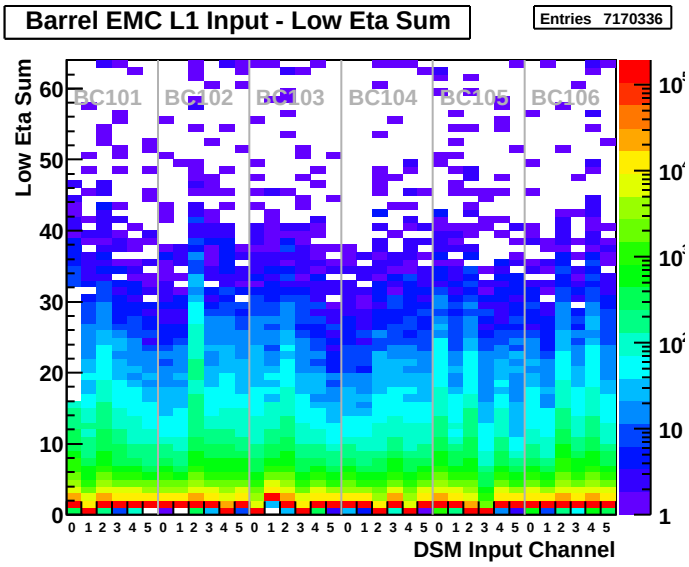
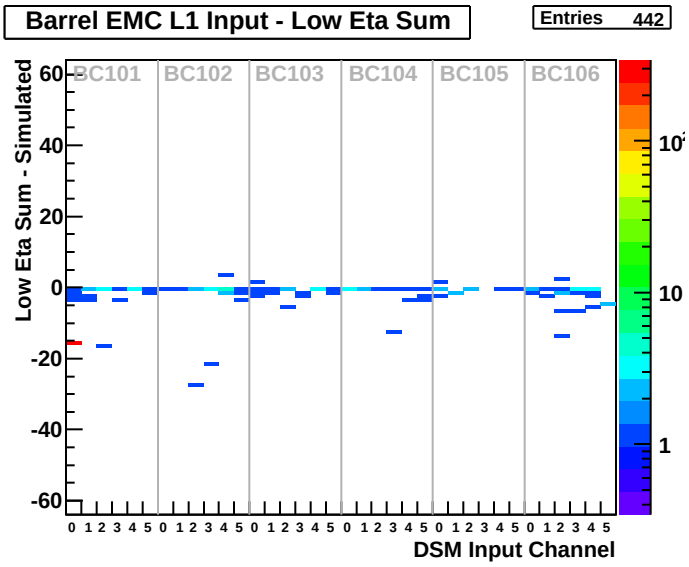
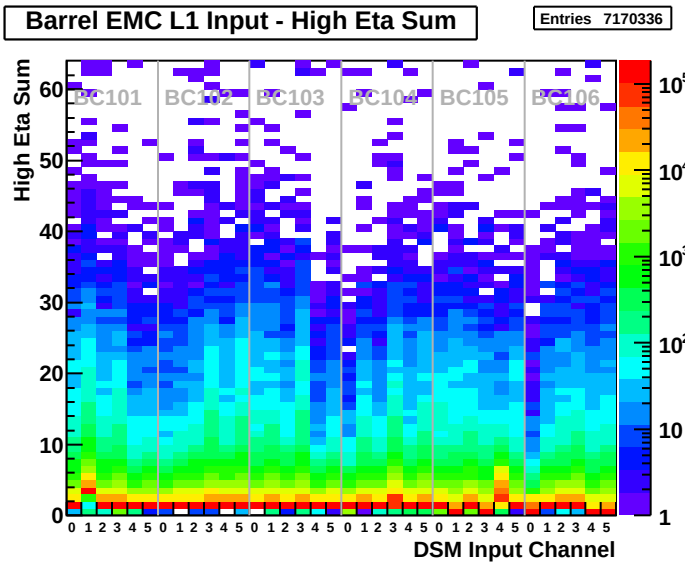
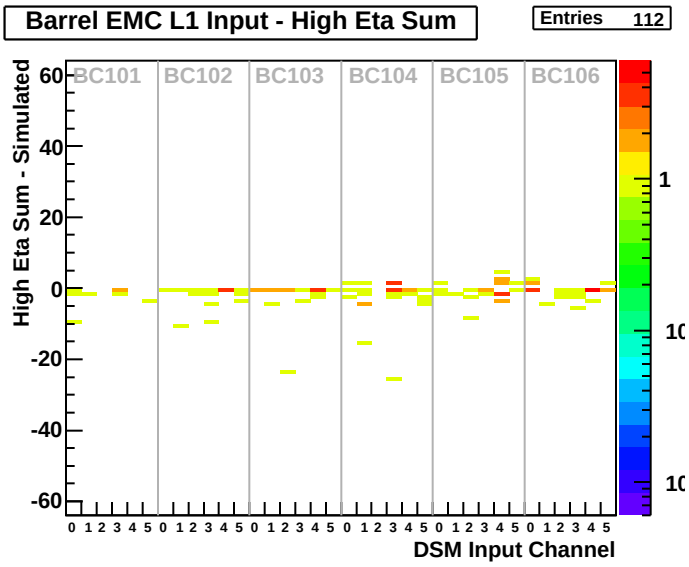
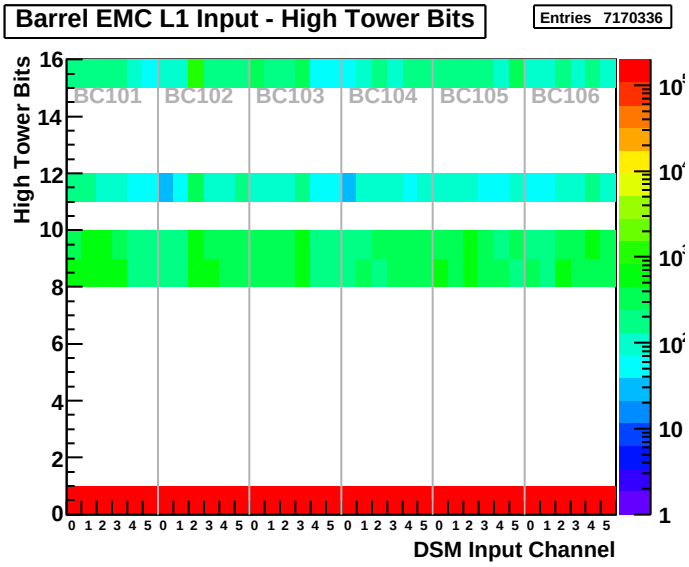
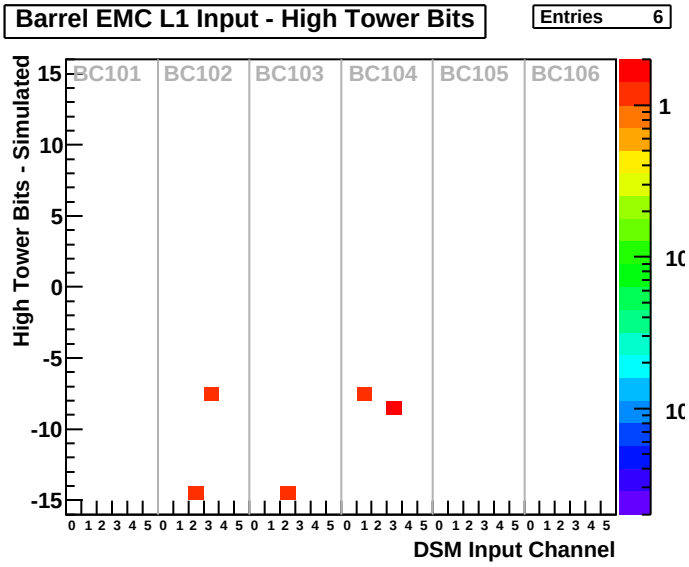

Barrel EMC L1 Input - Low Eta Sum
Entries 7170336
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

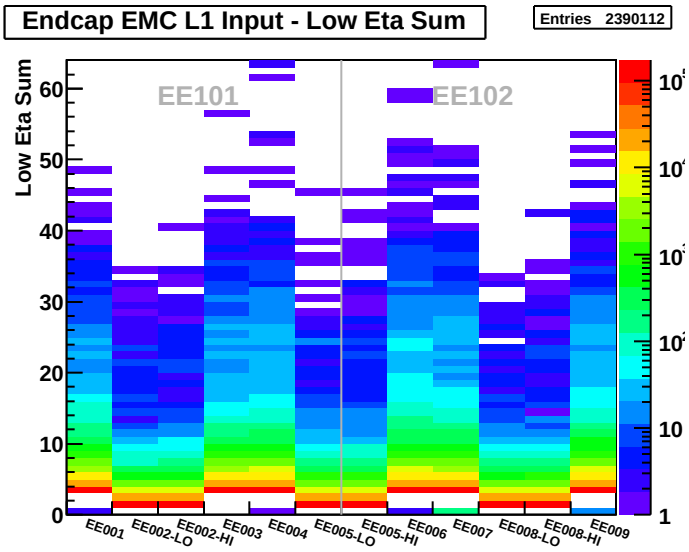

Barrel EMC L1 Input - Low Eta Sum
Entries 442
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

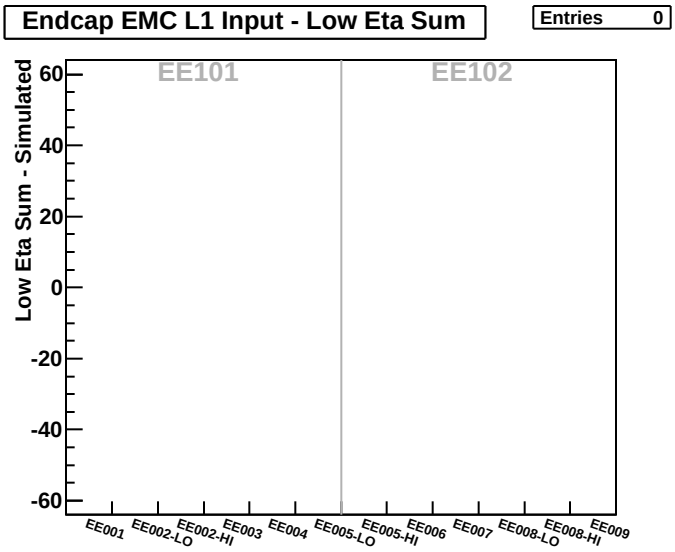

Barrel EMC L1 Input - High Eta Sum
Entries 7170336
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

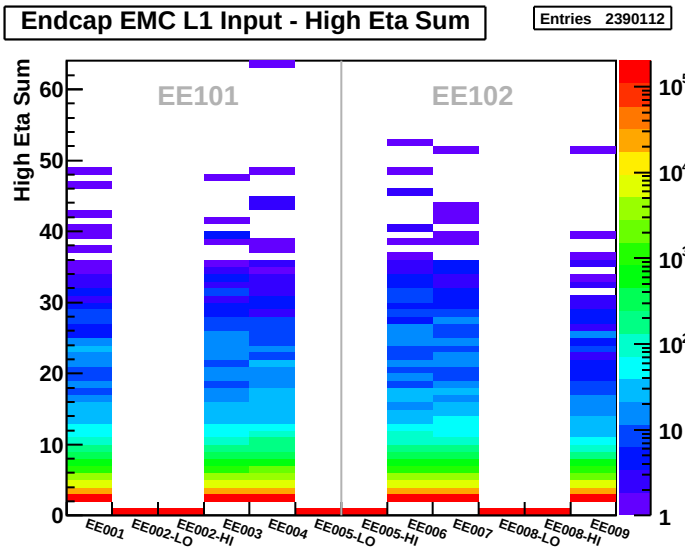

Barrel EMC L1 Input - High Eta Sum
Entries 112
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

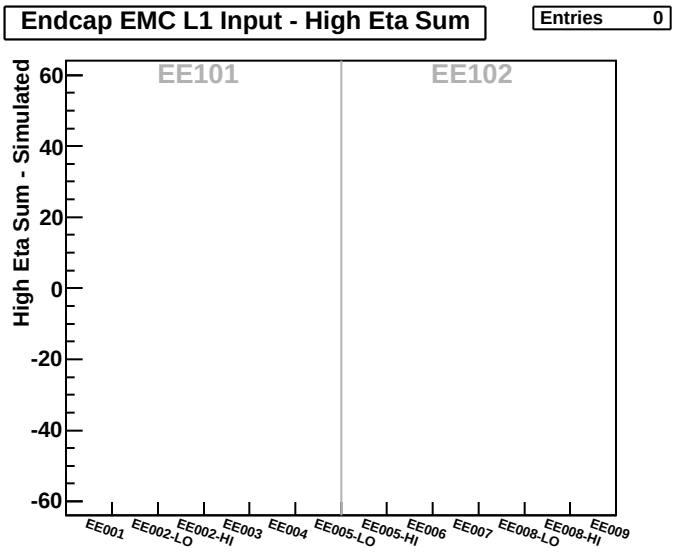

Barrel EMC L1 Input - High Tower Bits
Entries 7170336
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

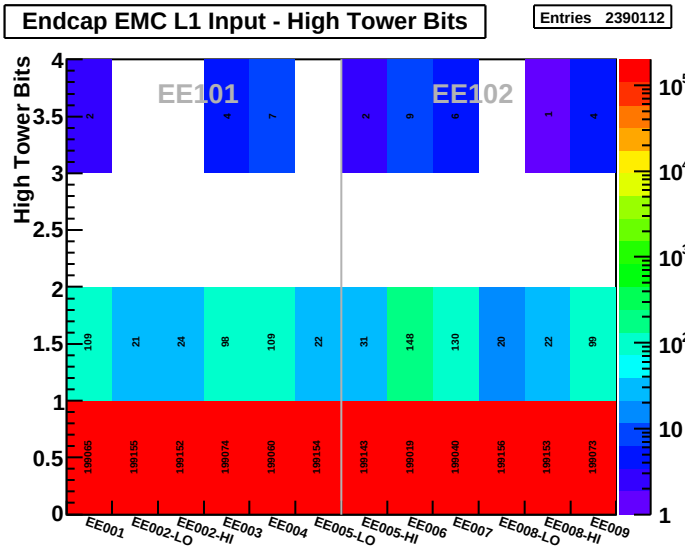

Barrel EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

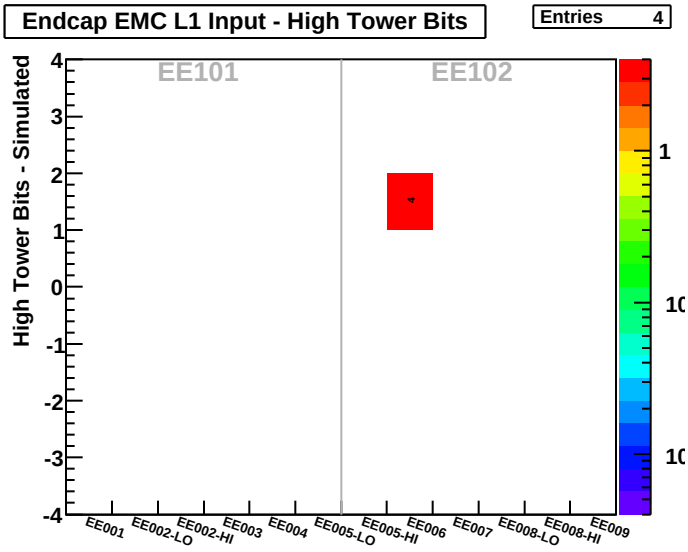

Endcap EMC L1 Input - Low Eta Sum
Entries 2390112
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

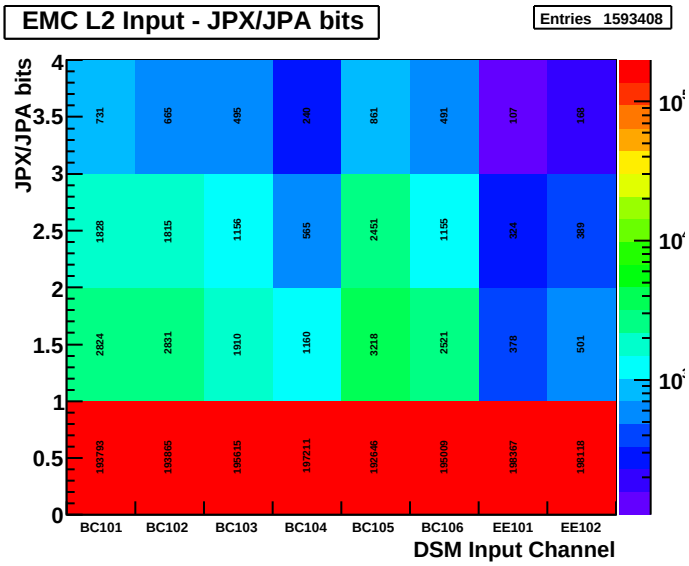

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

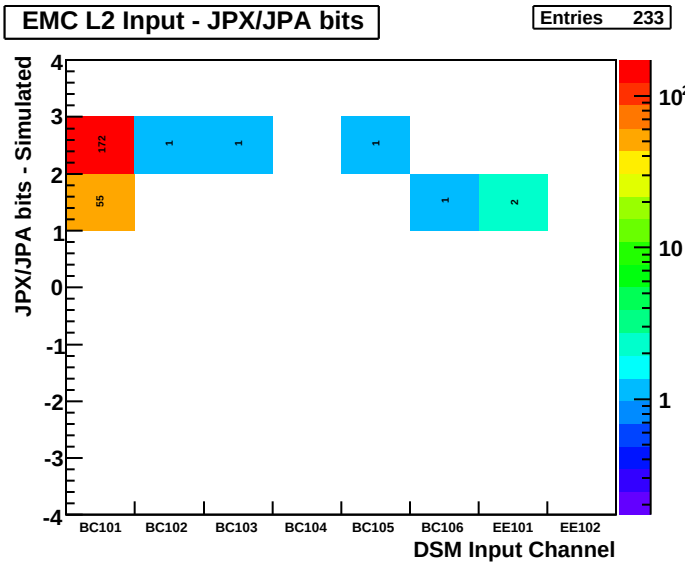

Endcap EMC L1 Input - High Eta Sum
Entries 2390112
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

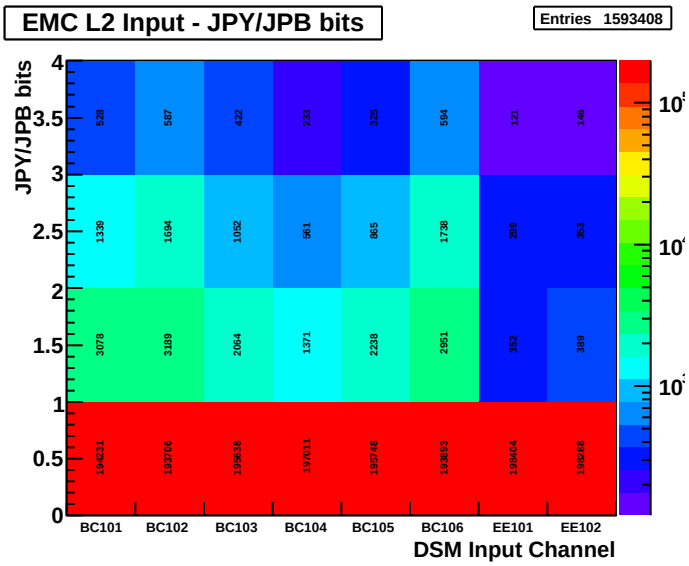

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

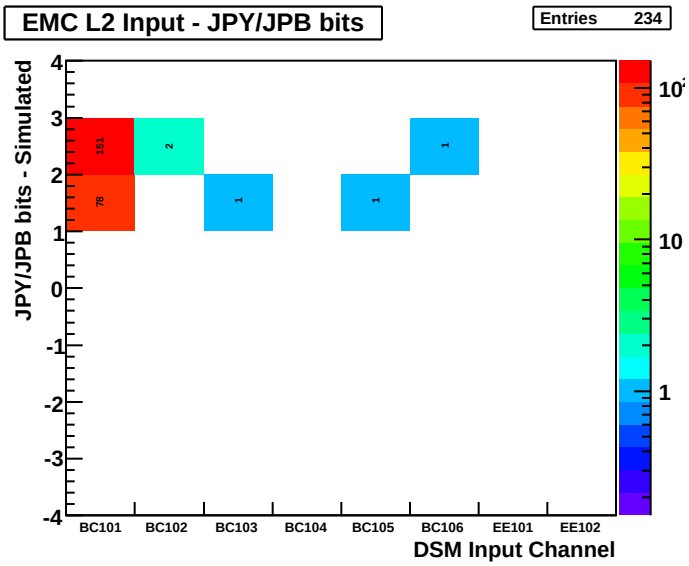

Endcap EMC L1 Input - High Tower Bits
Entries 2390112
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

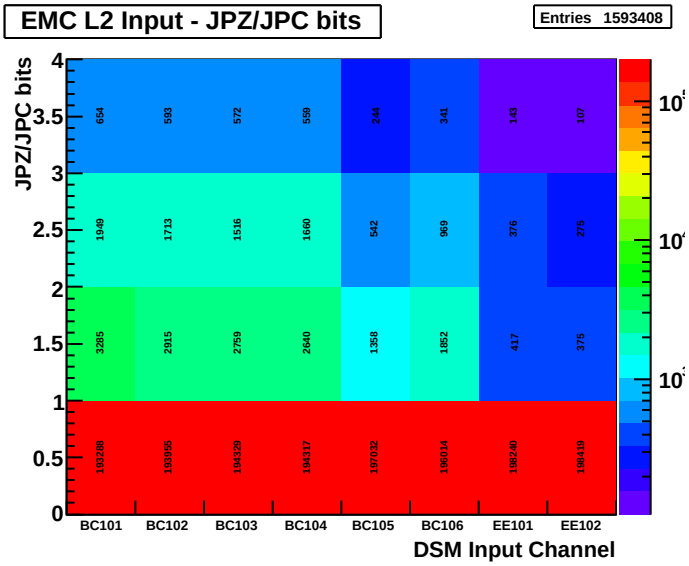

Endcap EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

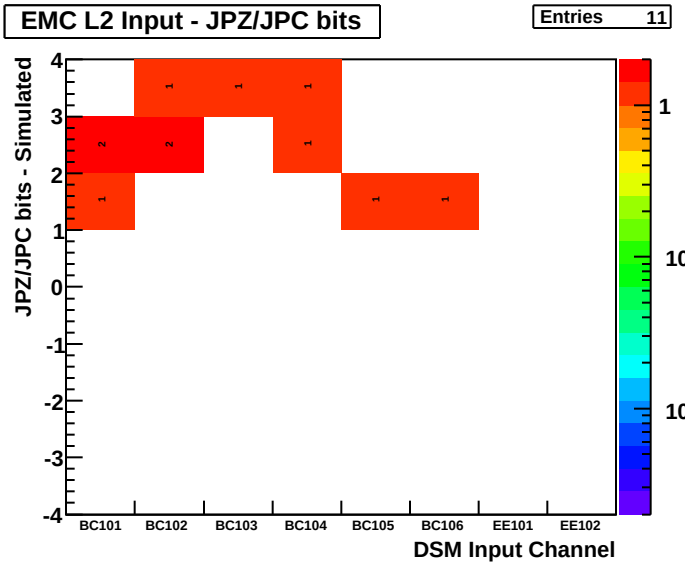

EMC L2 Input - JPX/JPA bits
Entries 1593408
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
731 665 495 240 861 491 107 168
1828 1815 1156 565 2451 1155 324 389
2824 2831 1910 1160 3218 2521 378 501
193793 193865 195615 197211 192646 195009 198367 198118

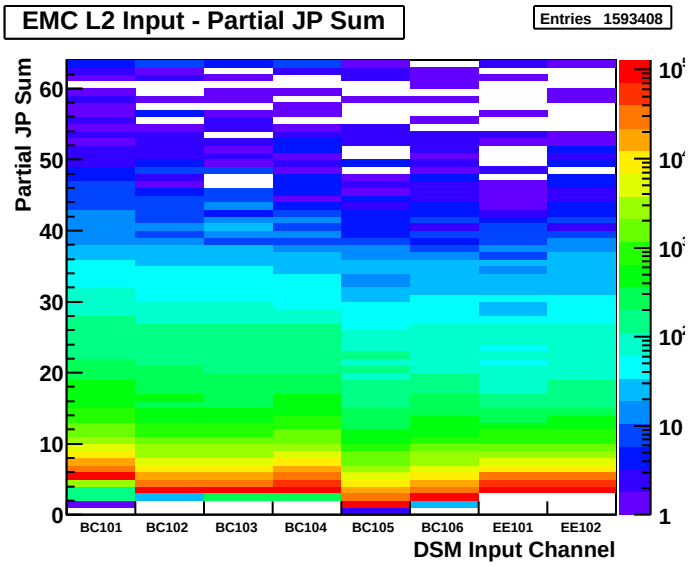

EMC L2 Input - JPX/JPA bits
Entries 233
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
172 1 1 1
55 1 2

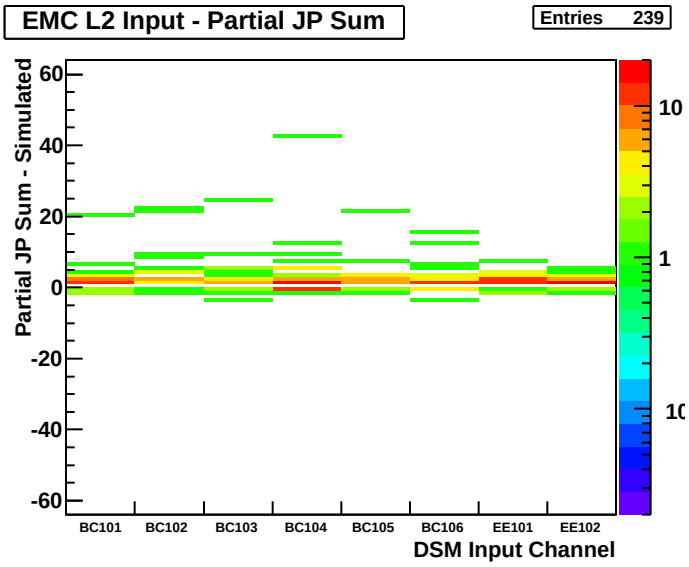

EMC L2 Input - JPY/JPB bits
Entries 1593408
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
528 587 422 233 325 594 121 146
1339 1694 1052 561 865 1738 299 353
3078 3189 2064 1371 2238 2951 352 389
194231 193706 195638 197011 195748 193893 198404 198288

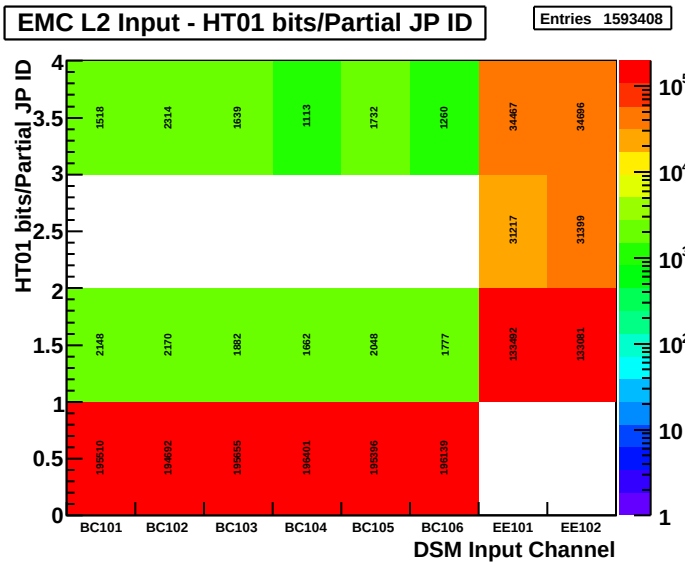

EMC L2 Input - JPY/JPB bits
Entries 234
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
151 2 1
78 1 1

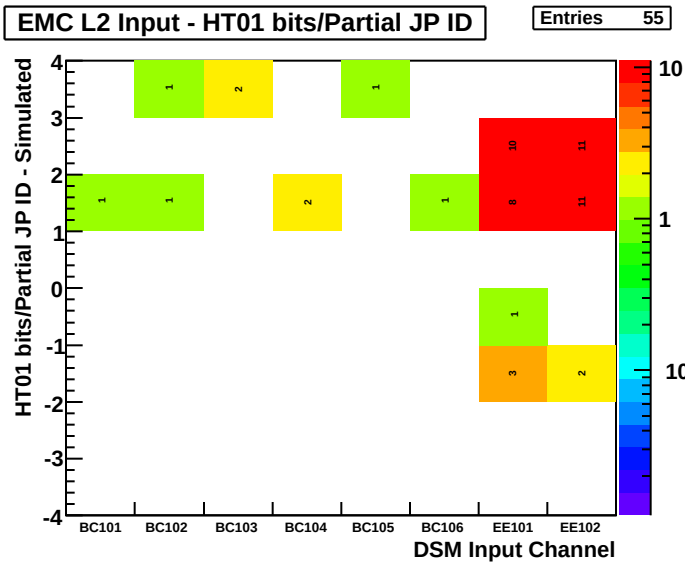

EMC L2 Input - JPZ/JPC bits
Entries 1593408
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
654 593 572 559 244 341 143 107
1949 1713 1516 1660 542 969 376 275
3285 2915 2759 2640 1358 1852 417 375
193288 193955 194239 194317 197032 196014 198240 198419

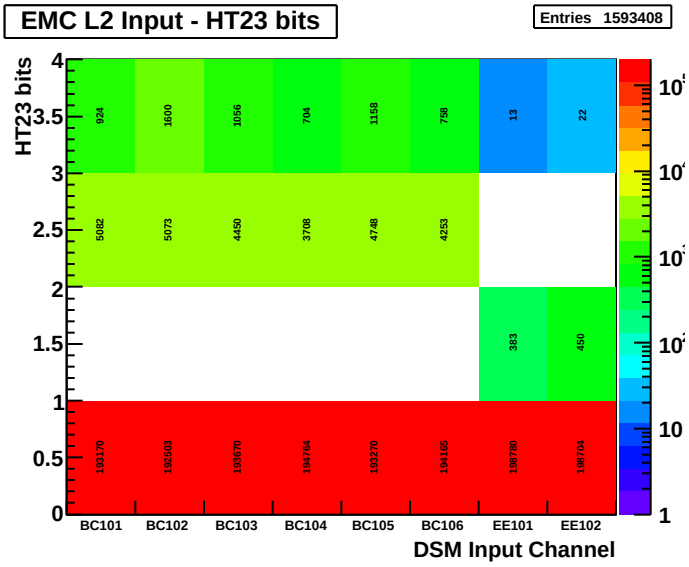

EMC L2 Input - JPZ/JPC bits
Entries 11
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
2 2 1
1 1

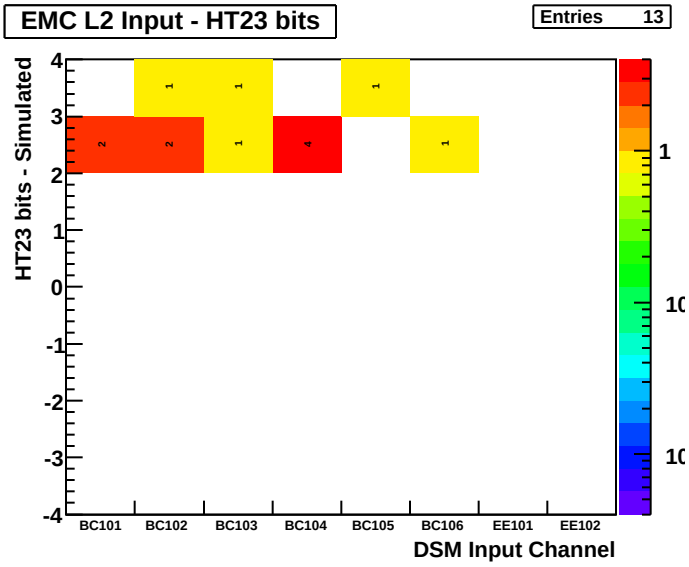

EMC L2 Input - Partial JP Sum
Entries 1593408
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

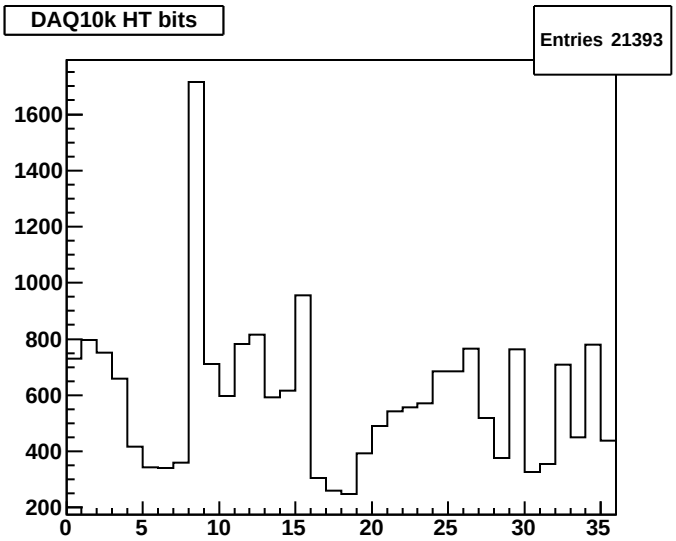

EMC L2 Input - Partial JP Sum
Entries 239
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

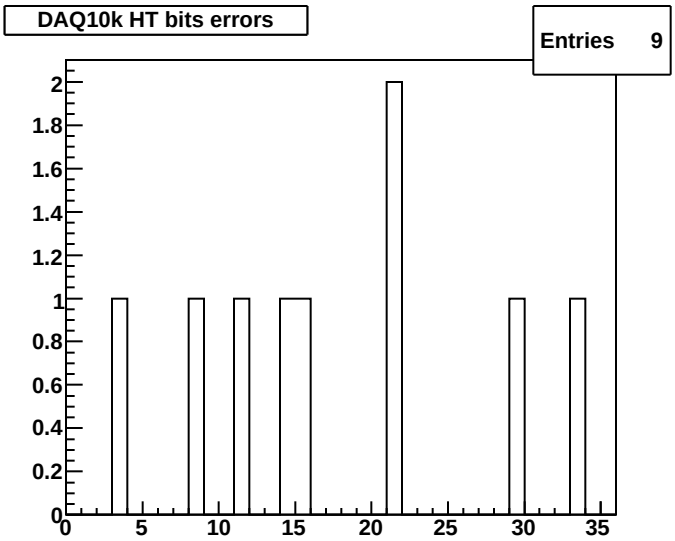

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1593408
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

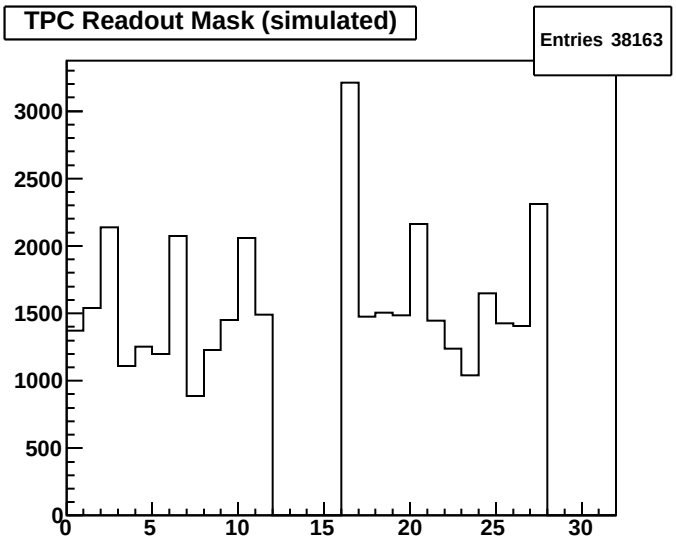

EMC L2 Input - HT01 bits/Partial JP ID
Entries 55
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

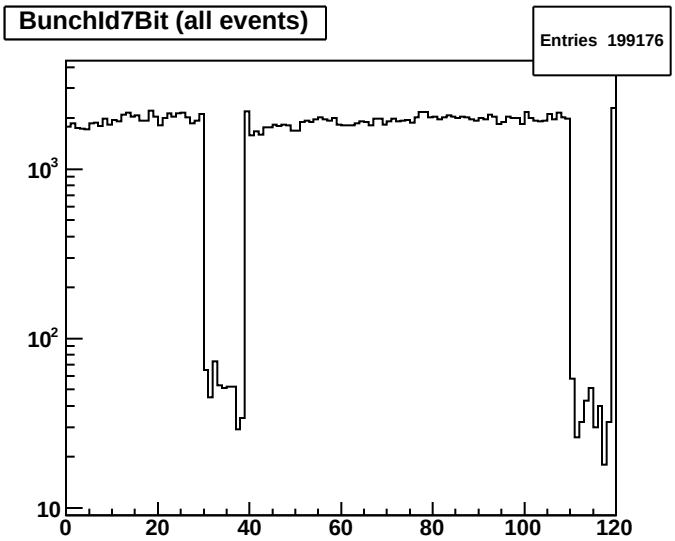

EMC L2 Input - HT23 bits
Entries 1593408
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

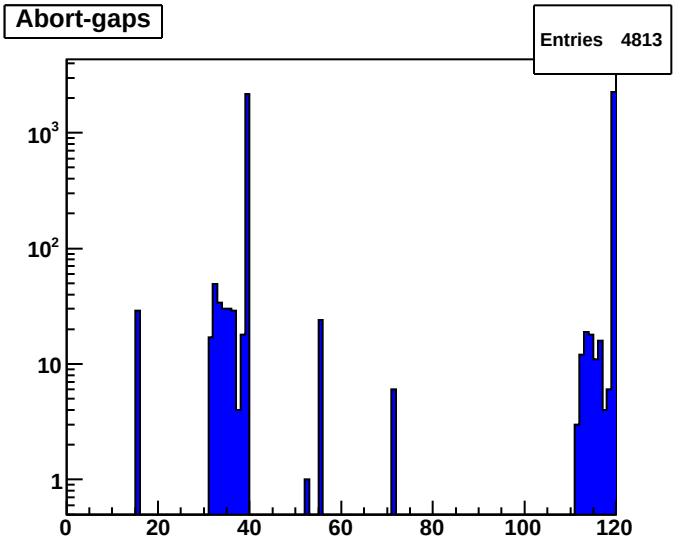

EMC L2 Input - HT23 bits
Entries 13
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

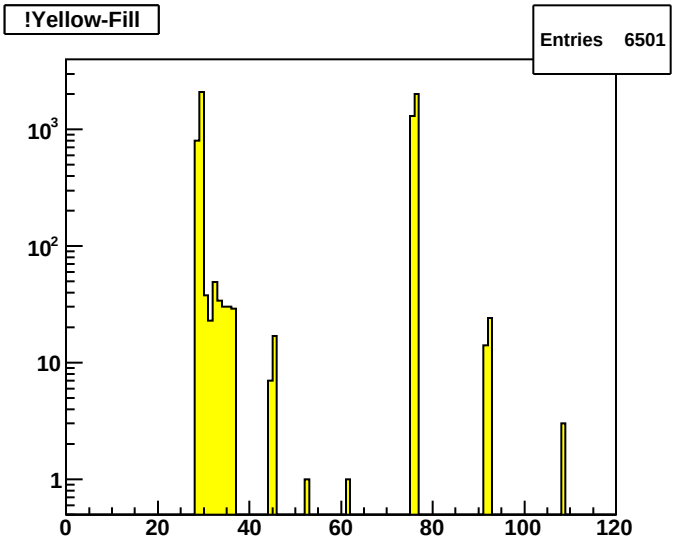

DAQ10k HT bits
Entries 21393

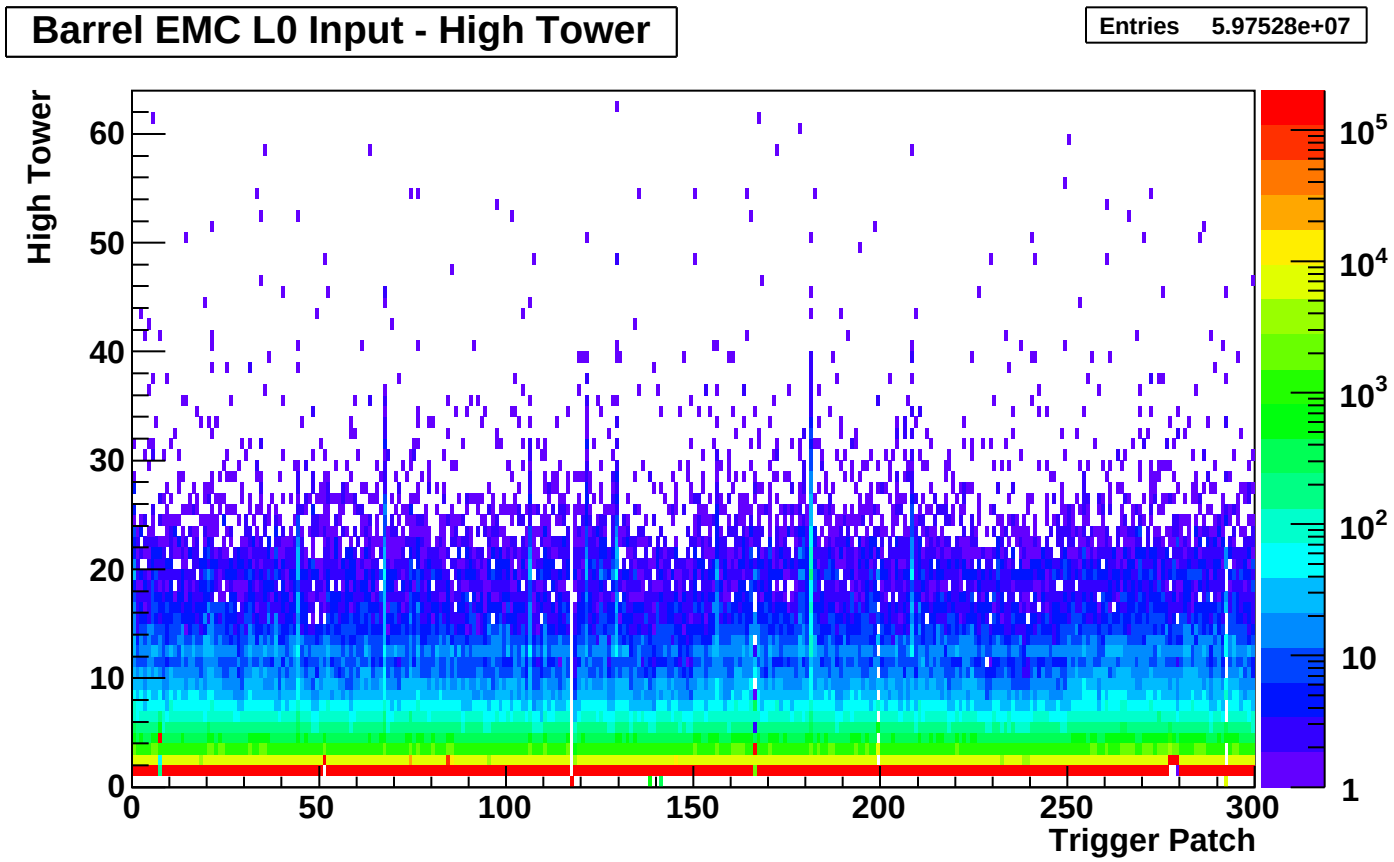

DAQ10k HT bits errors
Entries 9

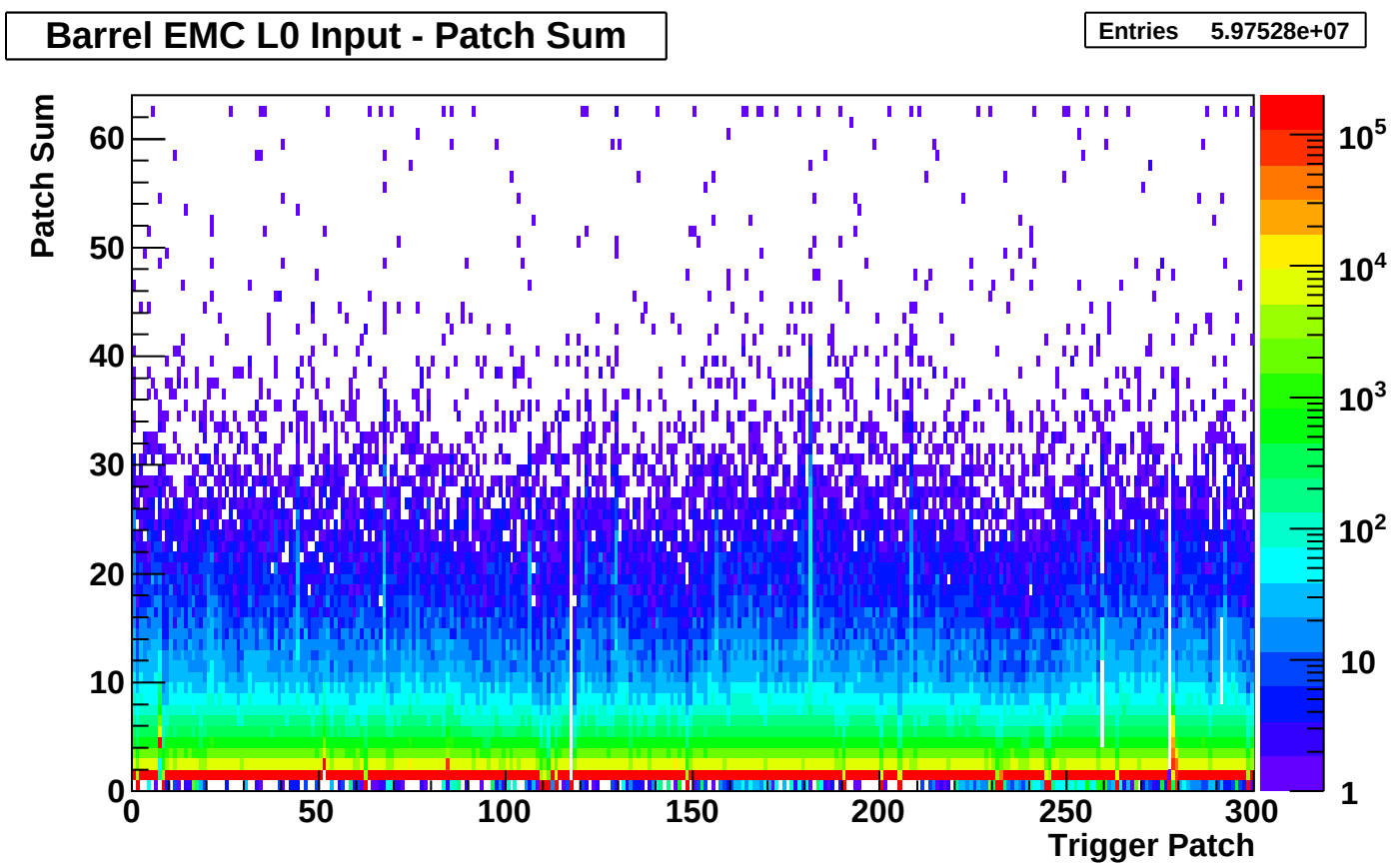

TPC Readout Mask (simulated)
Entries 38163

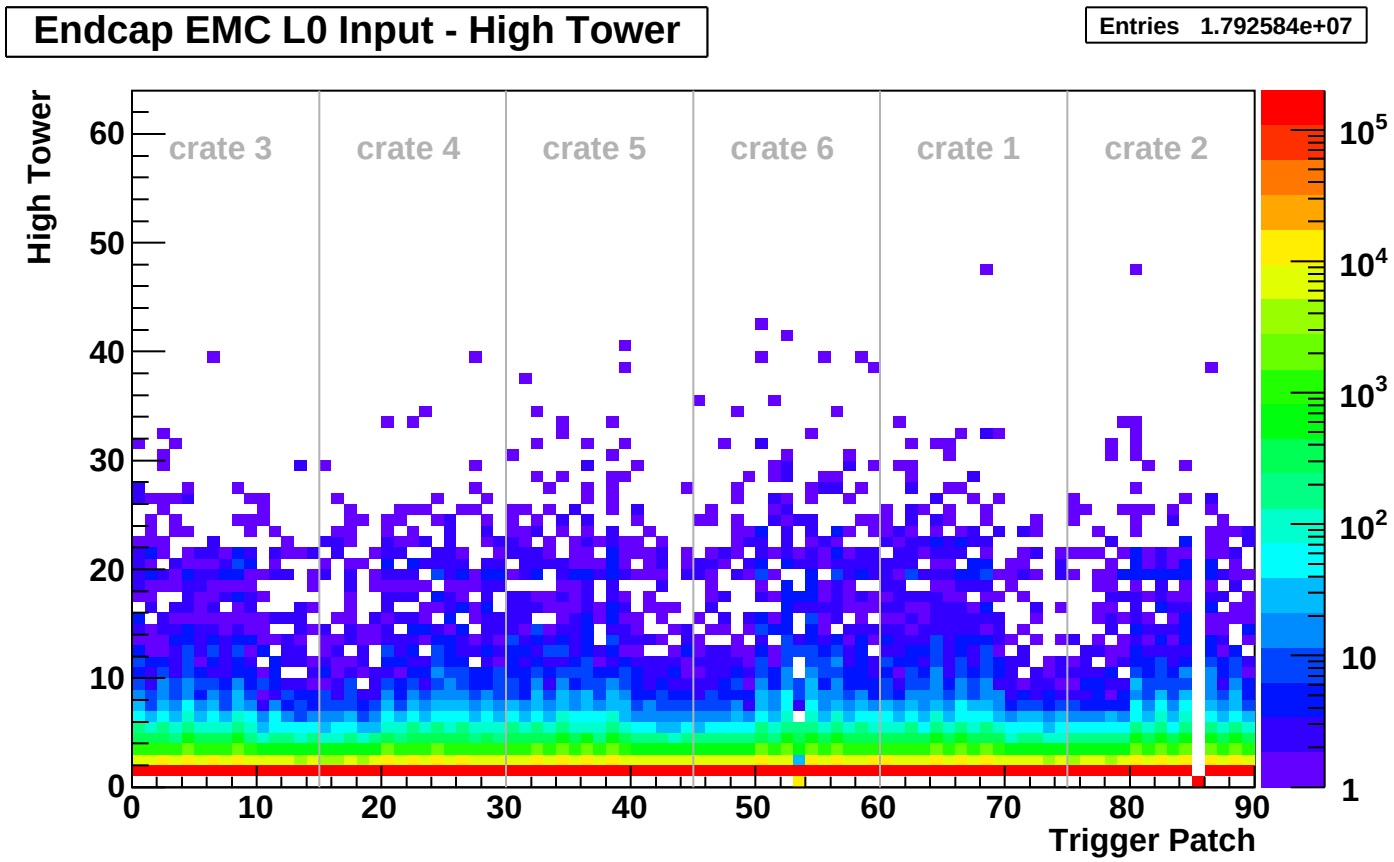

BunchId7Bit (all events)
Entries 199176

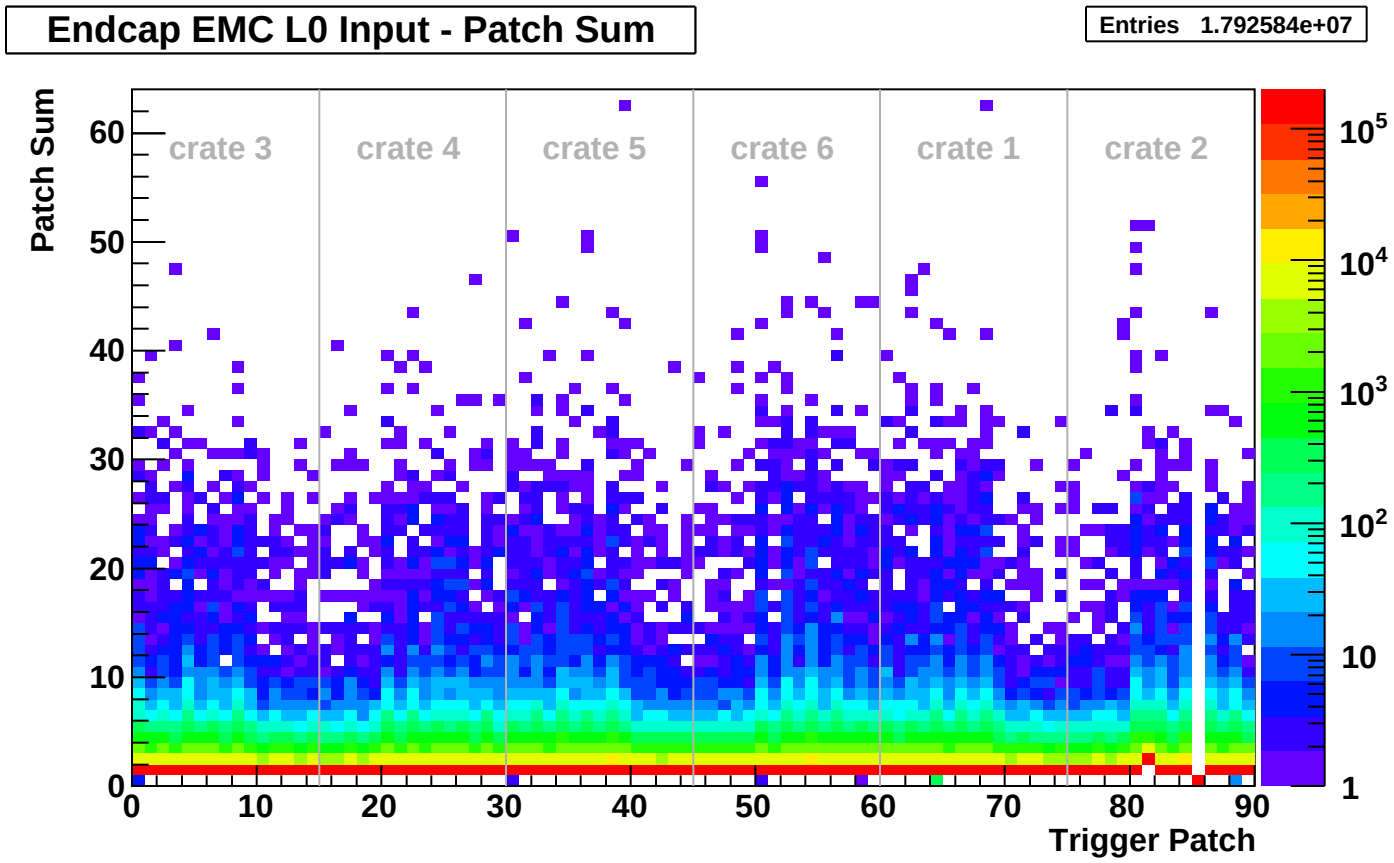

Abort-gaps
Entries 4813

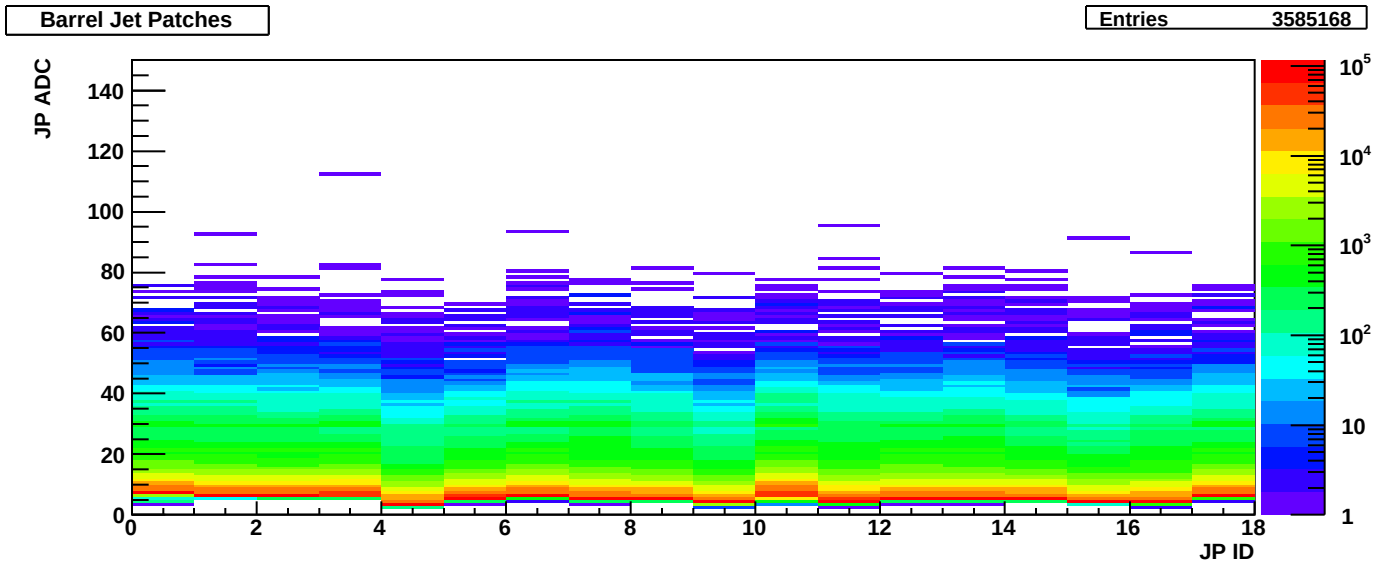

!Yellow-Fill
Entries 6501

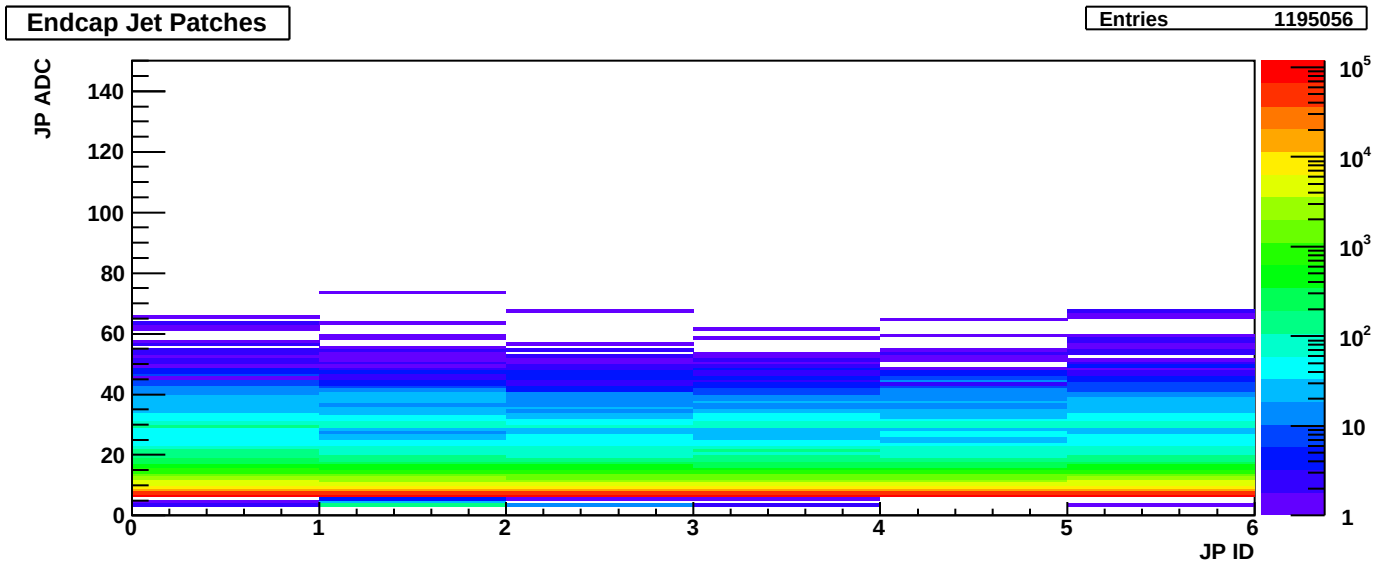

Barrel EMC L0 Input - High Tower
Entries 5.97528e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

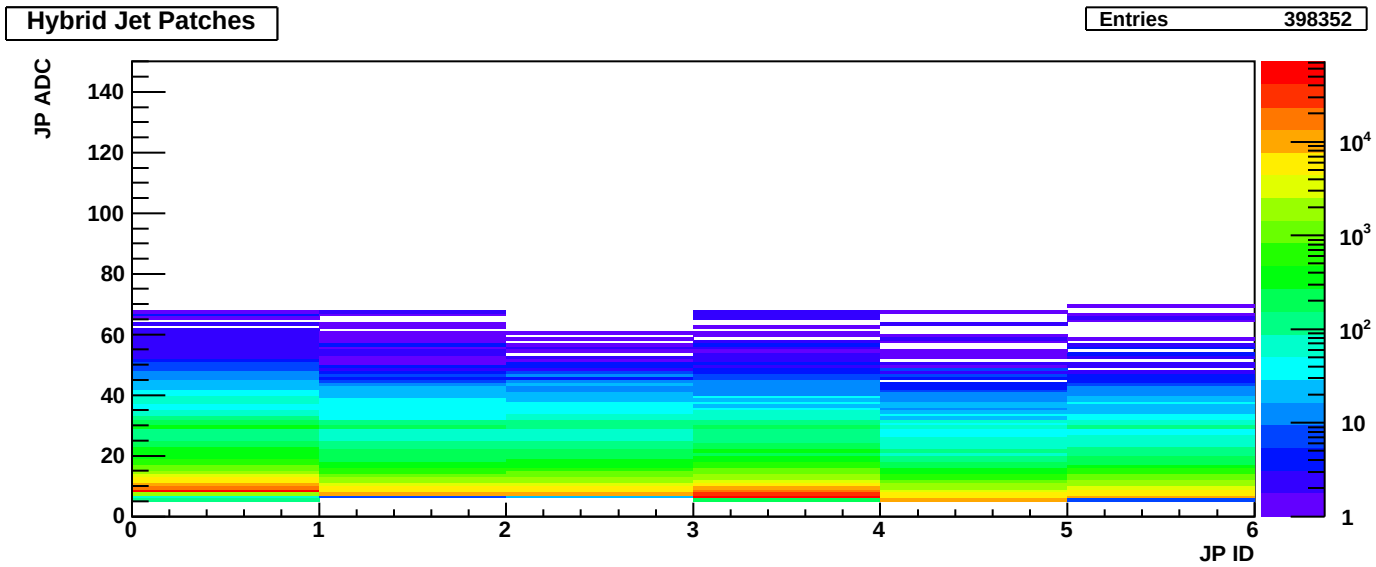

Barrel EMC L0 Input - Patch Sum
Entries 5.97528e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

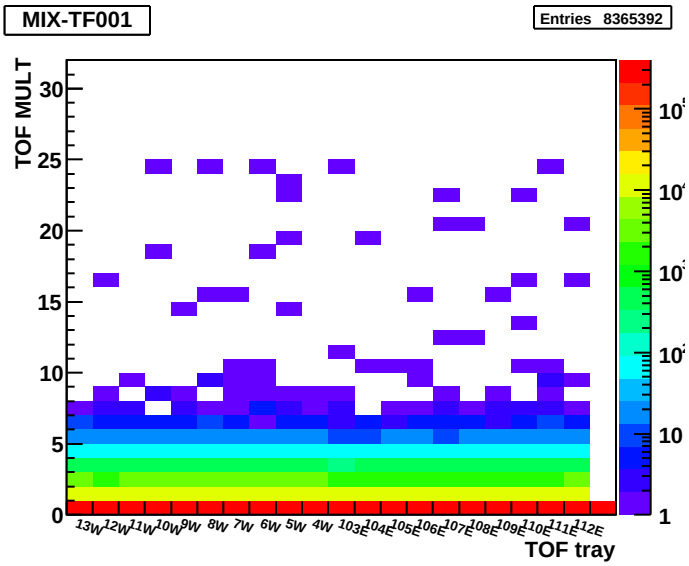

Endcap EMC L0 Input - High Tower
Entries 1.792584e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

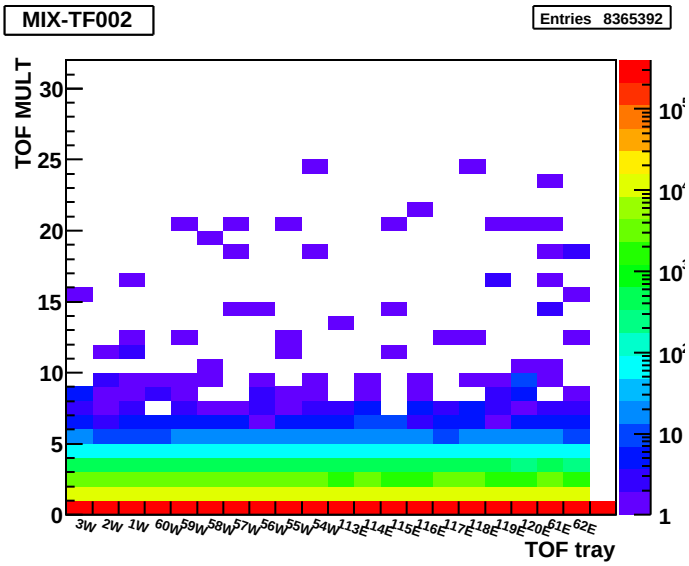

Endcap EMC L0 Input - Patch Sum
Entries 1.792584e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

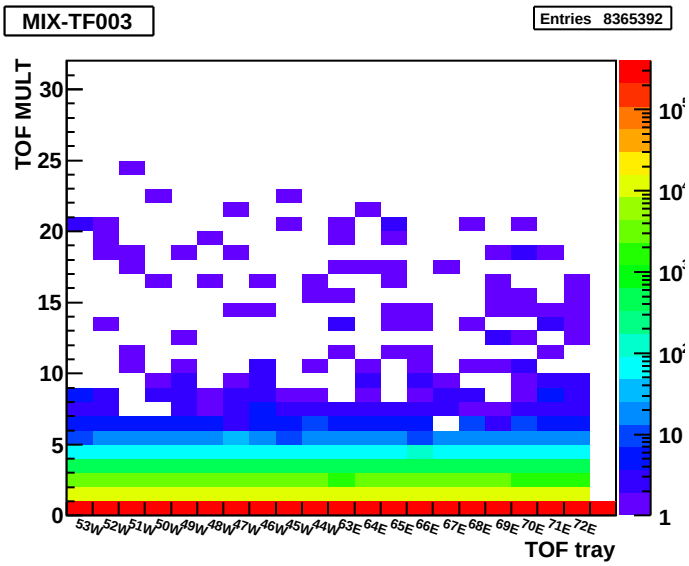

Barrel Jet Patches
Entries 3585168
JP ADC
JP ID

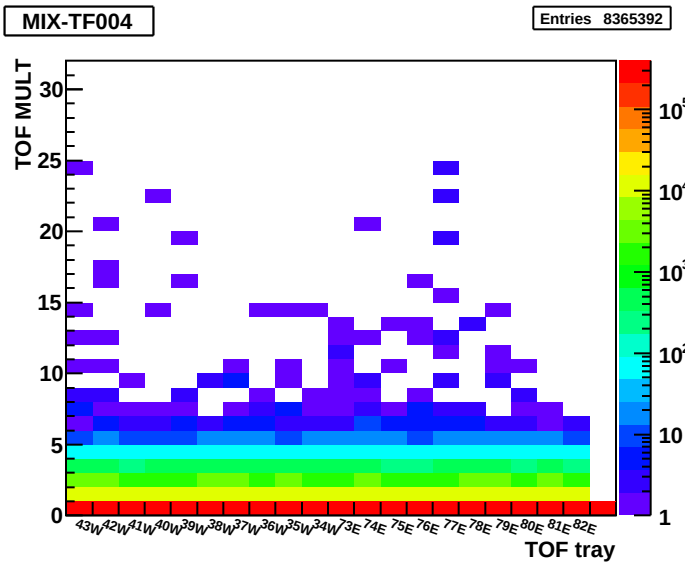

Endcap Jet Patches
Entries 1195056
JP ADC
JP ID

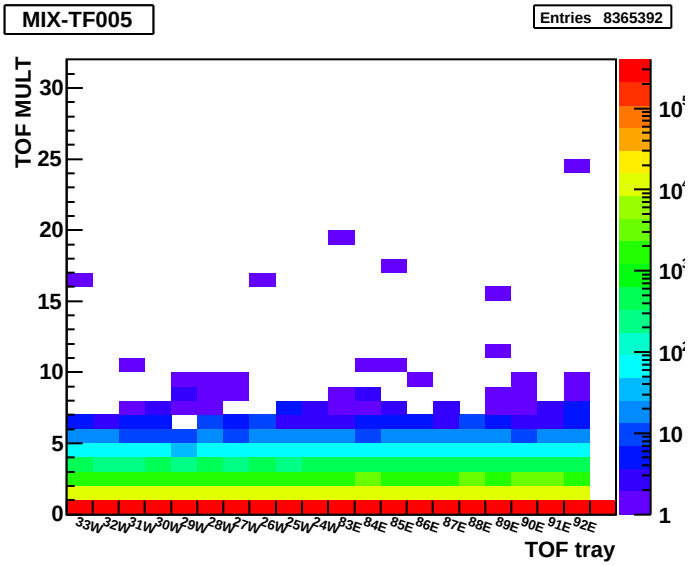

Hybrid Jet Patches
Entries 398352
JP ADC
JP ID

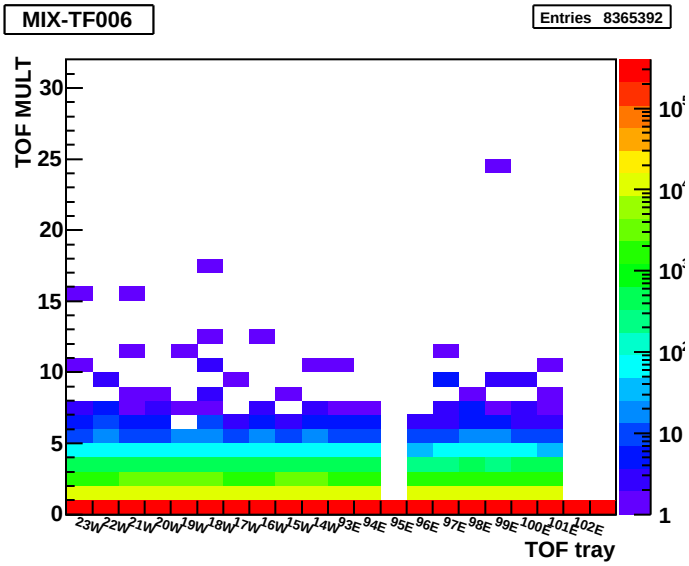

MIX-TF001
Entries 8365392
TOF MULT
TOF tray

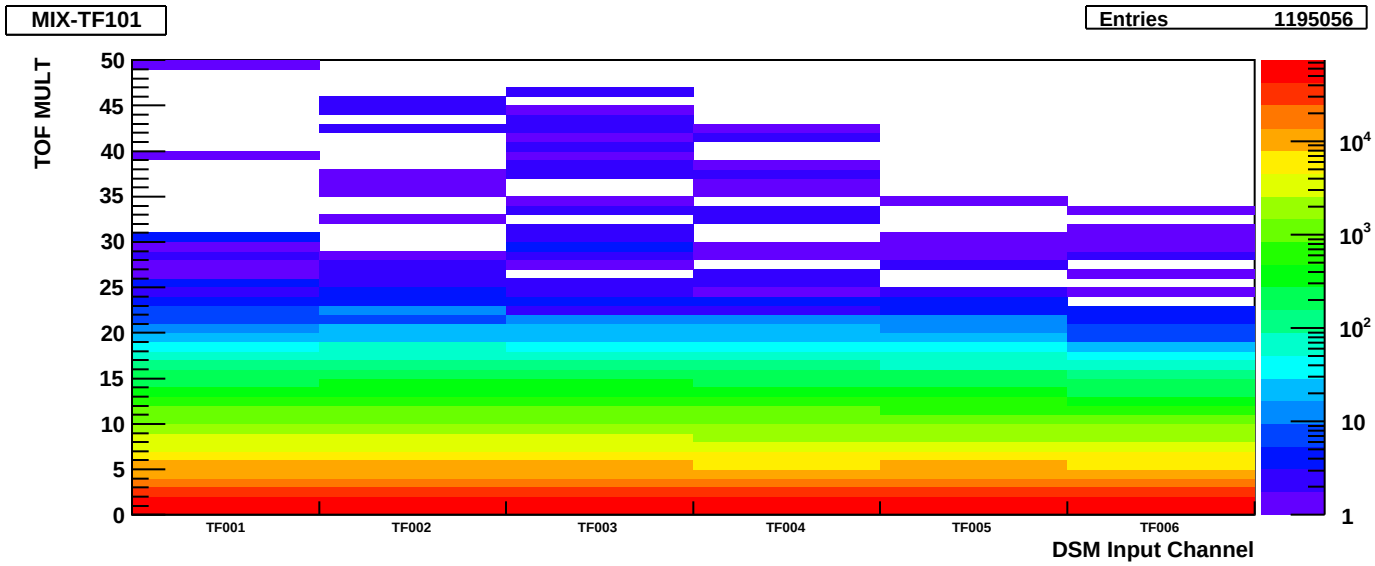

MIX-TF002
Entries 8365392
TOF MULT
TOF tray

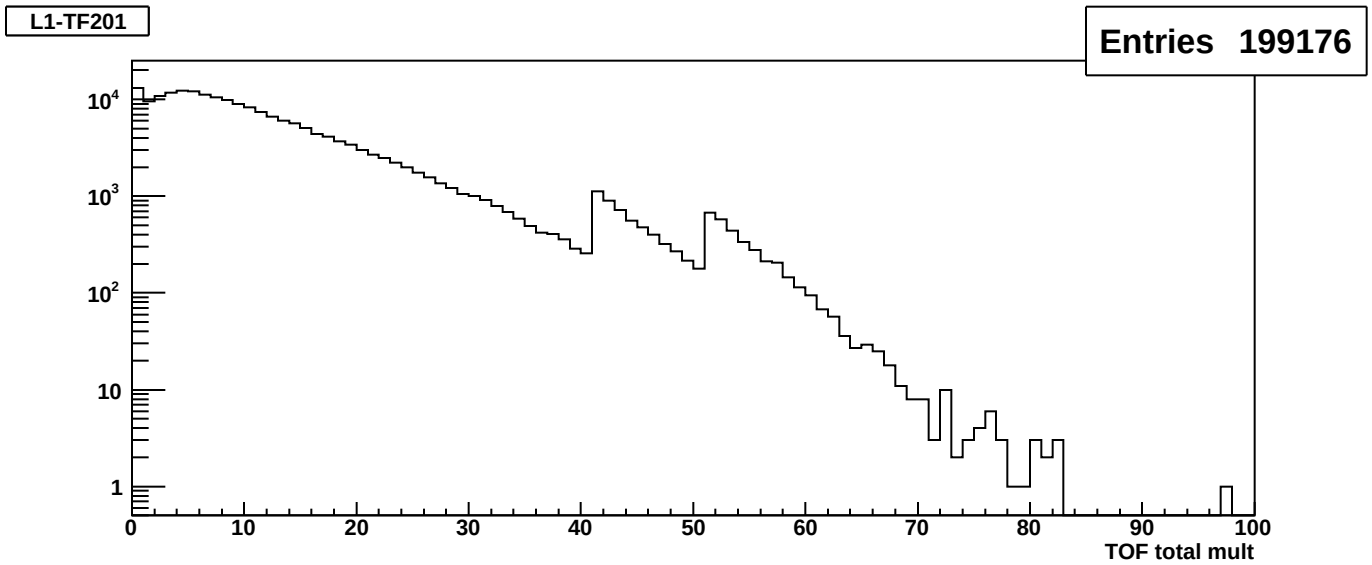

MIX-TF003
Entries 8365392
TOF MULT
TOF tray

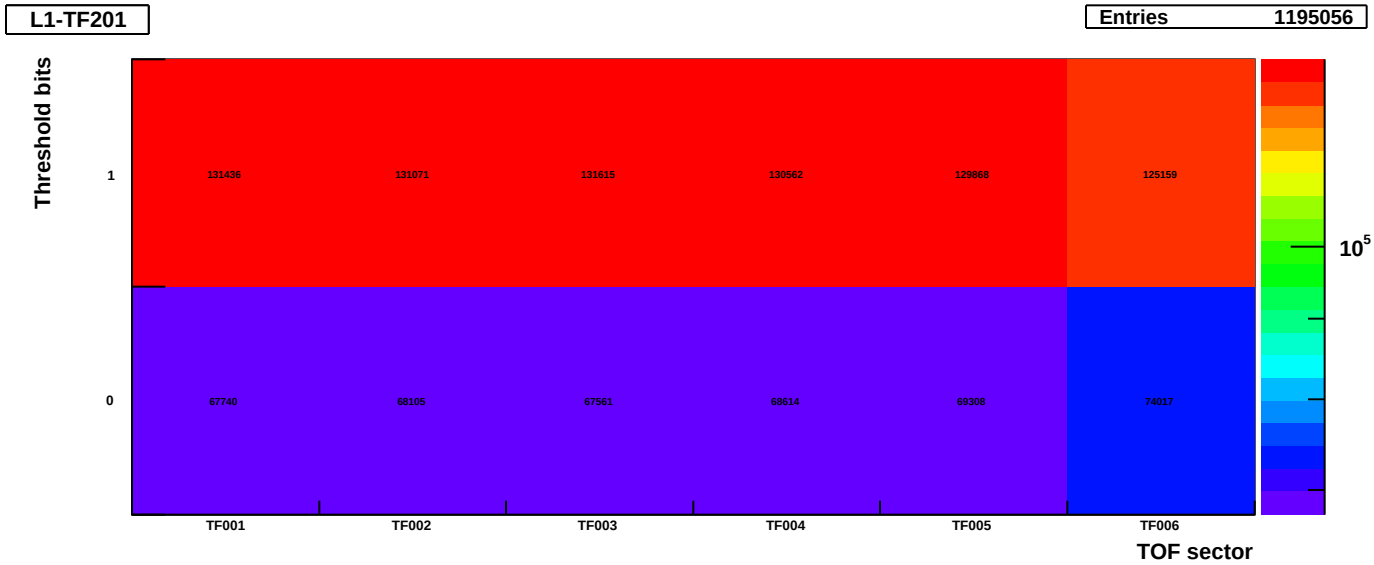

MIX-TF004
Entries 8365392
TOF MULT
TOF tray

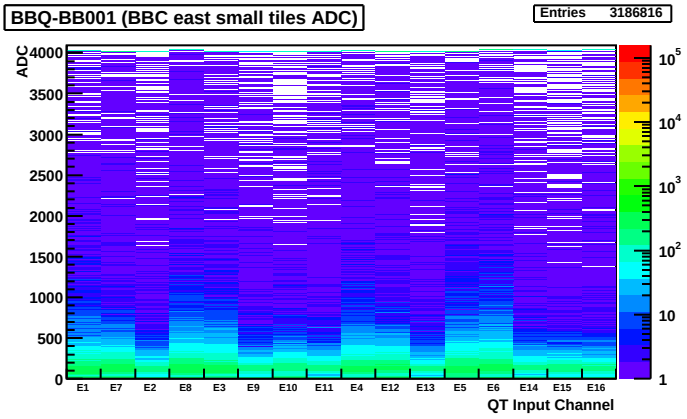

MIX-TF005
Entries 8365392
TOF MULT
TOF tray

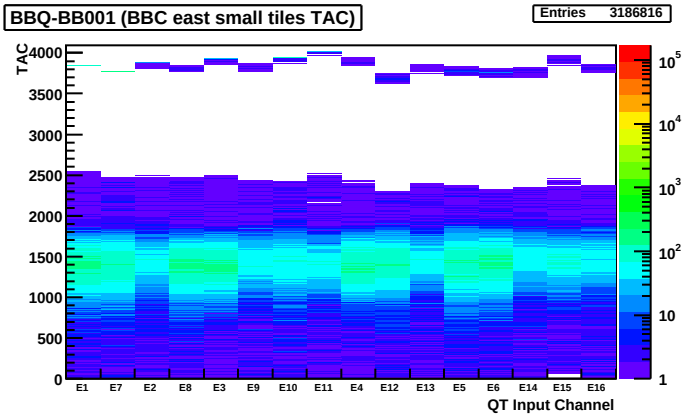

MIX-TF006
Entries 8365392
TOF MULT
TOF tray

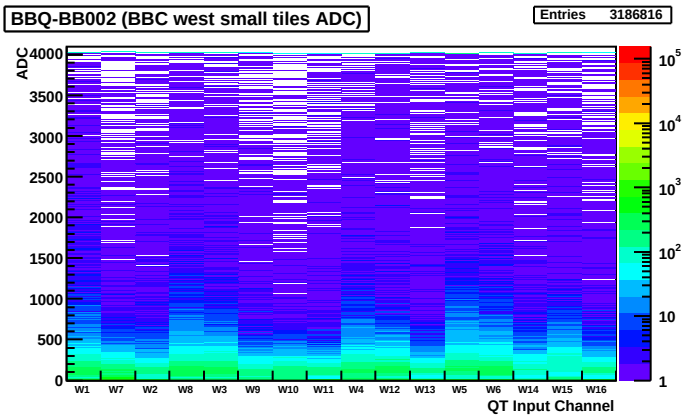

MIX-TF101
Entries 1195056
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

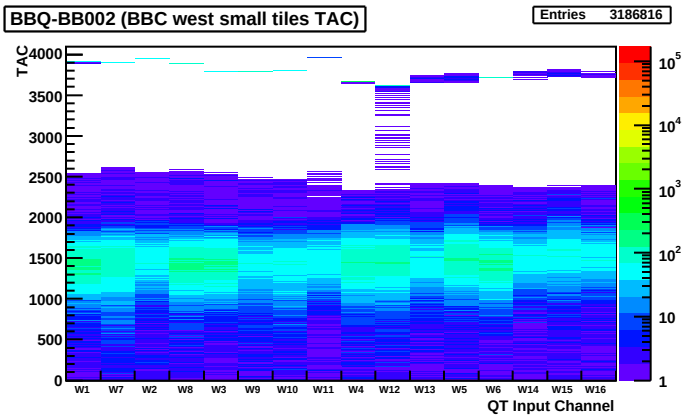

L1-TF201
Entries 199176
TOF total mult

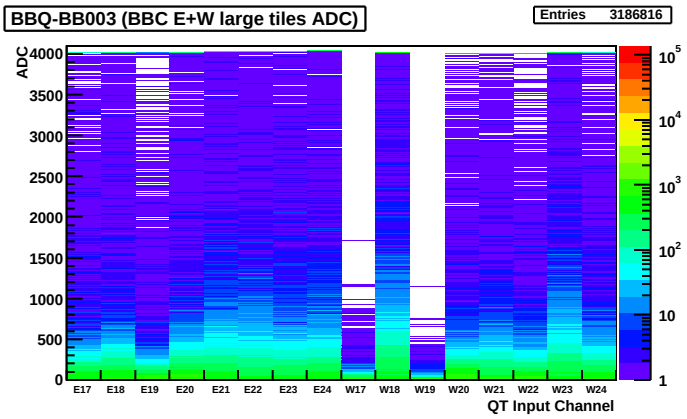

L1-TF201
Entries 1195056
Threshold bits
131436
131071
131615
130562
129868
125159
67740
68105
67561
68614
69308
74017
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

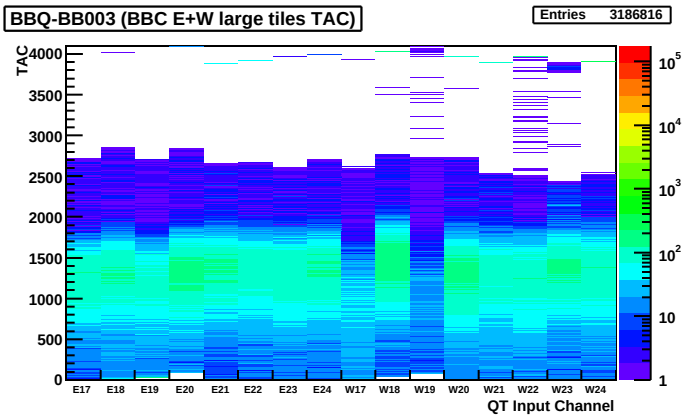

BBQ-BB001 (BBC east small tiles ADC)
Entries 3186816
ADC
QT Input Channel

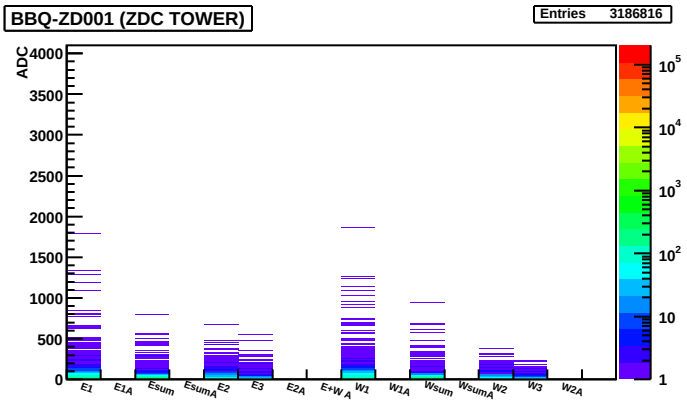

BBQ-BB001 (BBC east small tiles TAC)
Entries 3186816
TAC
QT Input Channel

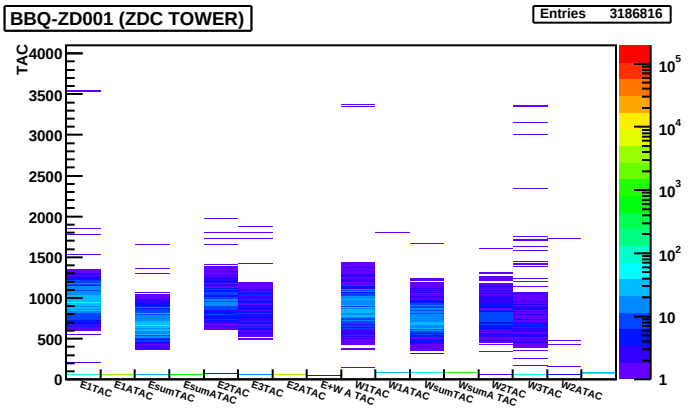

BBQ-BB002 (BBC west small tiles ADC)
Entries 3186816
ADC
QT Input Channel

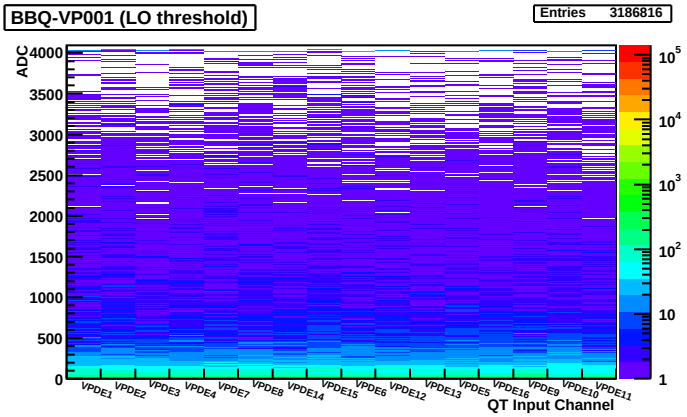

BBQ-BB002 (BBC west small tiles TAC)
Entries 3186816
TAC
QT Input Channel

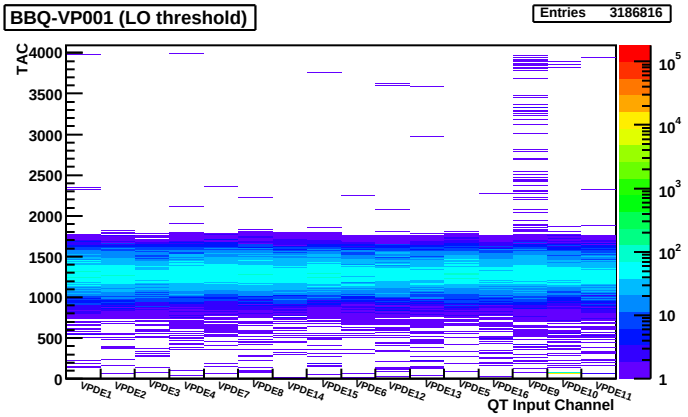

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3186816
ADC
QT Input Channel

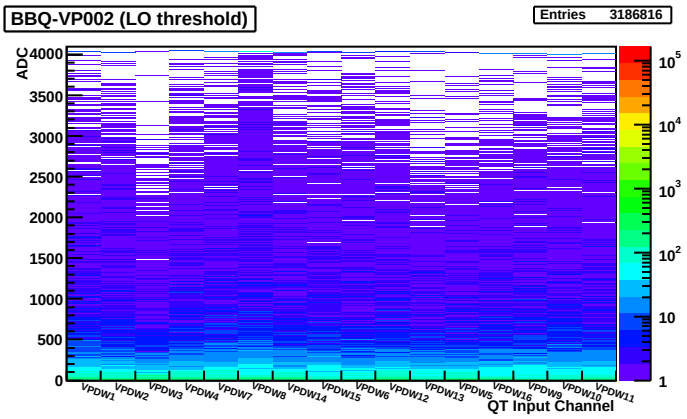

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3186816
TAC
QT Input Channel

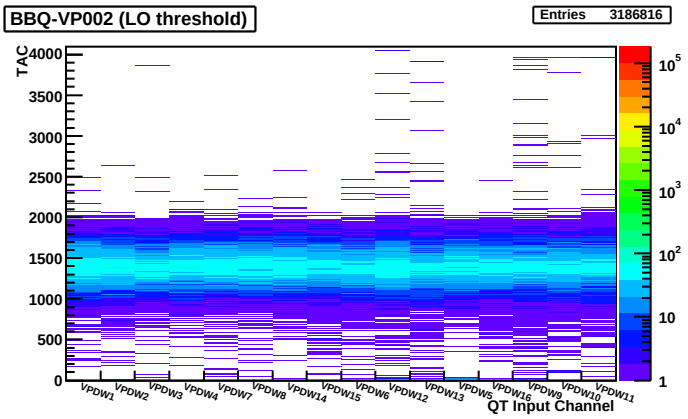

BBQ-ZD001 (ZDC TOWER)
Entries 3186816
ADC

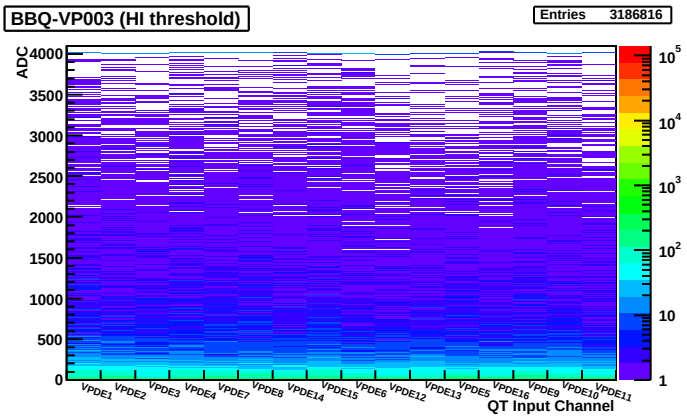

BBQ-ZD001 (ZDC TOWER)
Entries 3186816
TAC

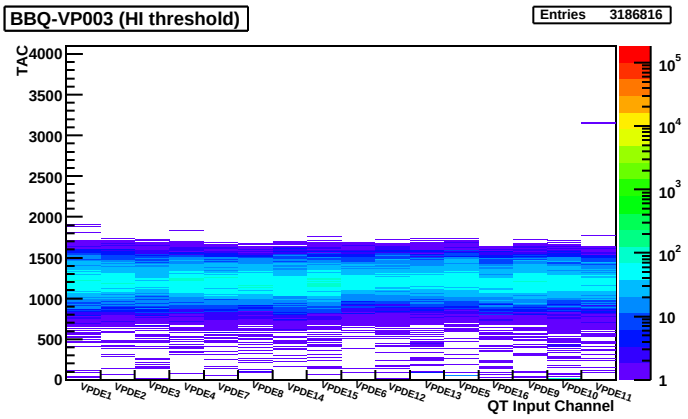

BBQ-VP001 (LO threshold)
Entries 3186816
ADC
QT Input Channel

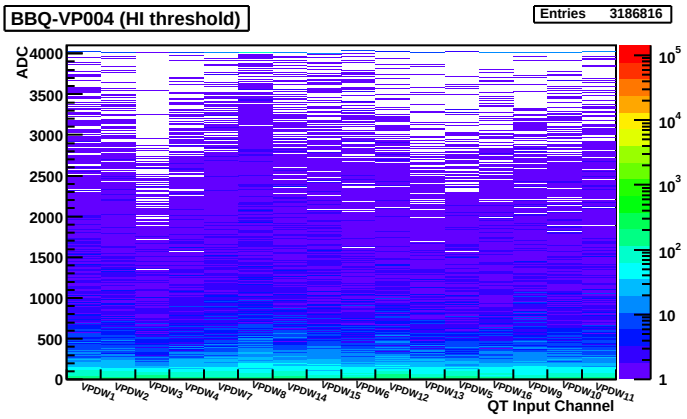

BBQ-VP001 (LO threshold)
Entries 3186816
TAC
QT Input Channel

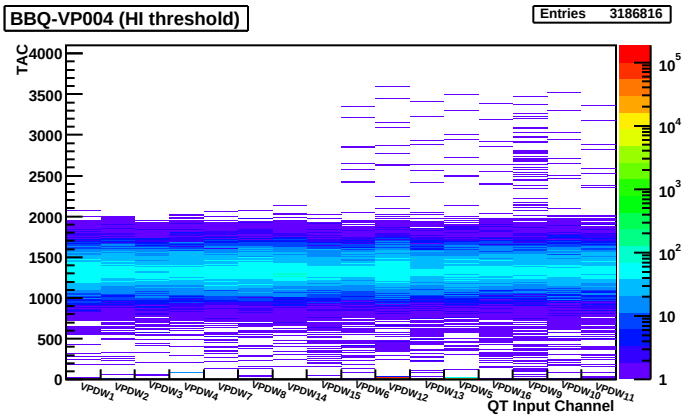

BBQ-VP002 (LO threshold)
Entries 3186816
ADC
QT Input Channel

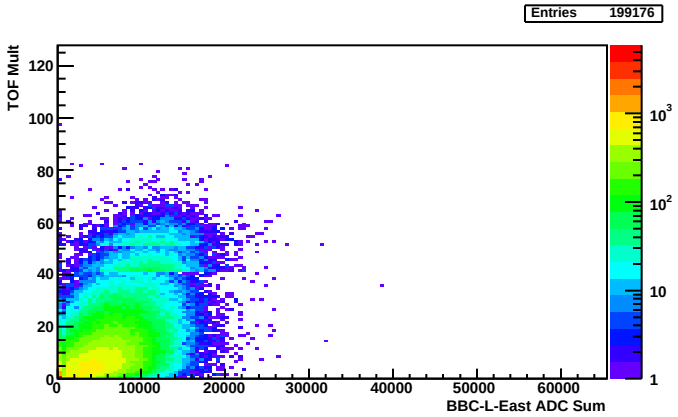

BBQ-VP002 (LO threshold)
Entries 3186816
TAC
QT Input Channel

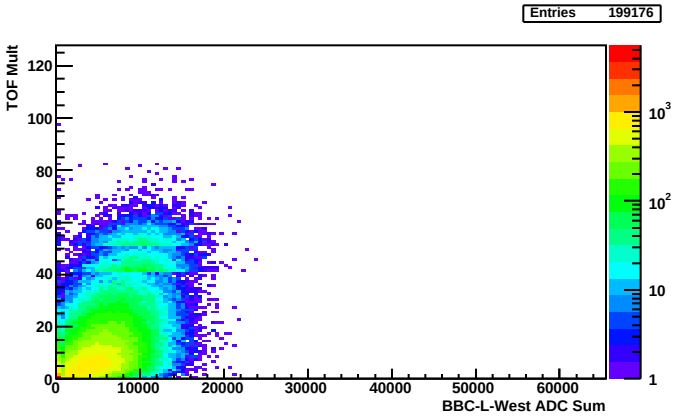

BBQ-VP003 (HI threshold)
Entries 3186816
ADC
QT Input Channel

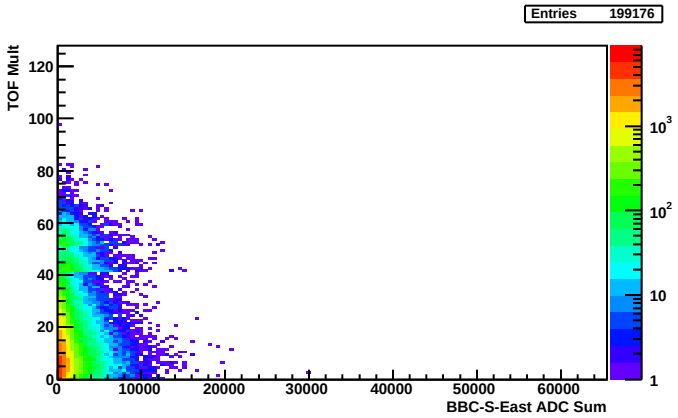

BBQ-VP003 (HI threshold)
Entries 3186816
TAC
QT Input Channel

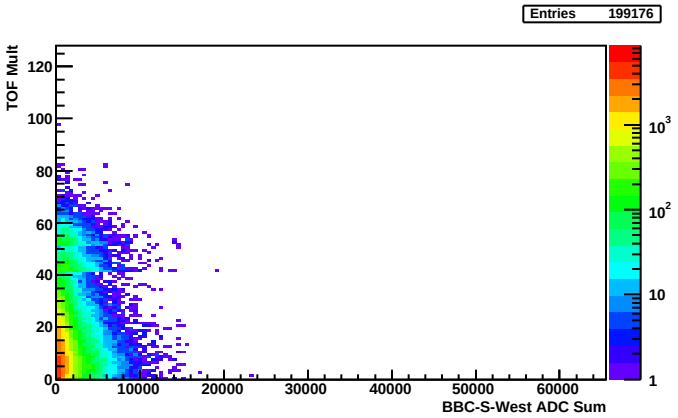

BBQ-VP004 (HI threshold)
Entries 3186816
ADC
QT Input Channel

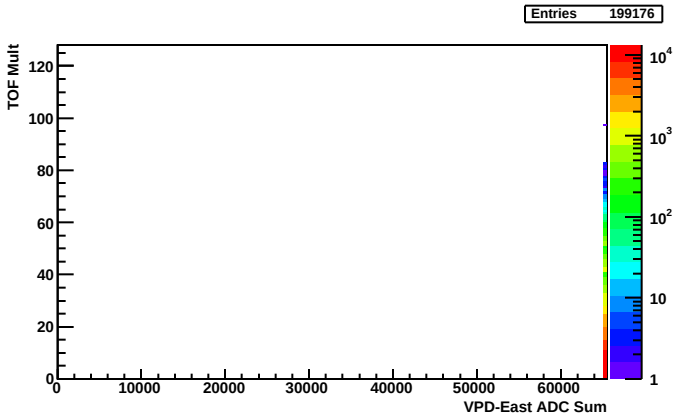

BBQ-VP004 (HI threshold)
Entries 3186816
TAC
QT Input Channel

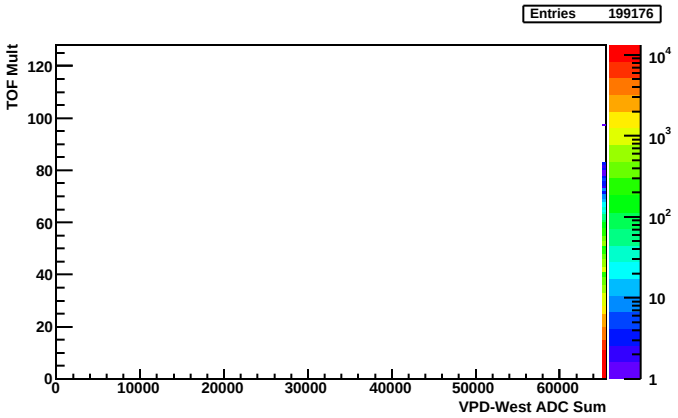

Entries 199176
TOF Mult
BBC-L-East ADC Sum

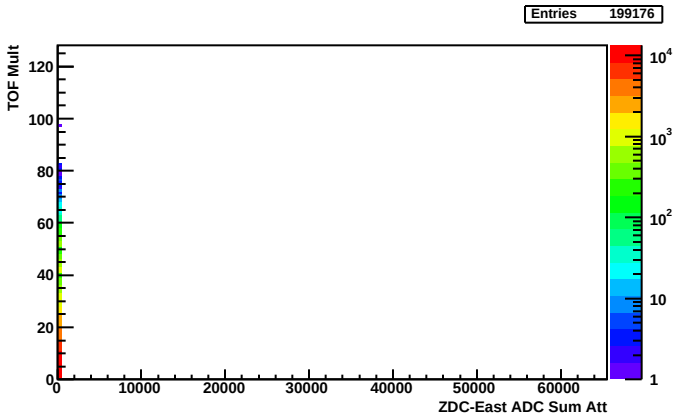

Entries 199176
TOF Mult
BBC-L-West ADC Sum

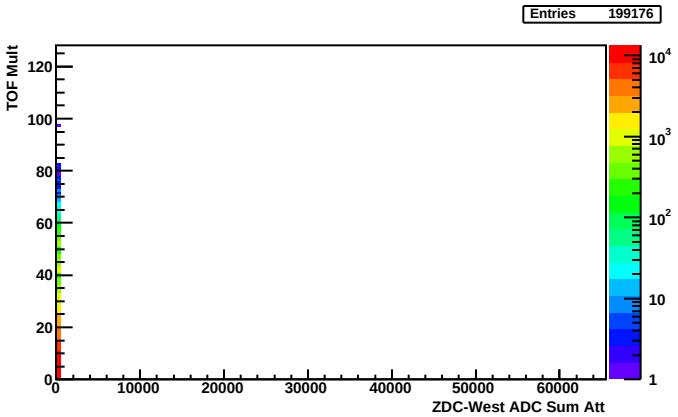

Entries 199176
TOF Mult
BBC-S-East ADC Sum

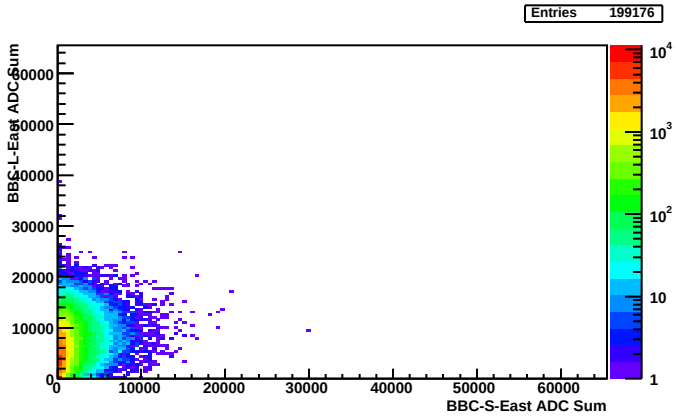

Entries 199176
TOF Mult
BBC-S-West ADC Sum

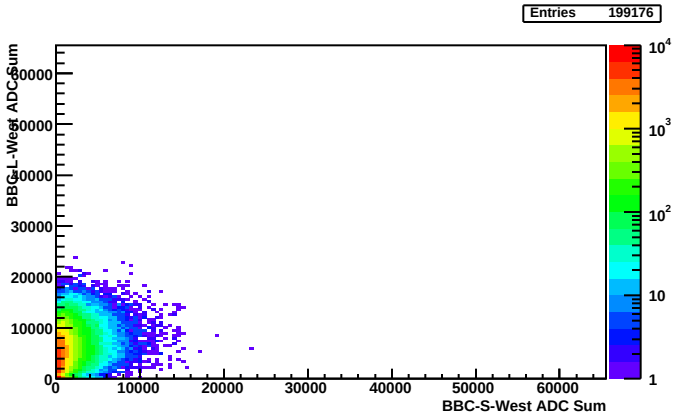

Entries 199176
TOF Mult
VPD-East ADC Sum

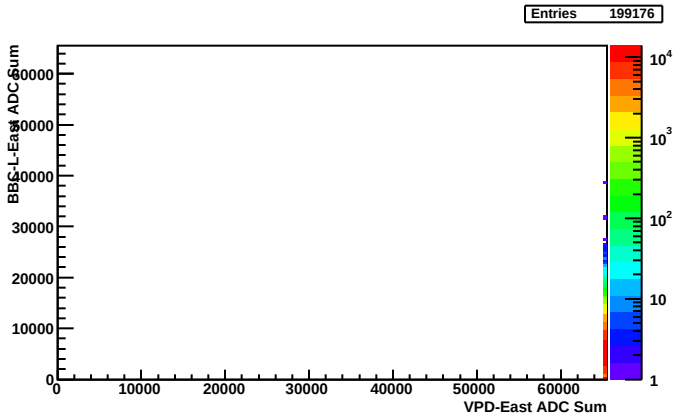

Entries 199176
TOF Mult
VPD-West ADC Sum

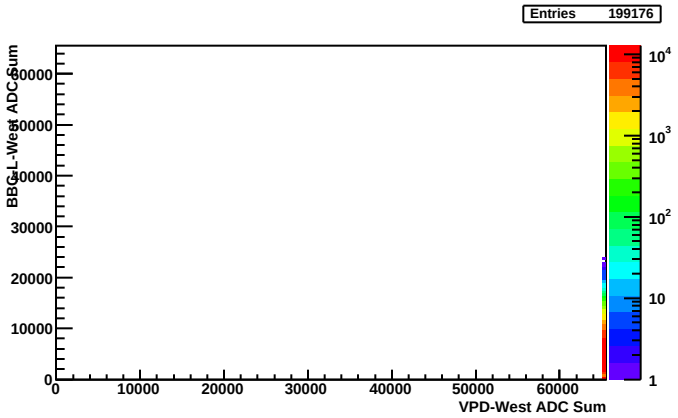

Entries 199176
TOF Mult
ZDC-East ADC Sum Att

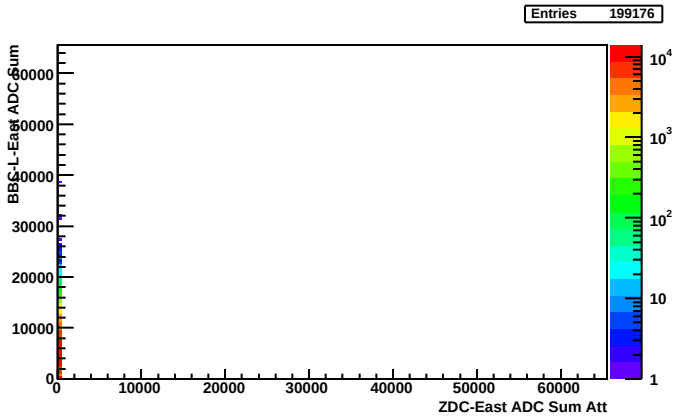

Entries 199176
TOF Mult
ZDC-West ADC Sum Att

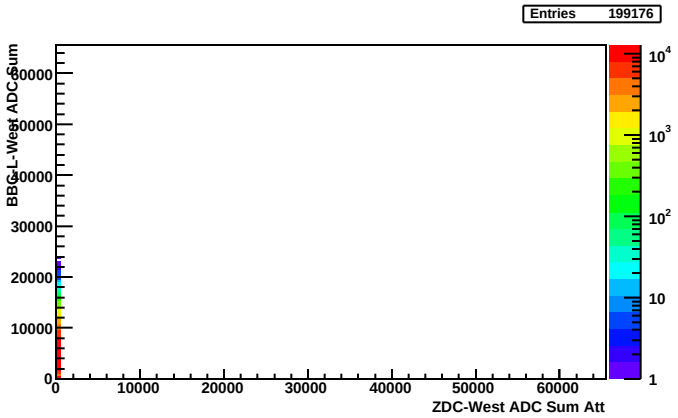

Entries 199176
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 199176
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 199176
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 199176
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 199176
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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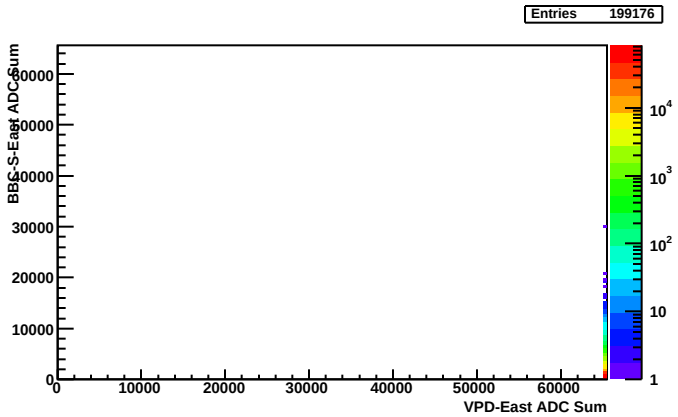
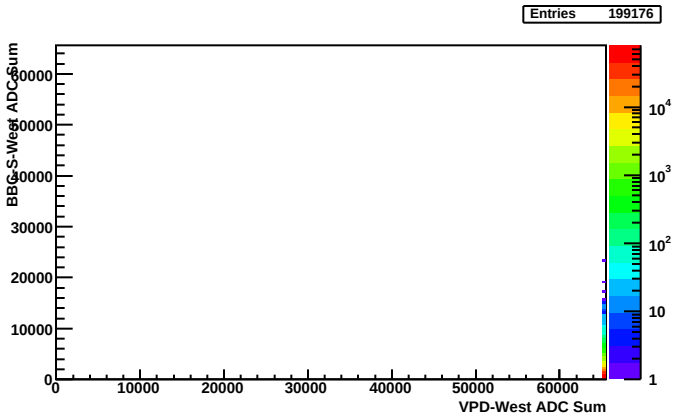
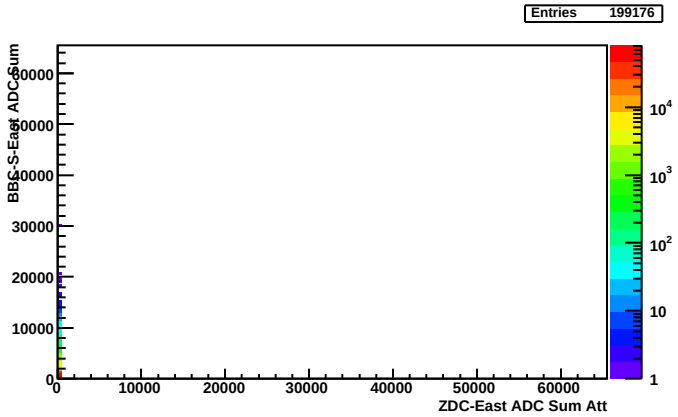
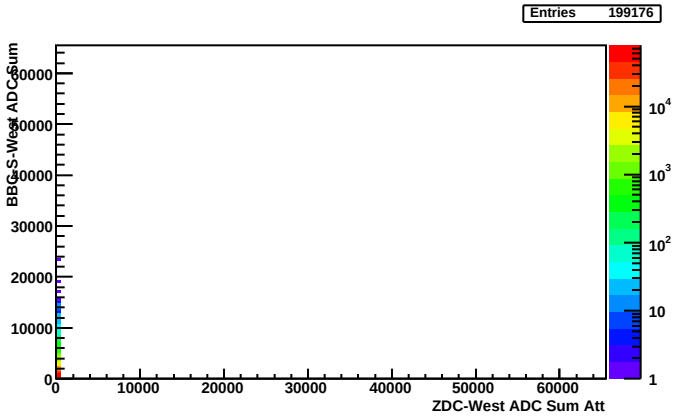
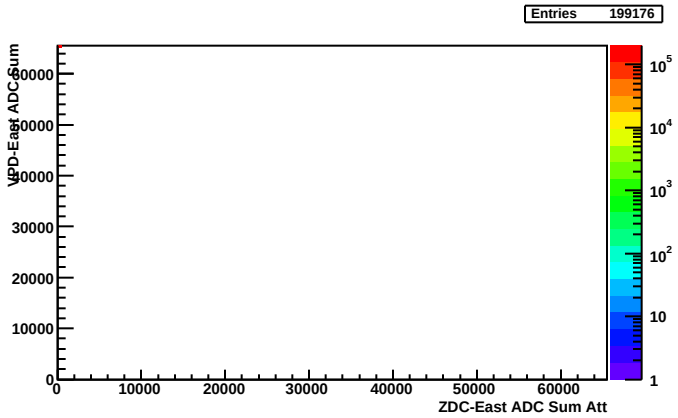
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 199176
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 199176
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 199176
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 199176
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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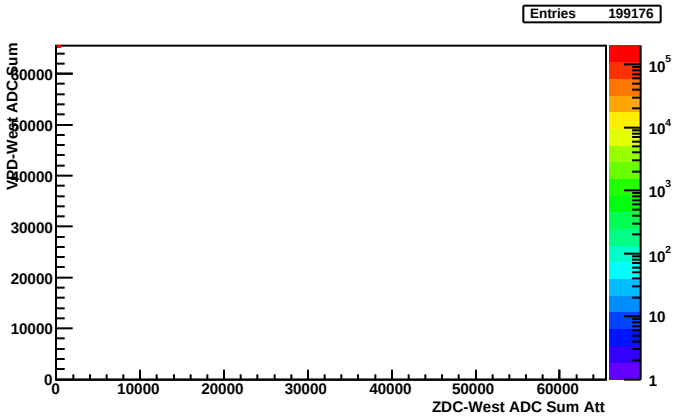
VPD-East ADC Sum
ZDC-East ADC Sum Att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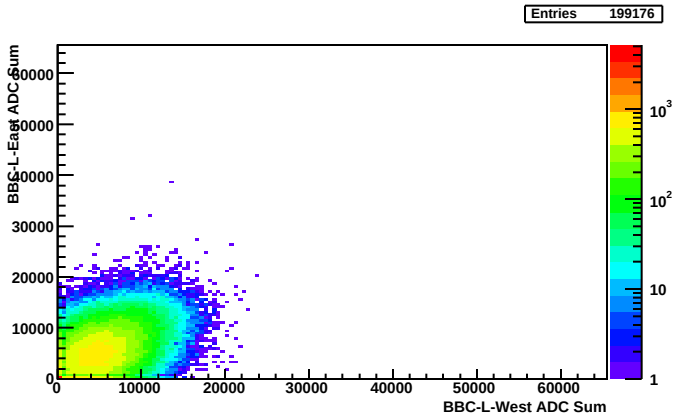
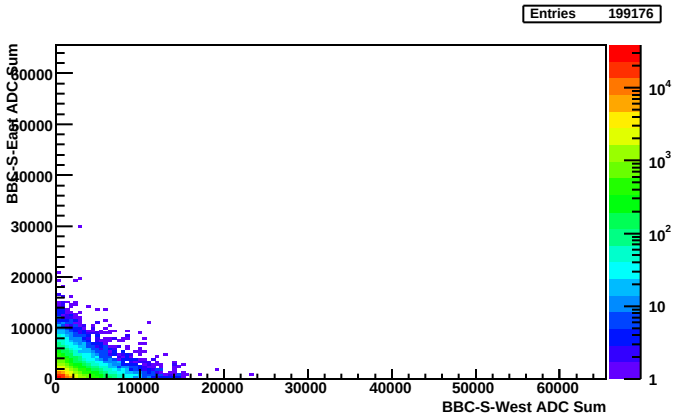
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 199176
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 199176
BBC-S-East ADC Sum
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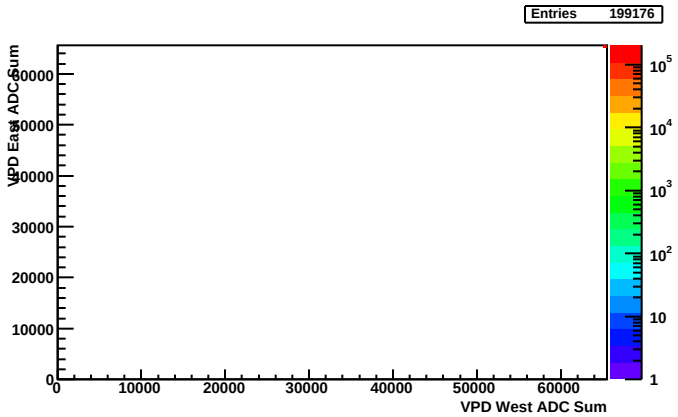
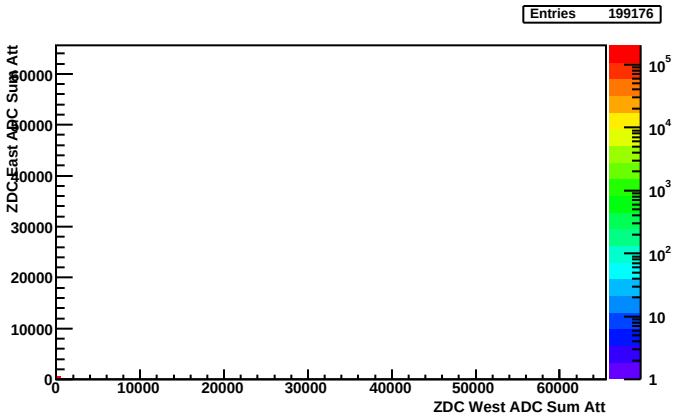
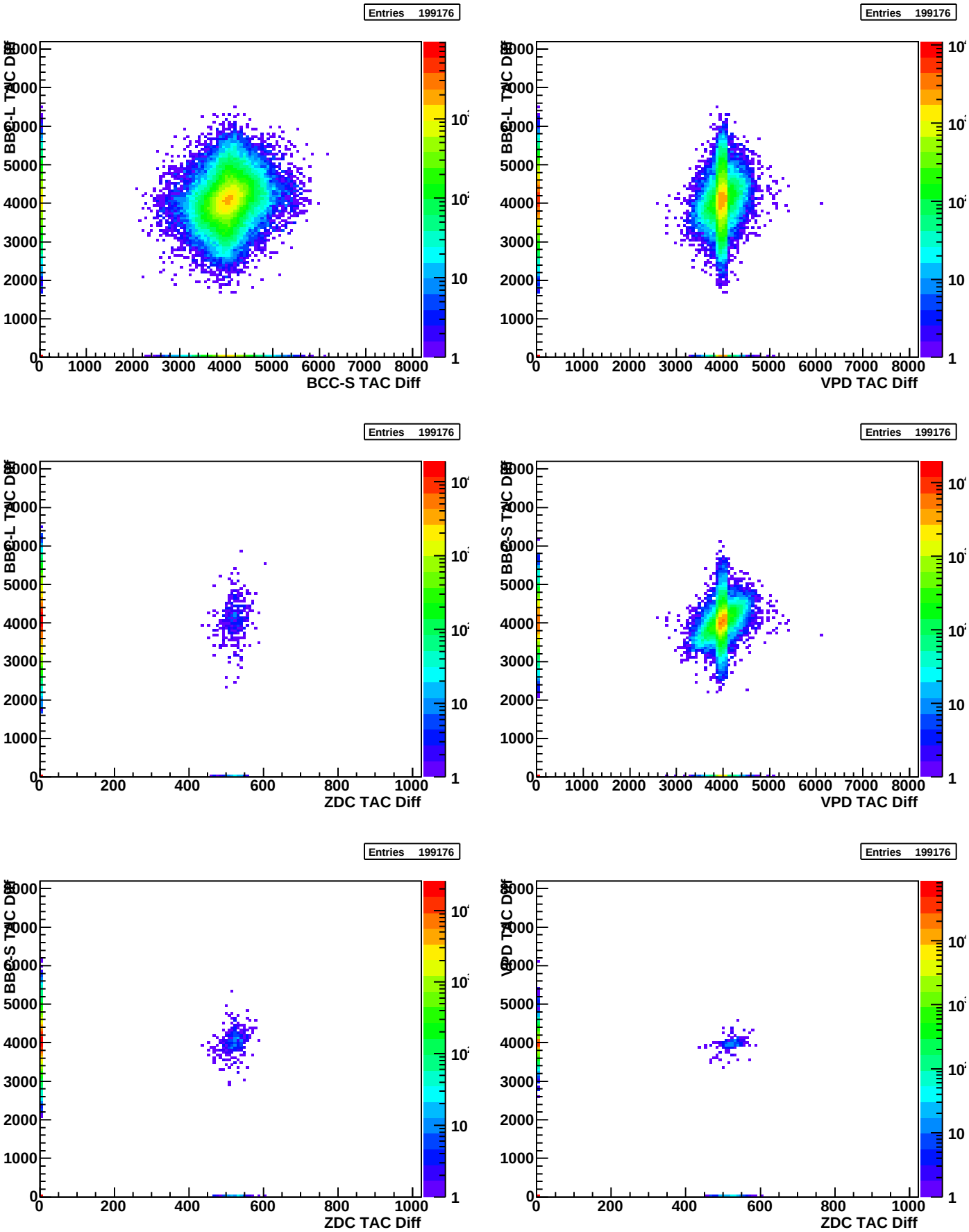
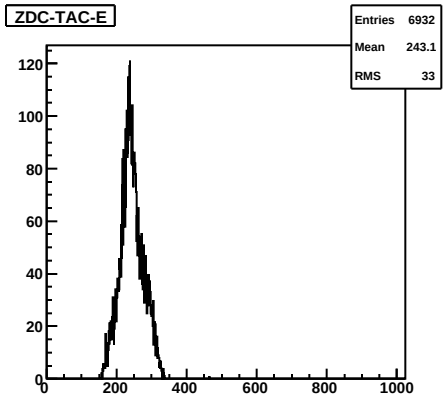
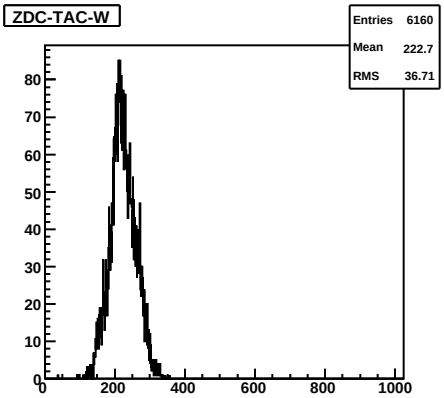
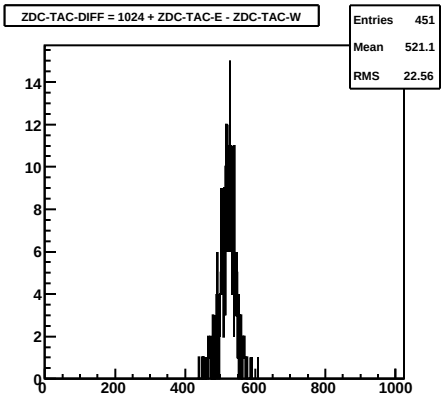

Entries 199176
VPD East ADC Sum
VPD West ADC Sum

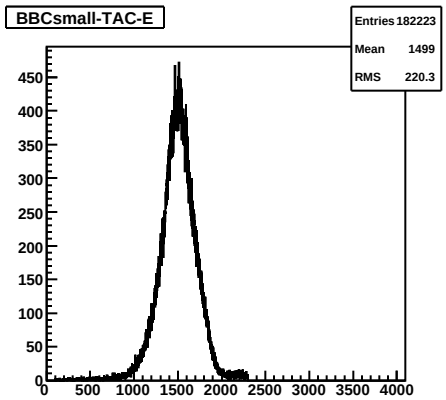

Entries 199176
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

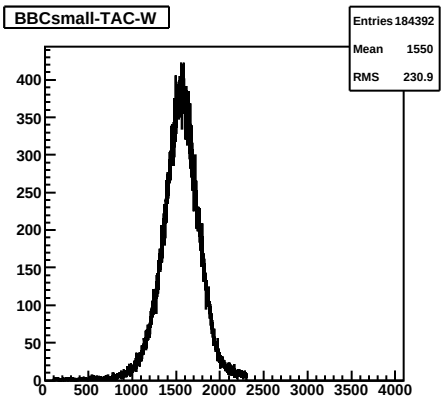

Entries 199176
BBC-L TAC Diff
BCC-S TAC Diff
Entries 199176
BBC-L TAC Diff
VPD TAC Diff
Entries 199176
BBC-L TAC Diff
ZDC TAC Diff
Entries 199176
BBC-S TAC Diff
VPD TAC Diff
Entries 199176
BBC-S TAC Diff
ZDC TAC Diff
Entries 199176
VPD TAC Diff
ZDC TAC Diff

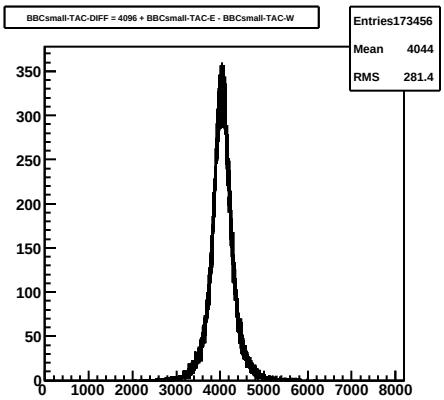

ZDC-TAC-E
Entries 6932
Mean 243.1
RMS 33

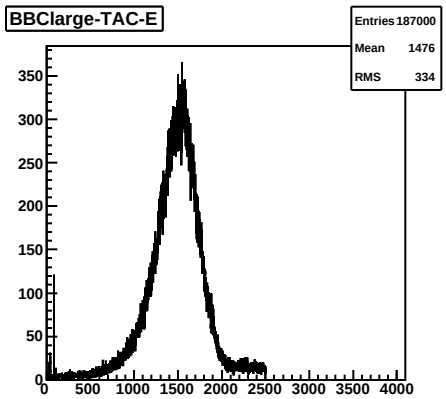

ZDC-TAC-W
Entries 6160
Mean 222.7
RMS 36.71

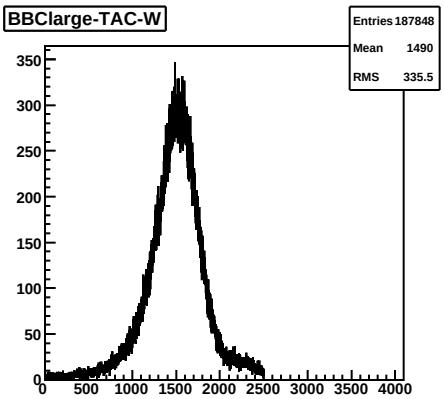

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 451
Mean 521.1
RMS 22.56

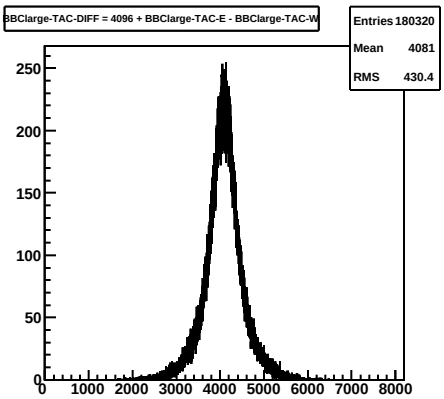

BBCsmall-TAC-E
Entries 182223
Mean 1499
RMS 220.3

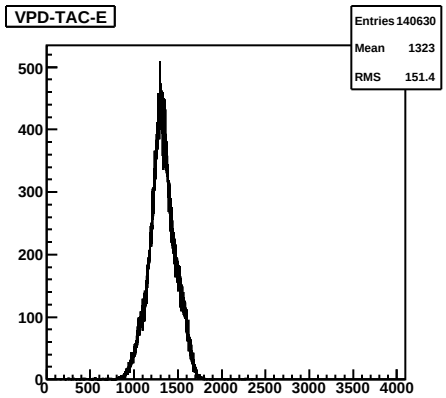

BBCsmall-TAC-W
Entries 184392
Mean 1550
RMS 230.9

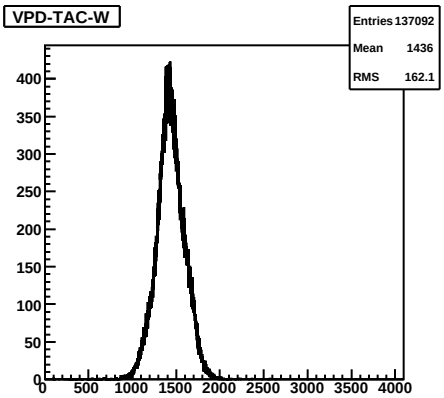

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries173456
Mean 4044
RMS 281.4

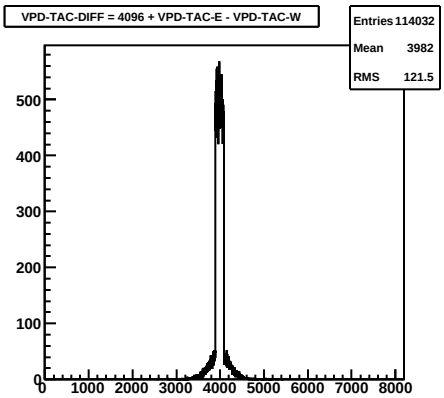

BBClarge-TAC-E
Entries 187000
Mean 1476
RMS 334

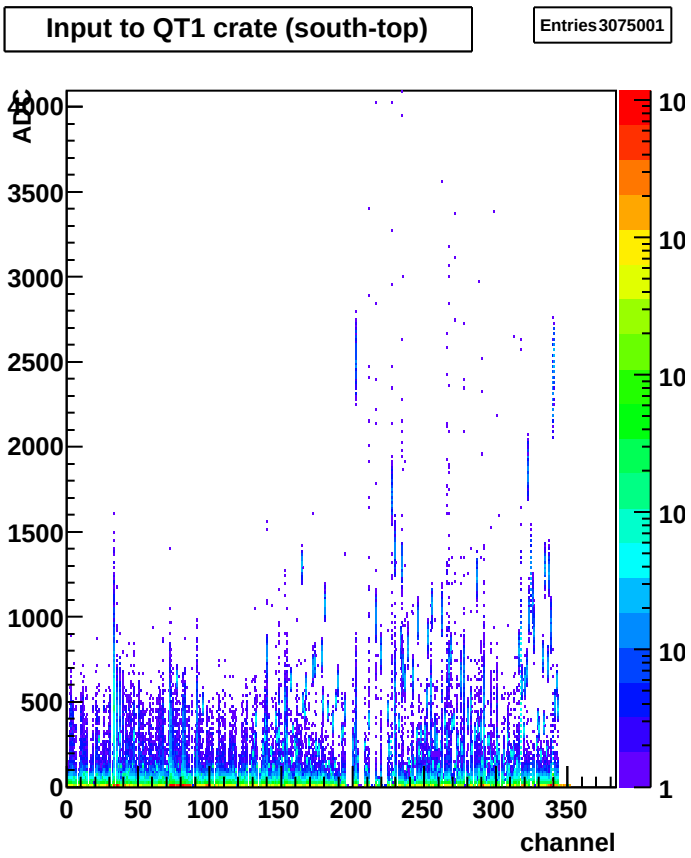

BBClarge-TAC-W
Entries 187848
Mean 1490
RMS 335.5

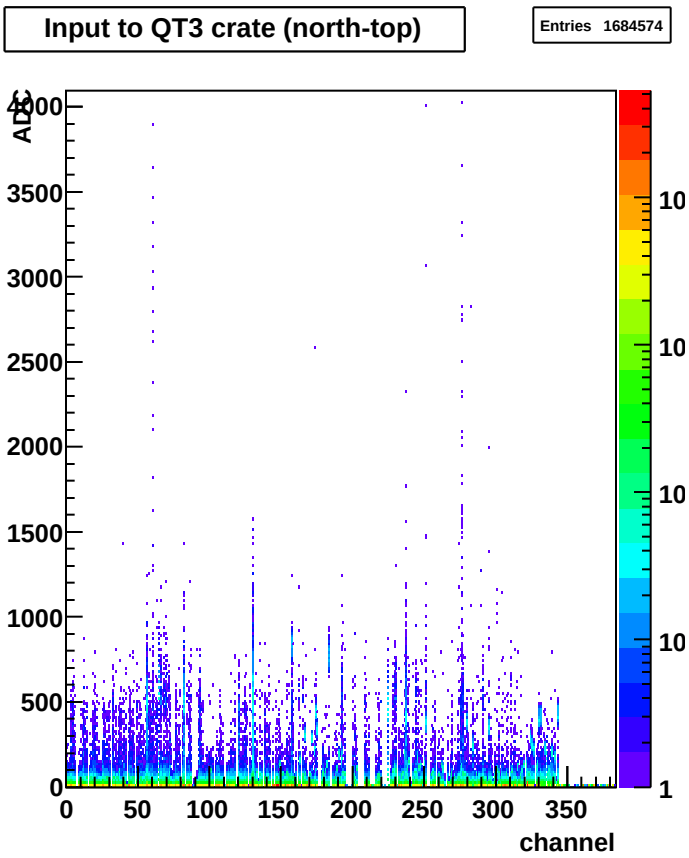

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 180320
Mean 4081
RMS 430.4

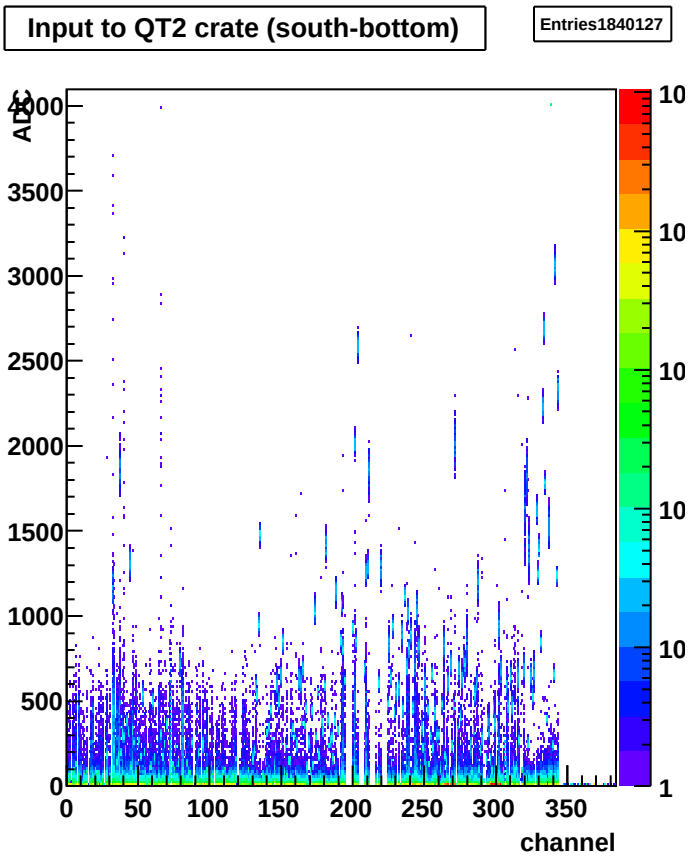

VPD-TAC-E
Entries 140630
Mean 1323
RMS 151.4

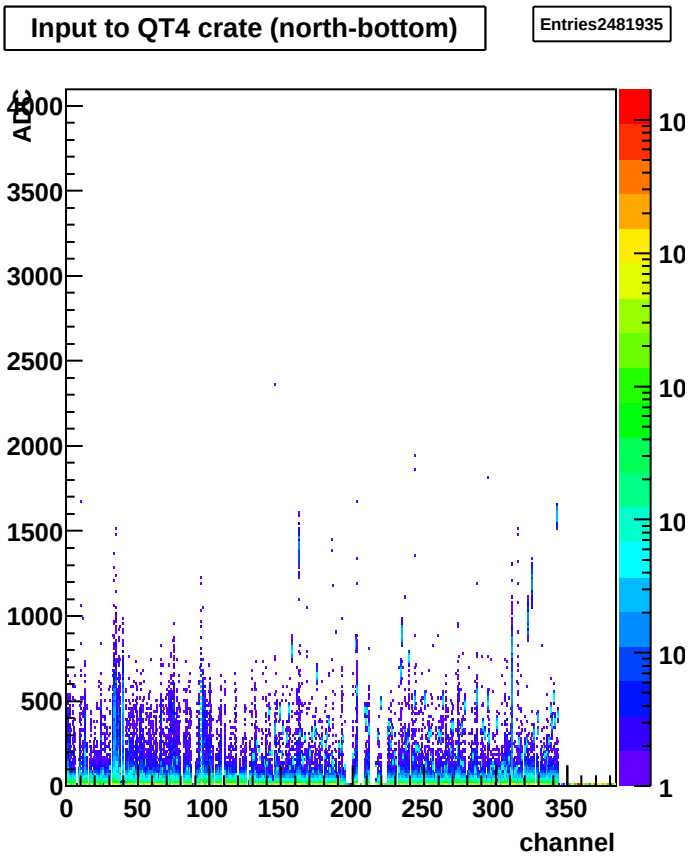

VPD-TAC-W
Entries 137092
Mean 1436
RMS 162.1

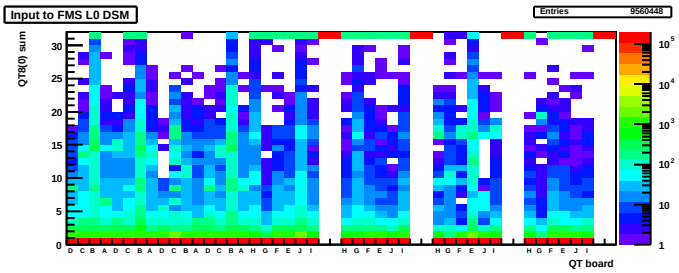

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 114032
Mean 3982
RMS 121.5

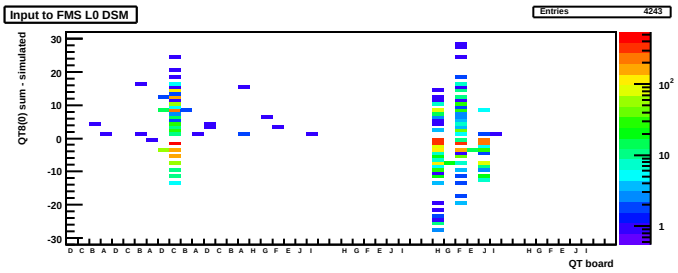

Input to QT1 crate (south-top)
Entries 3075001
ADC
channel

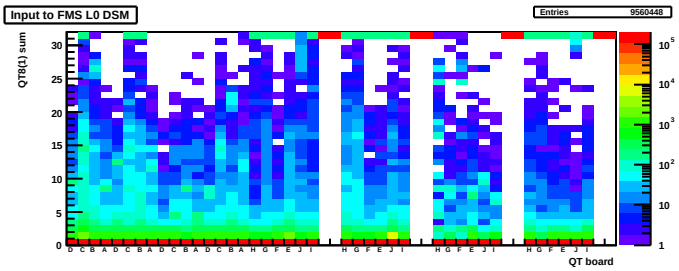

Input to QT3 crate (north-top)
Entries 1684574
ADC
channel

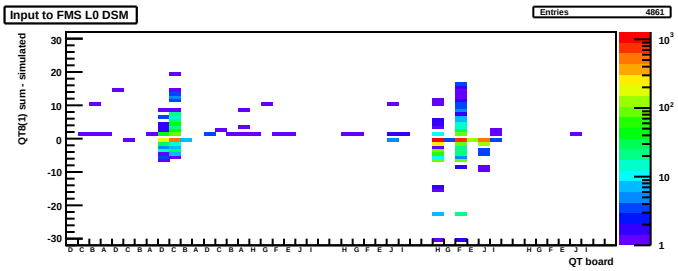

Input to QT2 crate (south-bottom)
Entries 1840127
ADC
channel

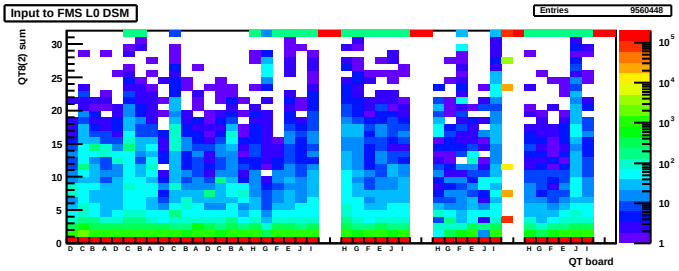

Input to QT4 crate (north-bottom)
Entries 2481935
ADC
channel

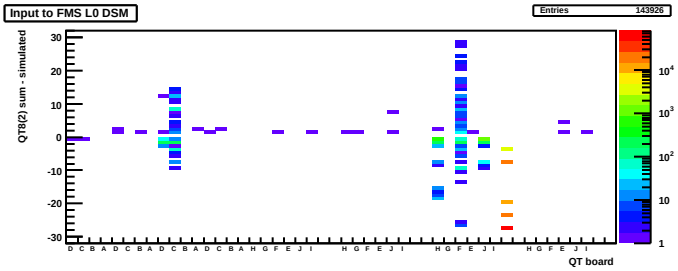

Input to FMS L0 DSM
Entries 9560448
QT8(0) sum
QT board

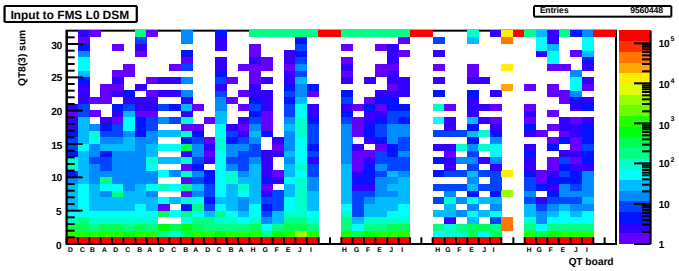

Input to FMS L0 DSM
Entries 4243
QT8(0) sum - simulated
QT board

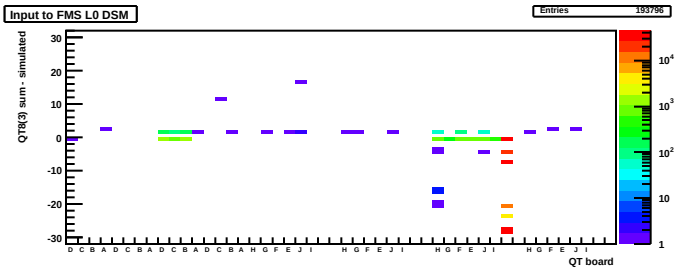

Input to FMS L0 DSM
Entries 9560448
QT8(1) sum
QT board

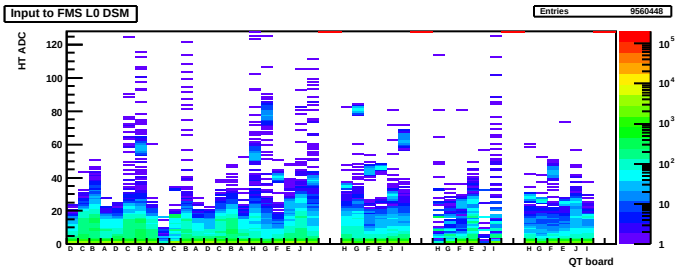

Input to FMS L0 DSM
Entries 4861
QT8(1) sum - simulated
QT board

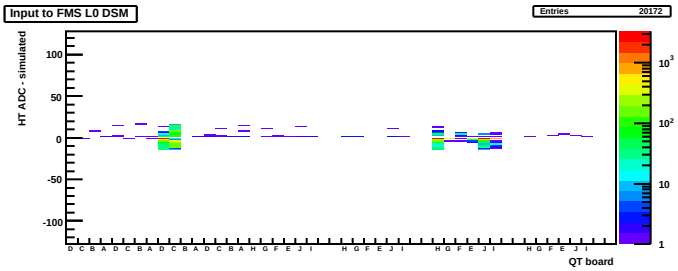

Input to FMS L0 DSM
Entries 9560448
QT8(2) sum
QT board

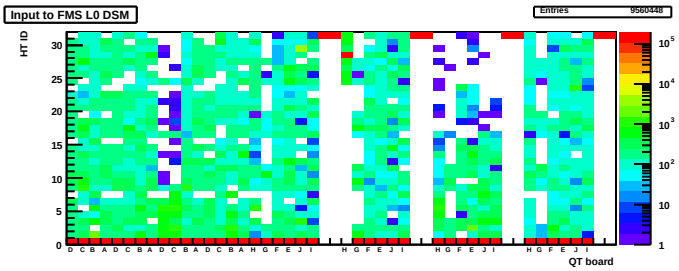

Input to FMS L0 DSM
Entries 143926
QT8(2) sum - simulated
QT board

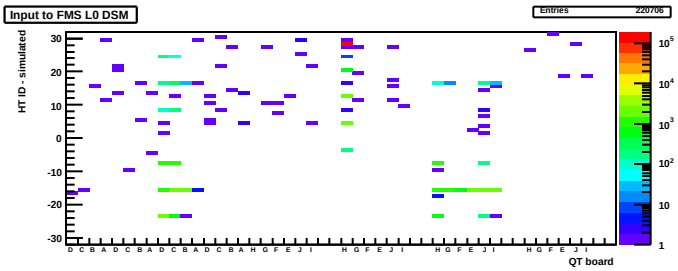

Input to FMS L0 DSM
Entries 9560448
QT8(3) sum
QT board

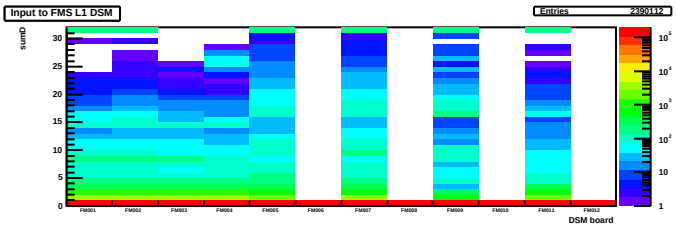

Input to FMS L0 DSM
Entries 193796
QT8(3) sum - simulated
QT board

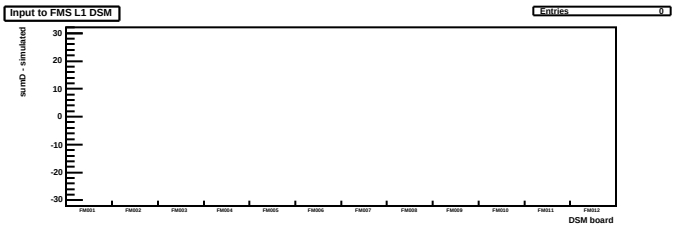

Input to FMS L0 DSM
Entries 9560448
HT ADC
QT board

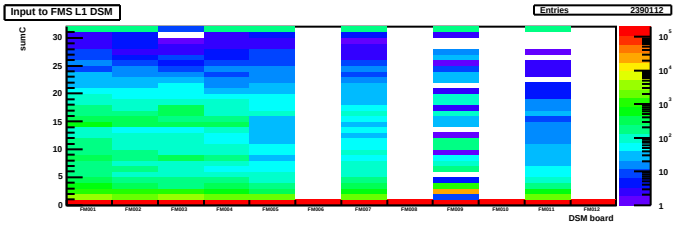

Input to FMS L0 DSM
Entries 20172
HT ADC - simulated
QT board

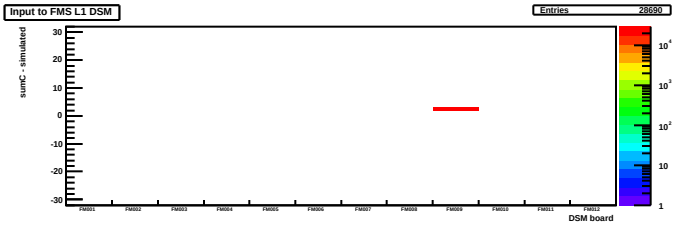

Input to FMS L0 DSM
Entries 9560448
HT ID
QT board

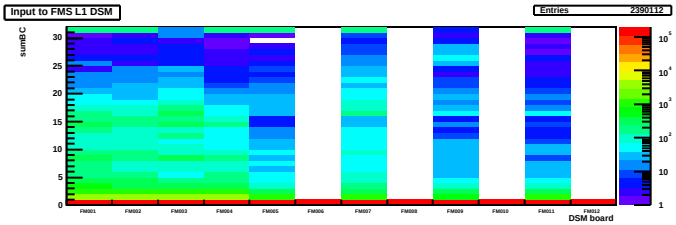

Input to FMS L0 DSM
Entries 220706
HT ID - simulated
QT board

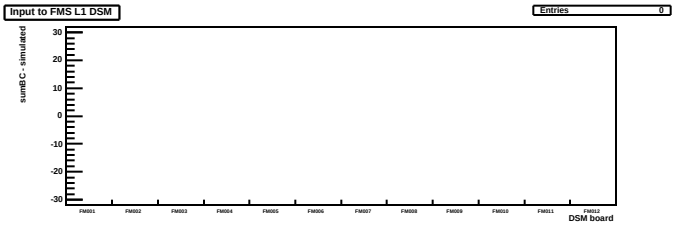

Input to FMS L1 DSM
Entries 2390112
sumD
DSM board

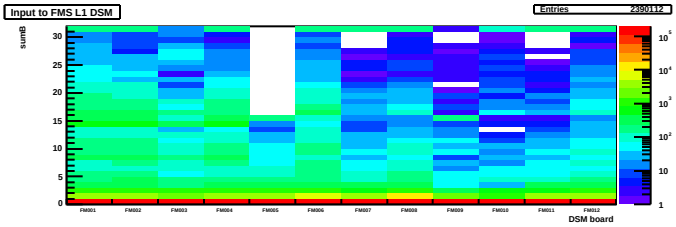

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

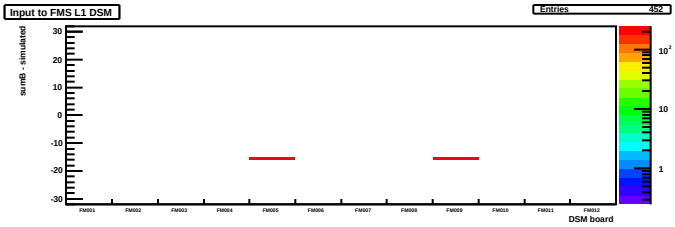

Input to FMS L1 DSM
Entries 2390112
sumC
DSM board

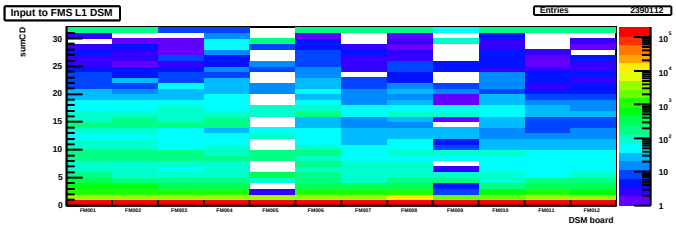

Input to FMS L1 DSM
Entries 28690
sumC - simulated
DSM board

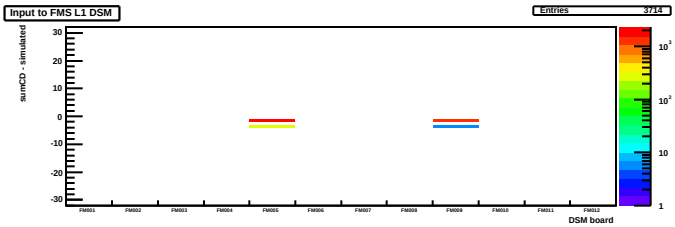

Input to FMS L1 DSM
Entries 2390112
sumBC
DSM board

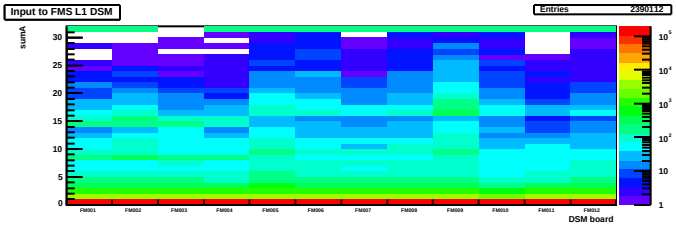

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

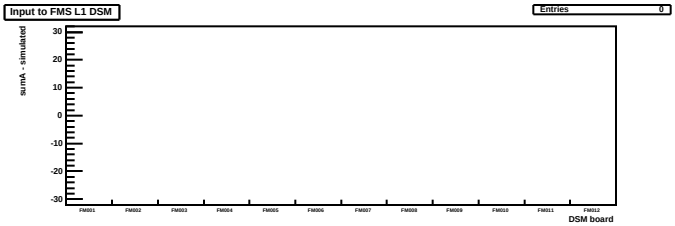

Input to FMS L1 DSM
Entries 2390112
sumB
DSM board

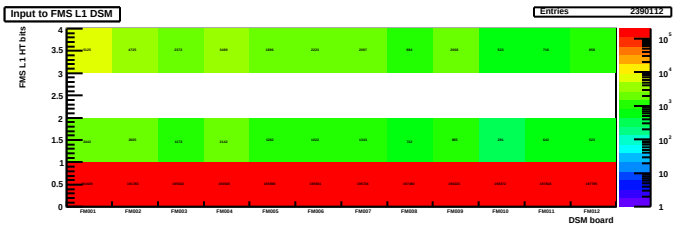

Input to FMS L1 DSM
Entries 452
sumB - simulated
DSM board

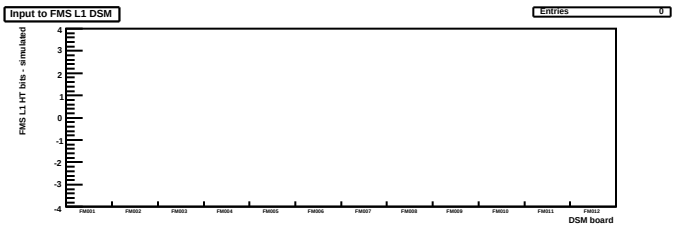

Input to FMS L1 DSM
Entries 2390112
sumCD
DSM board

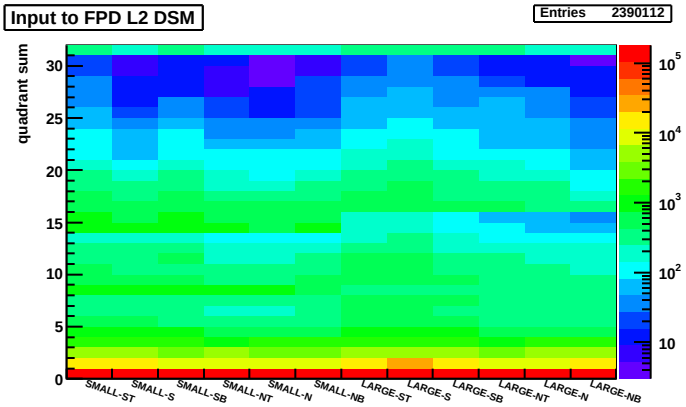

Input to FMS L1 DSM
Entries 3714
sumCD - simulated
DSM board

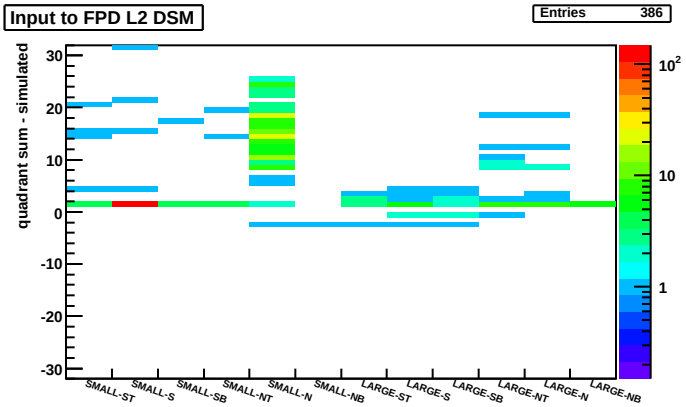

Input to FMS L1 DSM
Entries 2390112
sumA
DSM board

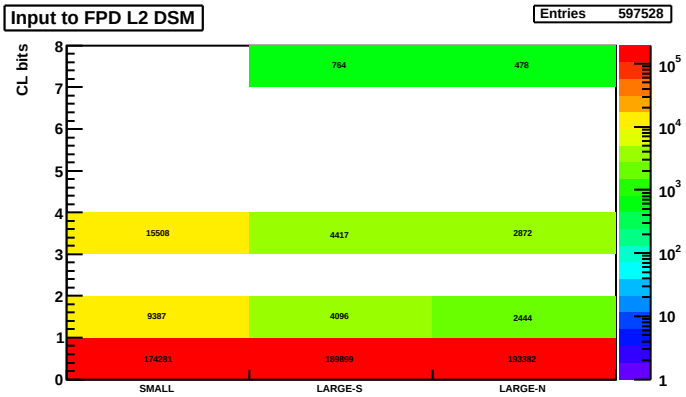

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

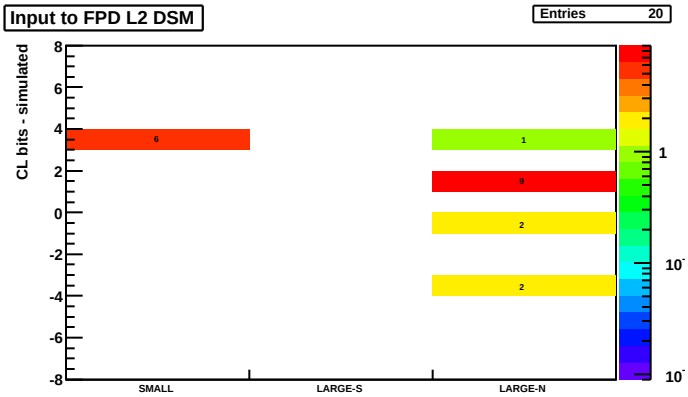

Input to FMS L1 DSM
Entries 2390112
FMS L1 HT bits
DSM board

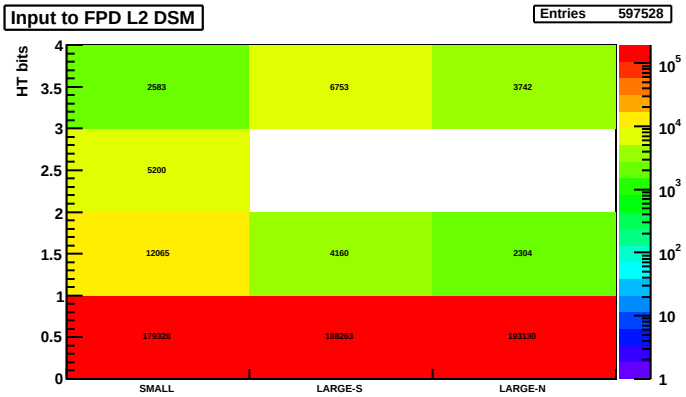

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

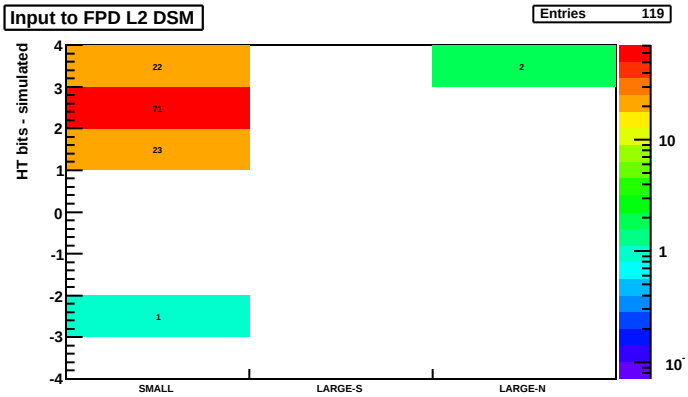

Input to FPD L2 DSM
Entries 2390112
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

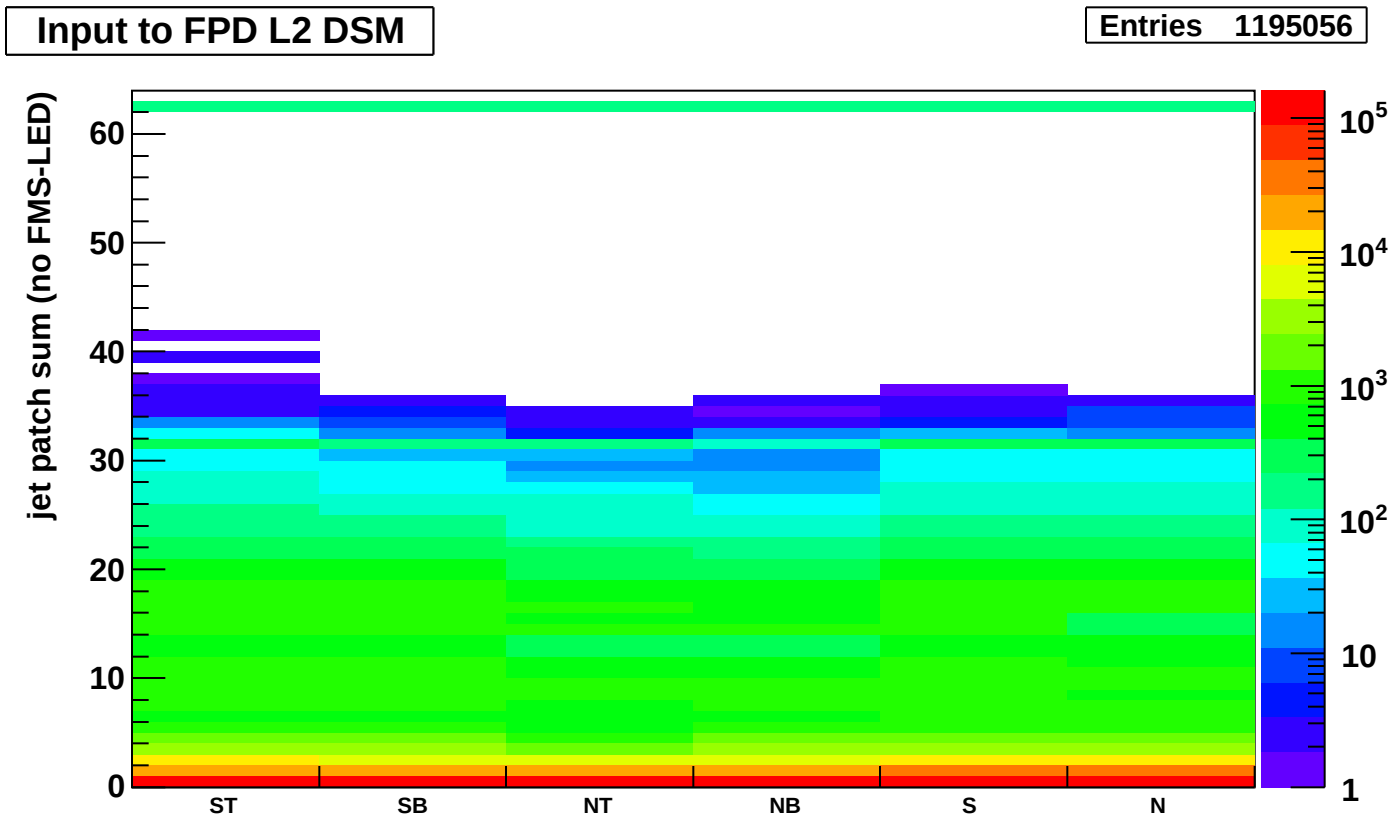

Input to FPD L2 DSM
Entries 386
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

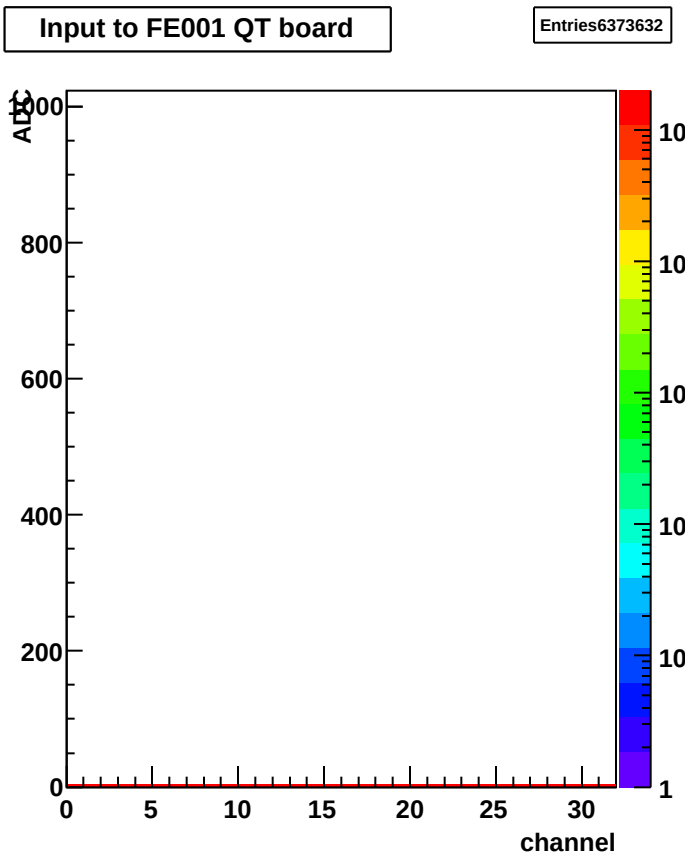

Input to FPD L2 DSM
Entries 597528
CL bits
764
478
15508
4417
2872
9387
4096
2444
174281
189899
193382
SMALL
LARGE-S
LARGE-N

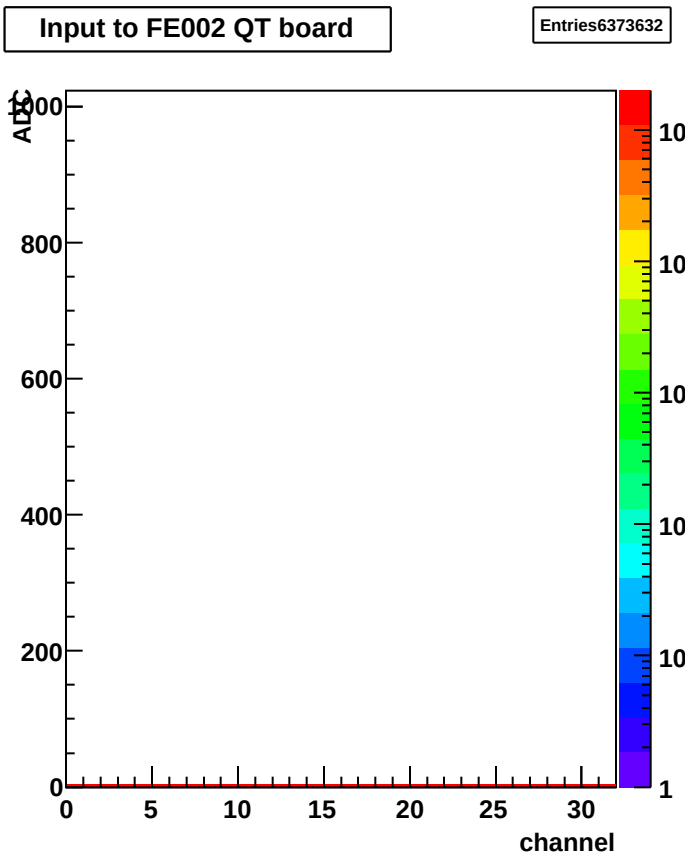

Input to FPD L2 DSM
Entries 20
CL bits - simulated
6
1
9
2
2
SMALL
LARGE-S
LARGE-N

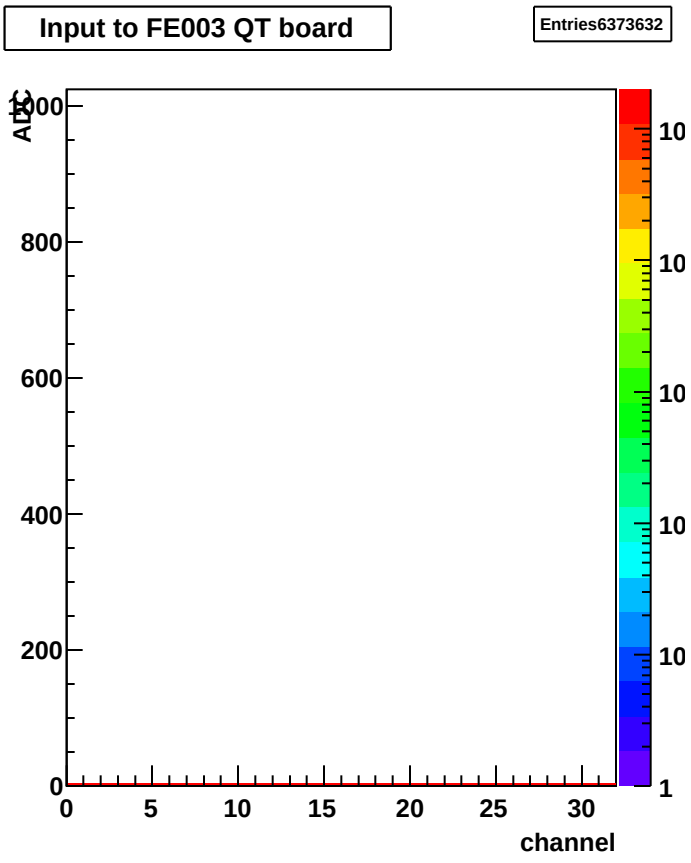

Input to FPD L2 DSM
Entries 597528
HT bits
2583
6753
3742
5200
12065
4160
2304
179328
188263
193130
SMALL
LARGE-S
LARGE-N

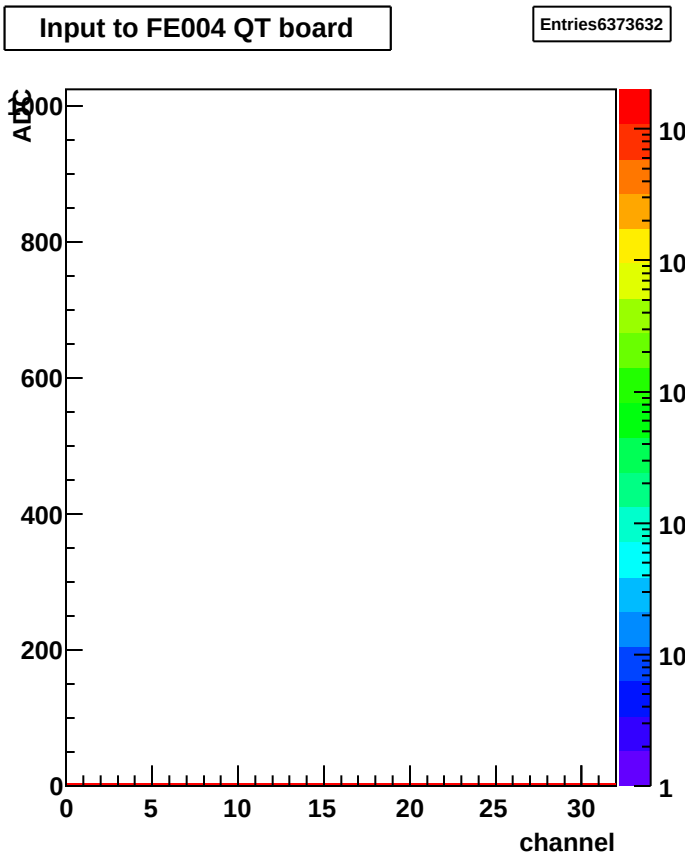

Input to FPD L2 DSM
Entries 119
HT bits - simulated
22
2
71
23
1
SMALL
LARGE-S
LARGE-N

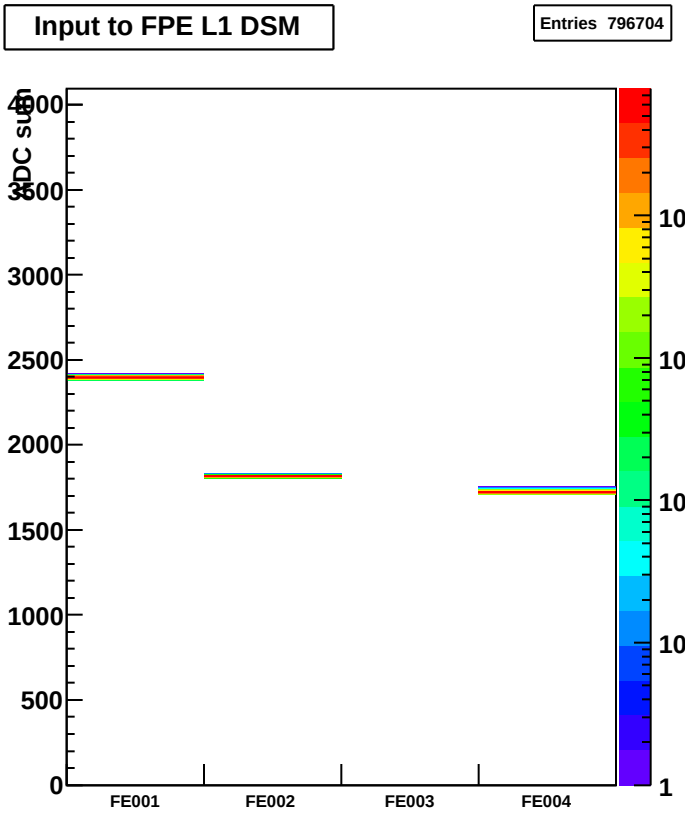

Input to FPD L2 DSM
Entries 1195056
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries6373632
ADC
channel

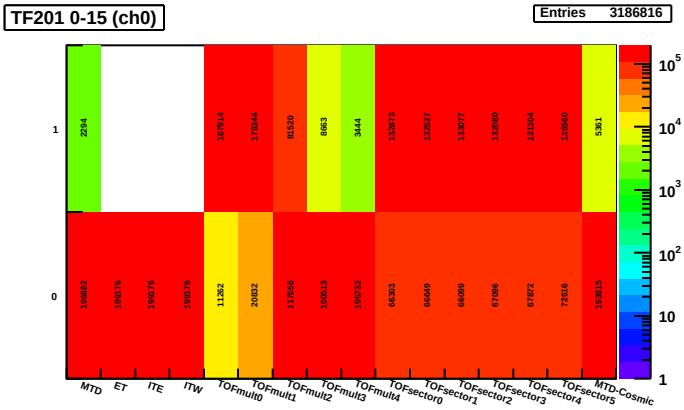

Input to FE002 QT board
Entries6373632
ADC
channel


Input to FE003 QT board
Entries6373632
ADC
channel

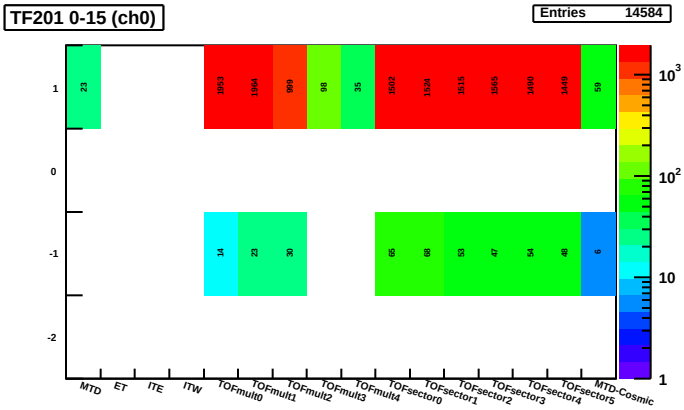

Input to FE004 QT board
Entries6373632
ADC
channel


Input to FPE L1 DSM
Entries 796704
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

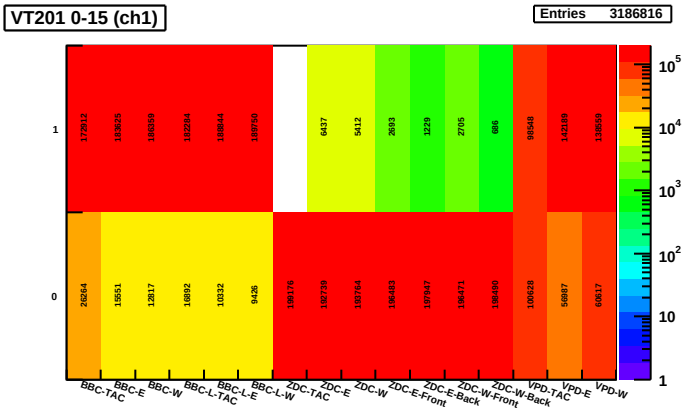
TF201 0-15 (ch0)



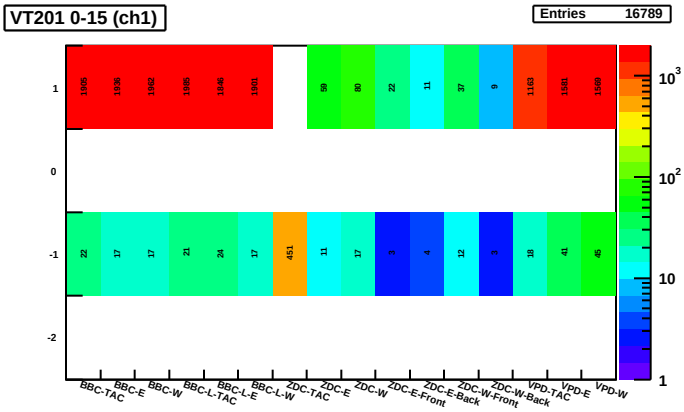
TF201 0-15 (ch0)



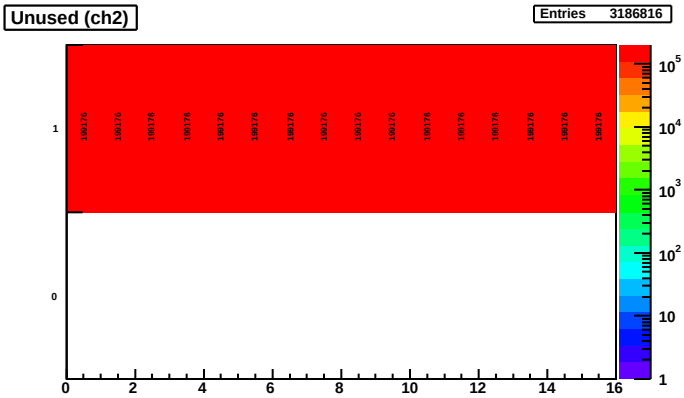
VT201 0-15 (ch1)

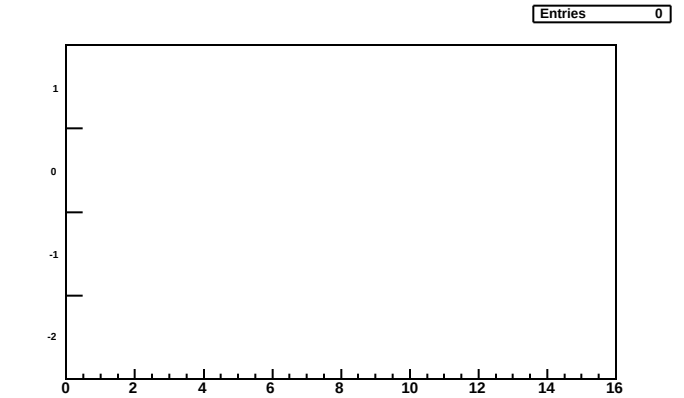


VT201 0-15 (ch1)

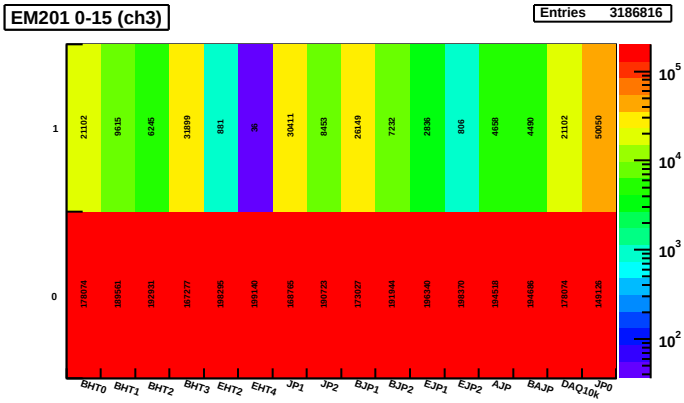


Unused (ch2)

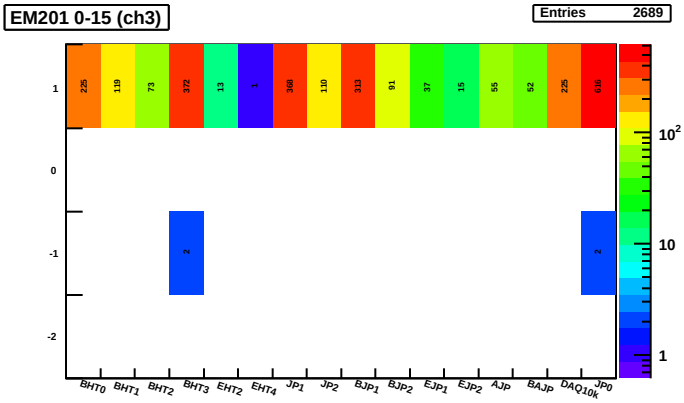




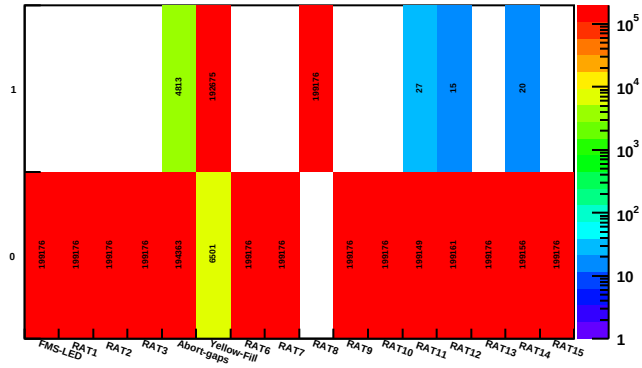
EM201 0-15 (ch3)

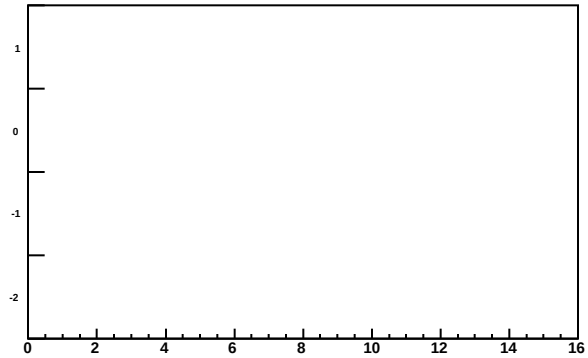


EM201 0-15 (ch3)

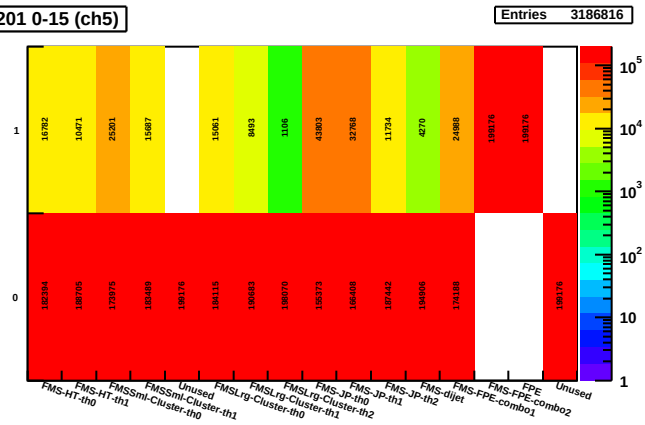


RAT board (ch4)





FP201 0-15 (ch5)



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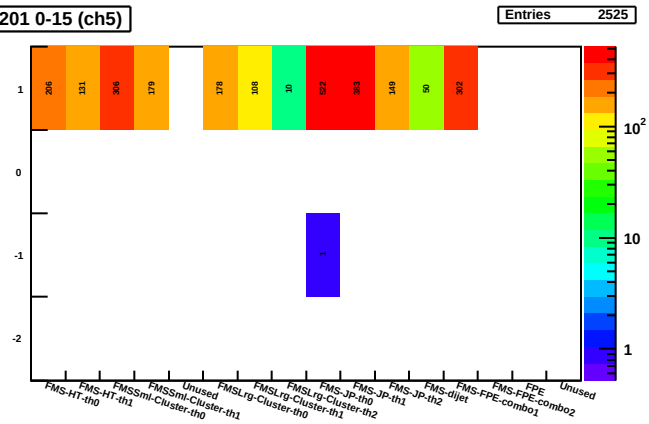

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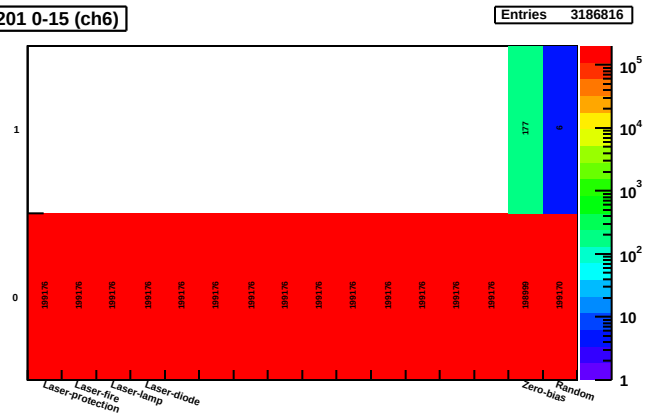

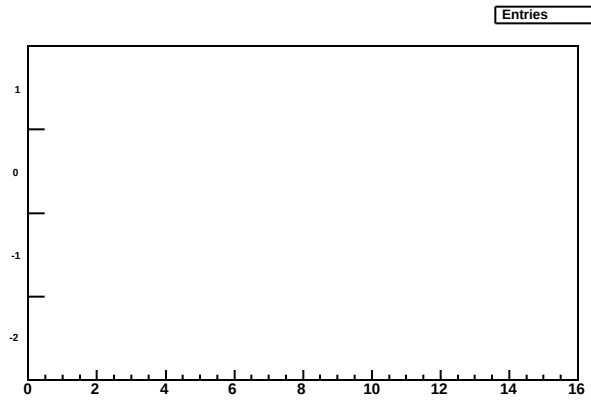
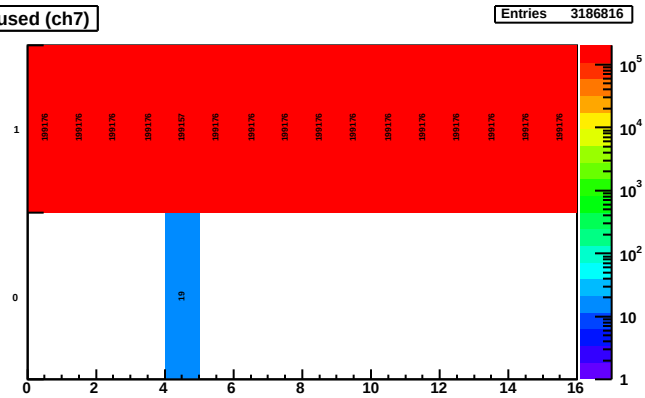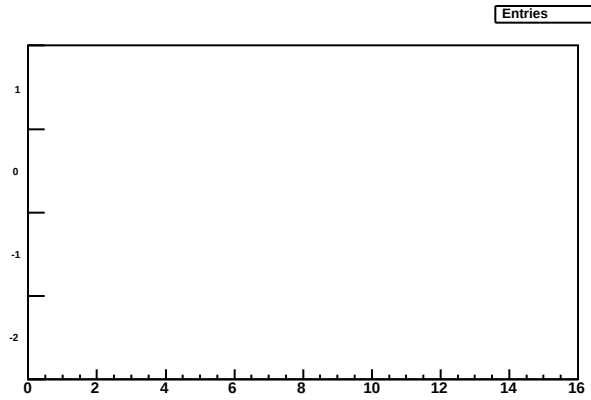
Unused (ch7)