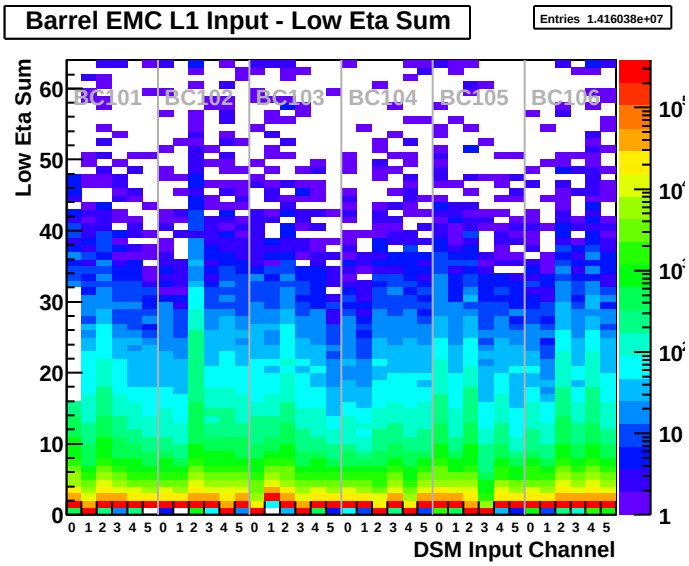
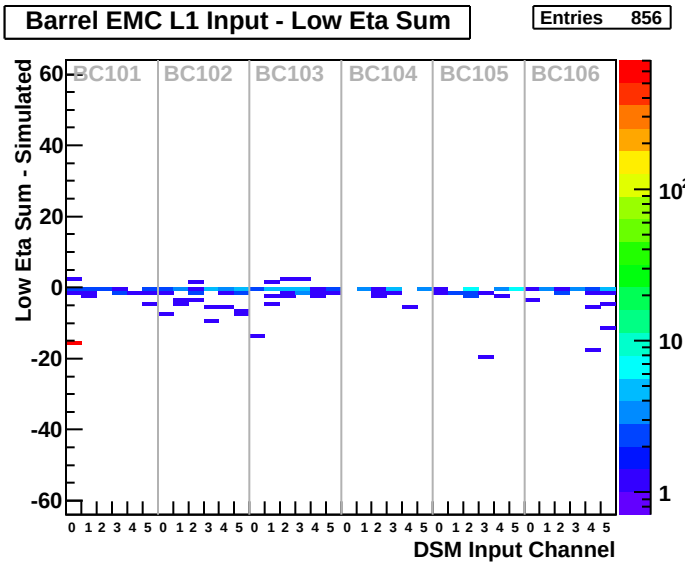
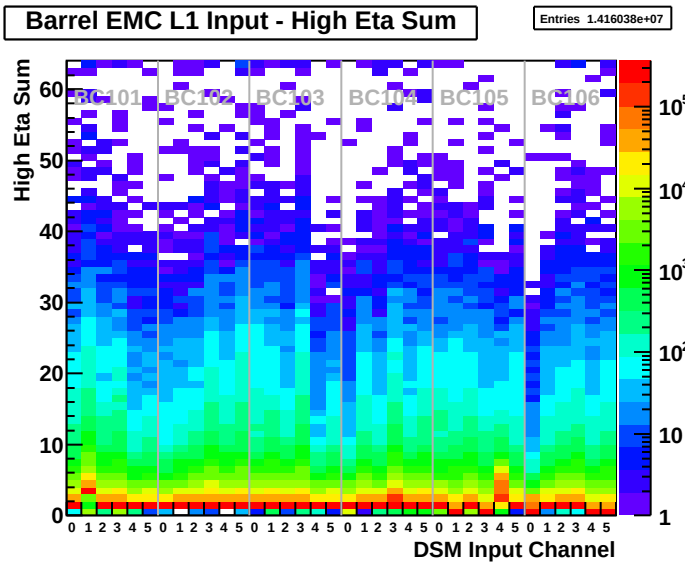
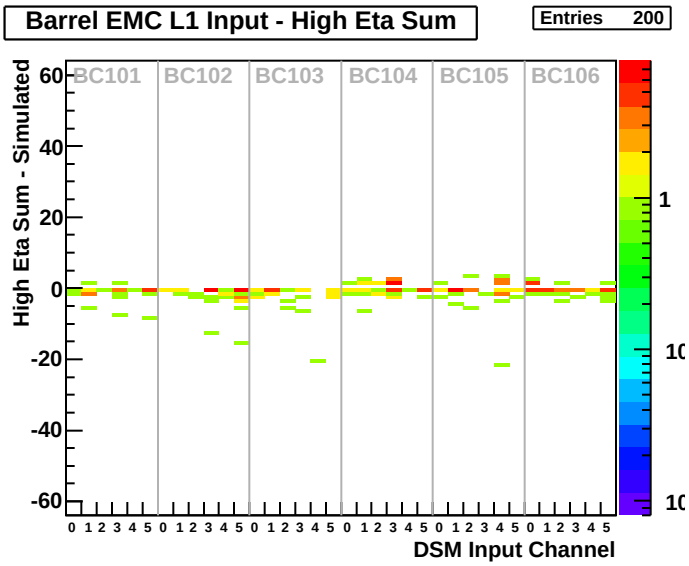
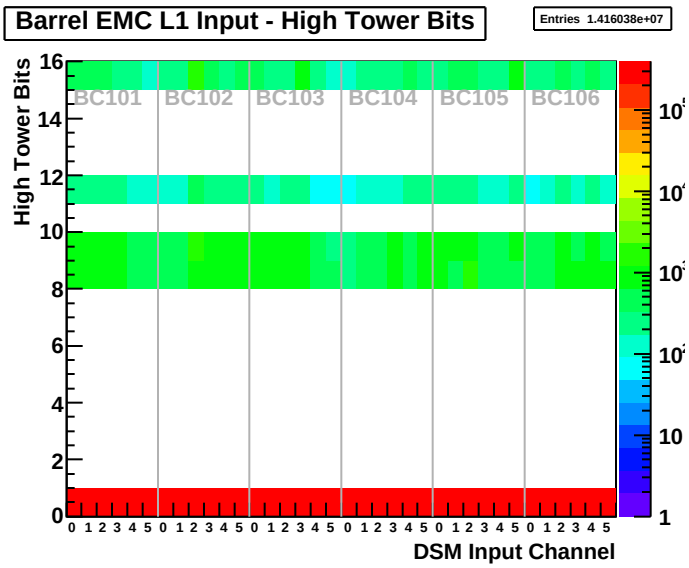
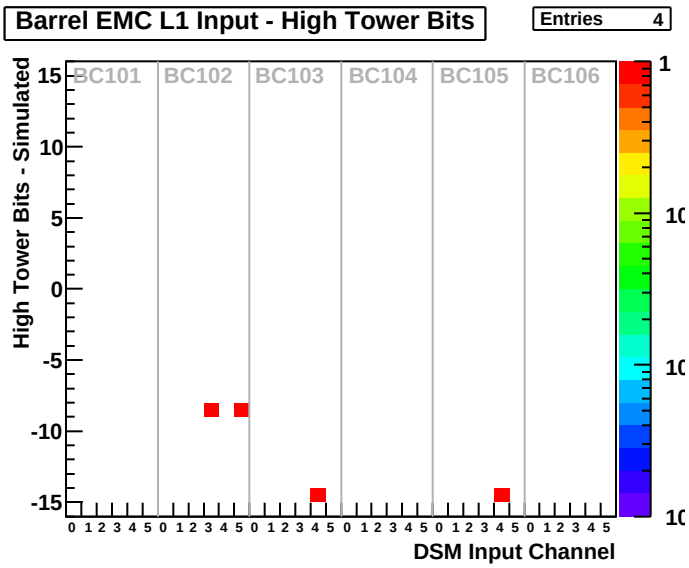

Barrel EMC L1 Input - Low Eta Sum
Entries 1.416038e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

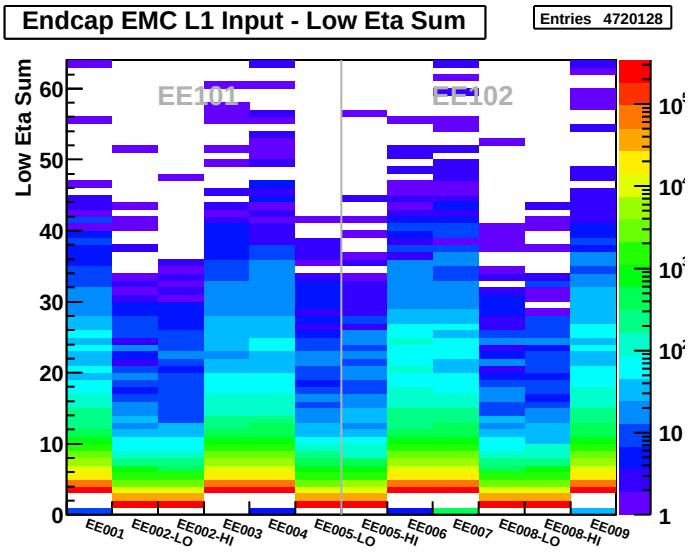

Barrel EMC L1 Input - Low Eta Sum
Entries 856
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

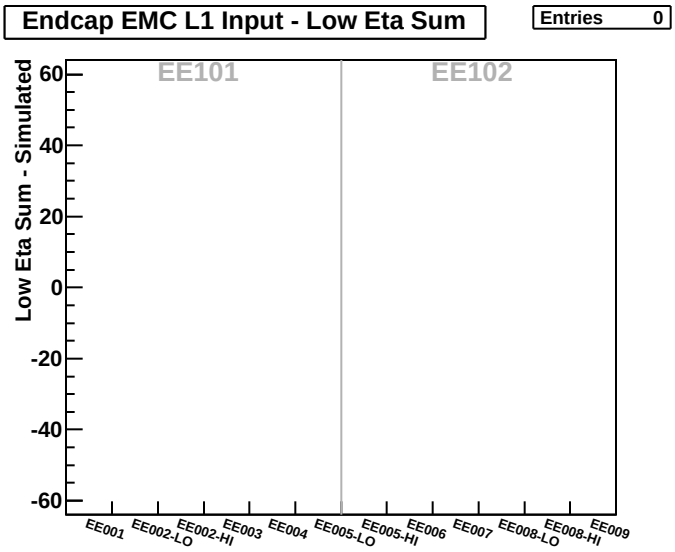

Barrel EMC L1 Input - High Eta Sum
Entries 1.416038e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

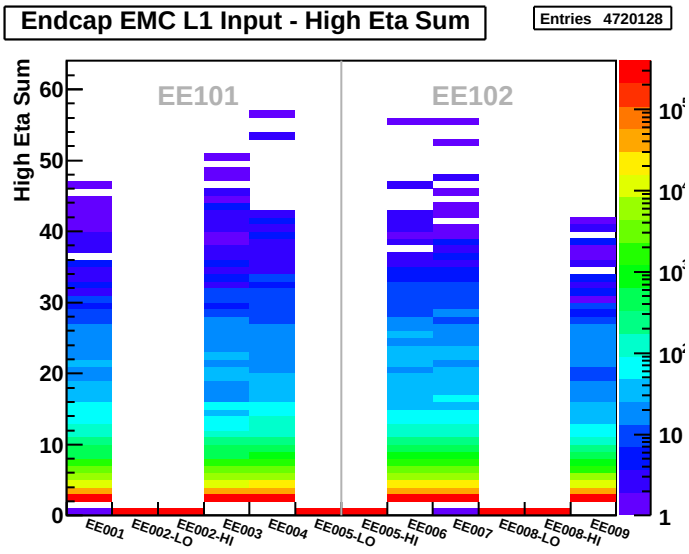

Barrel EMC L1 Input - High Eta Sum
Entries 200
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

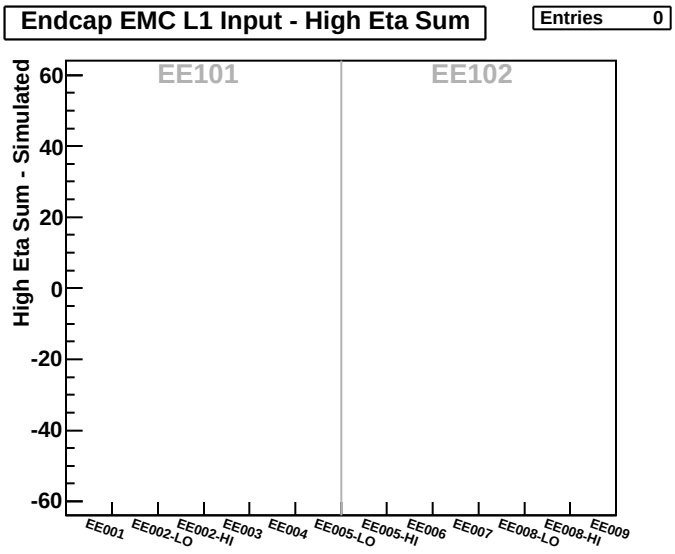

Barrel EMC L1 Input - High Tower Bits
Entries 1.416038e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

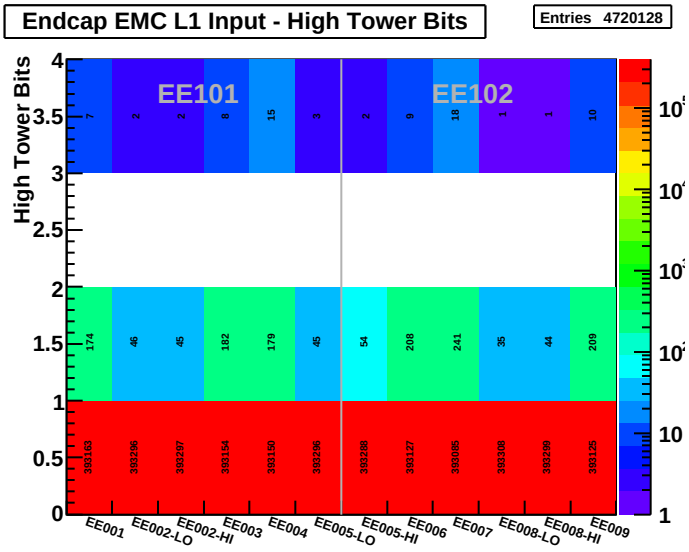

Barrel EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

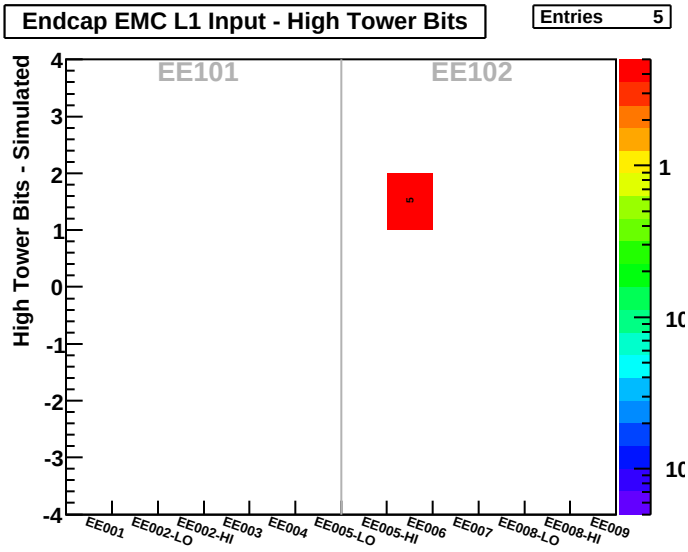

Endcap EMC L1 Input - Low Eta Sum
Entries 4720128
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

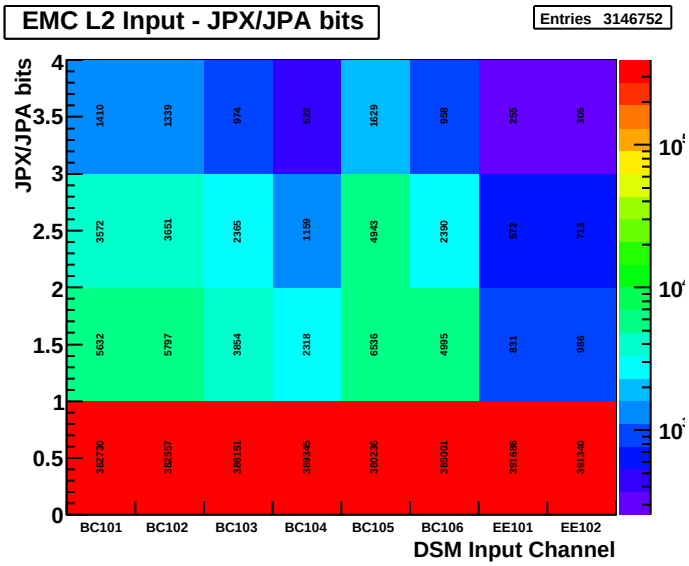

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

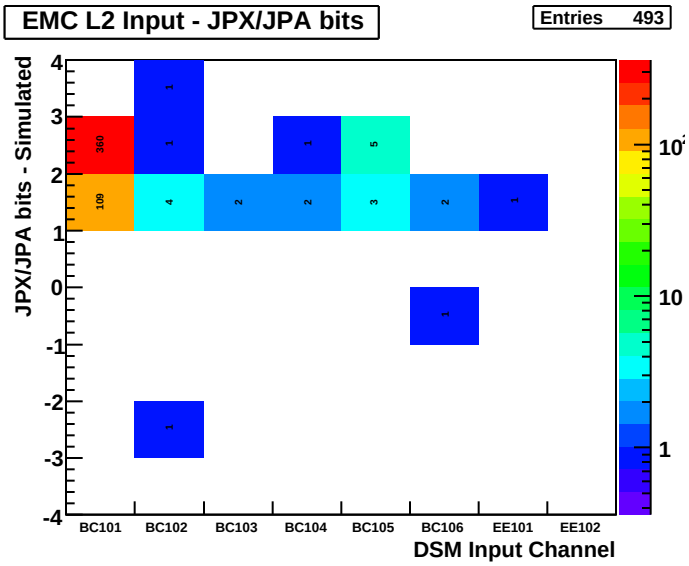

Endcap EMC L1 Input - High Eta Sum
Entries 4720128
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

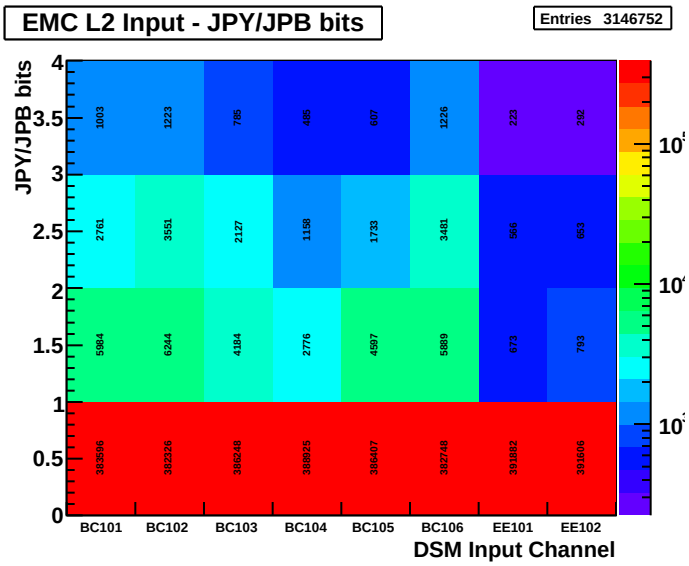

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

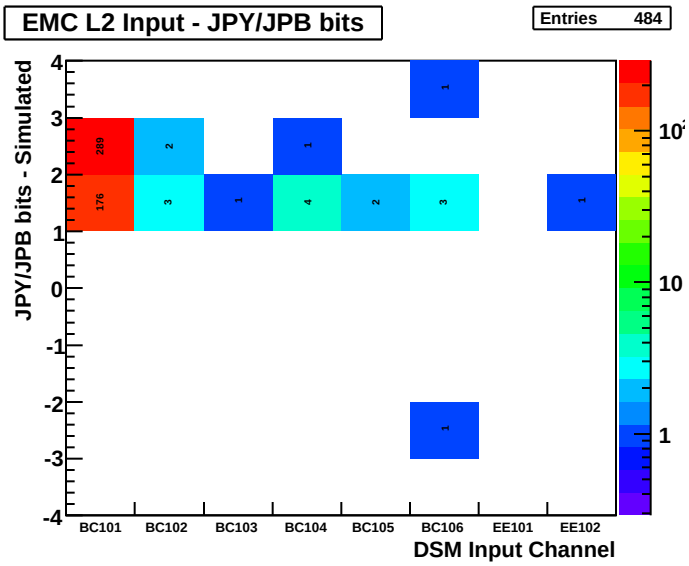

Endcap EMC L1 Input - High Tower Bits
Entries 4720128
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

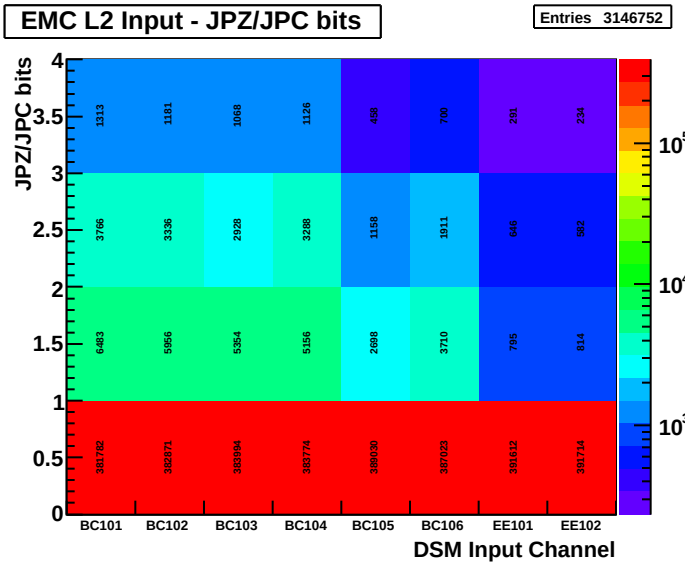

Endcap EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

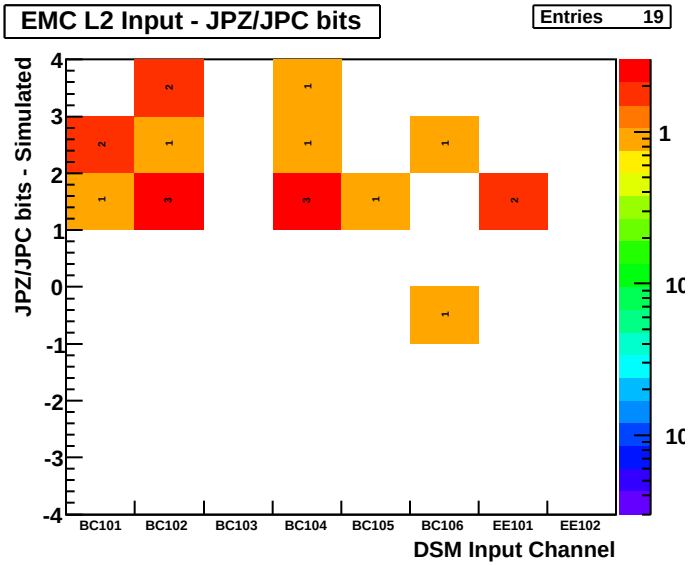

EMC L2 Input - JPX/JPA bits
Entries 3146752
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

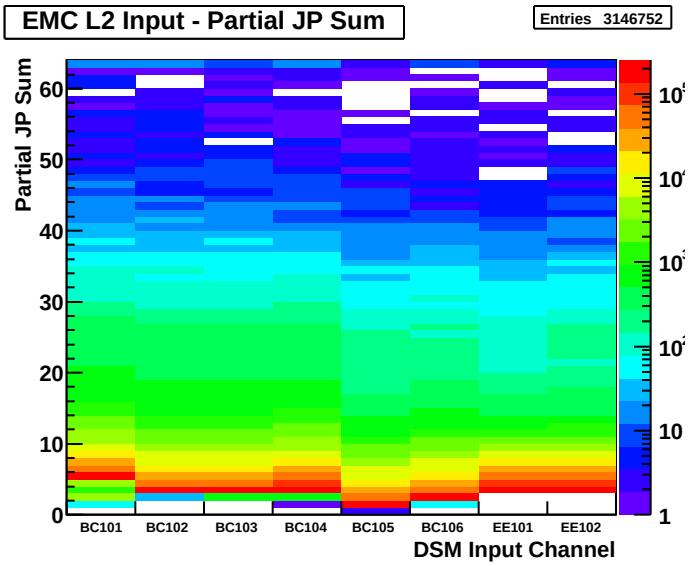

EMC L2 Input - JPX/JPA bits
Entries 493
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

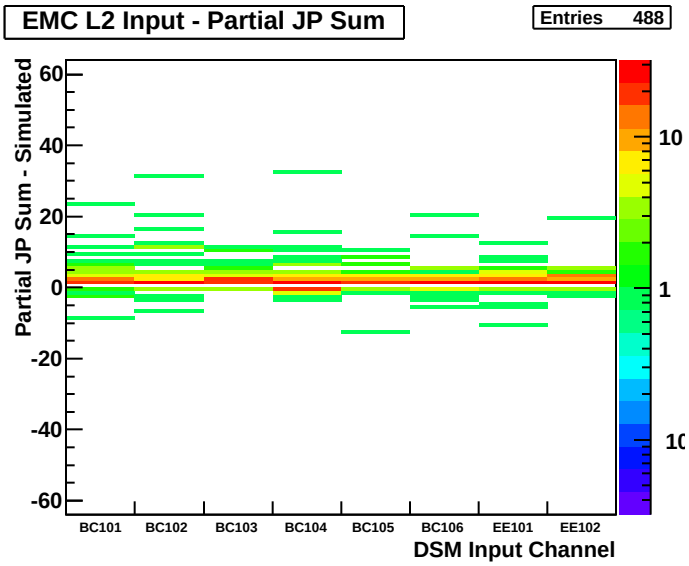

EMC L2 Input - JPY/JPB bits
Entries 3146752
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

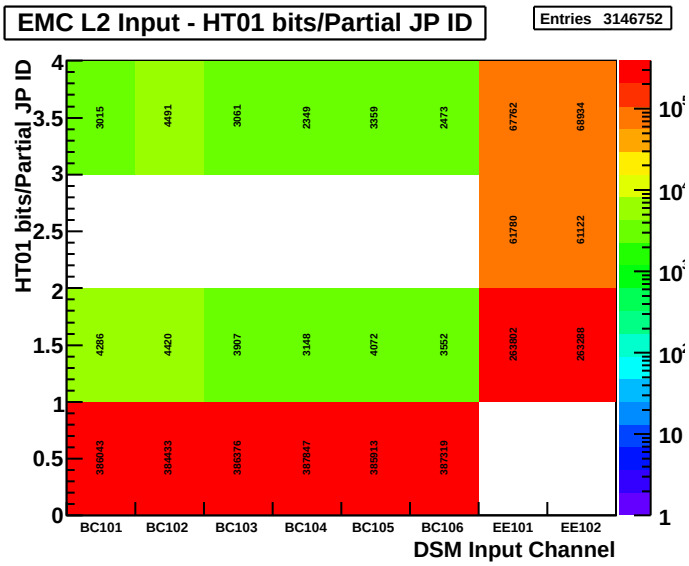

EMC L2 Input - JPY/JPB bits
Entries 484
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

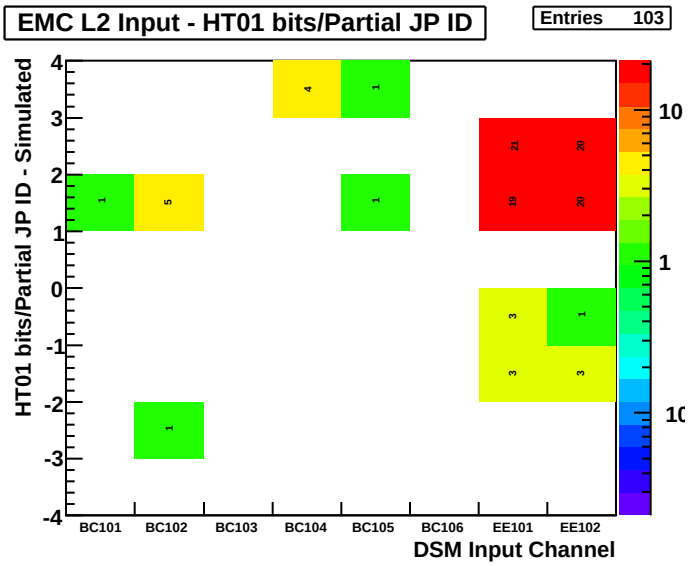

EMC L2 Input - JPZ/JPC bits
Entries 3146752
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

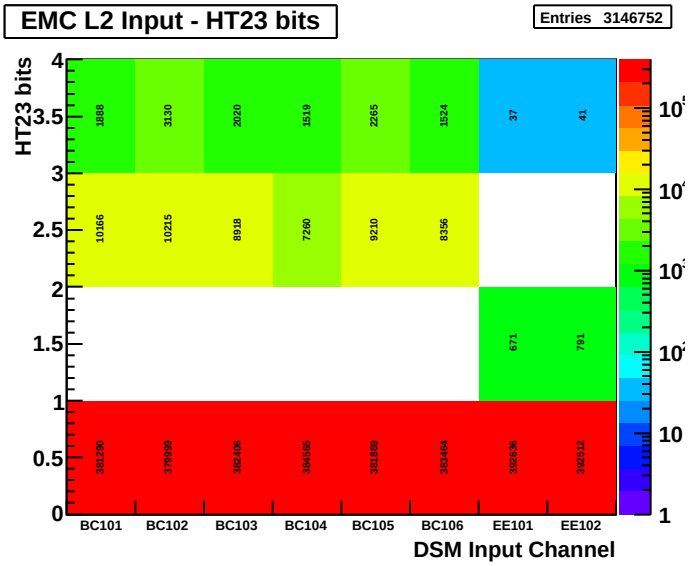

EMC L2 Input - JPZ/JPC bits
Entries 19
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

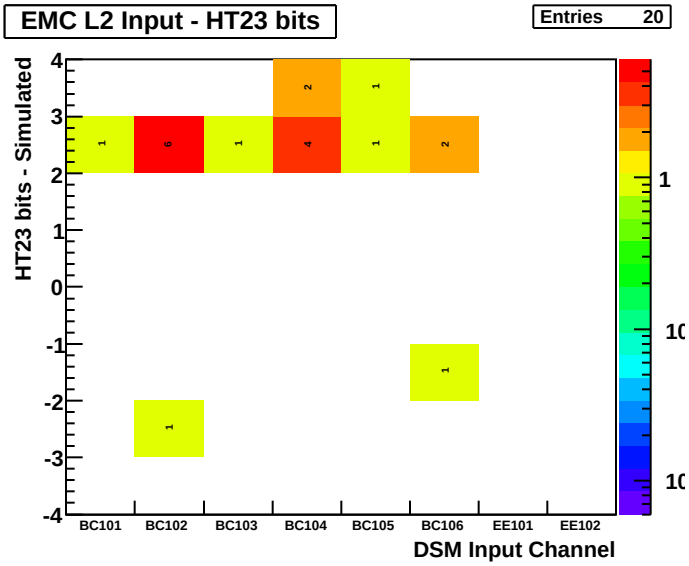

EMC L2 Input - Partial JP Sum
Entries 3146752
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

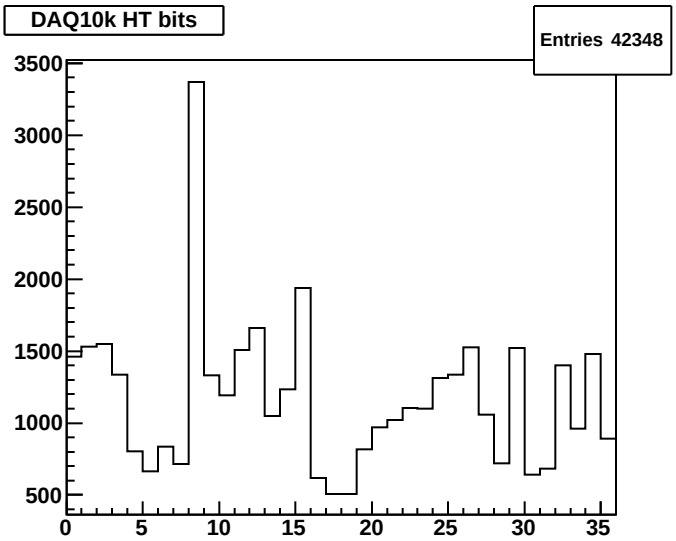

EMC L2 Input - Partial JP Sum
Entries 488
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

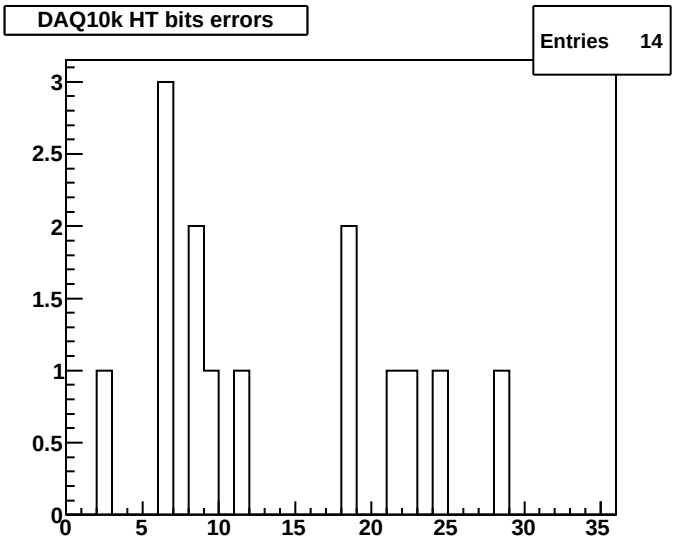

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3146752
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

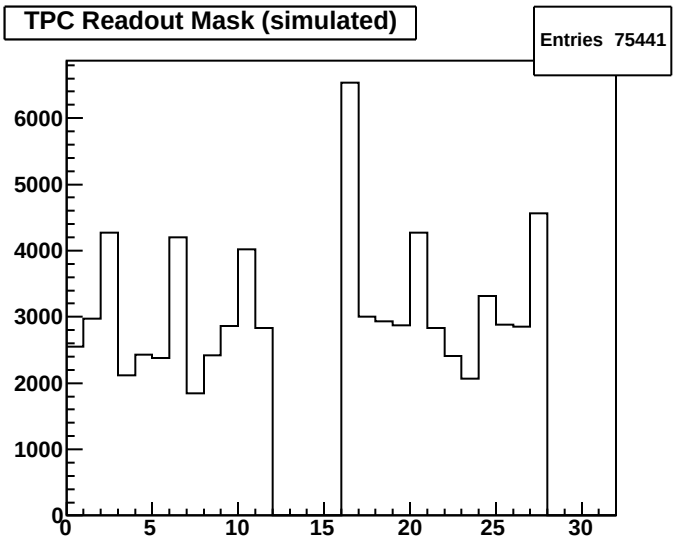

EMC L2 Input - HT01 bits/Partial JP ID
Entries 103
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

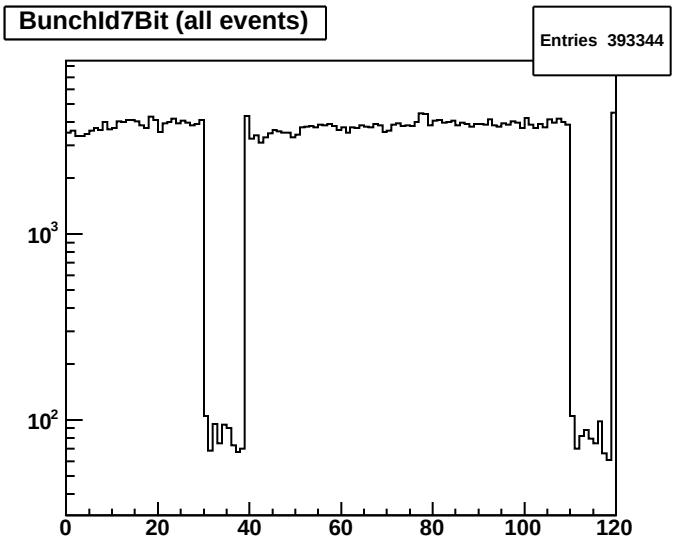

EMC L2 Input - HT23 bits
Entries 3146752
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

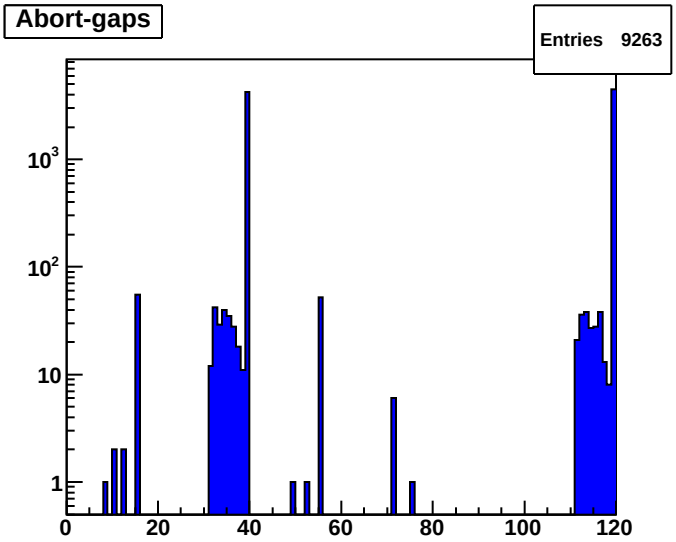

EMC L2 Input - HT23 bits
Entries 20
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

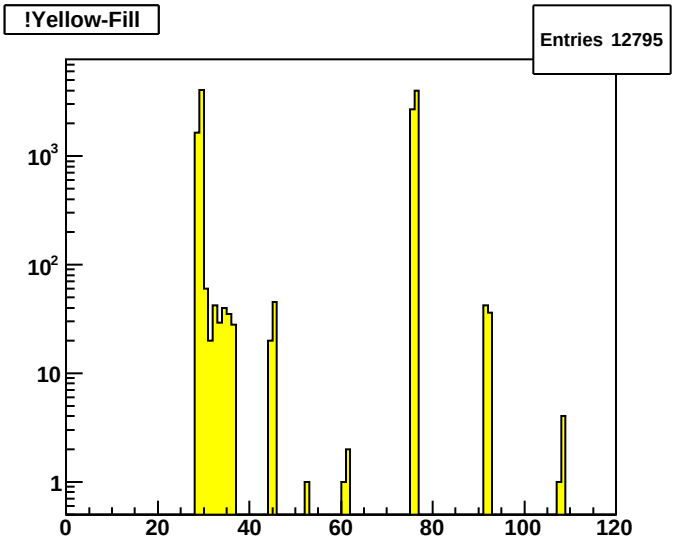

DAQ10k HT bits
Entries 42348

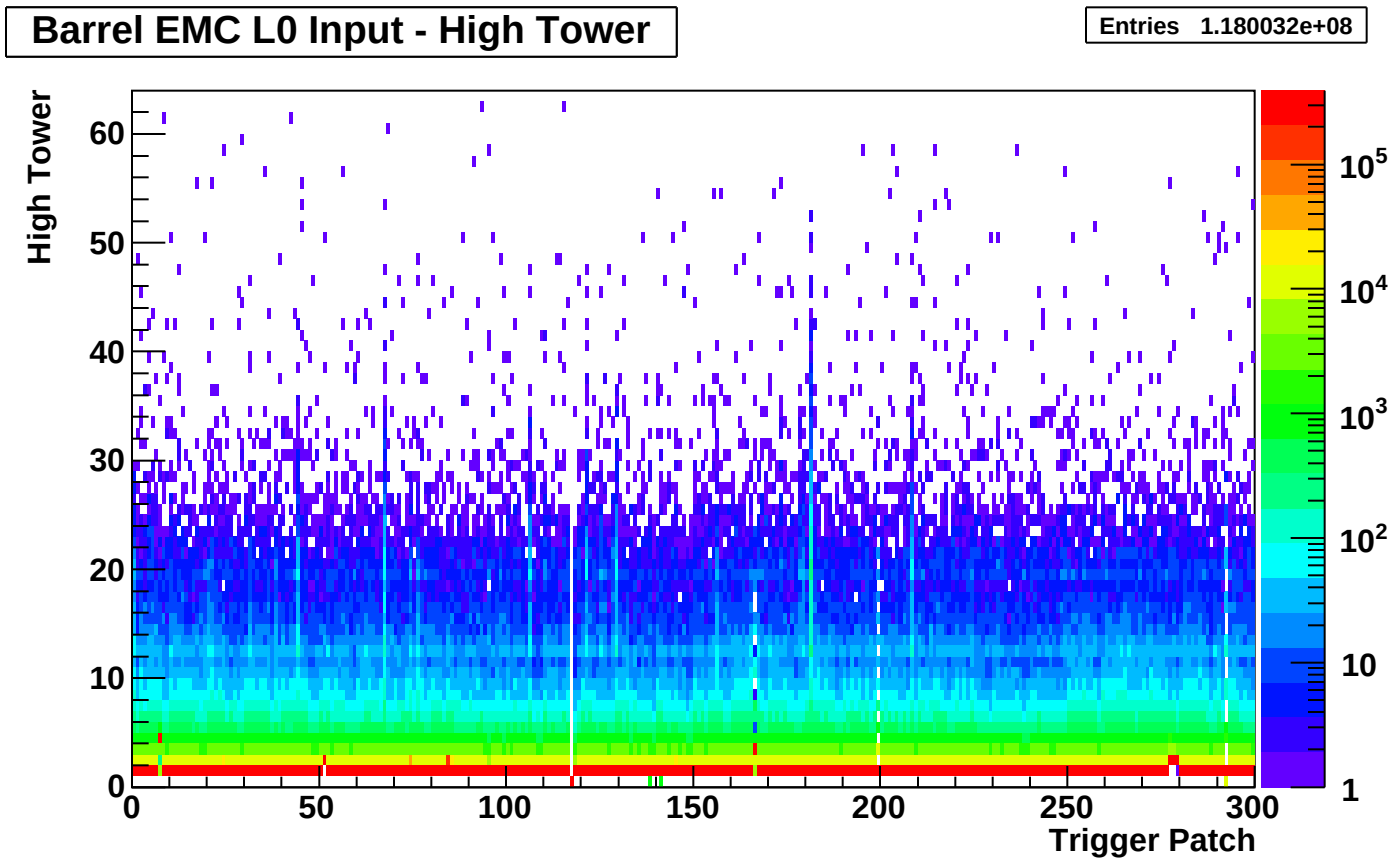

DAQ10k HT bits errors
Entries 14

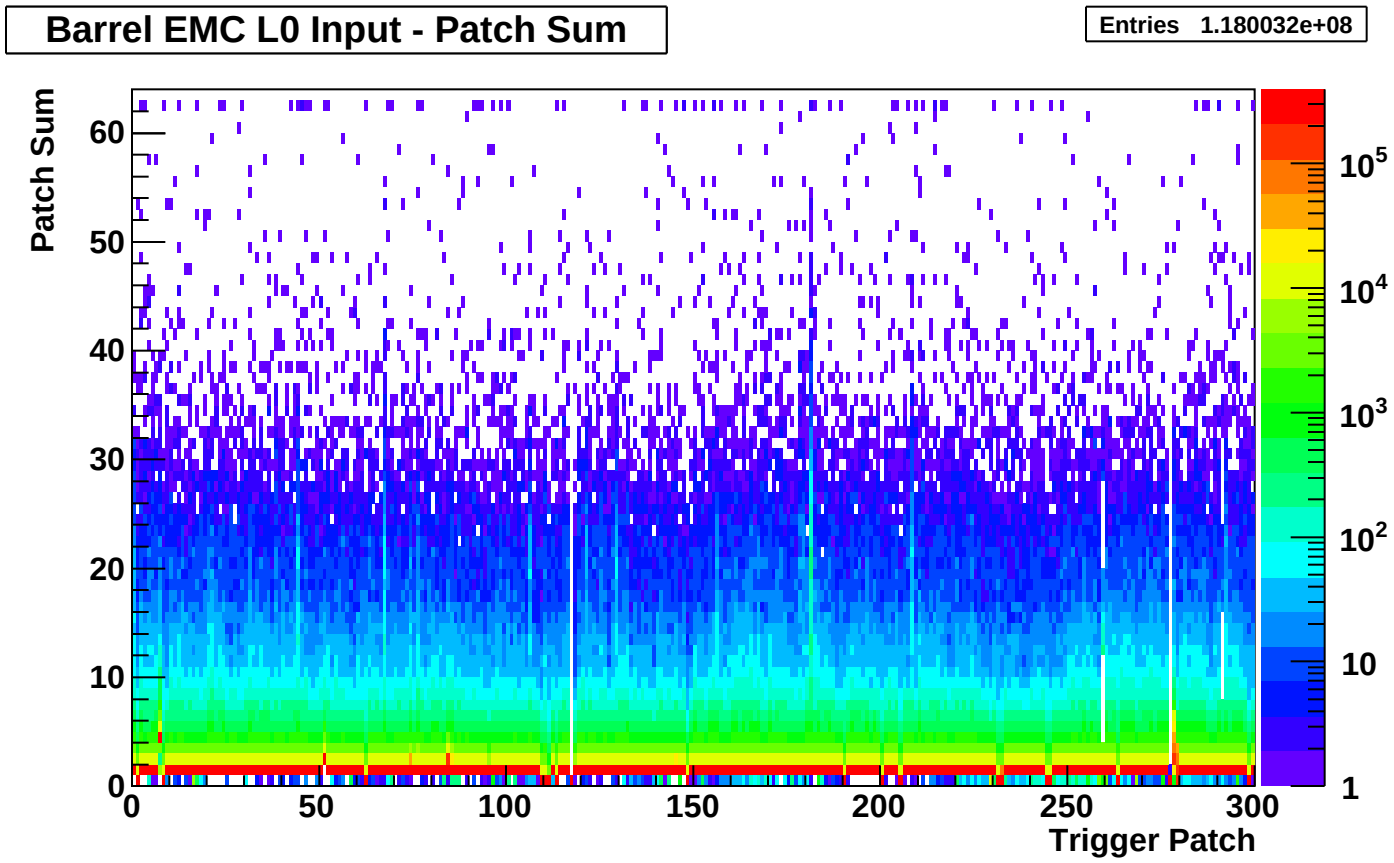

TPC Readout Mask (simulated)
Entries 75441

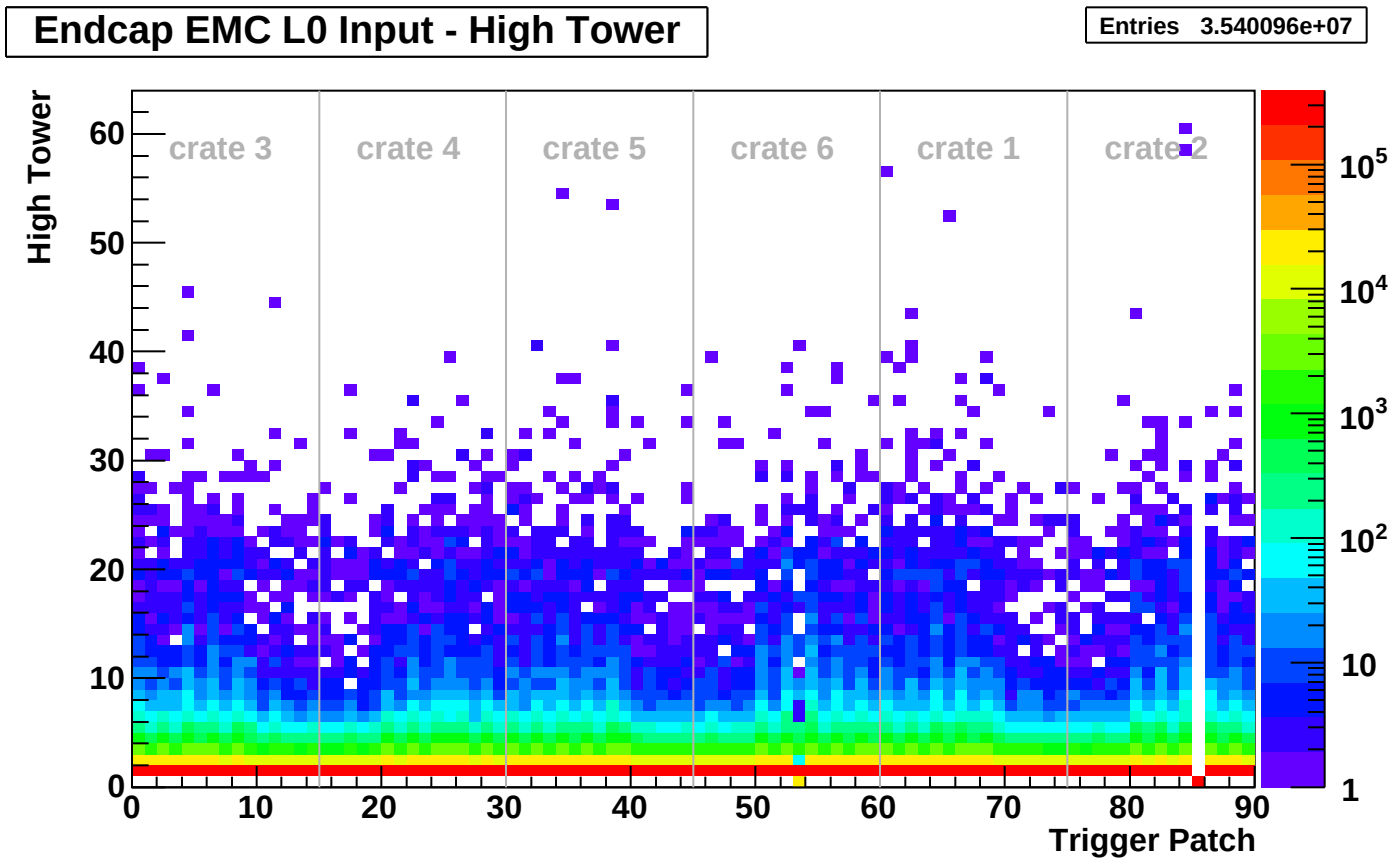

BunchId7Bit (all events)
Entries 393344

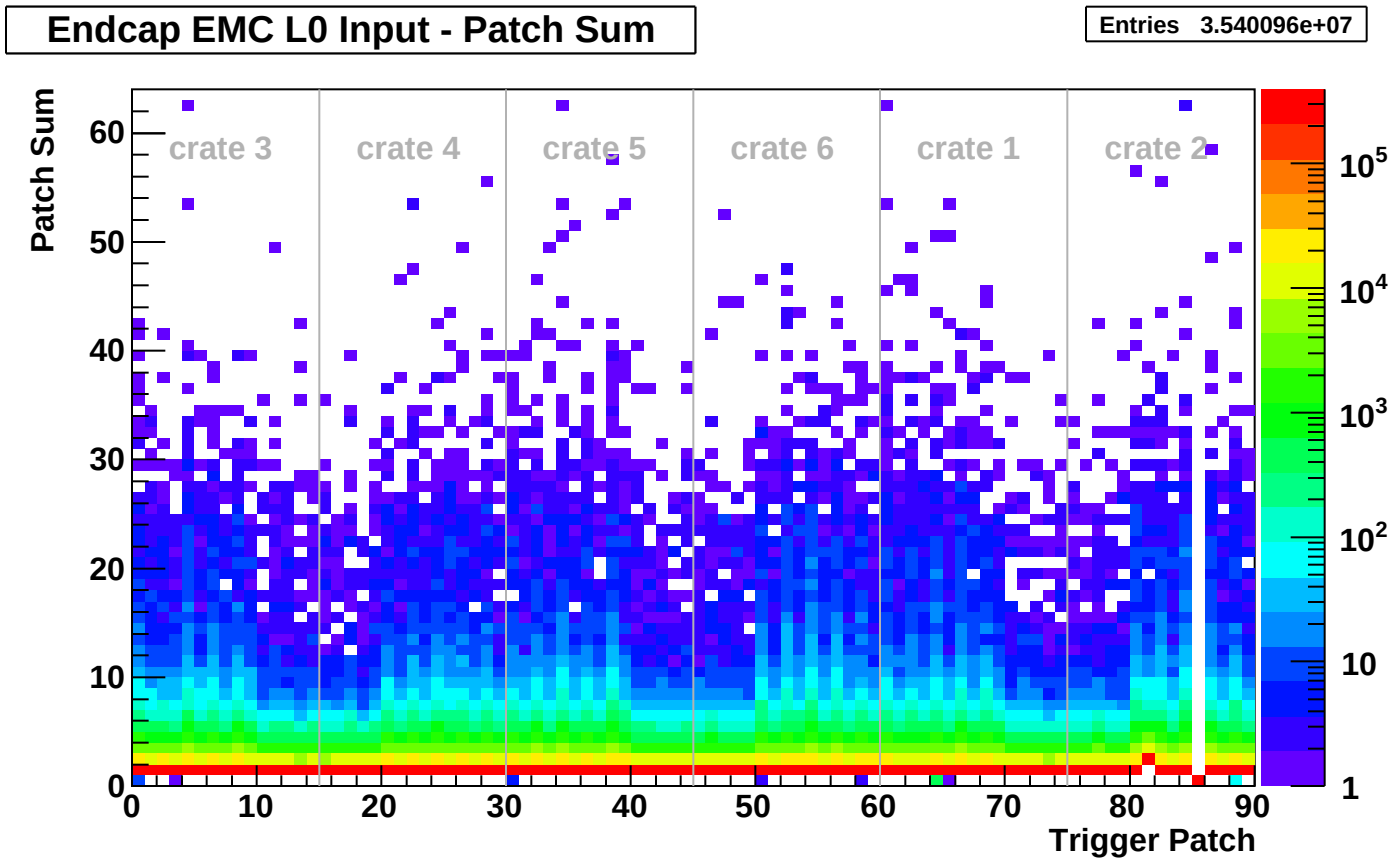

Abort-gaps
Entries 9263

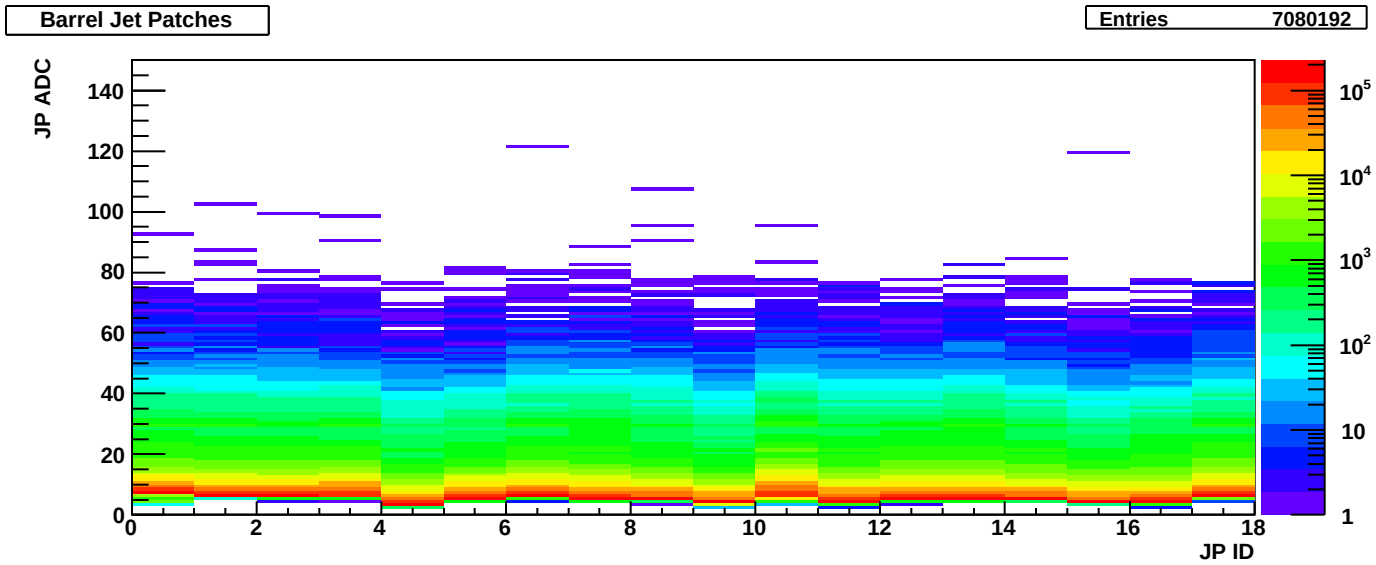

!Yellow-Fill
Entries 12795

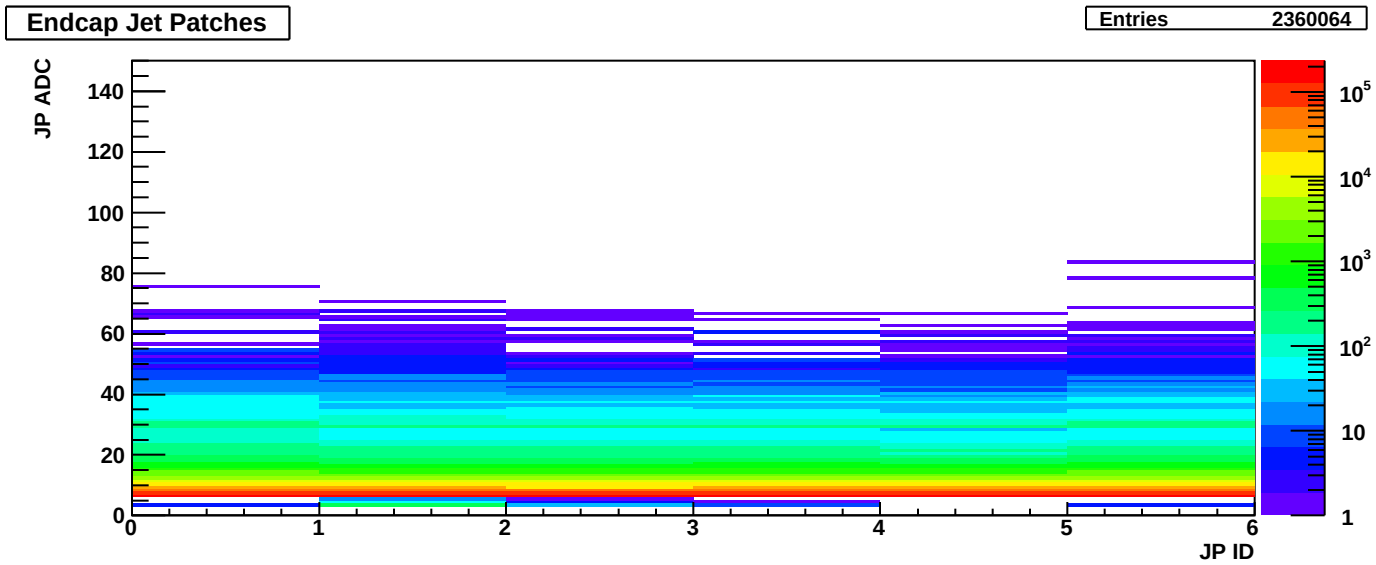

Barrel EMC L0 Input - High Tower
Entries 1.180032e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

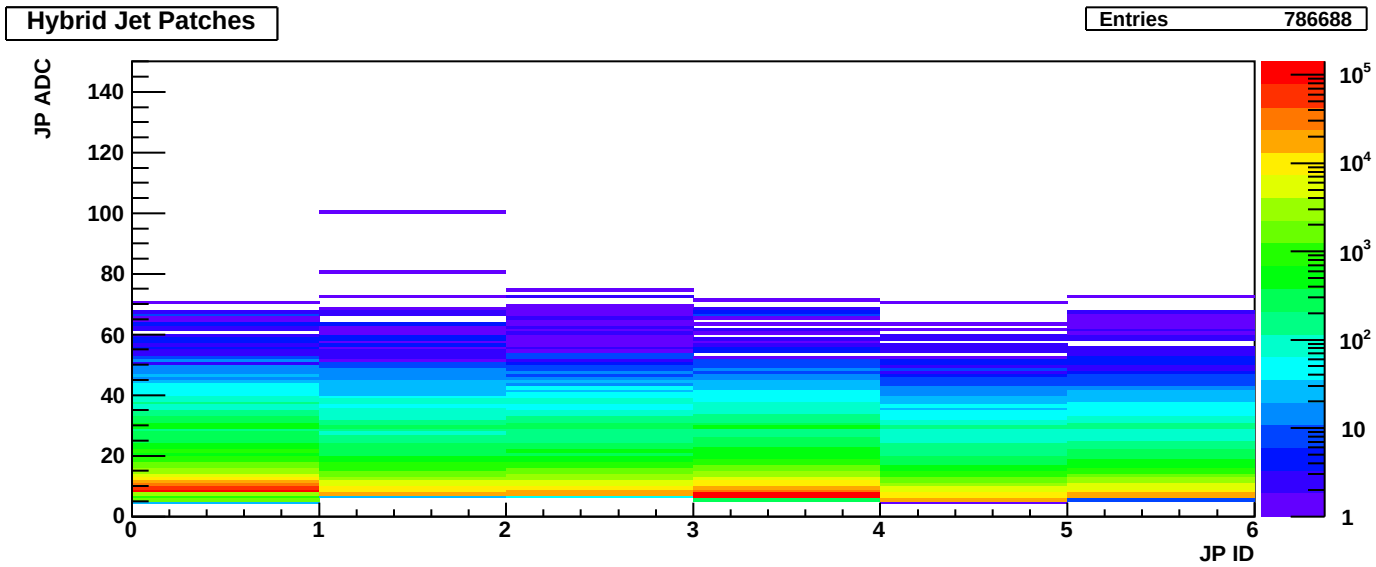

Barrel EMC L0 Input - Patch Sum
Entries 1.180032e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

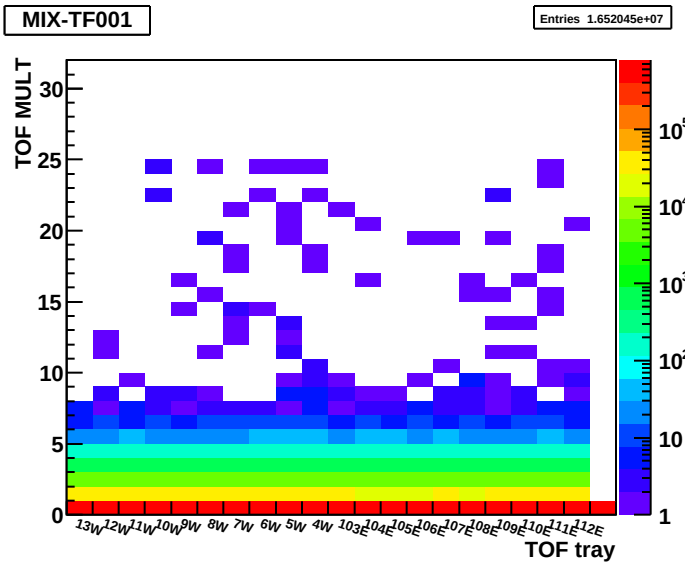

Endcap EMC L0 Input - High Tower
Entries 3.540096e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

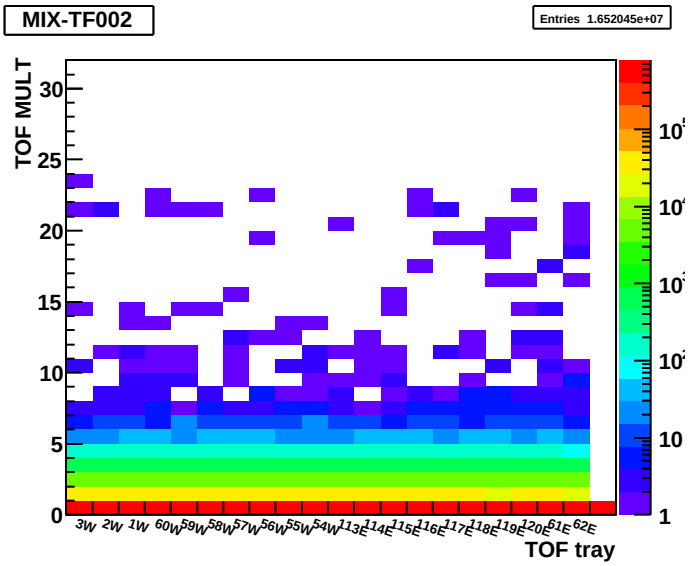

Endcap EMC L0 Input - Patch Sum
Entries 3.540096e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

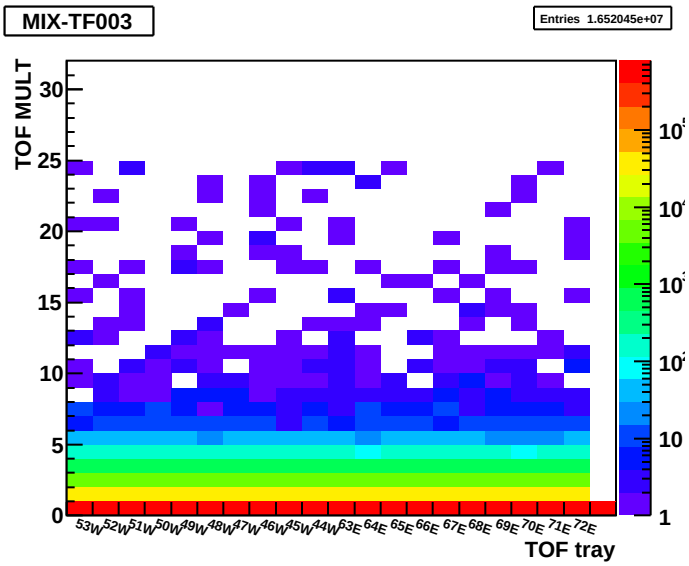

Barrel Jet Patches
Entries 7080192
JP ADC
JP ID

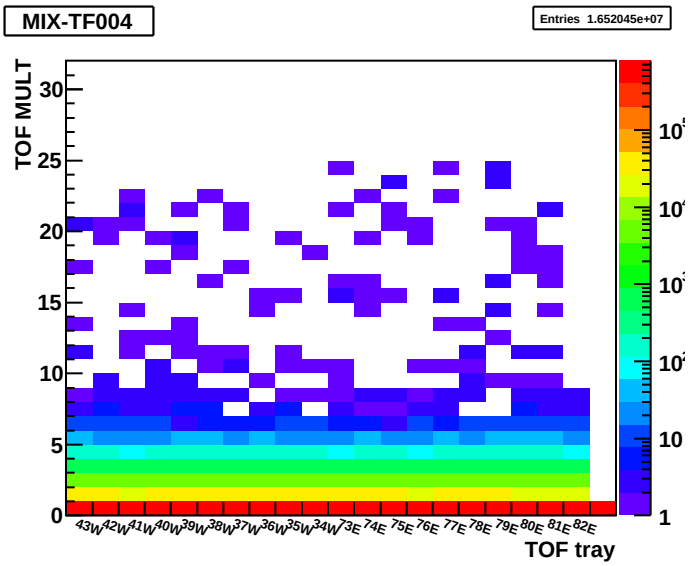

Endcap Jet Patches
Entries 2360064
JP ADC
JP ID

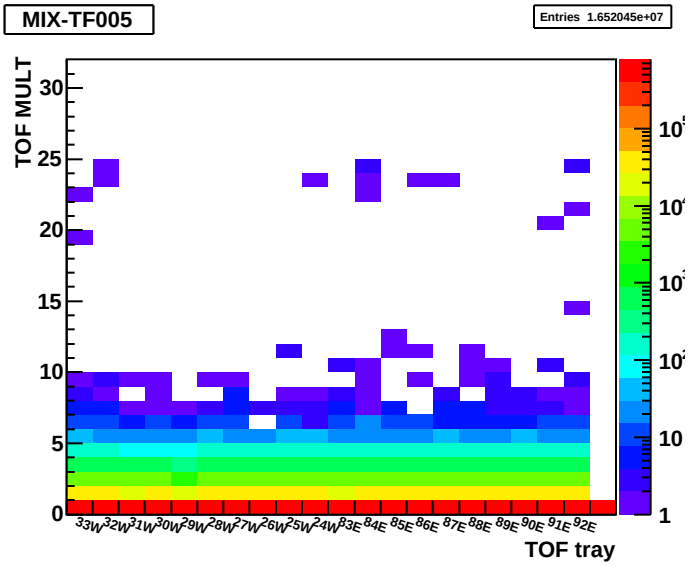

Hybrid Jet Patches
Entries 786688
JP ADC
JP ID

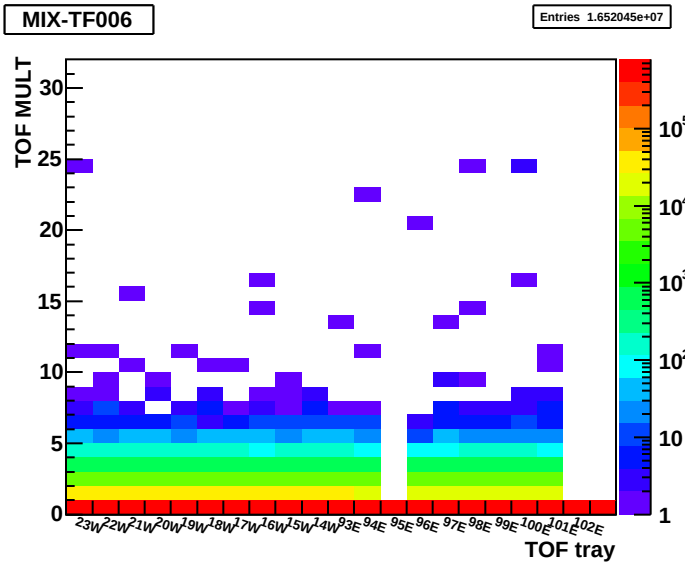

MIX-TF001
Entries 1.652045e+07
TOF MULT
TOF tray

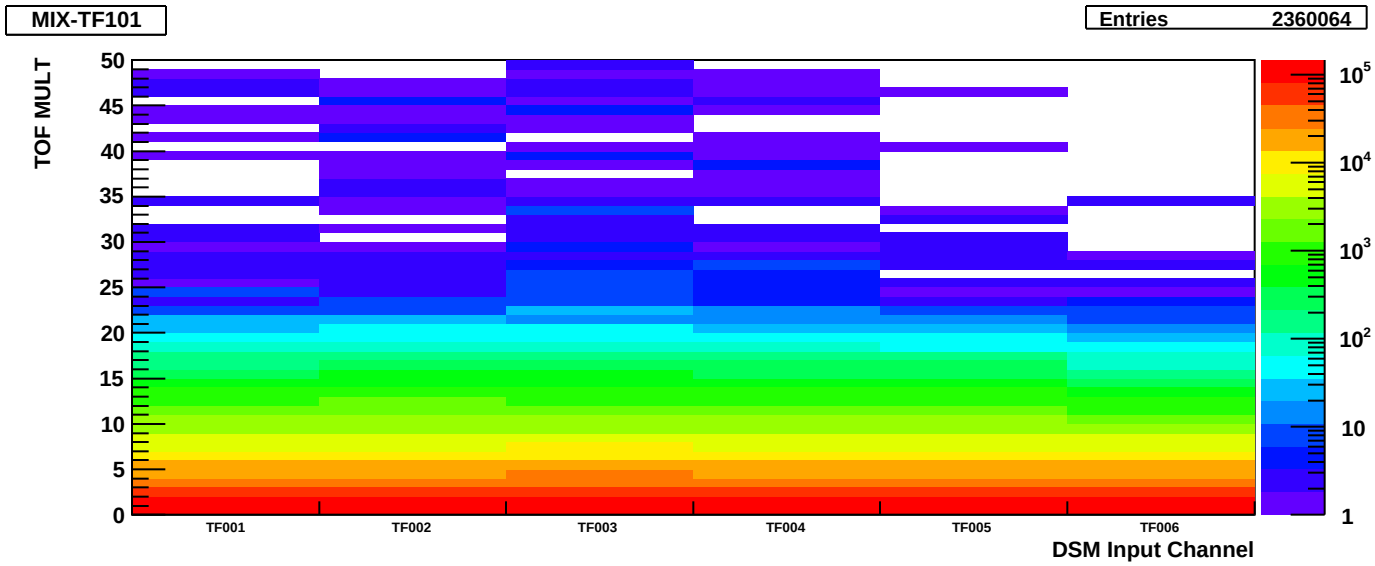

MIX-TF002
Entries 1.652045e+07
TOF MULT
TOF tray

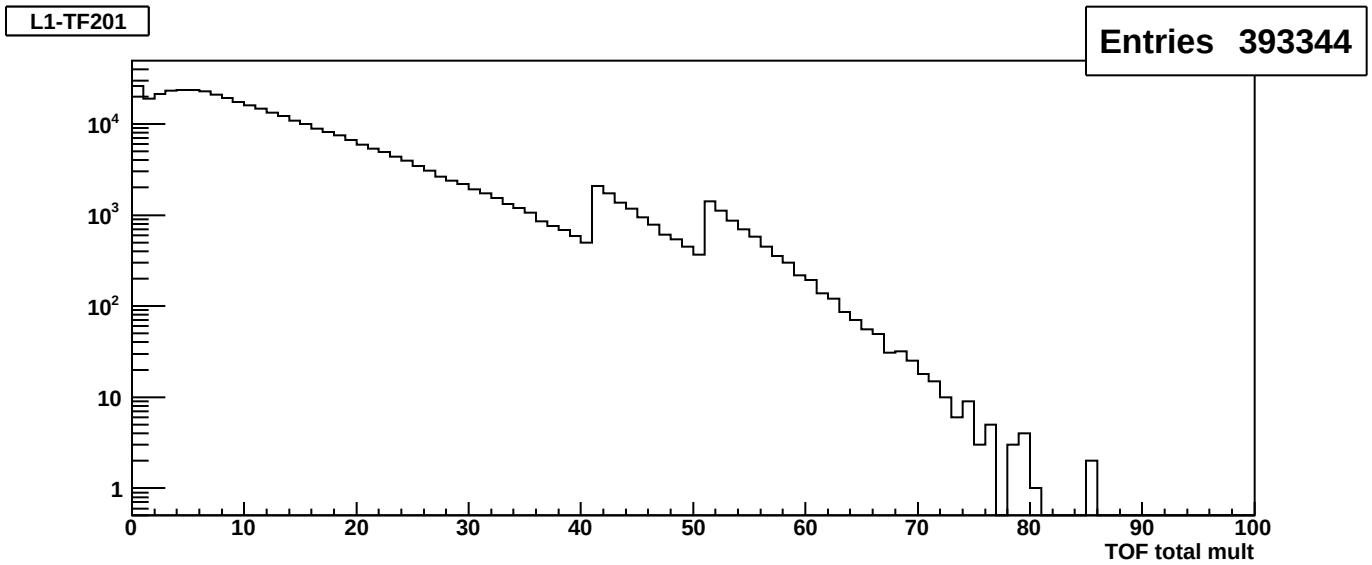

MIX-TF003
Entries 1.652045e+07
TOF MULT
TOF tray

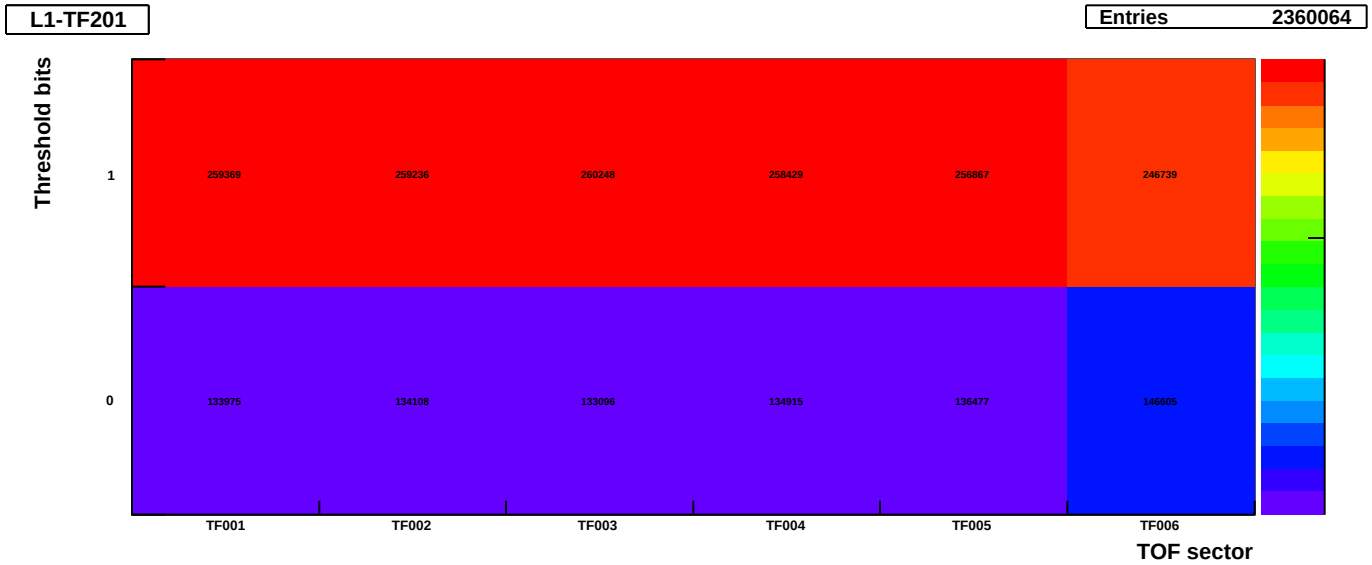

MIX-TF004
Entries 1.652045e+07
TOF MULT
TOF tray

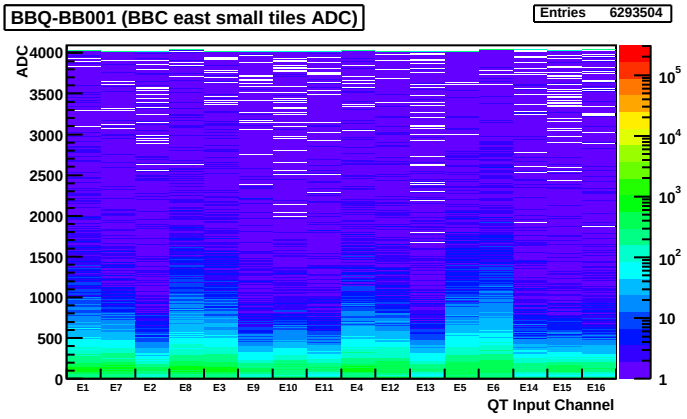

MIX-TF005
Entries 1.652045e+07
TOF MULT
TOF tray

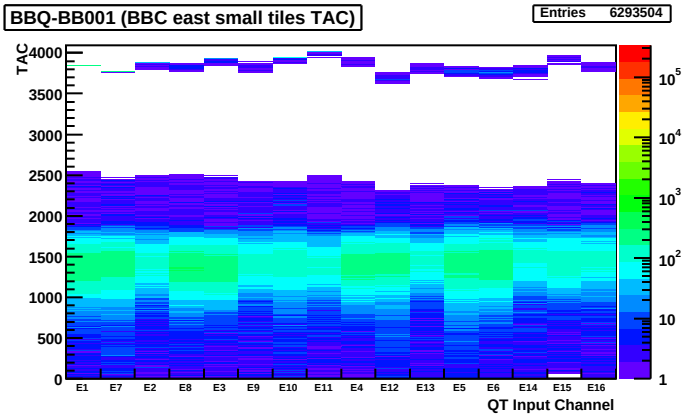

MIX-TF006
Entries 1.652045e+07
TOF MULT
TOF tray

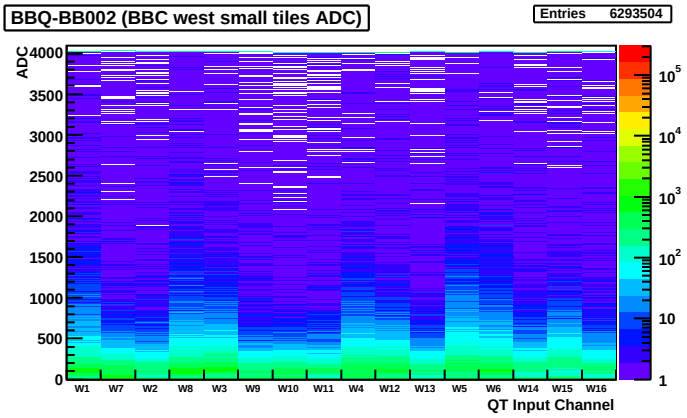

MIX-TF101
Entries 2360064
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

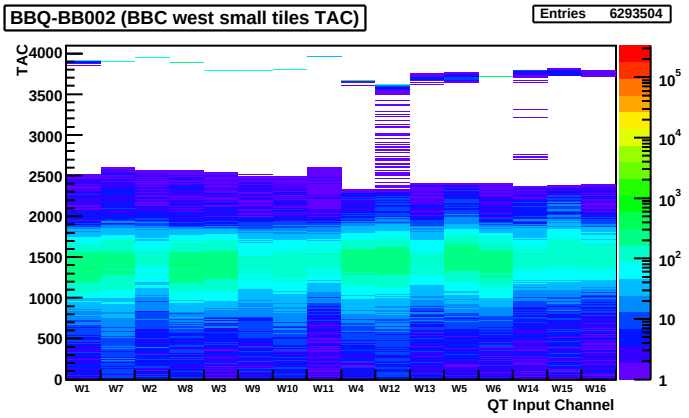

L1-TF201
Entries 393344
TOF total mult

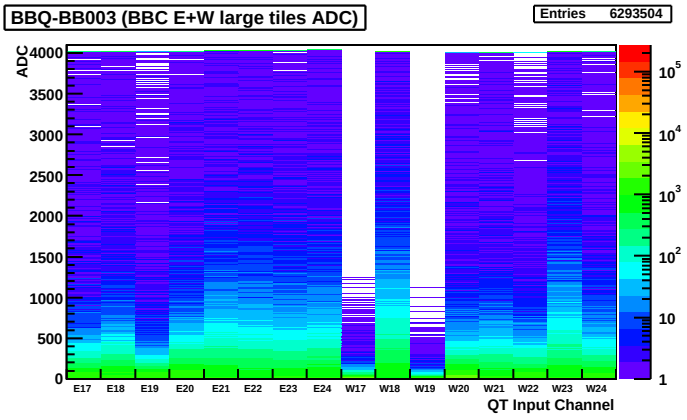

L1-TF201
Entries 2360064
Threshold bits
259369
259236
260248
258429
256867
246739
133975
134108
133096
134915
136477
146605
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

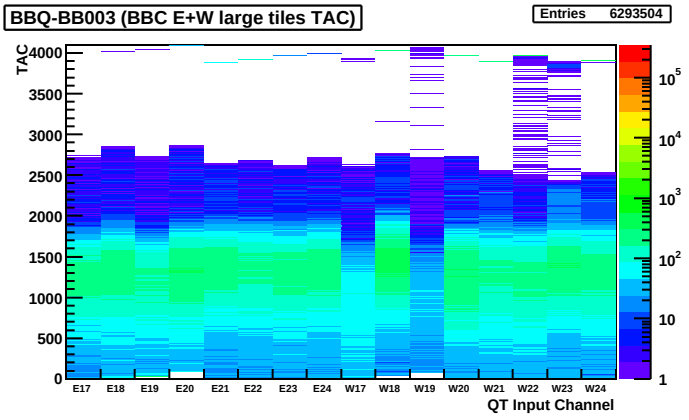

BBQ-BB001 (BBC east small tiles ADC)
Entries 6293504
ADC
QT Input Channel

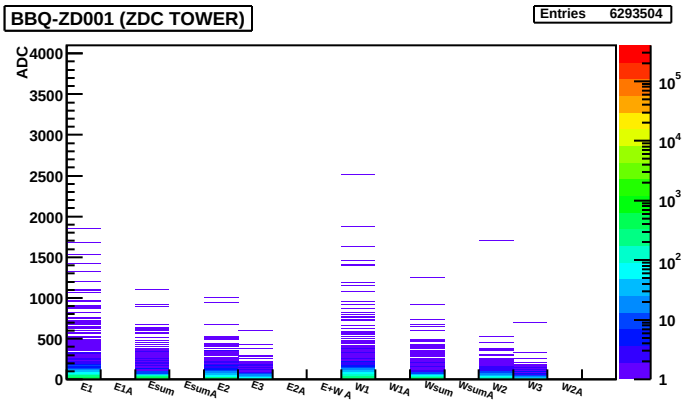

BBQ-BB001 (BBC east small tiles TAC)
Entries 6293504
TAC
QT Input Channel

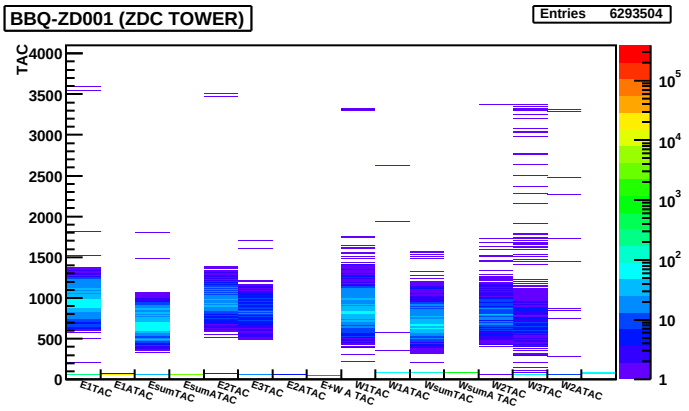

BBQ-BB002 (BBC west small tiles ADC)
Entries 6293504
ADC
QT Input Channel

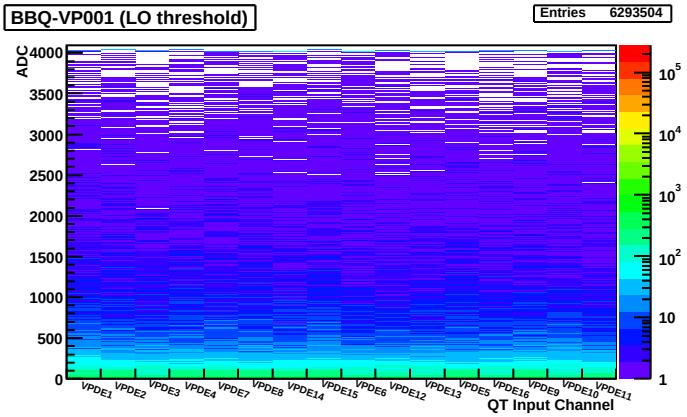

BBQ-BB002 (BBC west small tiles TAC)
Entries 6293504
TAC
QT Input Channel

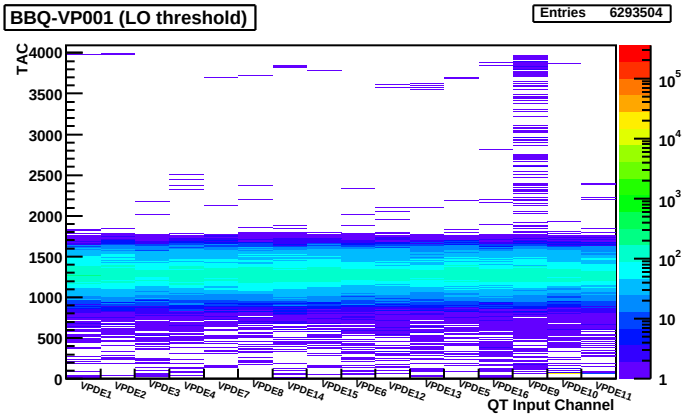

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6293504
ADC
QT Input Channel

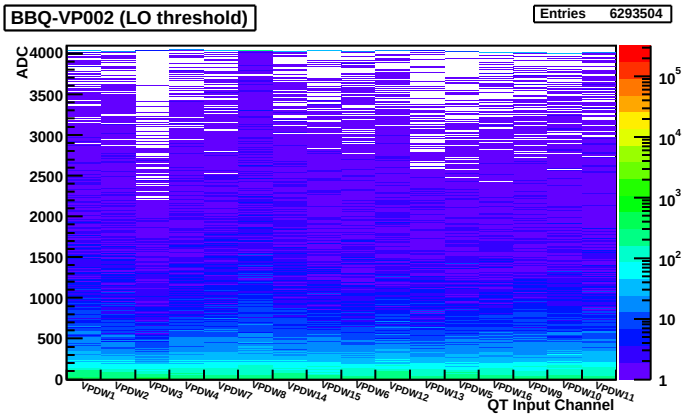

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6293504
TAC
QT Input Channel

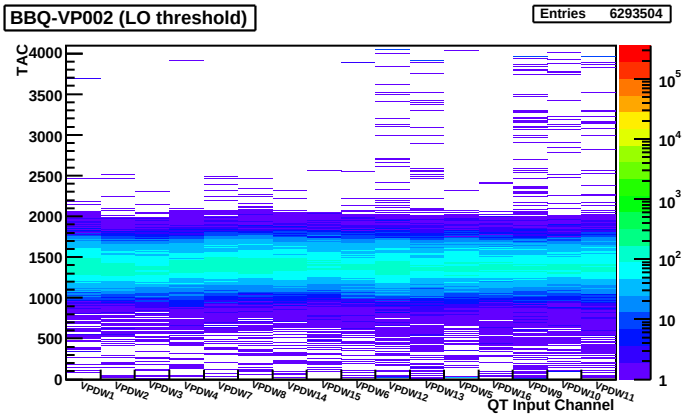

BBQ-ZD001 (ZDC TOWER)
Entries 6293504
ADC

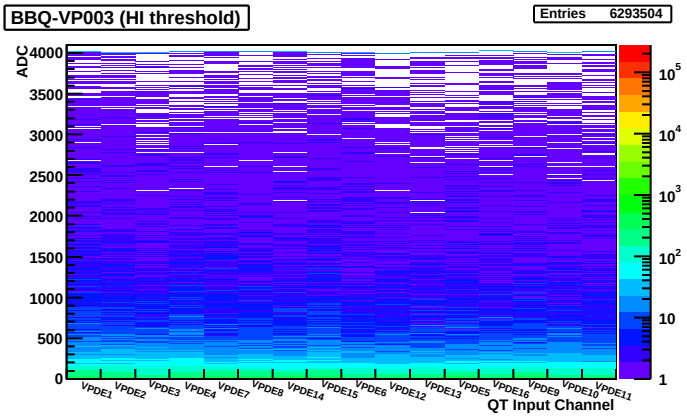

BBQ-ZD001 (ZDC TOWER)
Entries 6293504
TAC

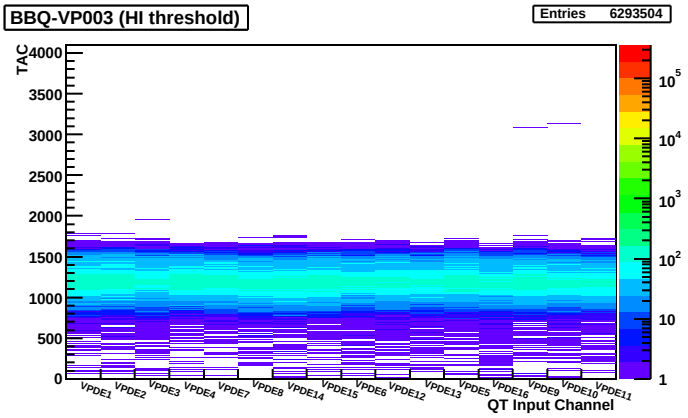

BBQ-VP001 (LO threshold)
Entries 6293504
ADC
QT Input Channel

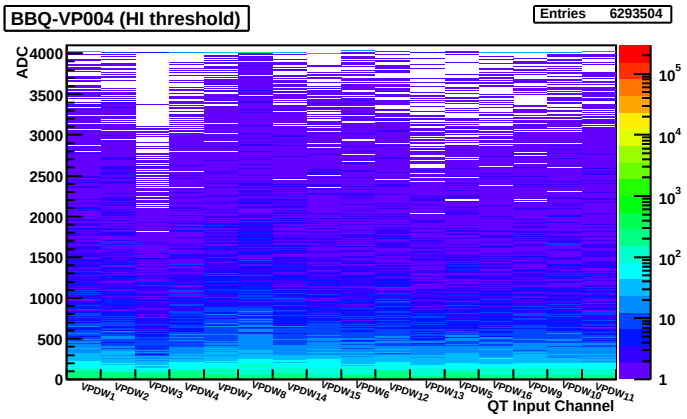

BBQ-VP001 (LO threshold)
Entries 6293504
TAC
QT Input Channel

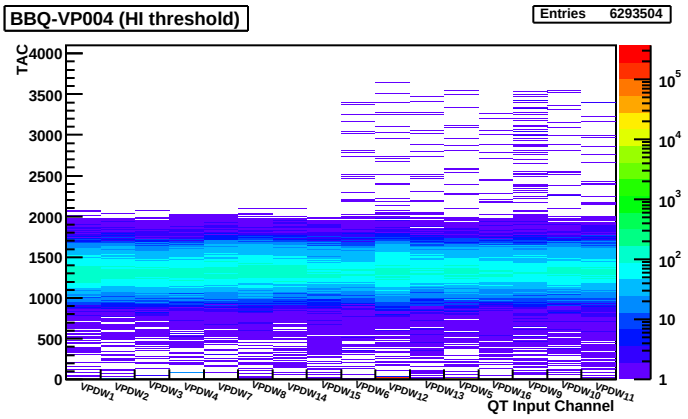

BBQ-VP002 (LO threshold)
Entries 6293504
ADC
QT Input Channel

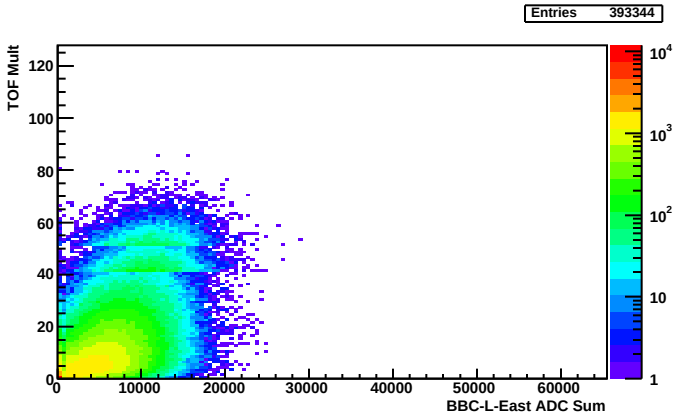

BBQ-VP002 (LO threshold)
Entries 6293504
TAC
QT Input Channel

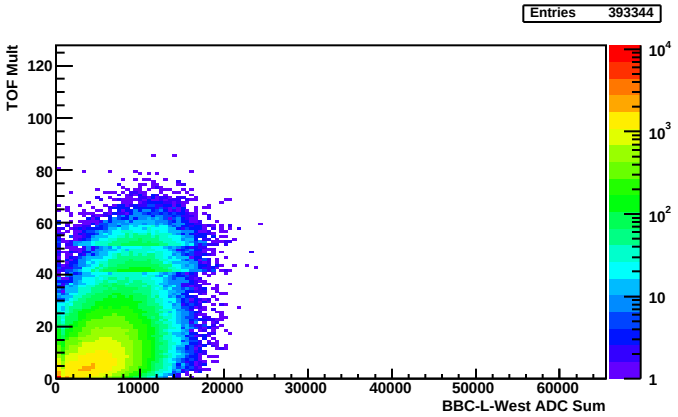

BBQ-VP003 (HI threshold)
Entries 6293504
ADC
QT Input Channel

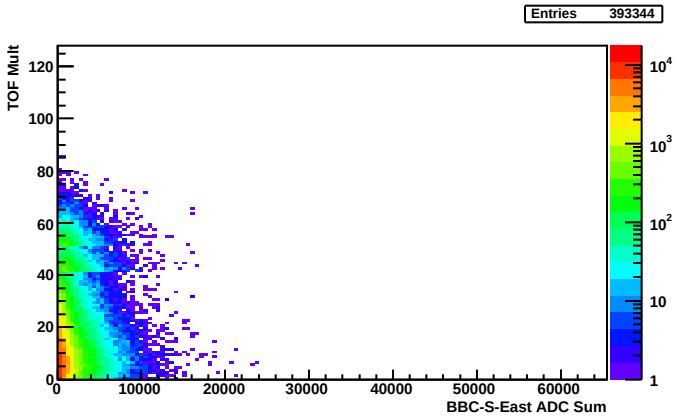

BBQ-VP003 (HI threshold)
Entries 6293504
TAC
QT Input Channel

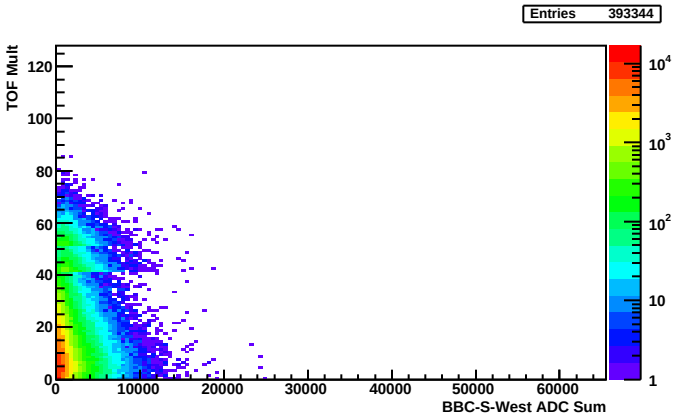

BBQ-VP004 (HI threshold)
Entries 6293504
ADC
QT Input Channel

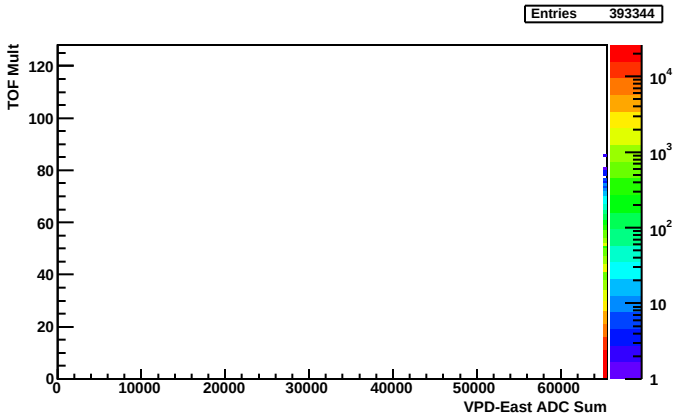

BBQ-VP004 (HI threshold)
Entries 6293504
TAC
QT Input Channel

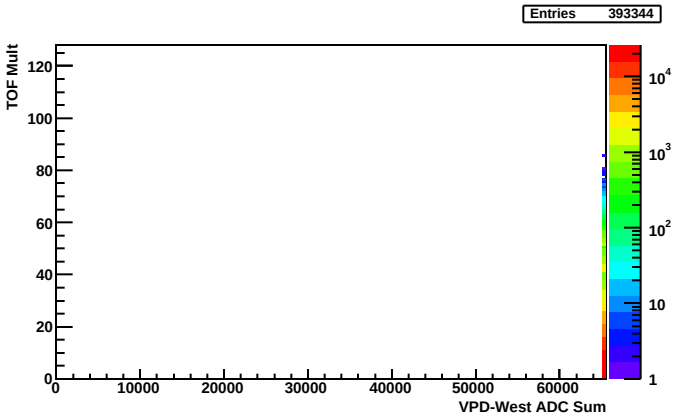

Entries 393344
TOF Mult
BBC-L-East ADC Sum

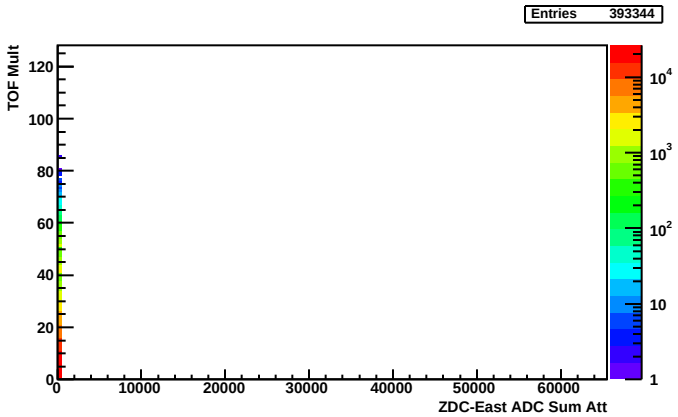

Entries 393344
TOF Mult
BBC-L-West ADC Sum

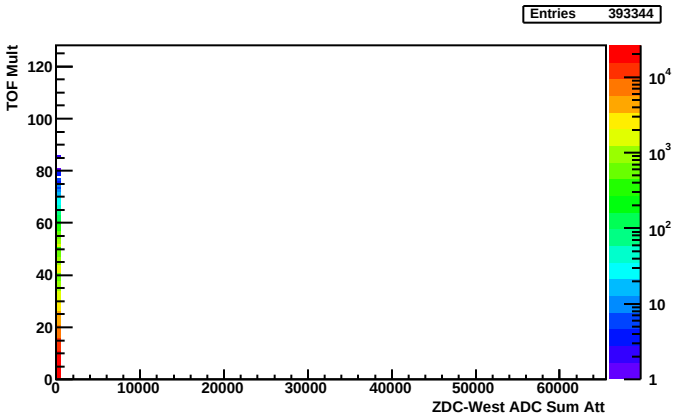

Entries 393344
TOF Mult
BBC-S-East ADC Sum

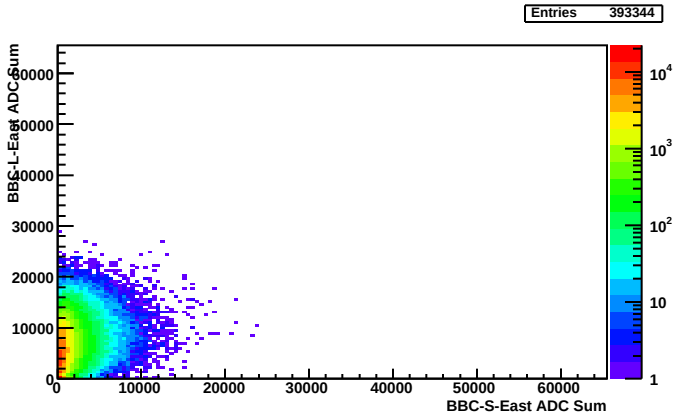

Entries 393344
TOF Mult
BBC-S-West ADC Sum

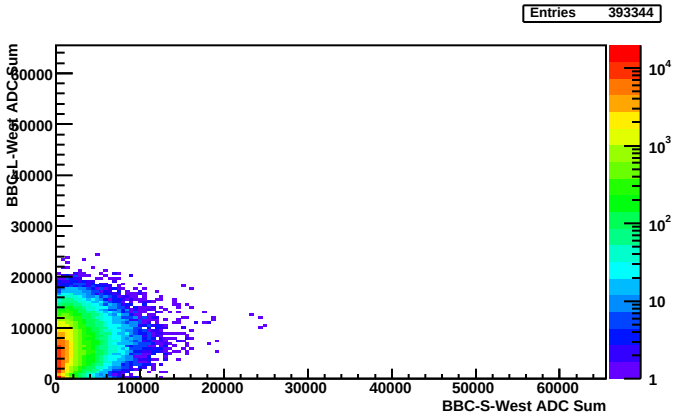

Entries 393344
TOF Mult
VPD-East ADC Sum

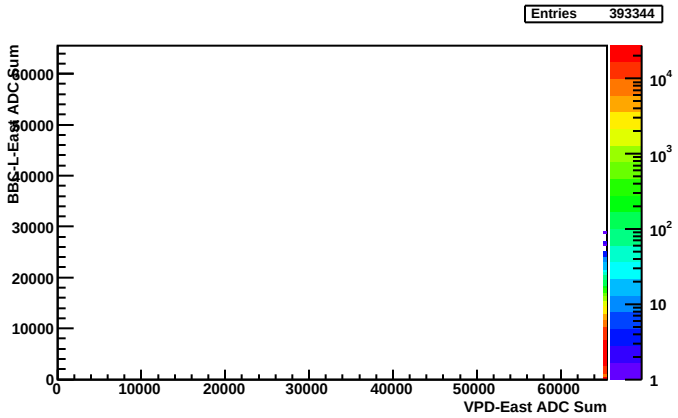

Entries 393344
TOF Mult
VPD-West ADC Sum

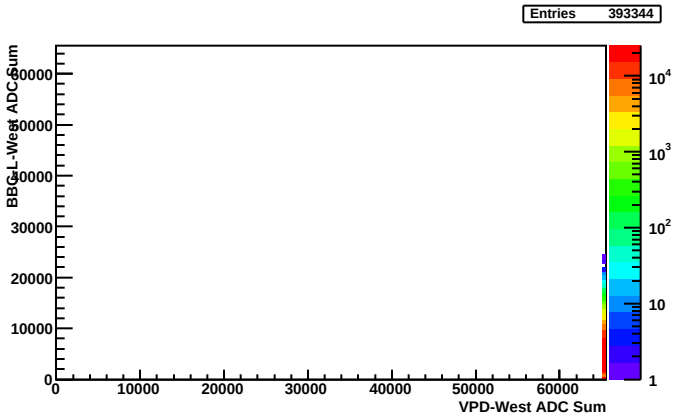

Entries 393344
TOF Mult
ZDC-East ADC Sum Att

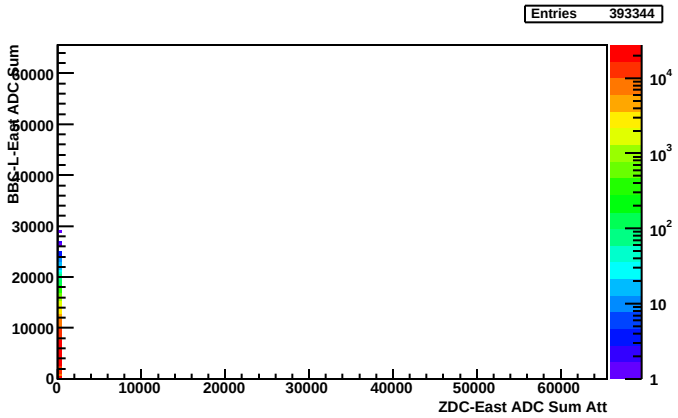

Entries 393344
TOF Mult
ZDC-West ADC Sum Att

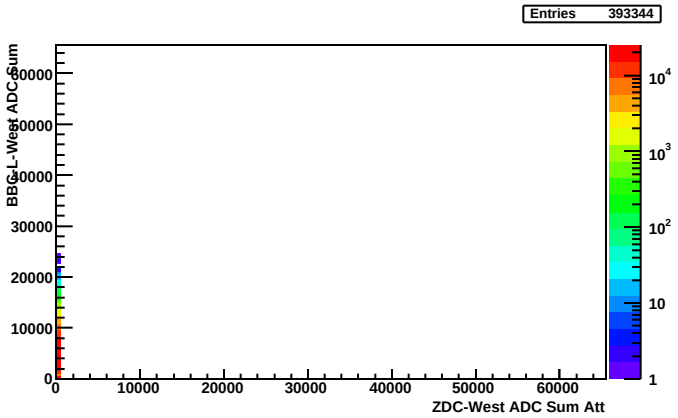

Entries 393344
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 393344
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 393344
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 393344
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 393344
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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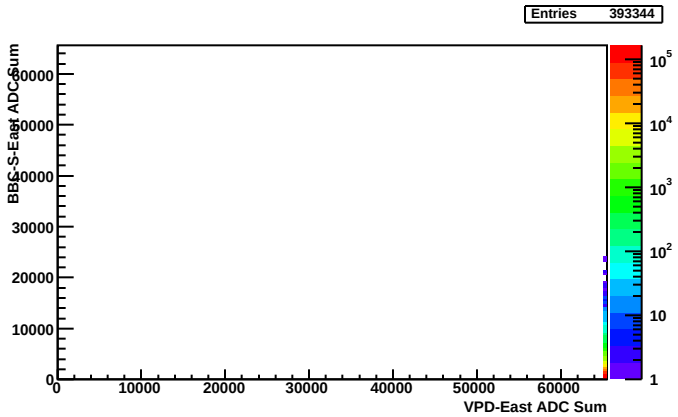
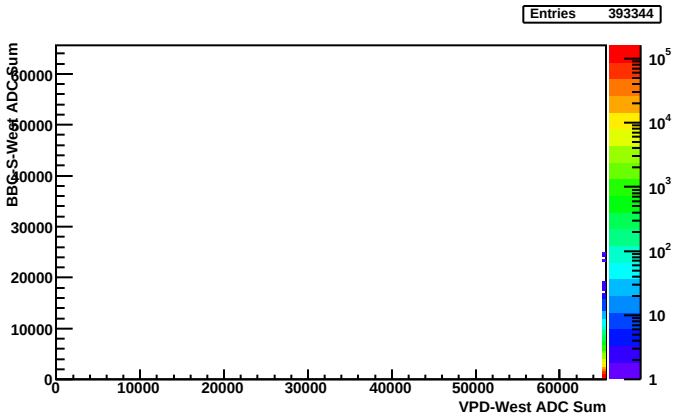
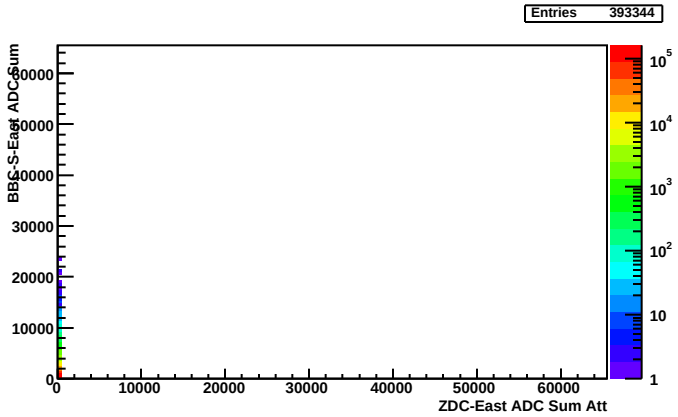
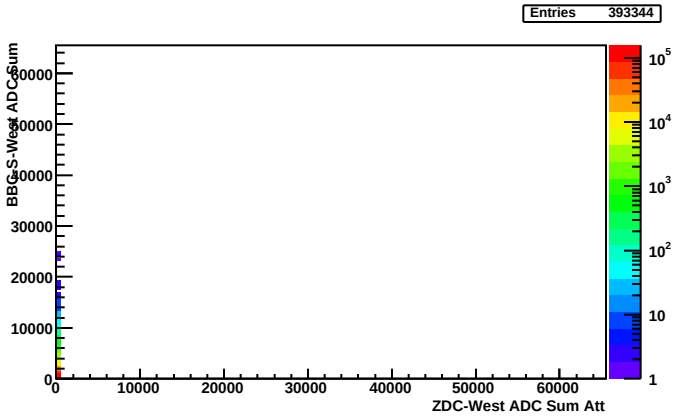
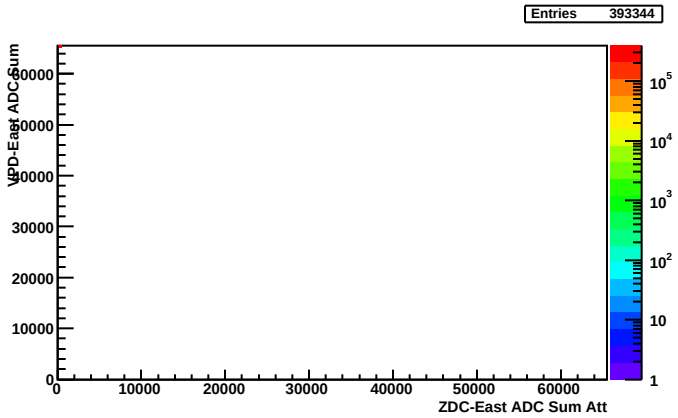
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

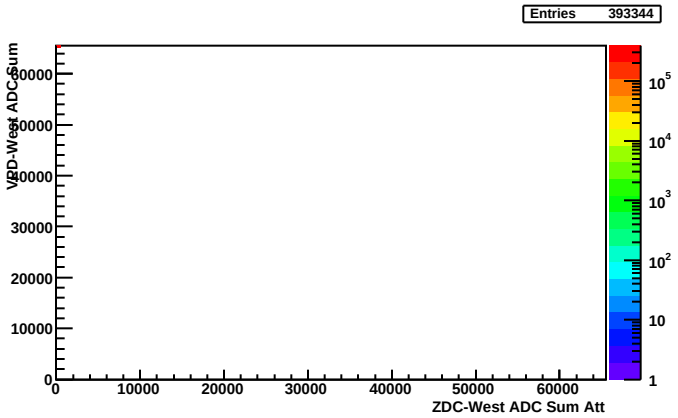

Entries 393344
BBC-S-East ADC Sum
VPD-East ADC Sum

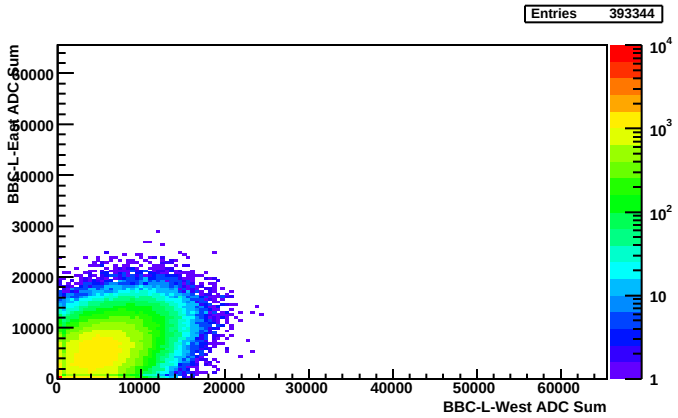

Entries 393344
BBC-S-West ADC Sum
VPD-West ADC Sum

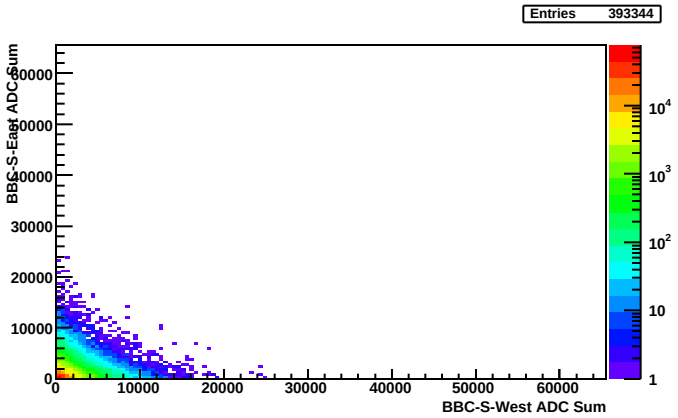

Entries 393344
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

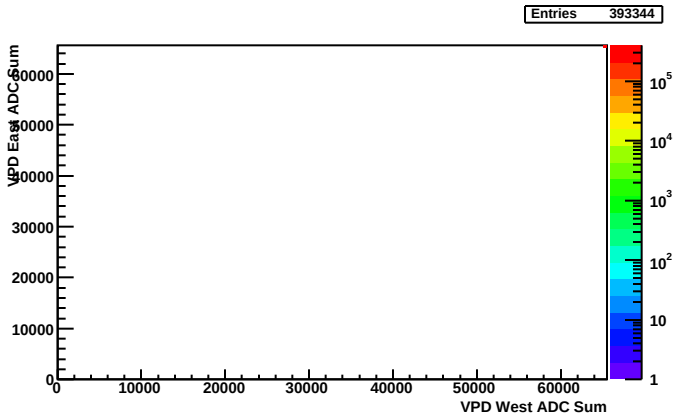

Entries 393344
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

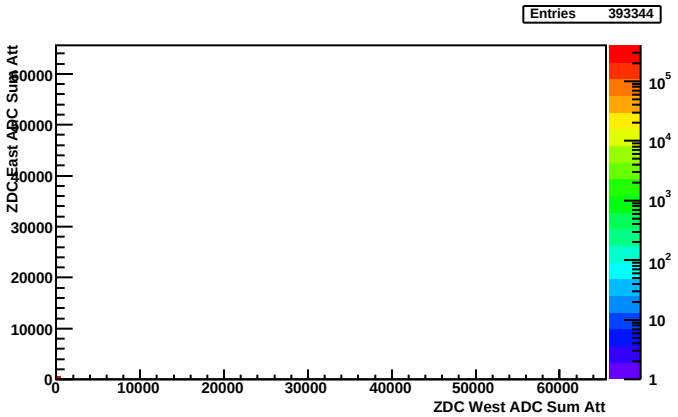

Entries 393344
VPD-East ADC Sum
ZDC-East ADC Sum Att

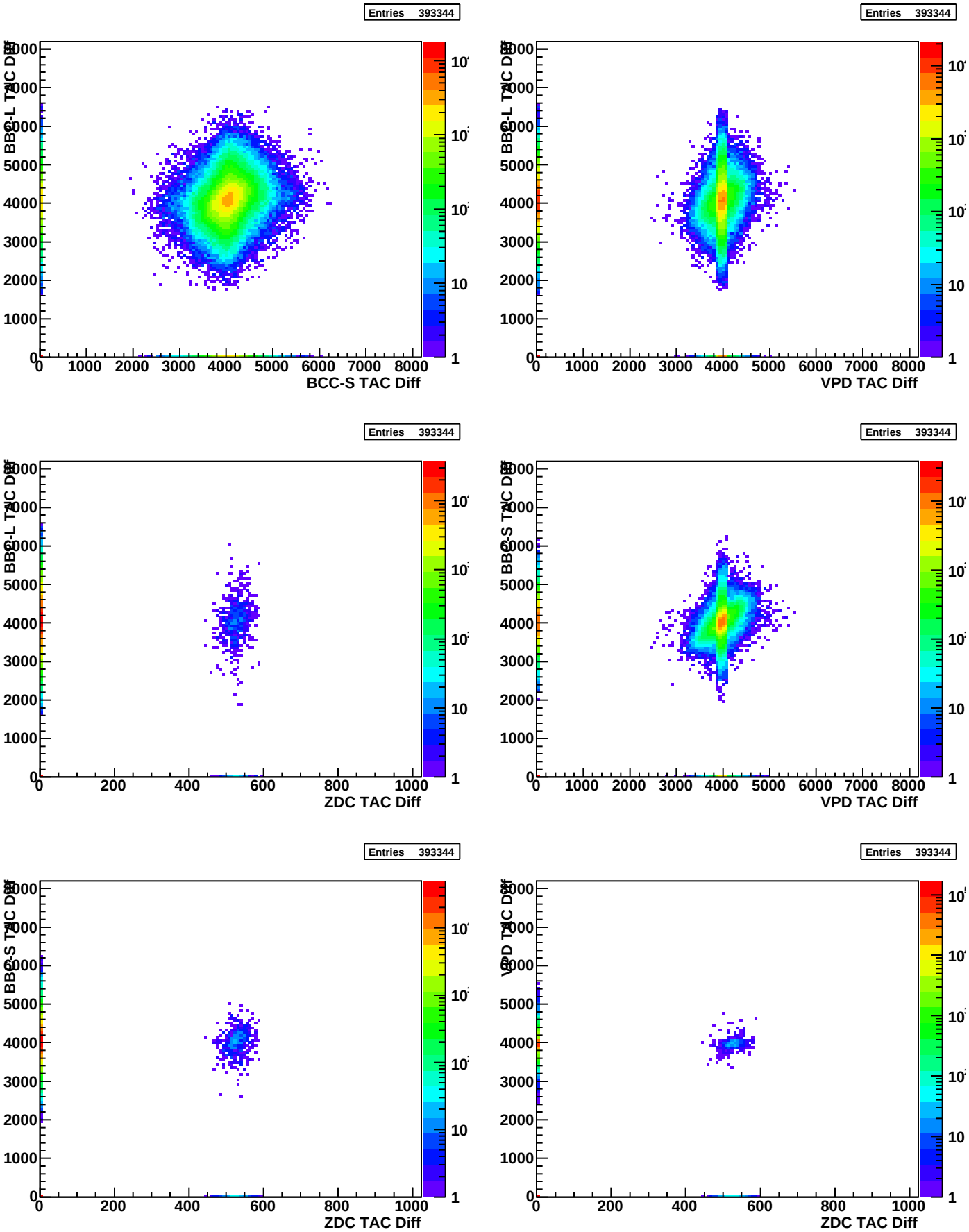

Entries 393344
VPD-West ADC Sum
ZDC-West ADC Sum Att

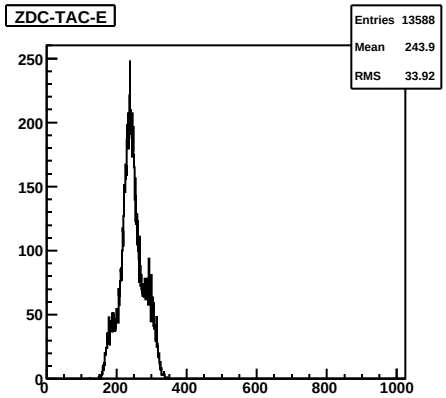

Entries 393344
BBC-L-East ADC Sum
BBC-L-West ADC Sum

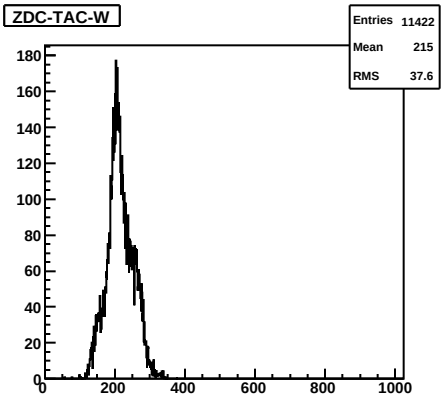

Entries 393344
BBC-S-East ADC Sum
BBC-S-West ADC Sum

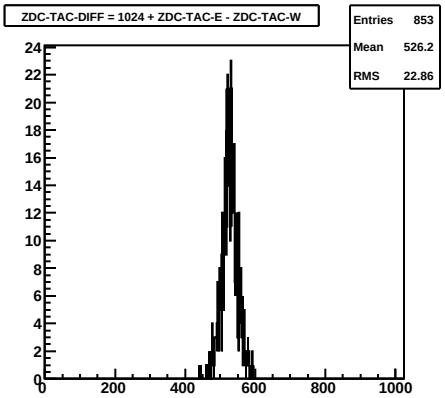

Entries 393344
VPD East ADC Sum
VPD West ADC Sum

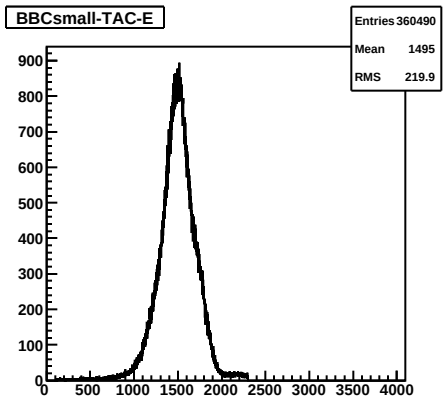

Entries 393344
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

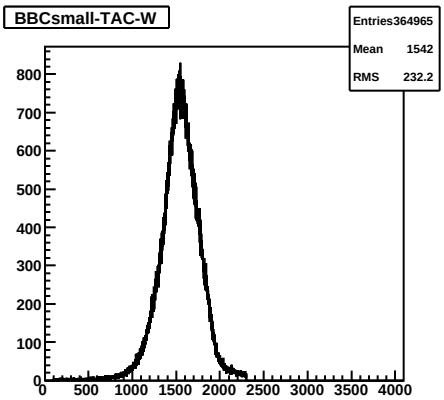

Entries 393344
BBC-L TAC Diff
BCC-S TAC Diff
Entries 393344
BBC-L TAC Diff
VPD TAC Diff
Entries 393344
BBC-L TAC Diff
ZDC TAC Diff
Entries 393344
BBC-S TAC Diff
VPD TAC Diff
Entries 393344
BBC-S TAC Diff
ZDC TAC Diff
Entries 393344
VPD TAC Diff
ZDC TAC Diff

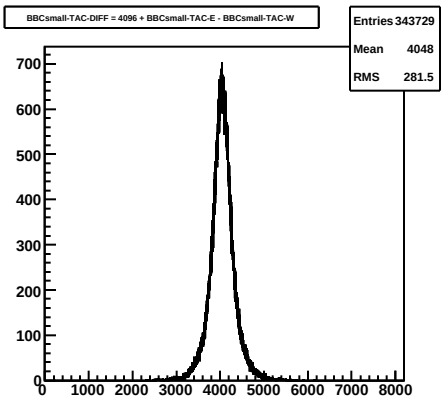

ZDC-TAC-E
Entries 13588
Mean 243.9
RMS 33.92

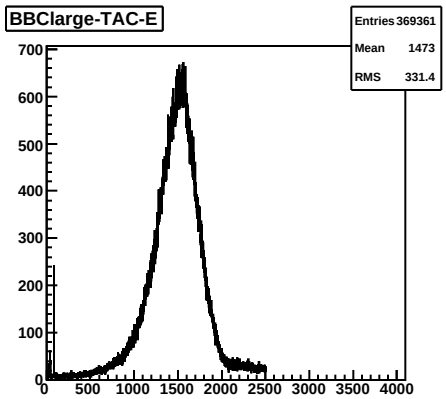

ZDC-TAC-W
Entries 11422
Mean 215
RMS 37.6

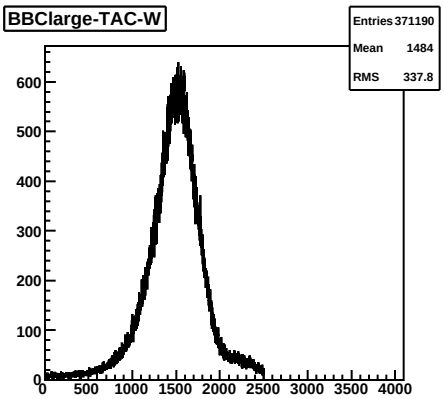

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 853
Mean 526.2
RMS 22.86

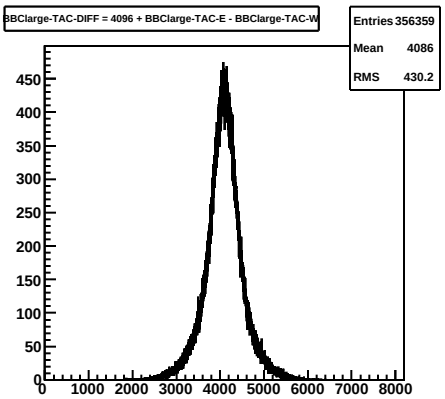

BBCsmall-TAC-E
Entries 360490
Mean 1495
RMS 219.9

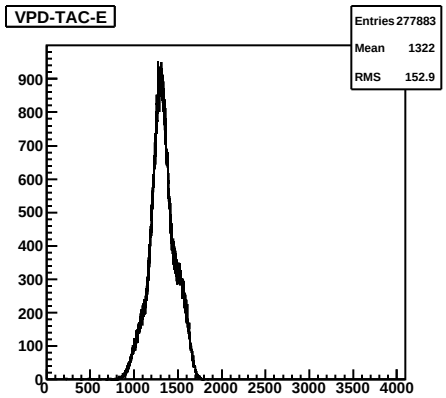

BBCsmall-TAC-W
Entries 364965
Mean 1542
RMS 232.2

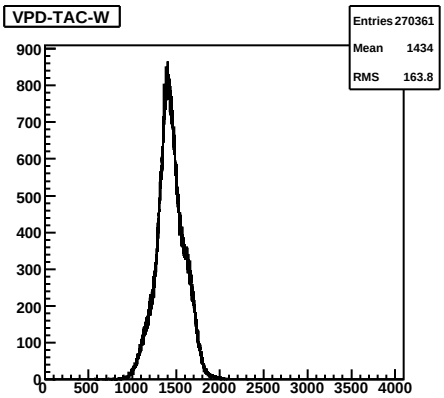

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 343729
Mean 4048
RMS 281.5

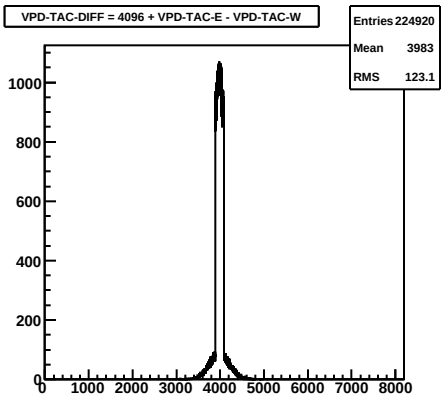

BBClarge-TAC-E
Entries 369361
Mean 1473
RMS 331.4

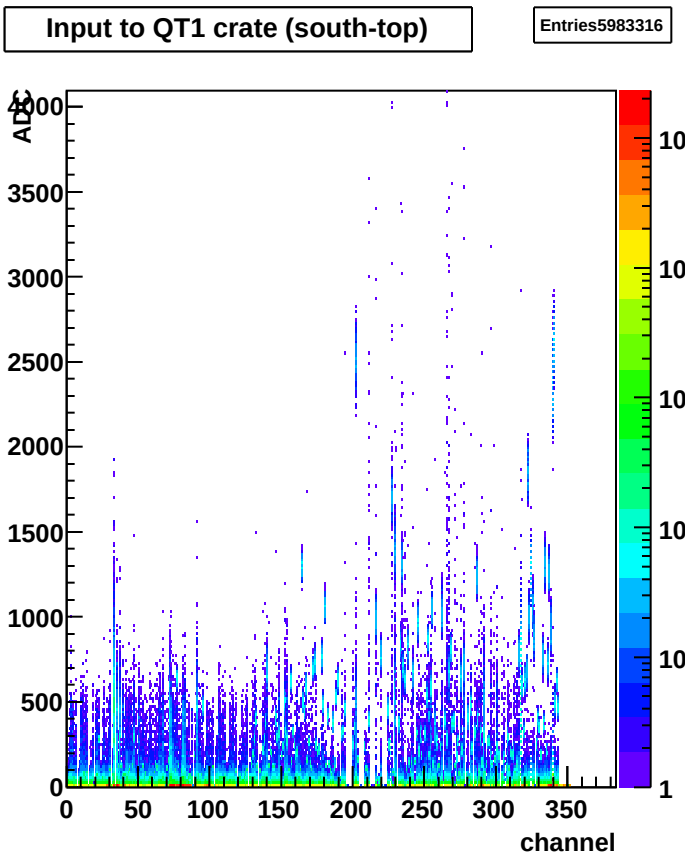

BBClarge-TAC-W
Entries 371190
Mean 1484
RMS 337.8

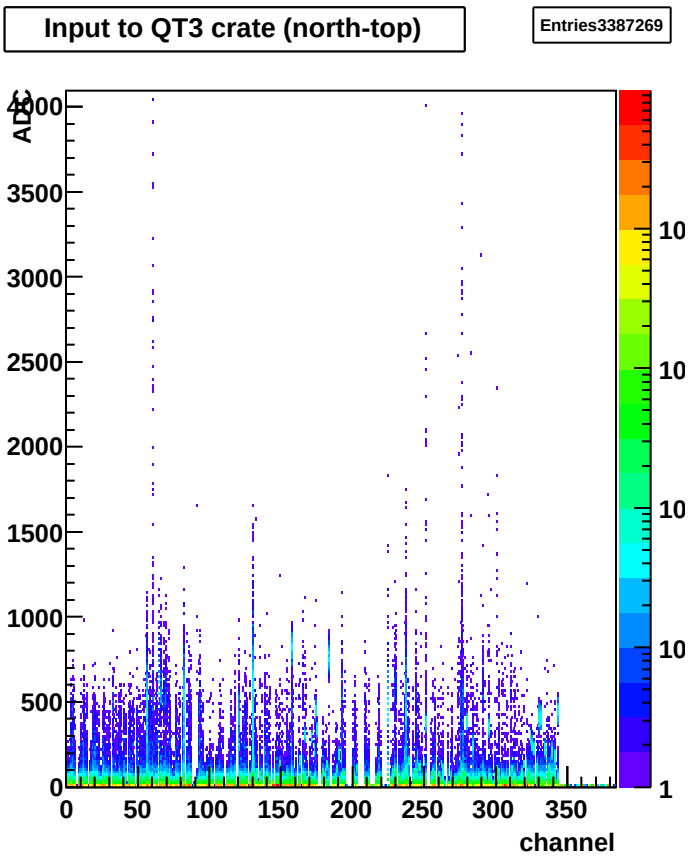

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 356359
Mean 4086
RMS 430.2

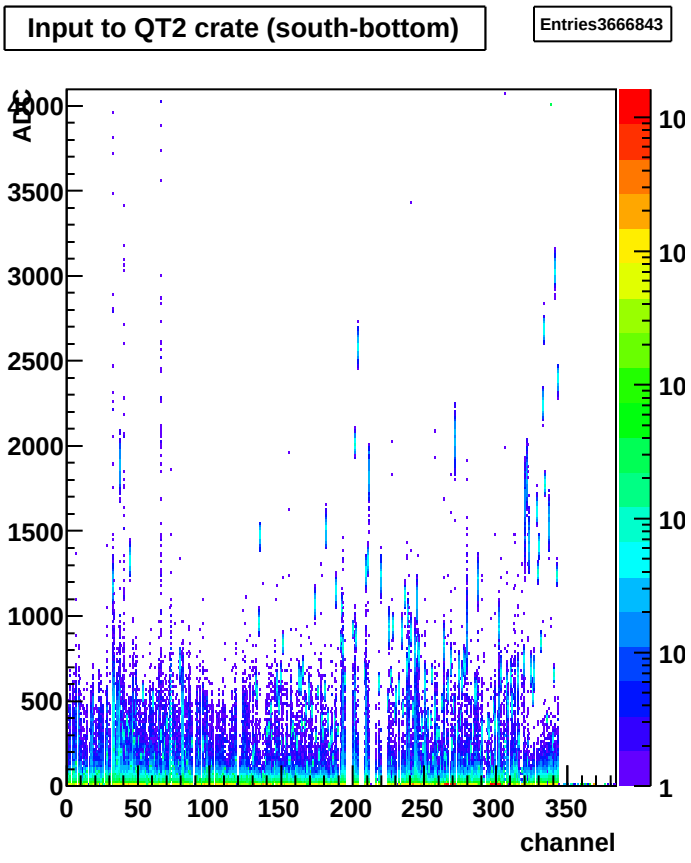

VPD-TAC-E
Entries 277883
Mean 1322
RMS 152.9

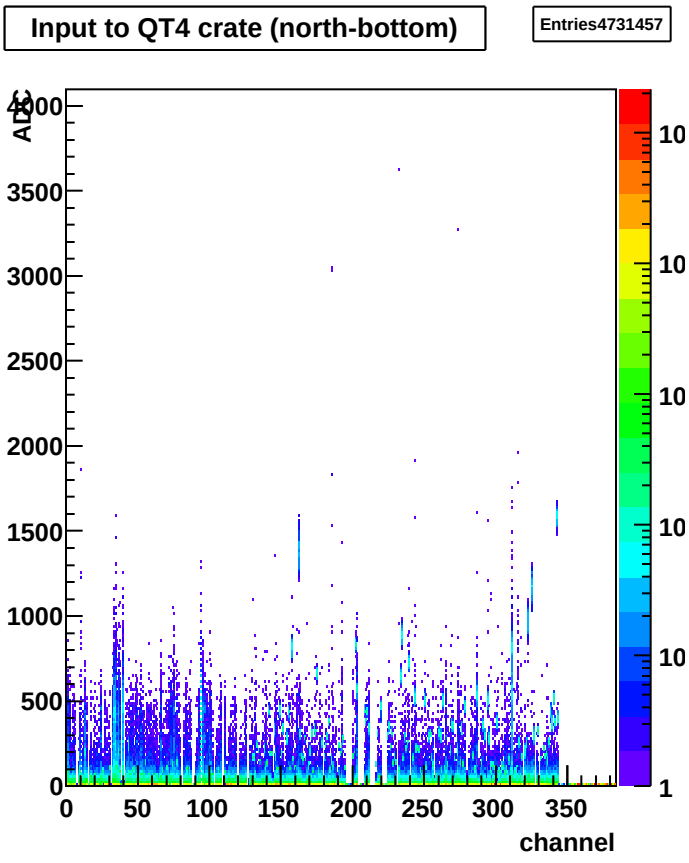

VPD-TAC-W
Entries 270361
Mean 1434
RMS 163.8

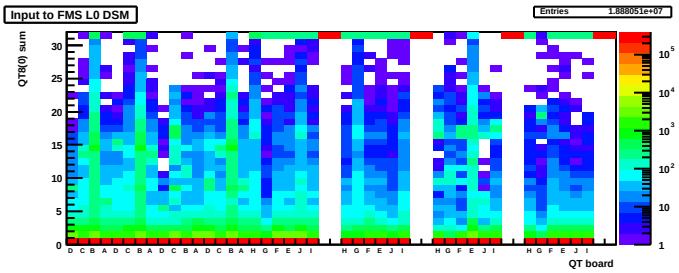

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 224920
Mean 3983
RMS 123.1

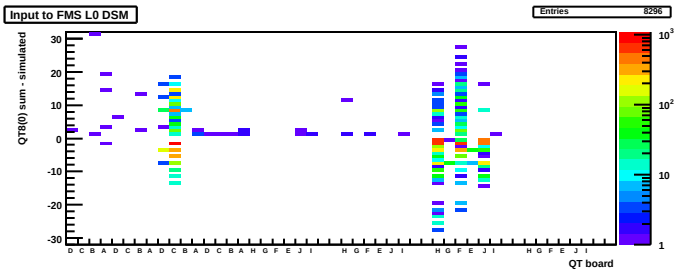

Input to QT1 crate (south-top)
Entries5983316
ADC
channel

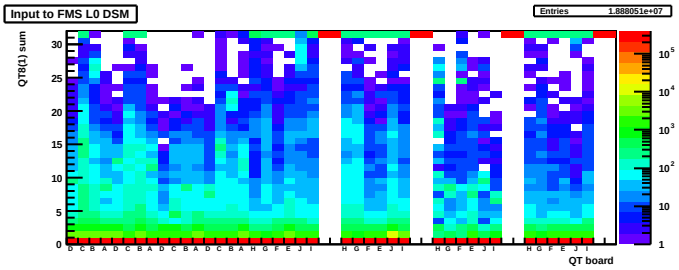

Input to QT3 crate (north-top)
Entries3387269
ADC
channel

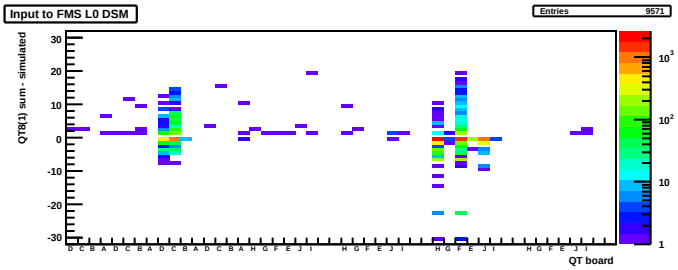

Input to QT2 crate (south-bottom)
Entries3666843
ADC
channel

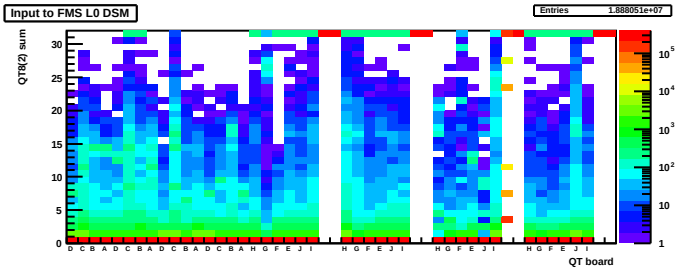

Input to QT4 crate (north-bottom)
Entries4731457
ADC
channel

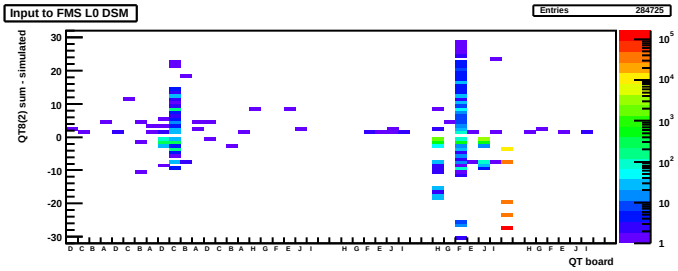

Input to FMS L0 DSM
Entries 1.888051e+07
QT8(0) sum
QT board

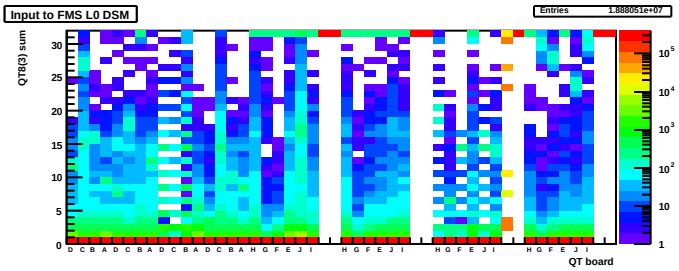

Input to FMS L0 DSM
Entries 8296
QT8(0) sum - simulated
QT board

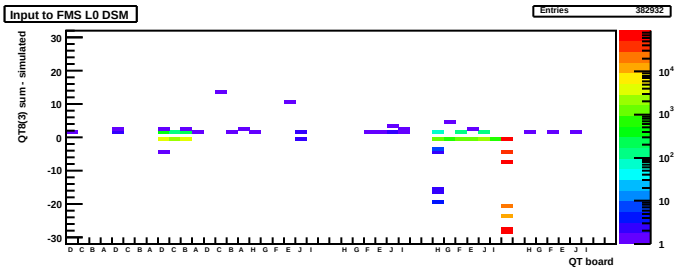

Input to FMS L0 DSM
Entries 1.888051e+07
QT8(1) sum
QT board

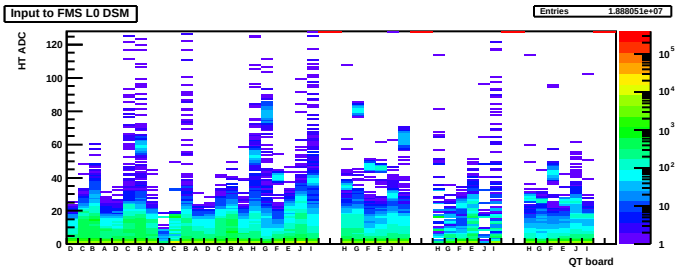

Input to FMS L0 DSM
Entries 9571
QT8(1) sum - simulated
QT board

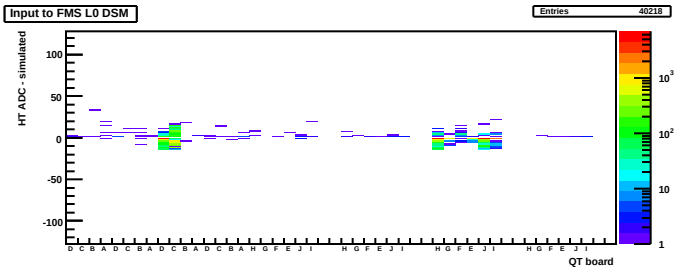

Input to FMS L0 DSM
Entries 1.888051e+07
QT8(2) sum
QT board

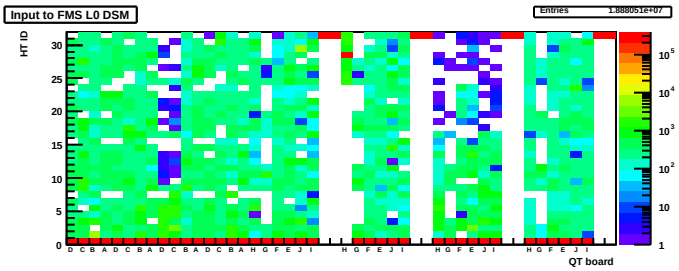

Input to FMS L0 DSM
Entries 284725
QT8(2) sum - simulated
QT board

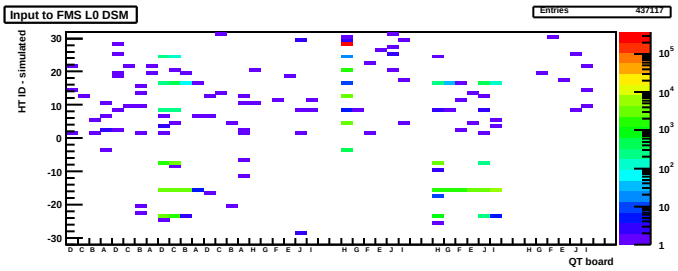

Input to FMS L0 DSM
Entries 1.888051e+07
QT8(3) sum
QT board

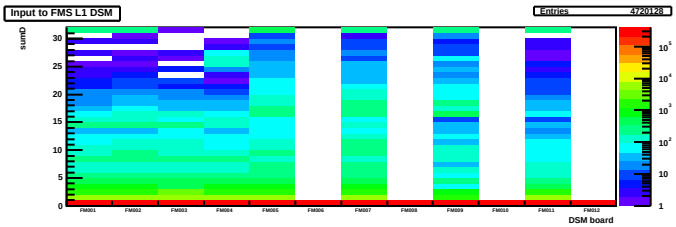

Input to FMS L0 DSM
Entries 382932
QT8(3) sum - simulated
QT board

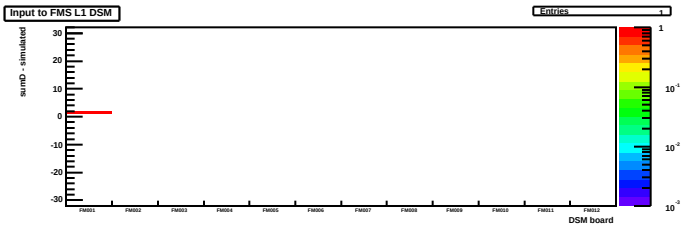

Input to FMS L0 DSM
Entries 1.888051e+07
HT ADC
QT board

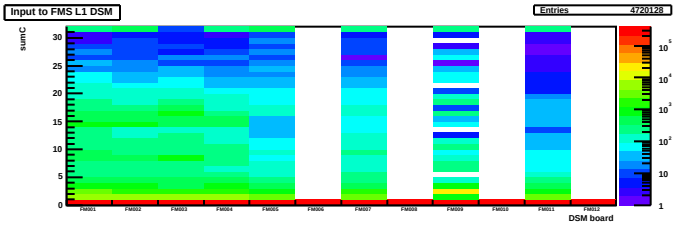

Input to FMS L0 DSM
Entries 40218
HT ADC - simulated
QT board

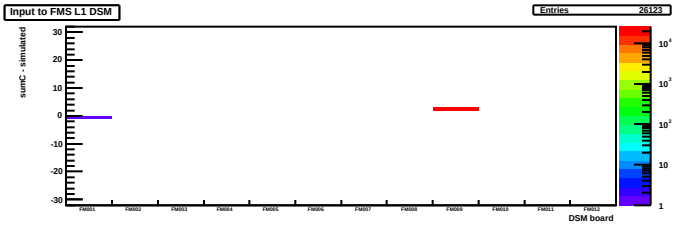

Input to FMS L0 DSM
Entries 1.888051e+07
HT ID
QT board

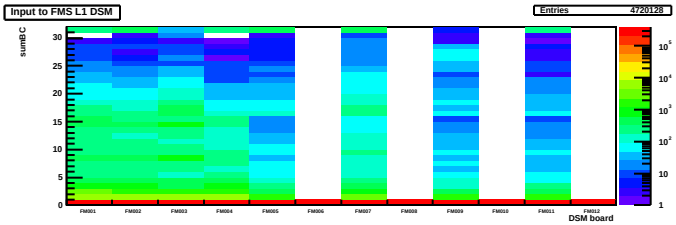

Input to FMS L0 DSM
Entries 437117
HT ID - simulated
QT board

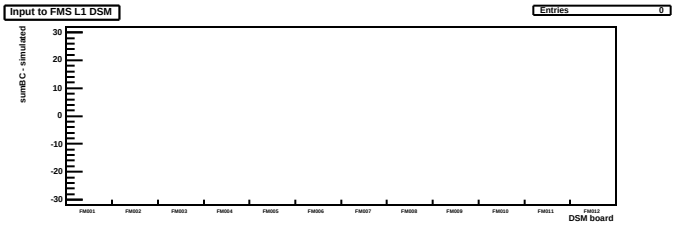

Input to FMS L1 DSM
Entries 4720128
sumD
DSM board

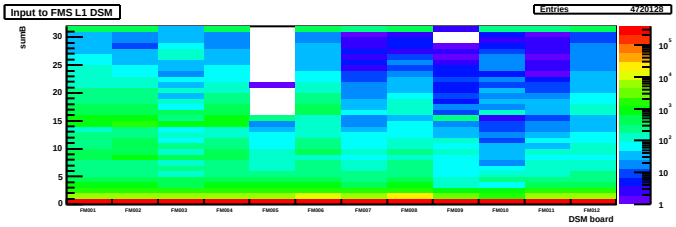

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

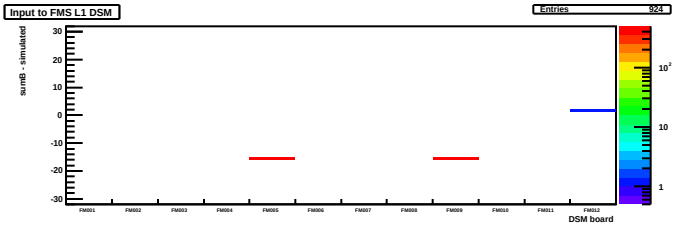

Input to FMS L1 DSM
Entries 4720128
sumC
DSM board

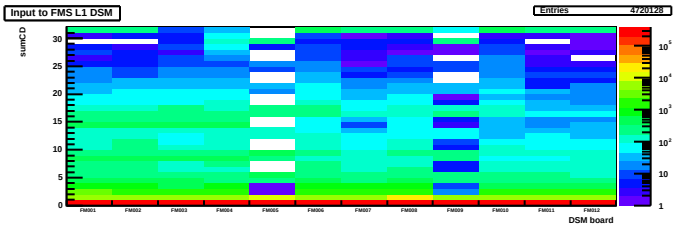

Input to FMS L1 DSM
Entries 26123
sumC - simulated
DSM board

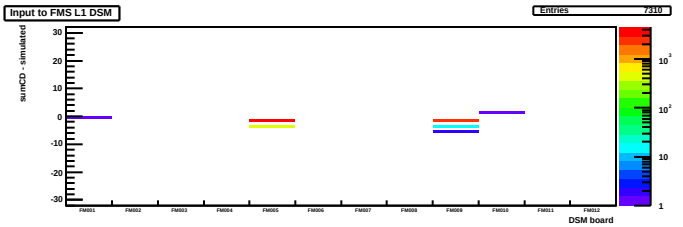

Input to FMS L1 DSM
Entries 4720128
sumBC
DSM board

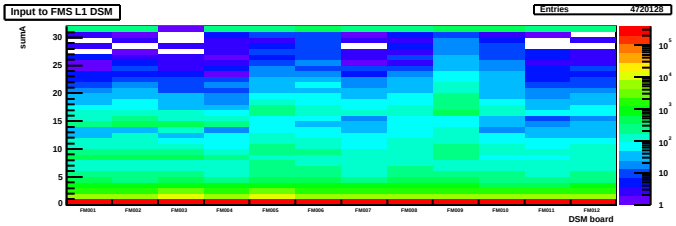

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

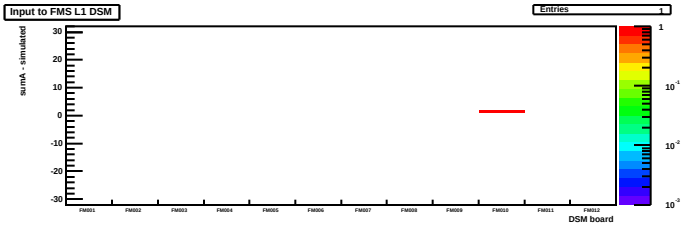

Input to FMS L1 DSM
Entries 4720128
sumB
DSM board

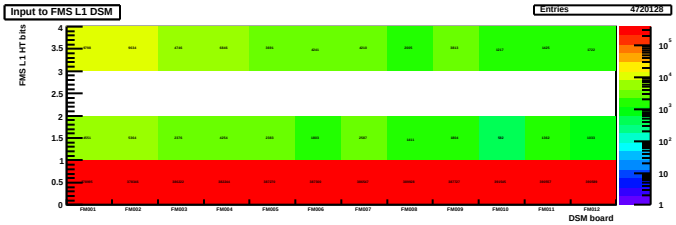

Input to FMS L1 DSM
Entries 924
sumB - simulated
DSM board

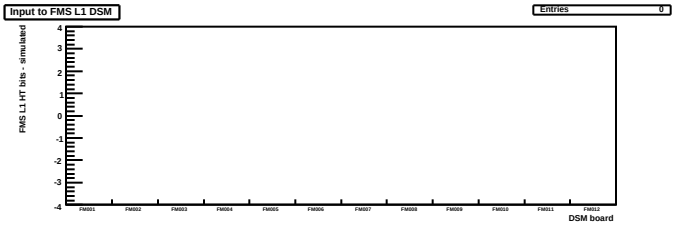

Input to FMS L1 DSM
Entries 4720128
sumCD
DSM board

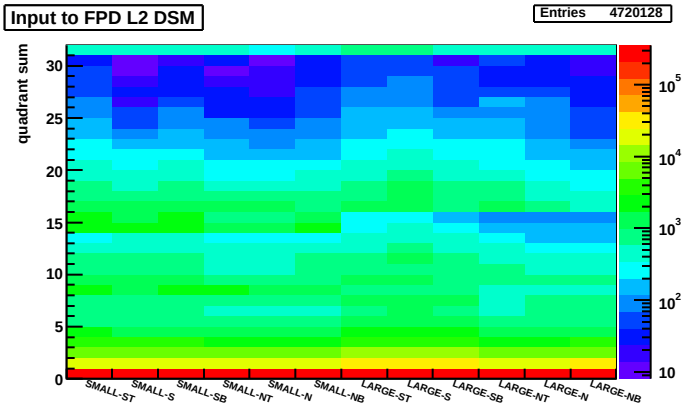

Input to FMS L1 DSM
Entries 7310
sumCD - simulated
DSM board

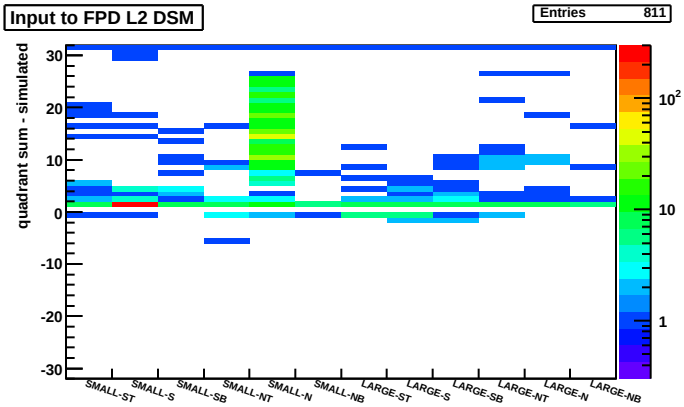

Input to FMS L1 DSM
Entries 4720128
sumA
DSM board

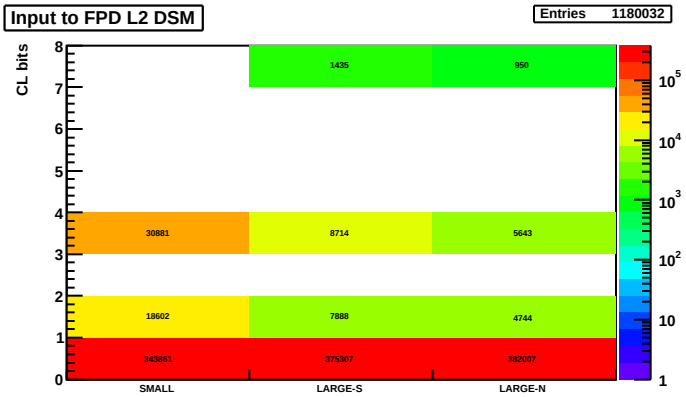

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

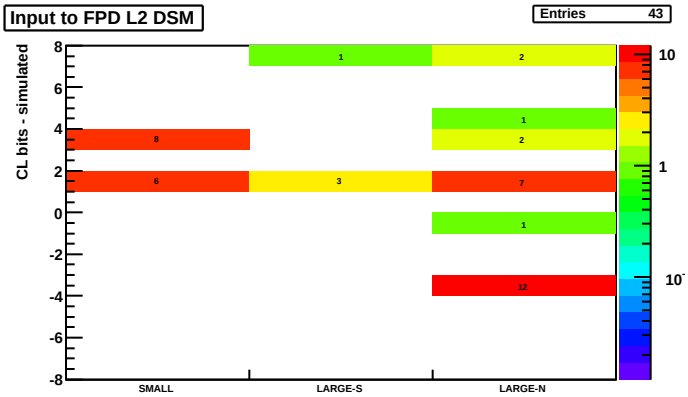

Input to FMS L1 DSM
Entries 4720128
FMS L1 HT bits
DSM board

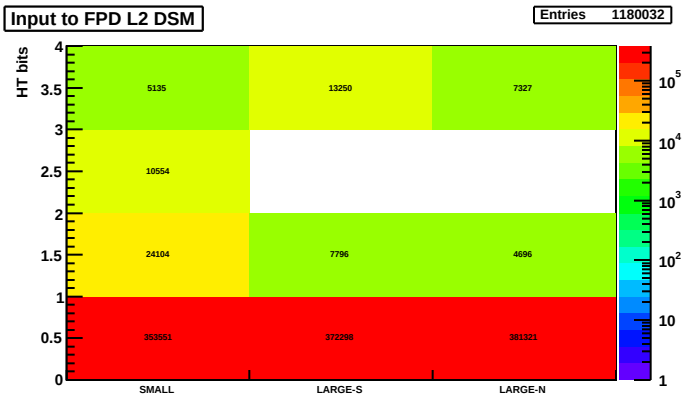

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

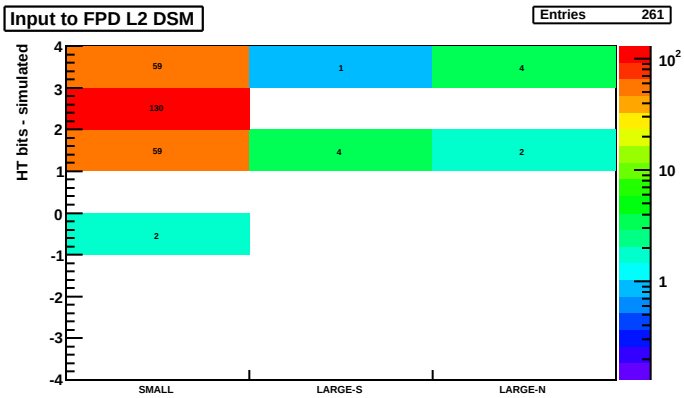

Input to FPD L2 DSM
Entries 4720128
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

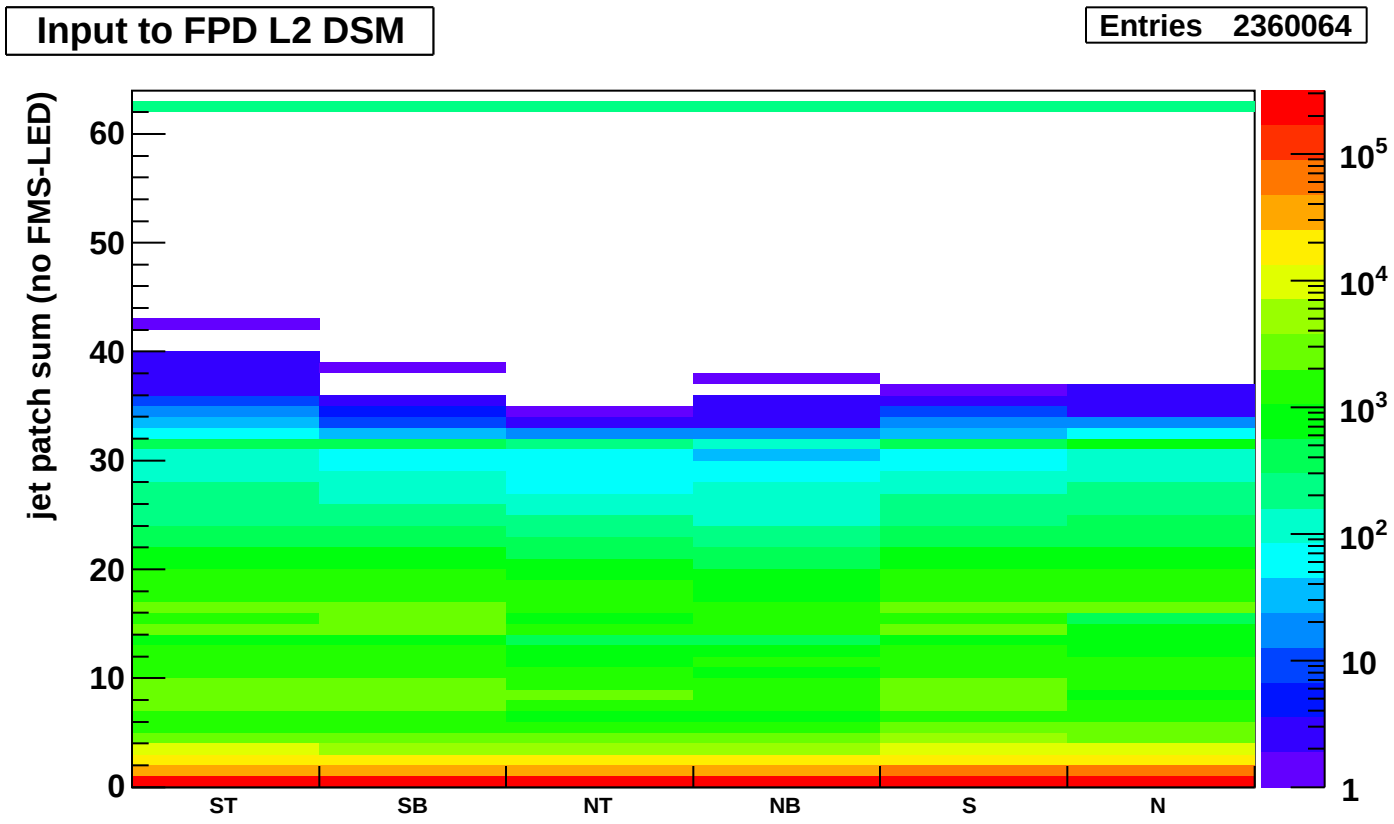

Input to FPD L2 DSM
Entries 811
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

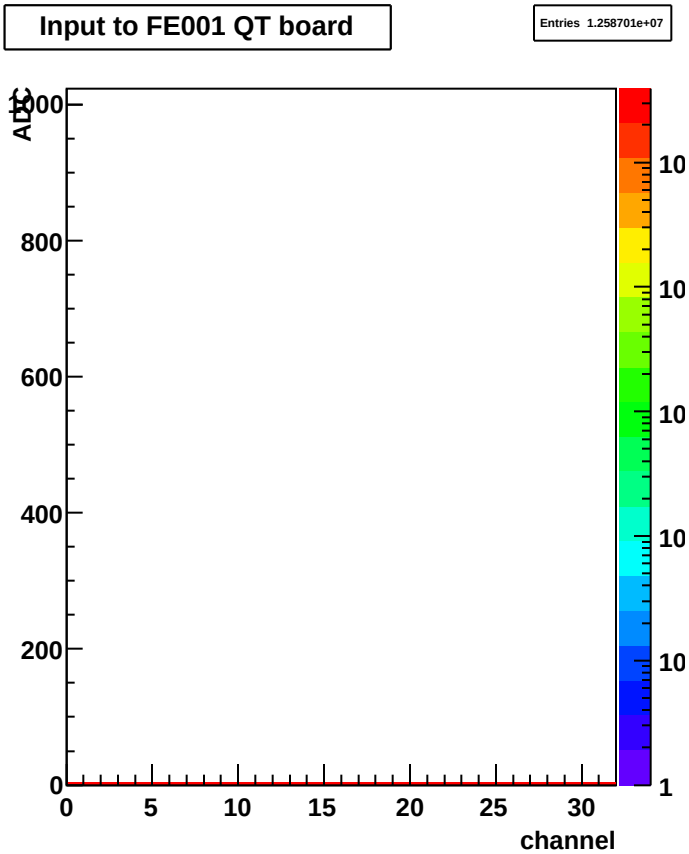

Input to FPD L2 DSM
Entries 1180032
CL bits
1435
950
30881
8714
5643
18602
7888
4744
343861
375307
382007
SMALL
LARGE-S
LARGE-N

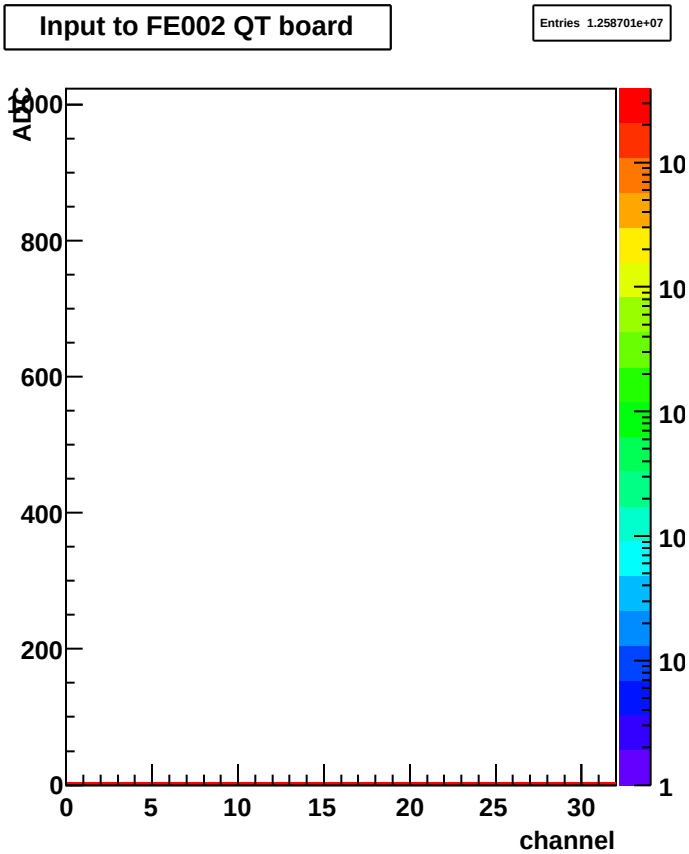

Input to FPD L2 DSM
Entries 43
CL bits - simulated
1
2
1
8
2
6
3
7
1
12
SMALL
LARGE-S
LARGE-N

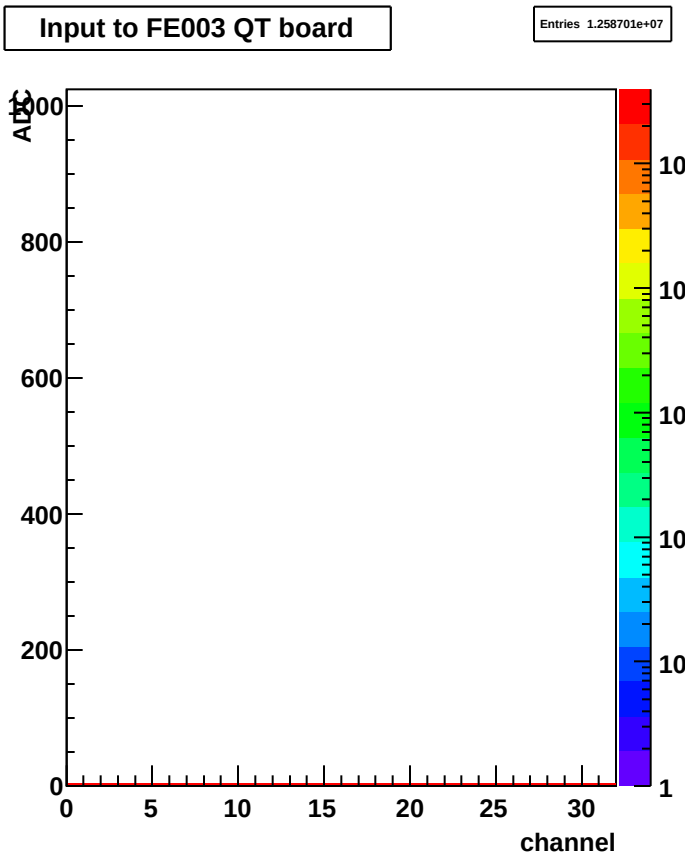

Input to FPD L2 DSM
Entries 1180032
HT bits
5135
13250
7327
10554
24104
7796
4696
353551
372298
381321
SMALL
LARGE-S
LARGE-N

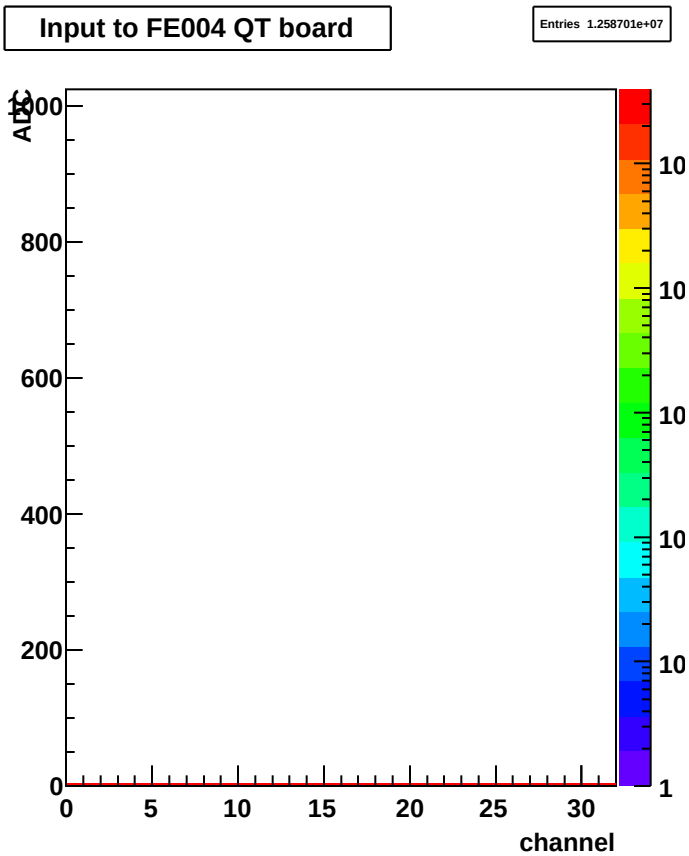

Input to FPD L2 DSM
Entries 261
HT bits - simulated
59
1
4
130
59
4
2
2
SMALL
LARGE-S
LARGE-N

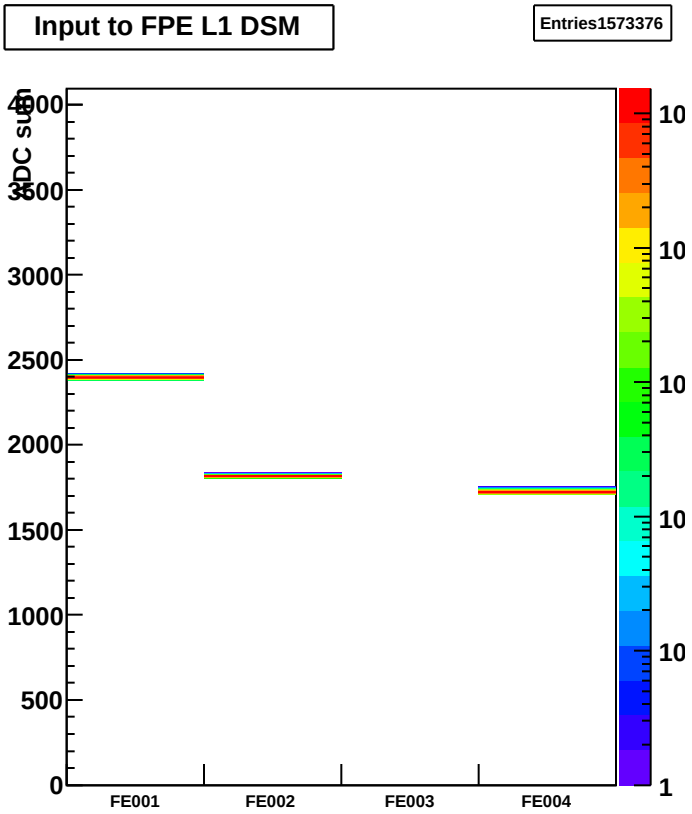

Input to FPD L2 DSM
Entries 2360064
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

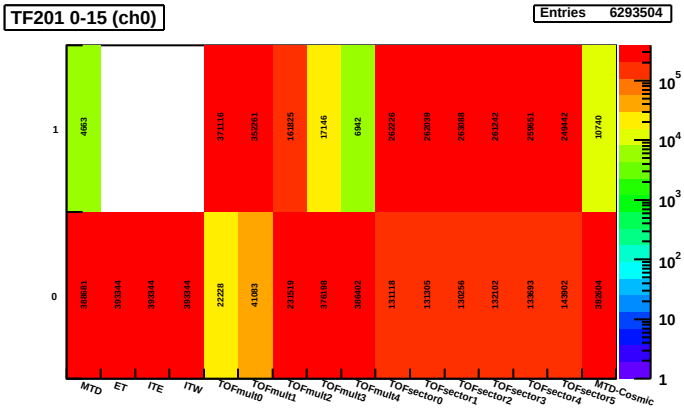

Input to FE001 QT board
Entries 1.258701e+07
ADC
channel

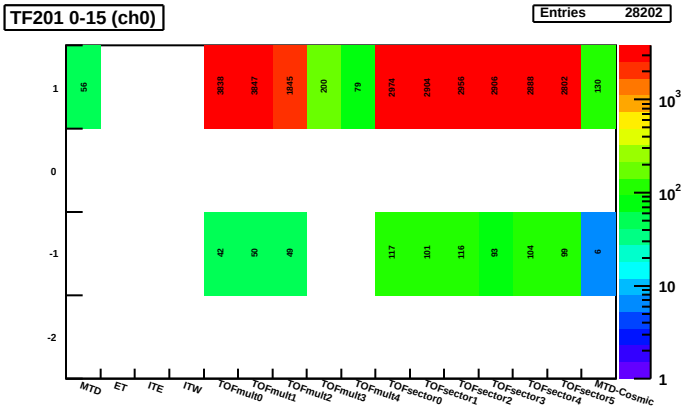

Input to FE002 QT board
Entries 1.258701e+07
ADC
channel

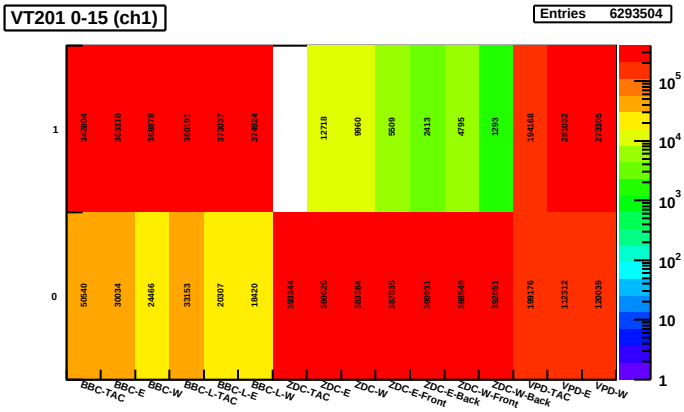

Input to FE003 QT board
Entries 1.258701e+07
ADC
channel

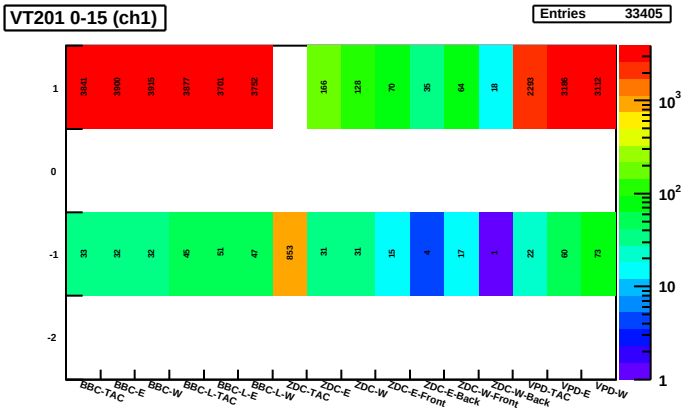

Input to FE004 QT board
Entries 1.258701e+07
ADC
channel

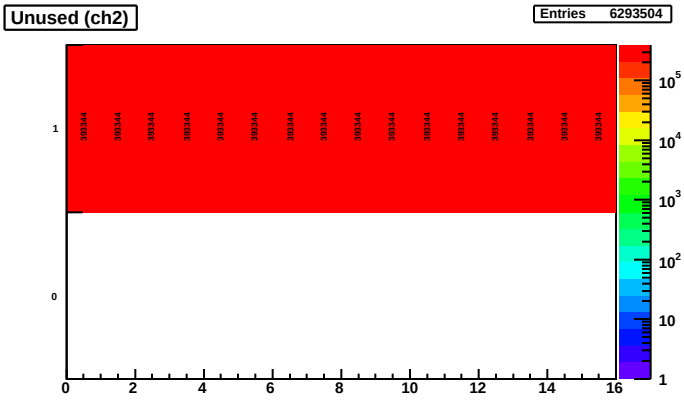

Input to FPE L1 DSM
Entries1573376
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

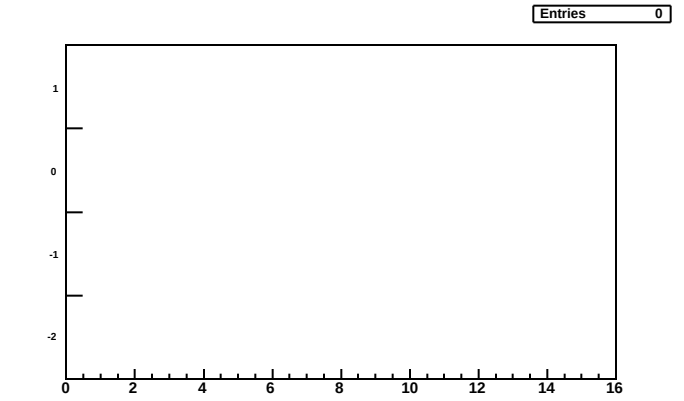

TF201 0-15 (ch0)
Entries 6293504
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

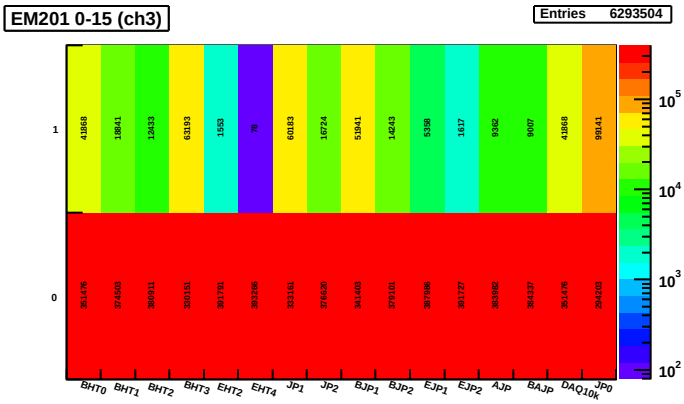

TF201 0-15 (ch0)
Entries 28202
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6293504
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 33405
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 6293504


Entries 0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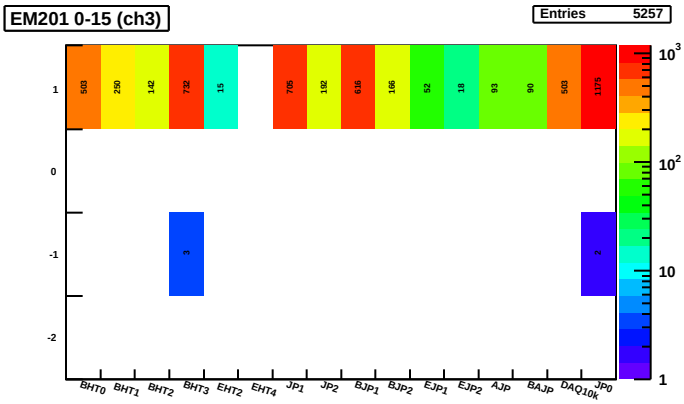
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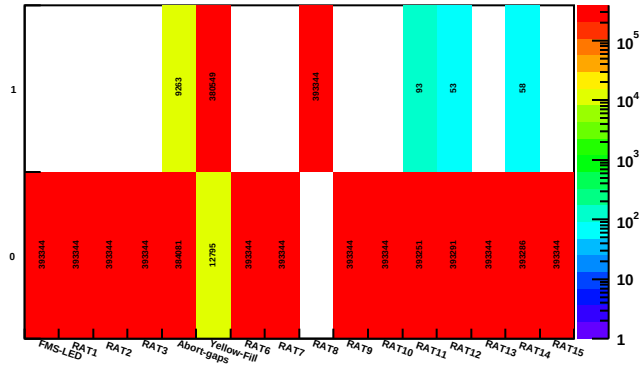
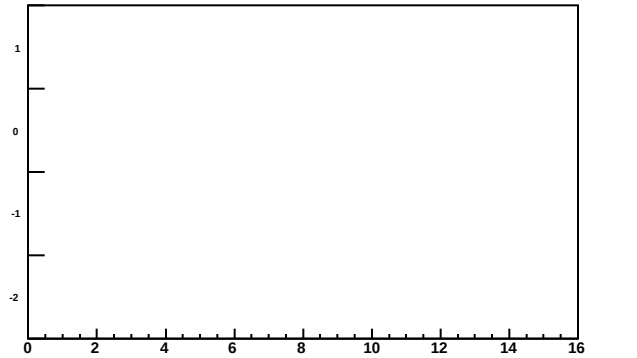
RAT board (ch4)





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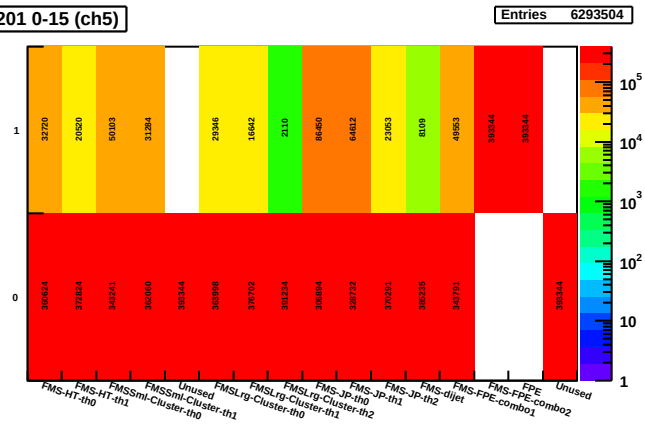

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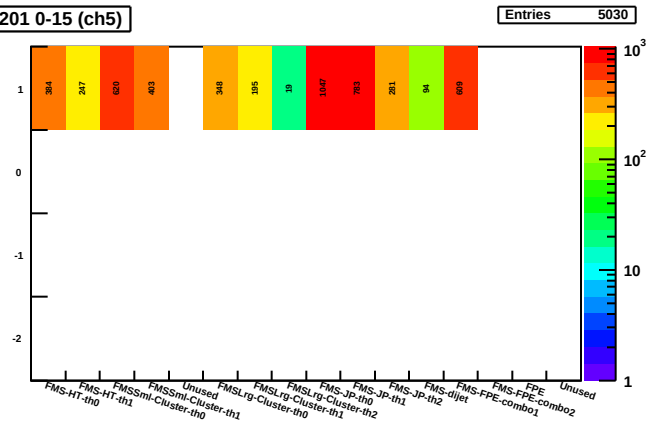

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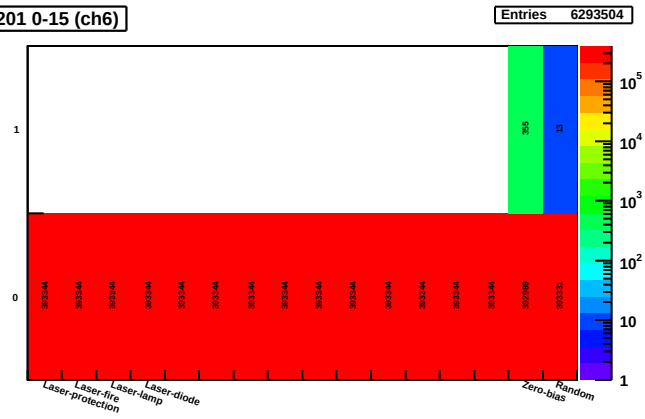

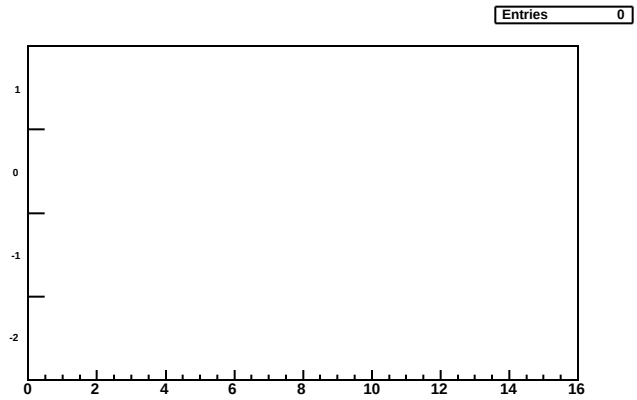
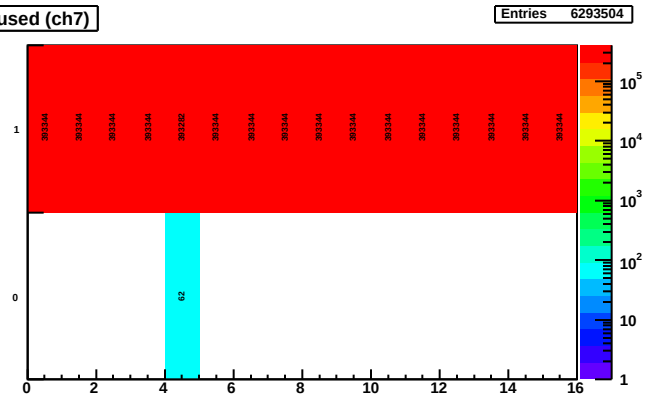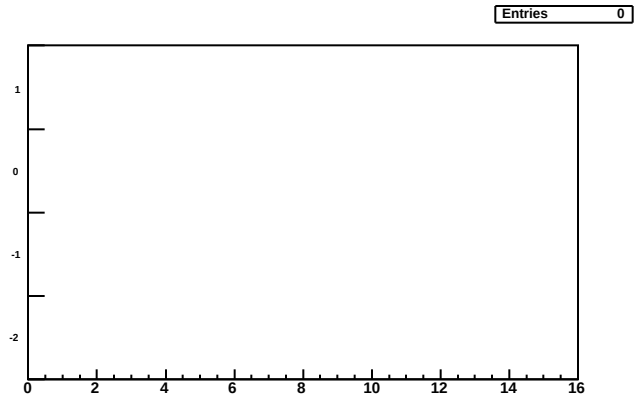
Unused (ch7)