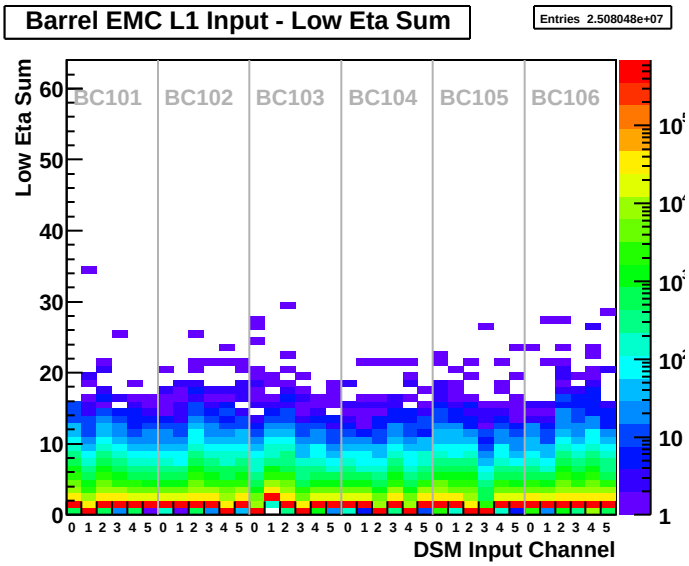
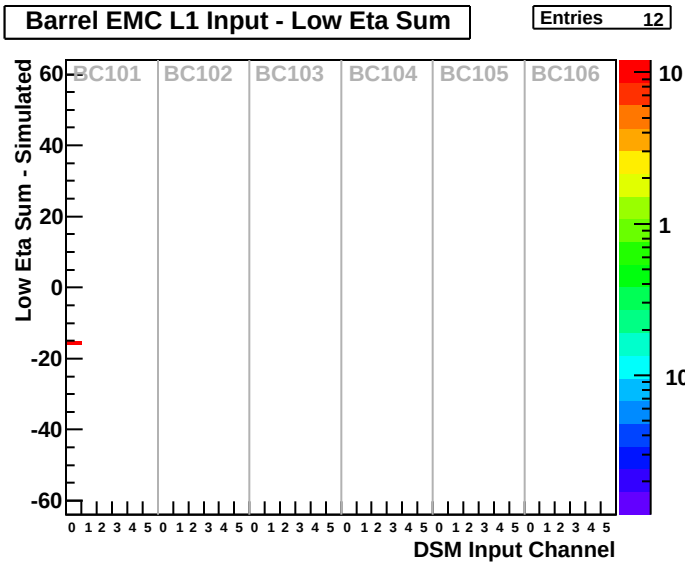
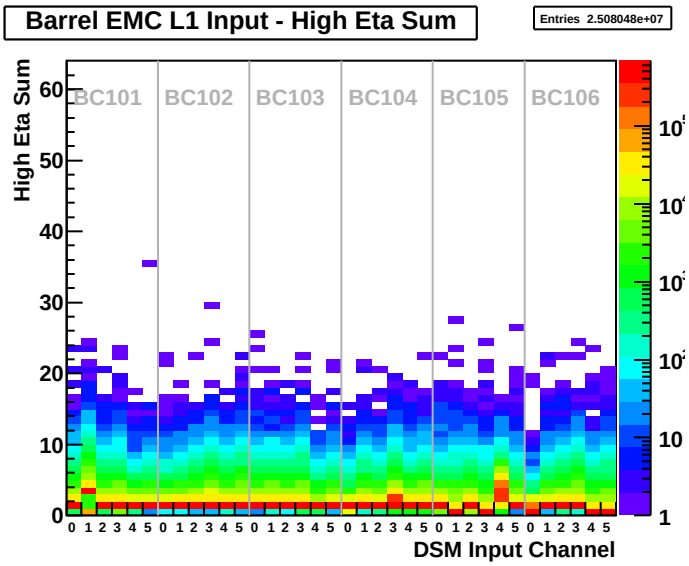
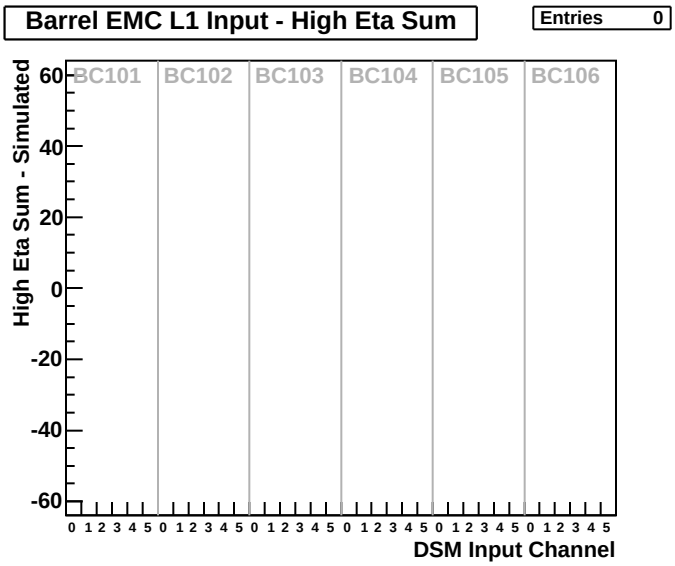
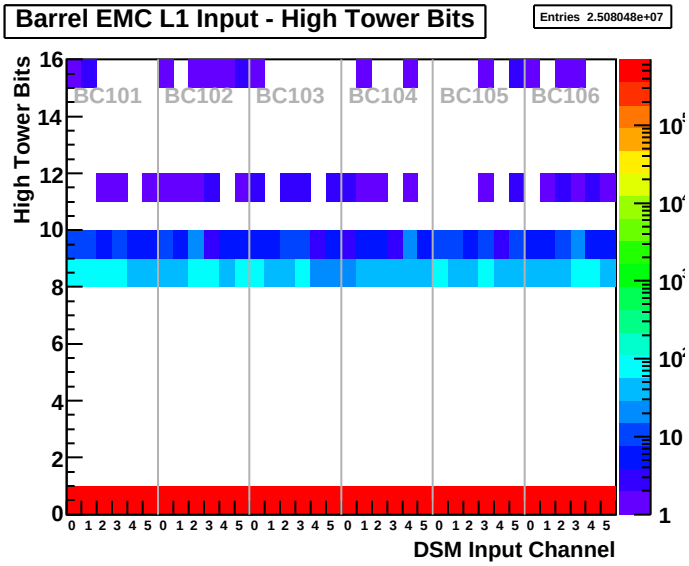
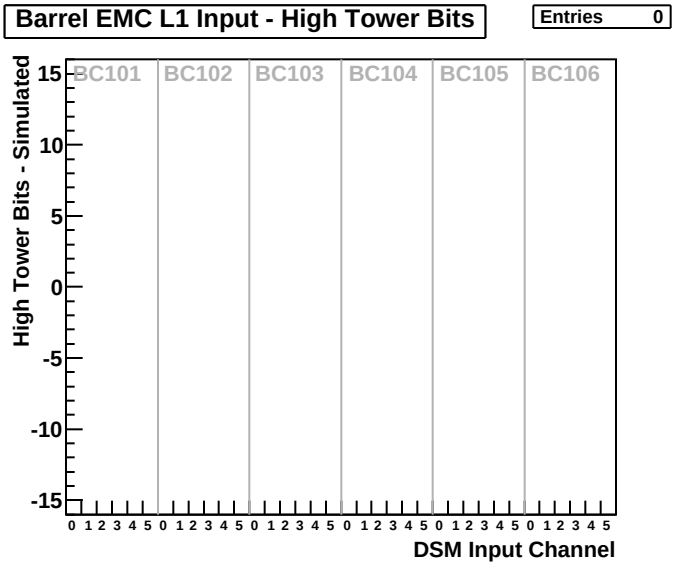

Barrel EMC L1 Input - Low Eta Sum
Entries 2.508048e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

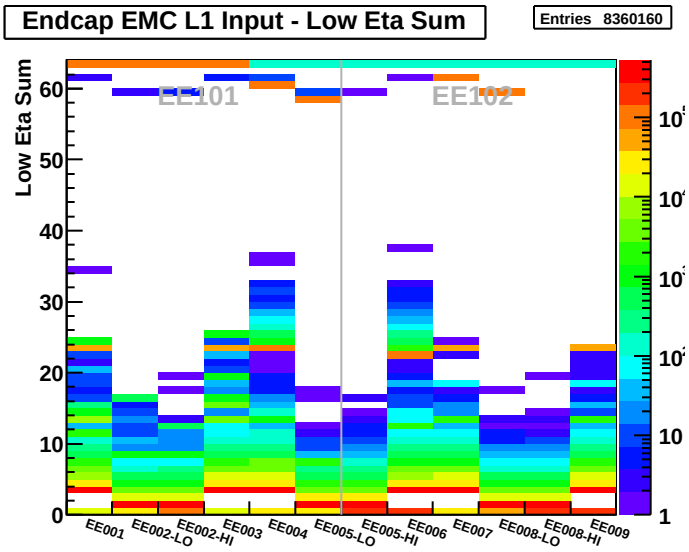

Barrel EMC L1 Input - Low Eta Sum
Entries 12
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

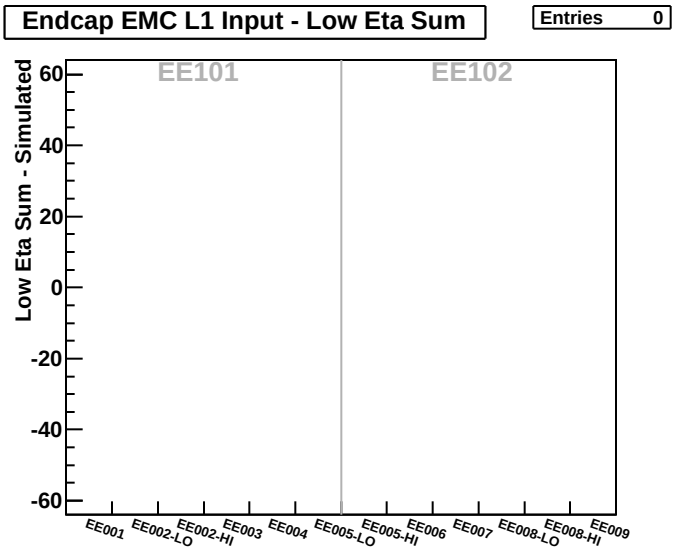

Barrel EMC L1 Input - High Eta Sum
Entries 2.508048e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

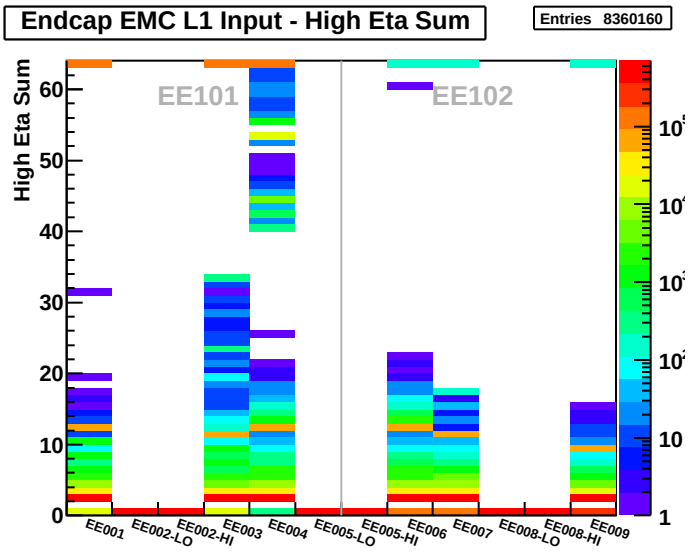

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

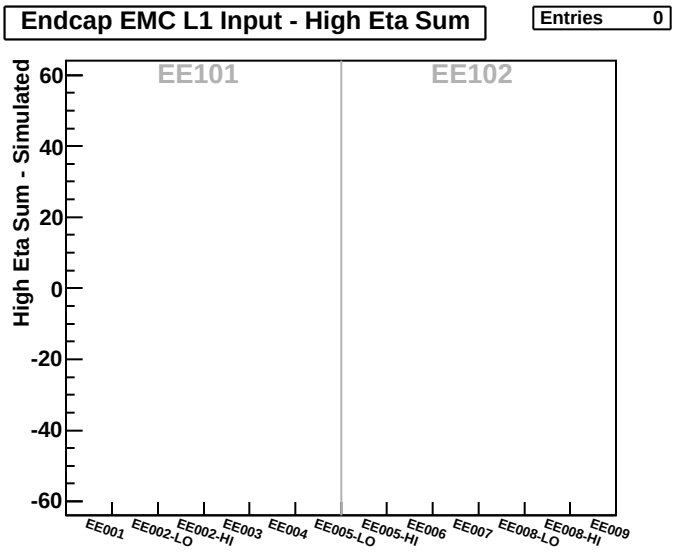

Barrel EMC L1 Input - High Tower Bits
Entries 2.508048e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

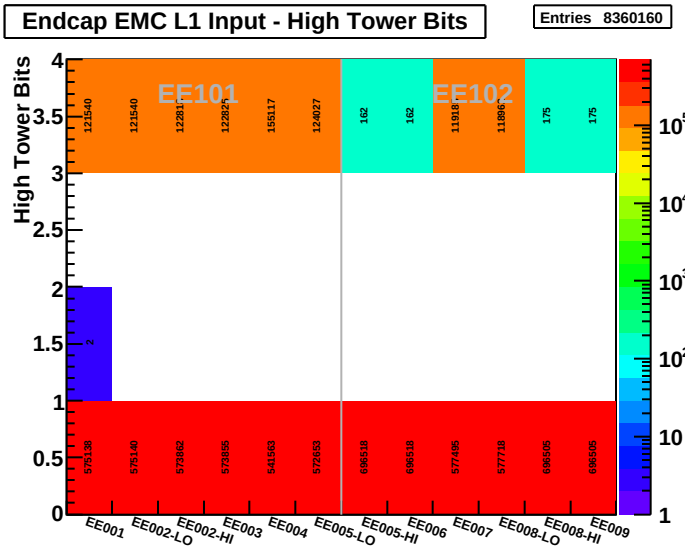

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

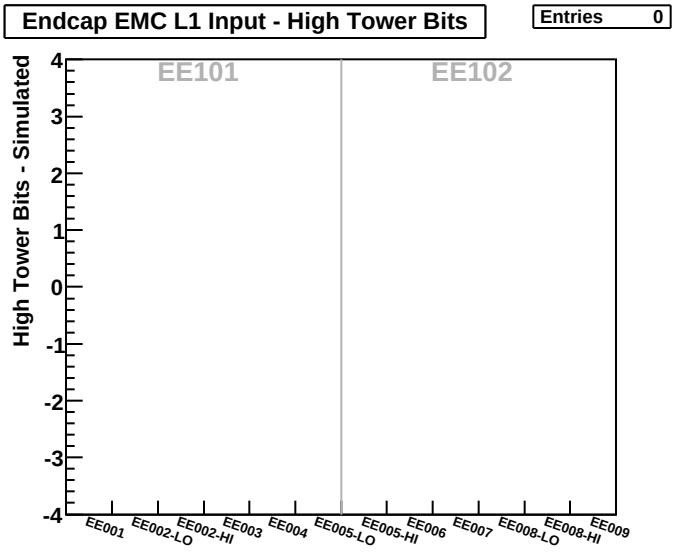

Endcap EMC L1 Input - Low Eta Sum
Entries 8360160
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

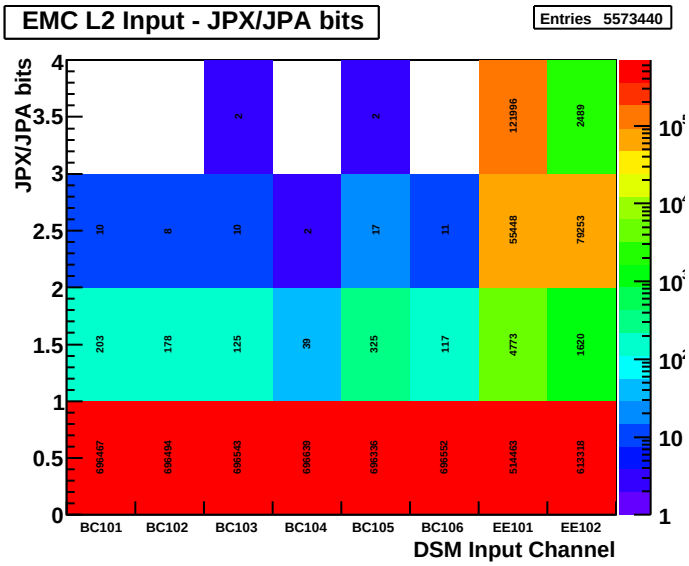

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

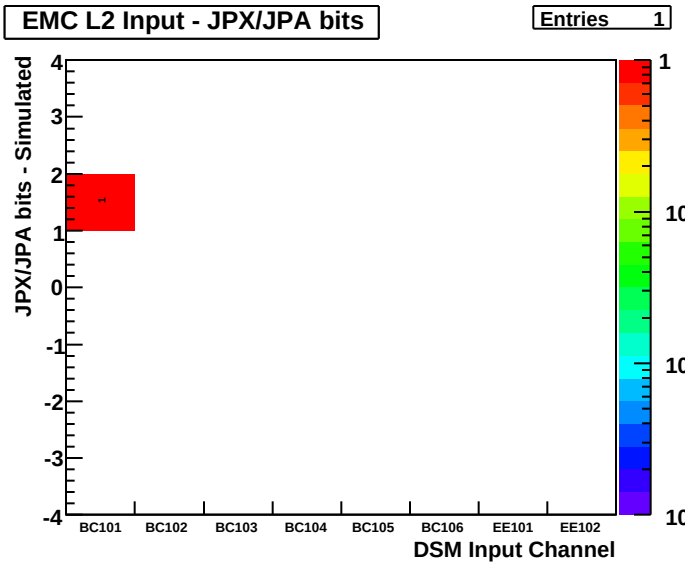

Endcap EMC L1 Input - High Eta Sum
Entries 8360160
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

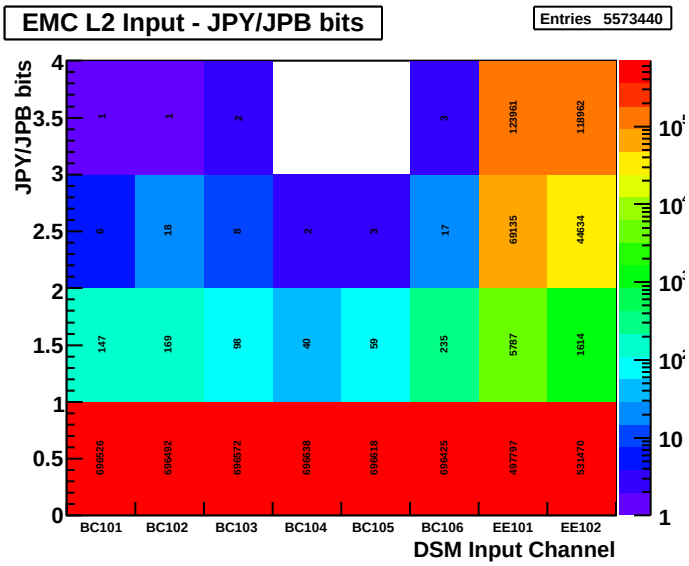

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

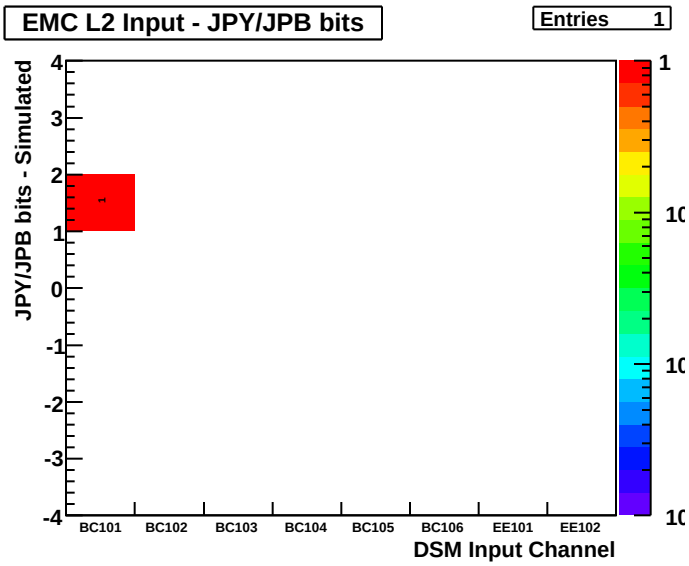

Endcap EMC L1 Input - High Tower Bits
Entries 8360160
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

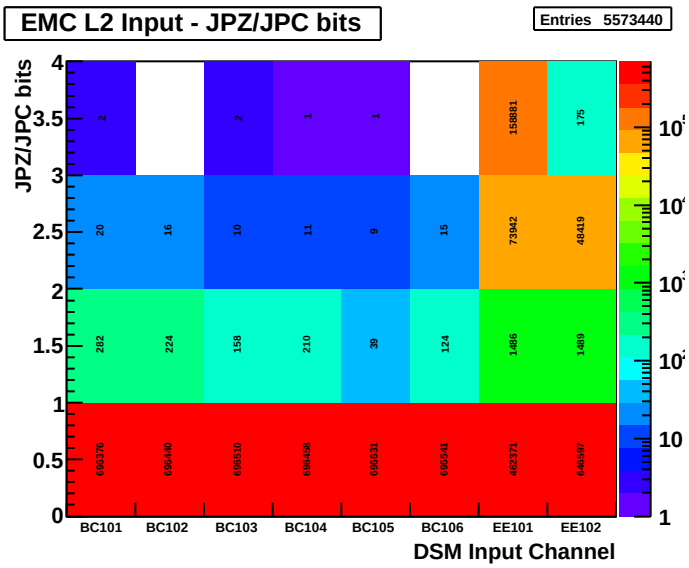

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

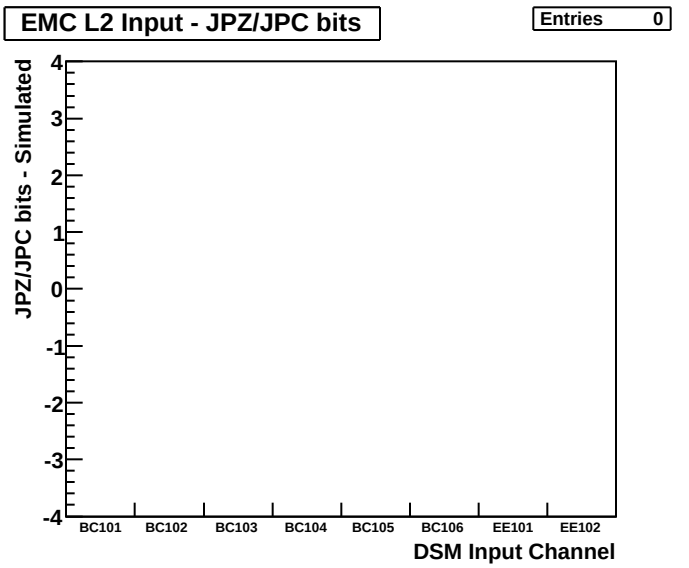

EMC L2 Input - JPX/JPA bits
Entries 5573440
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
0 0.5 1 1.5 2 2.5 3 3.5 4
1 10 10² 10³ 10⁴ 10⁵
696467 696494 696543 696639 696336 696552 514463 613318
203 178 125 39 325 117 4773 1620
10 8 10 2 17 11 55448 79253
2 2 121996 2489

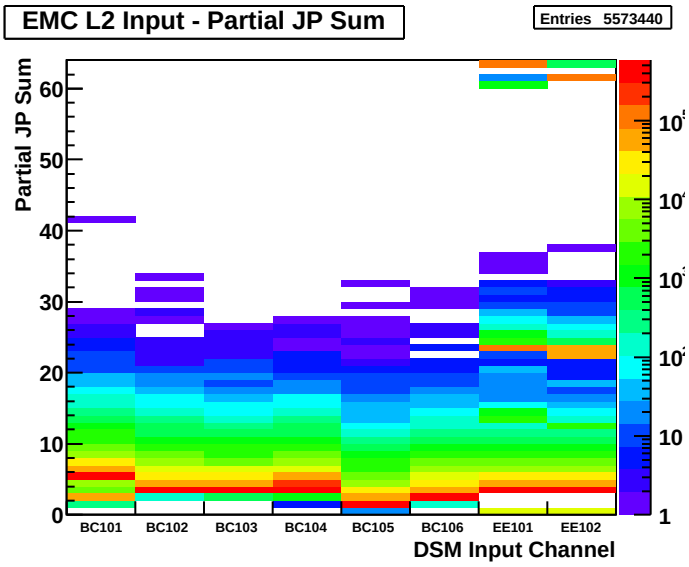

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
-4 -3 -2 -1 0 1 2 3 4
1 10 10² 10³ 10⁴ 10⁵
1

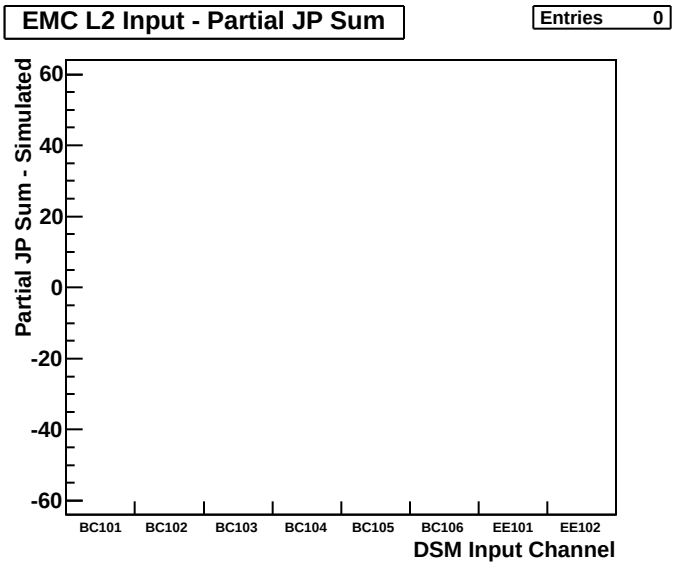

EMC L2 Input - JPY/JPB bits
Entries 5573440
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
0 0.5 1 1.5 2 2.5 3 3.5 4
1 10 10² 10³ 10⁴ 10⁵
696526 696492 696572 696638 696618 696425 497797 531470
147 169 98 40 59 235 5787 1614
6 18 8 2 3 17 69135 44634
1 1 2 3 123961 118962

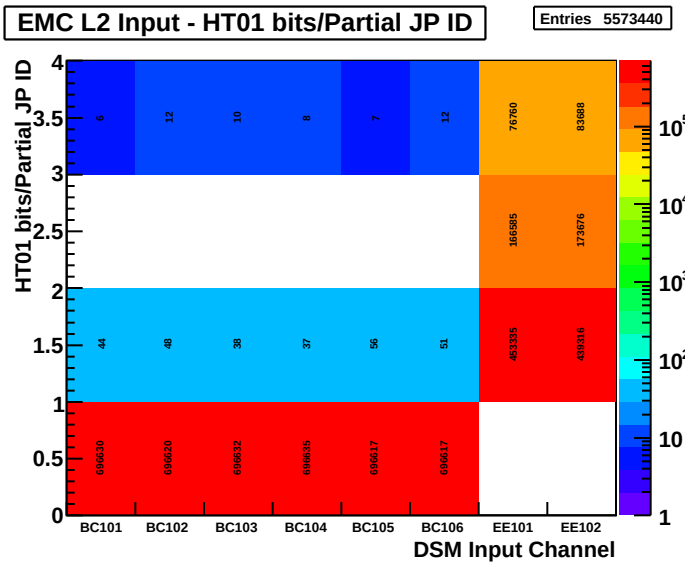

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
-4 -3 -2 -1 0 1 2 3 4
1 10 10² 10³ 10⁴ 10⁵
1

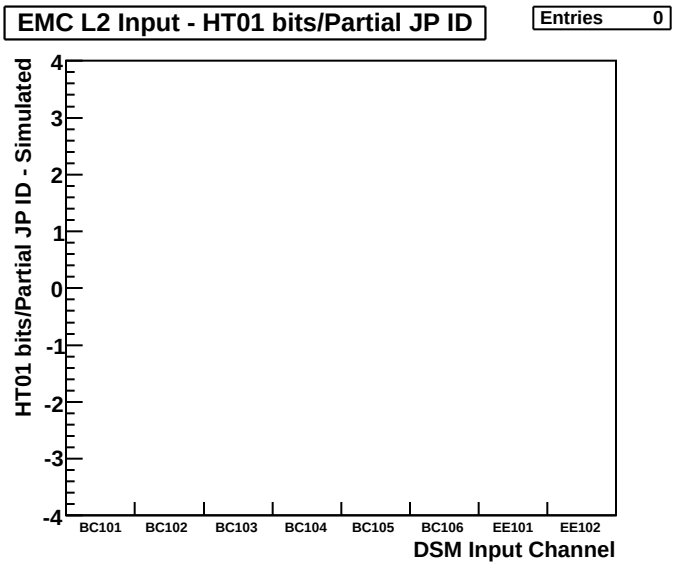

EMC L2 Input - JPZ/JPC bits
Entries 5573440
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
0 0.5 1 1.5 2 2.5 3 3.5 4
1 10 10² 10³ 10⁴ 10⁵
696376 696440 696510 696458 696631 696541 462371 646597
282 224 158 210 39 124 1486 1489
20 16 10 11 9 15 73942 48419
2 2 1 1 158881 175

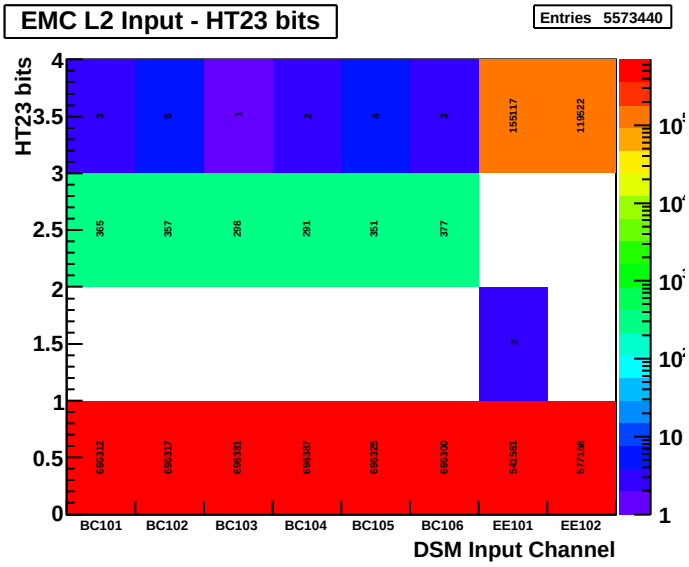

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
-4 -3 -2 -1 0 1 2 3 4

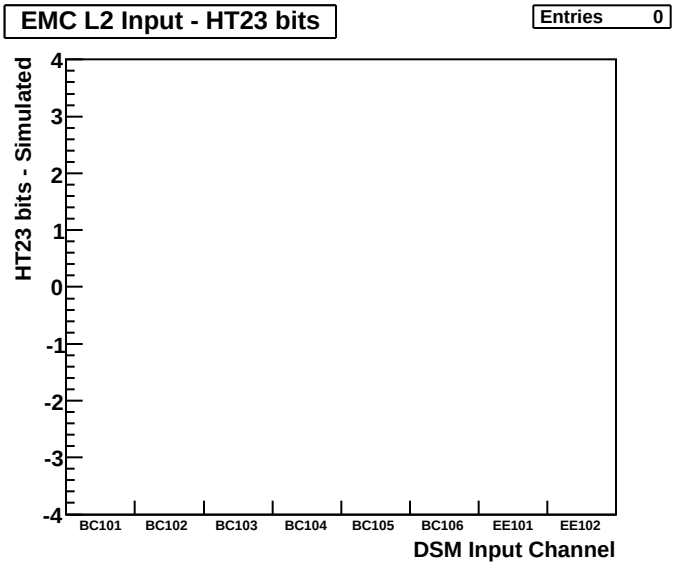

EMC L2 Input - Partial JP Sum
Entries 5573440
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

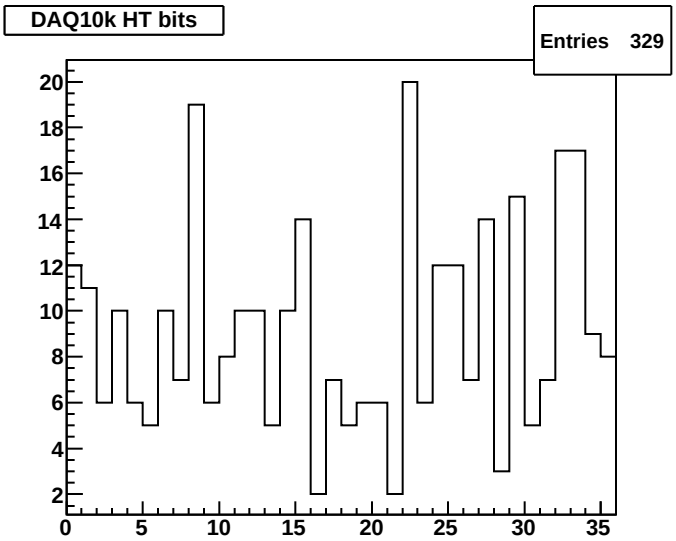

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

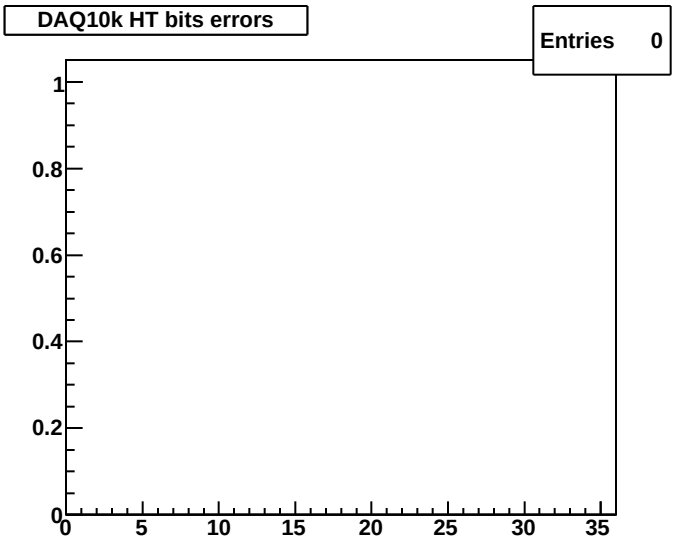

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5573440
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

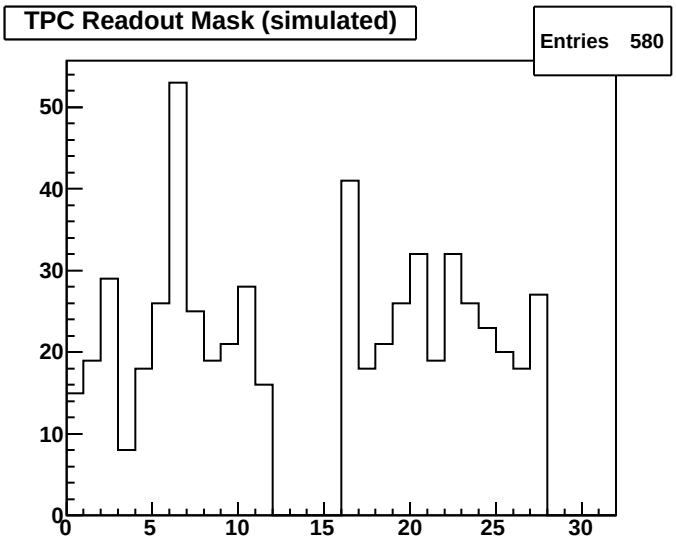

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

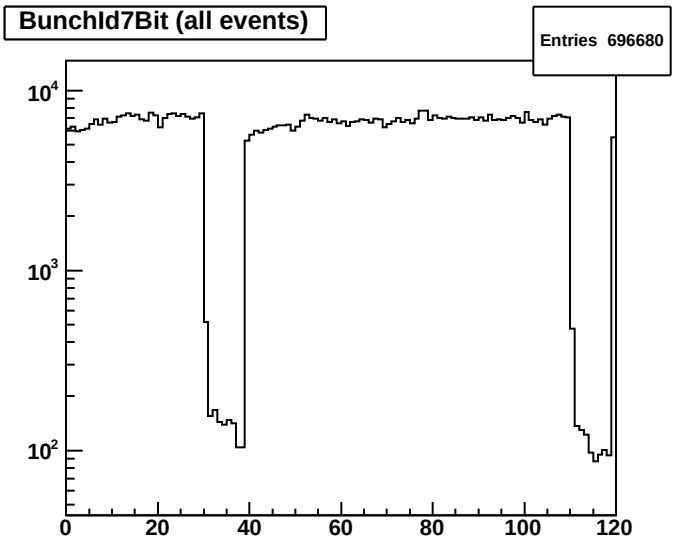

EMC L2 Input - HT23 bits
Entries 5573440
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

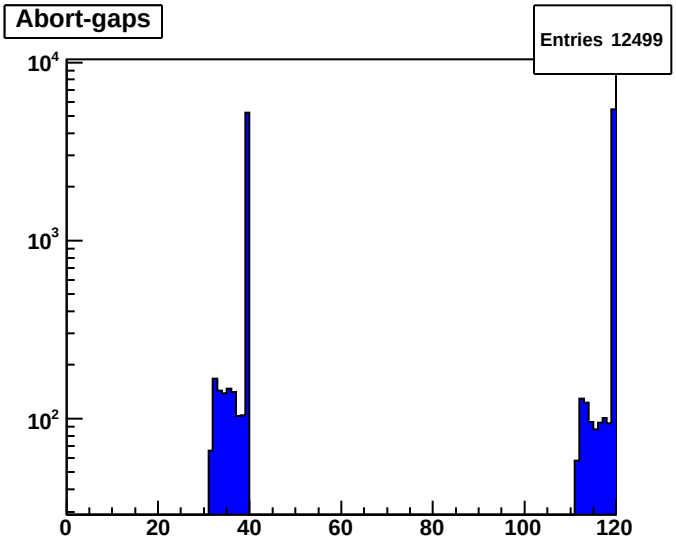

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

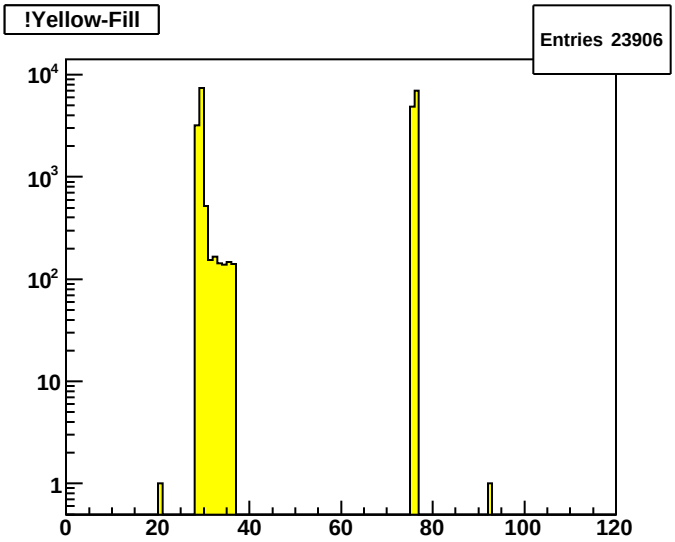

DAQ10k HT bits
Entries 329

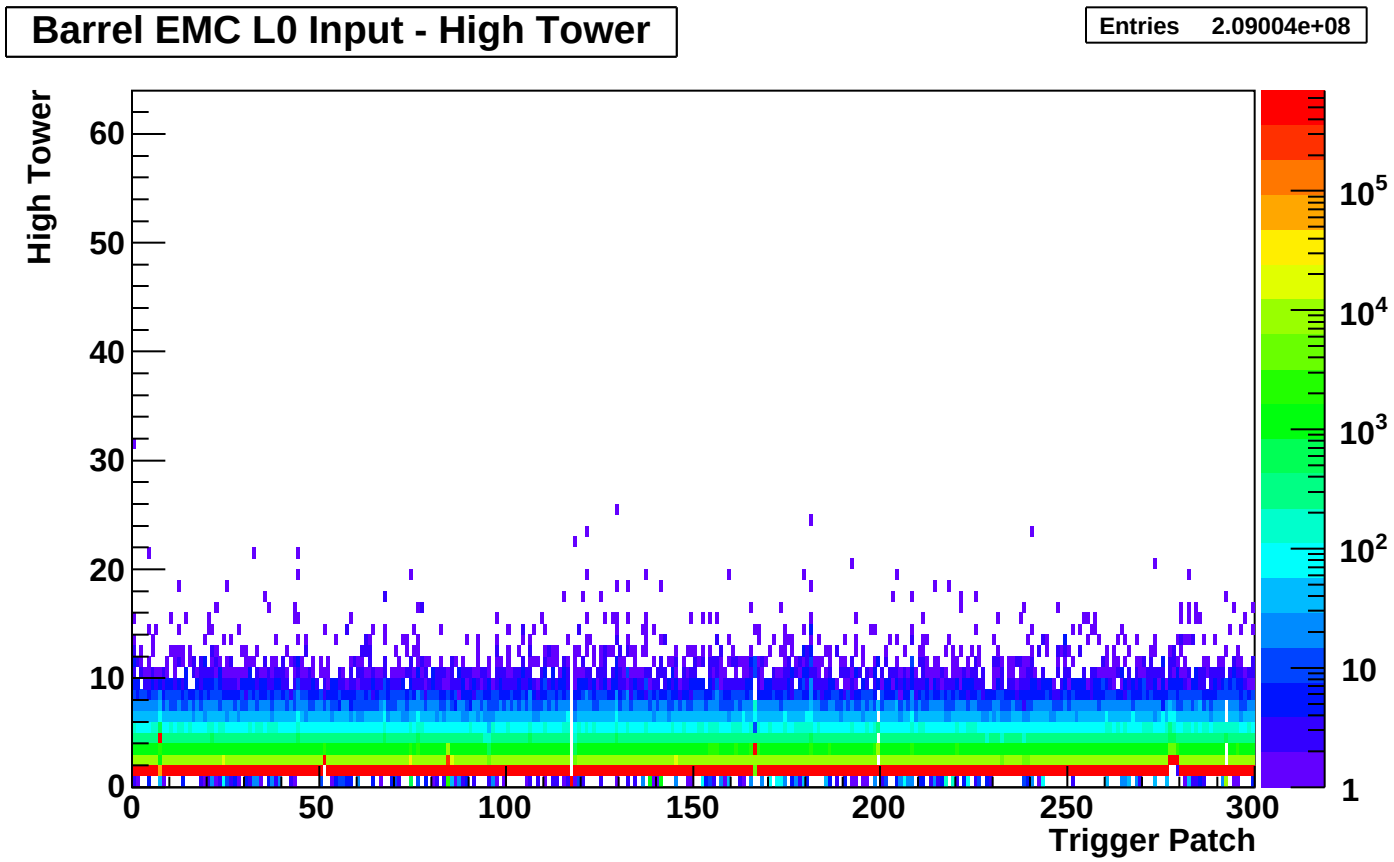

DAQ10k HT bits errors
Entries 0

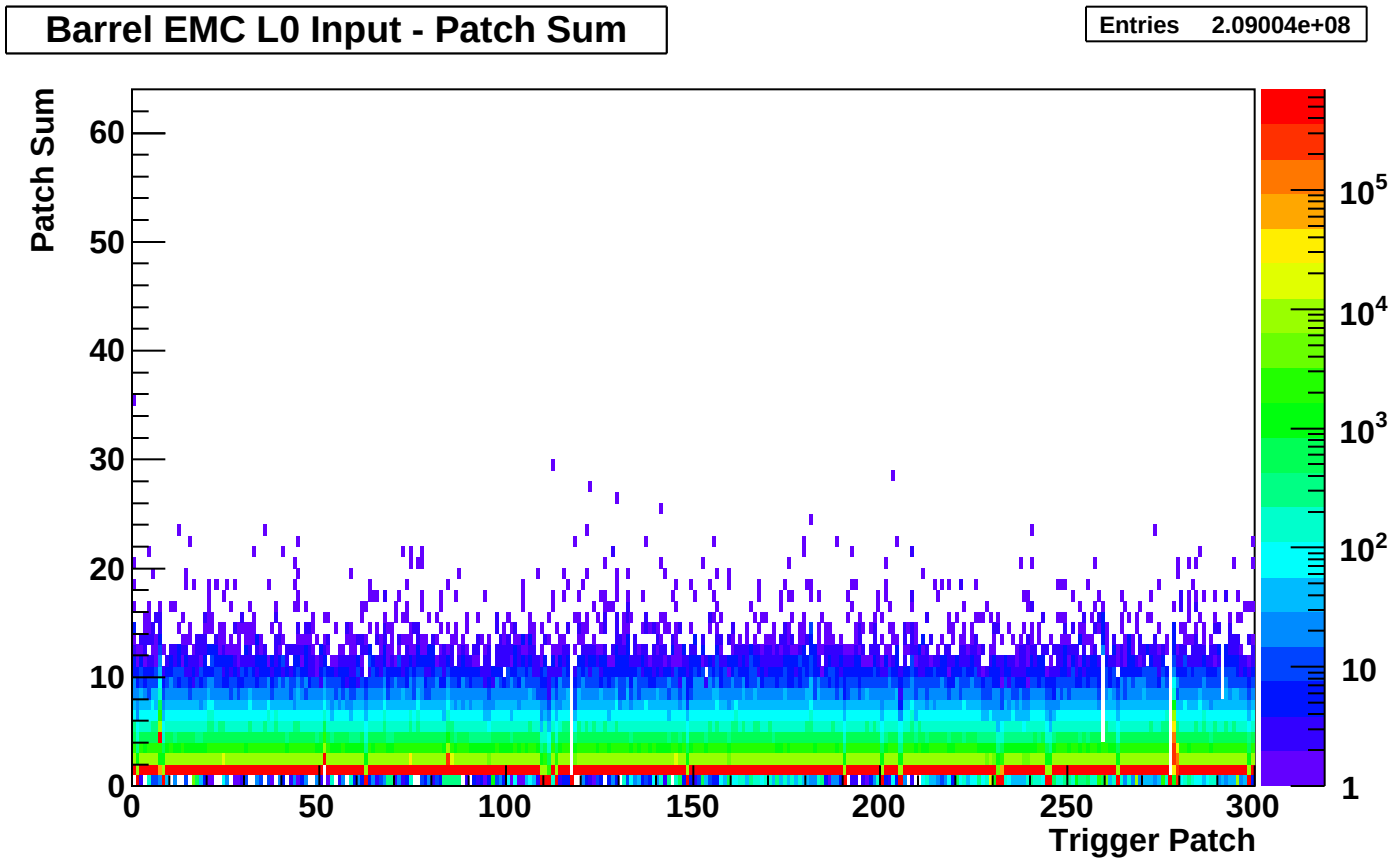

TPC Readout Mask (simulated)
Entries 580

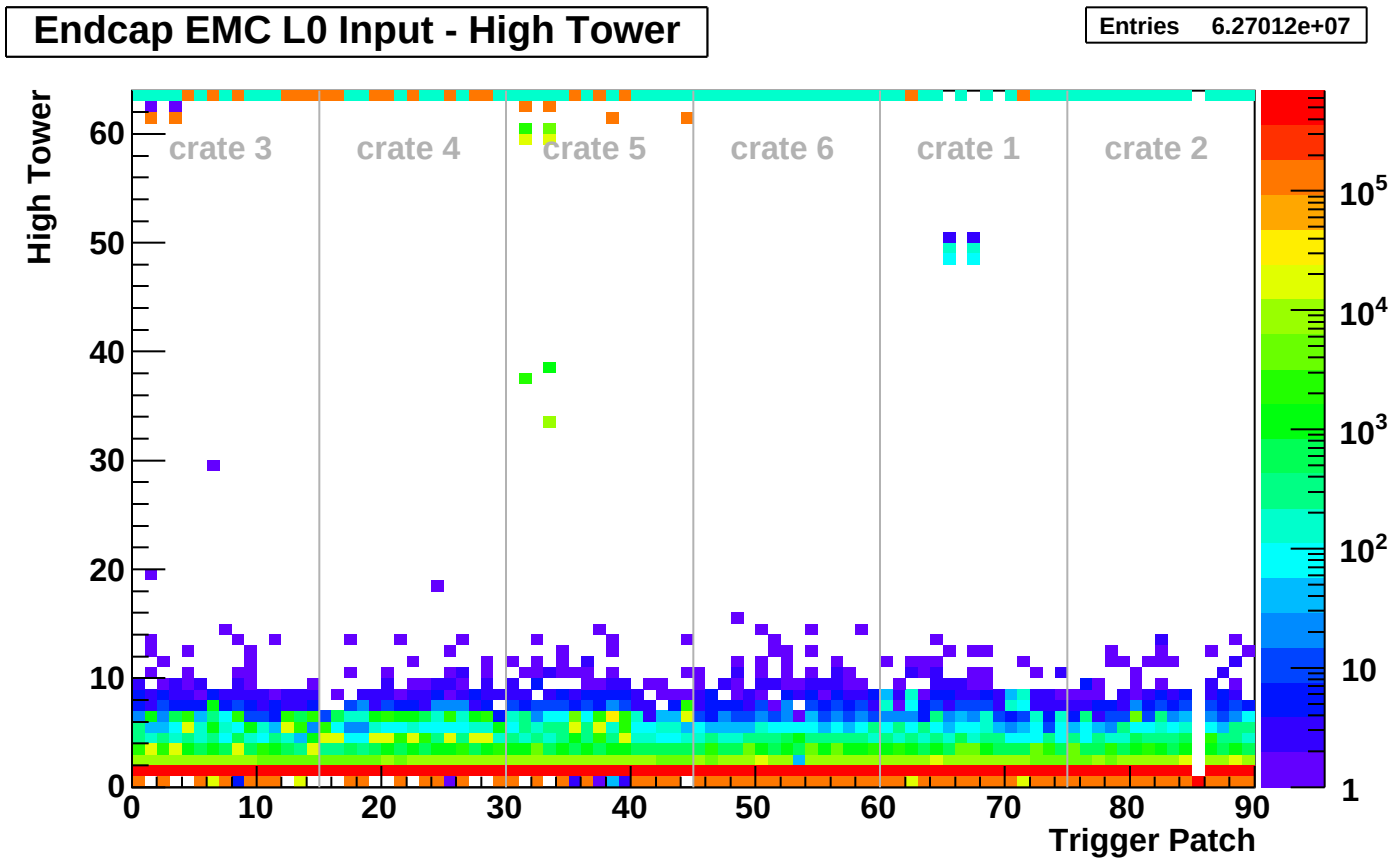

BunchId7Bit (all events)
Entries 696680

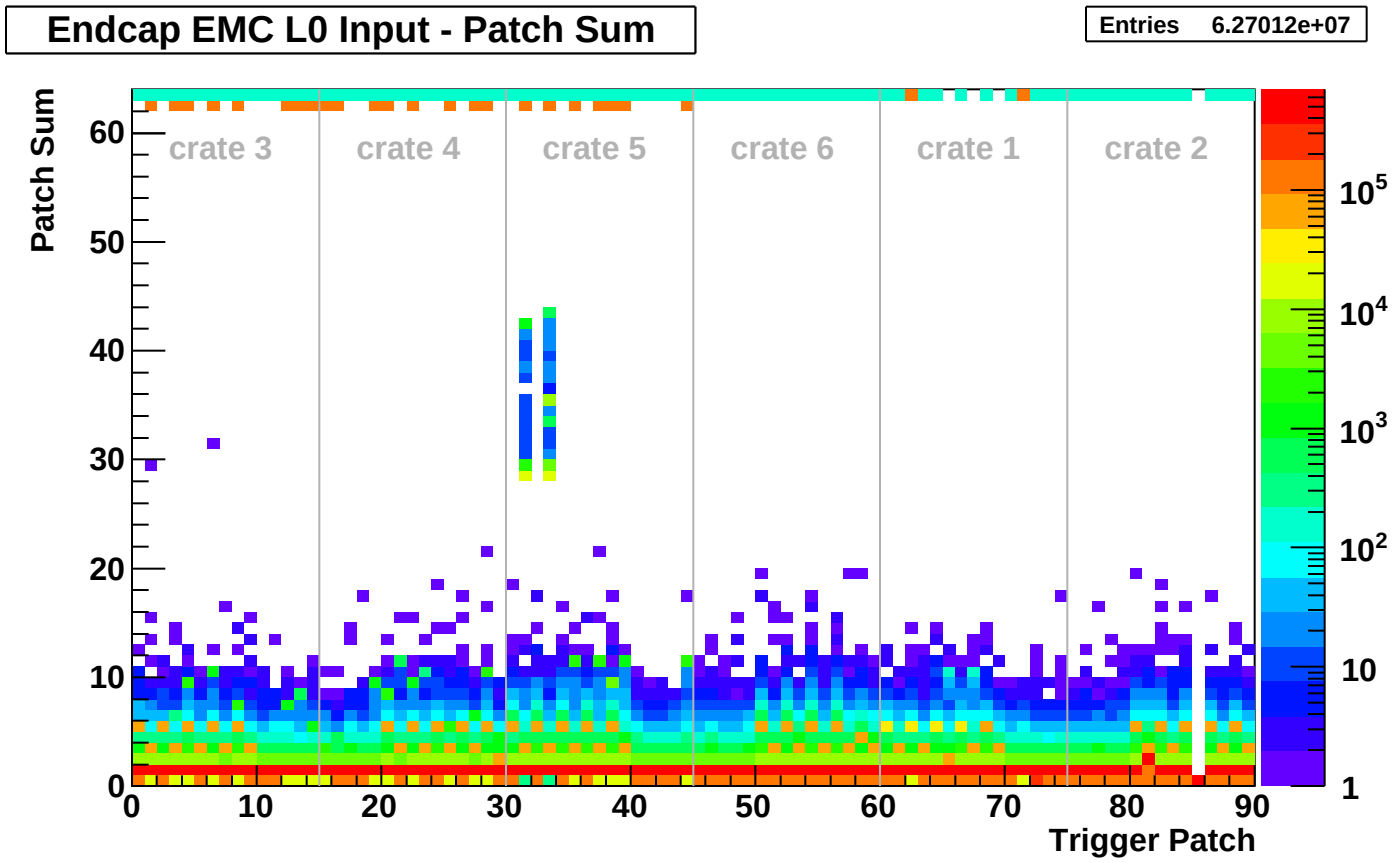

Abort-gaps
Entries 12499

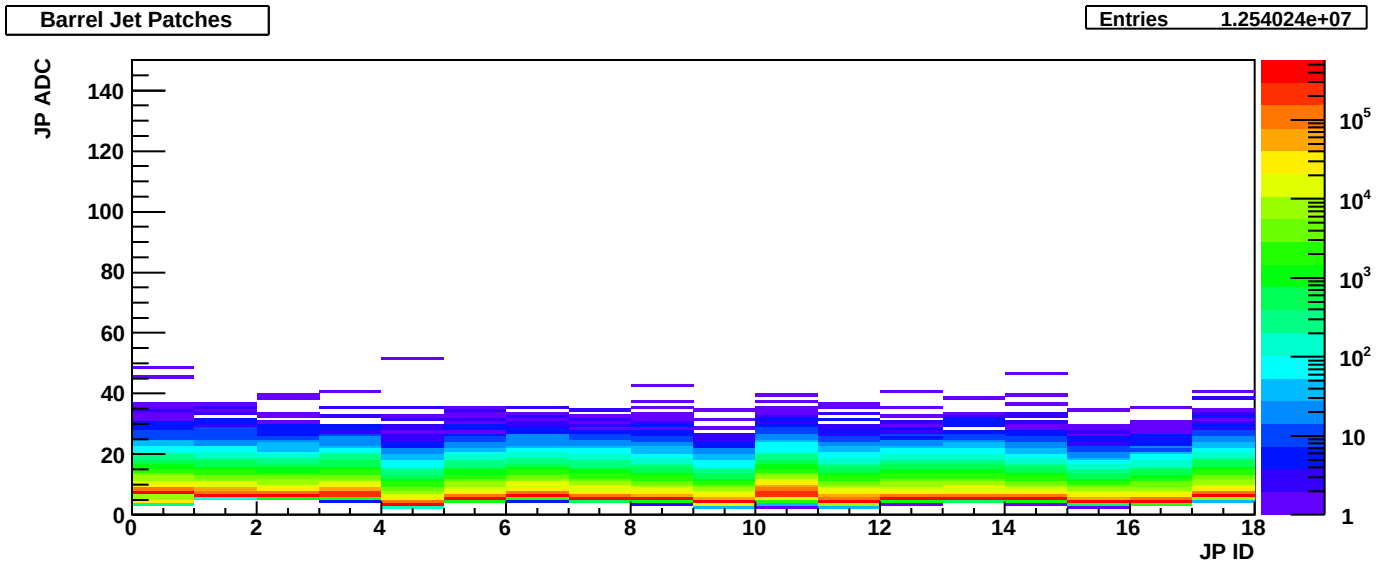

!Yellow-Fill
Entries 23906

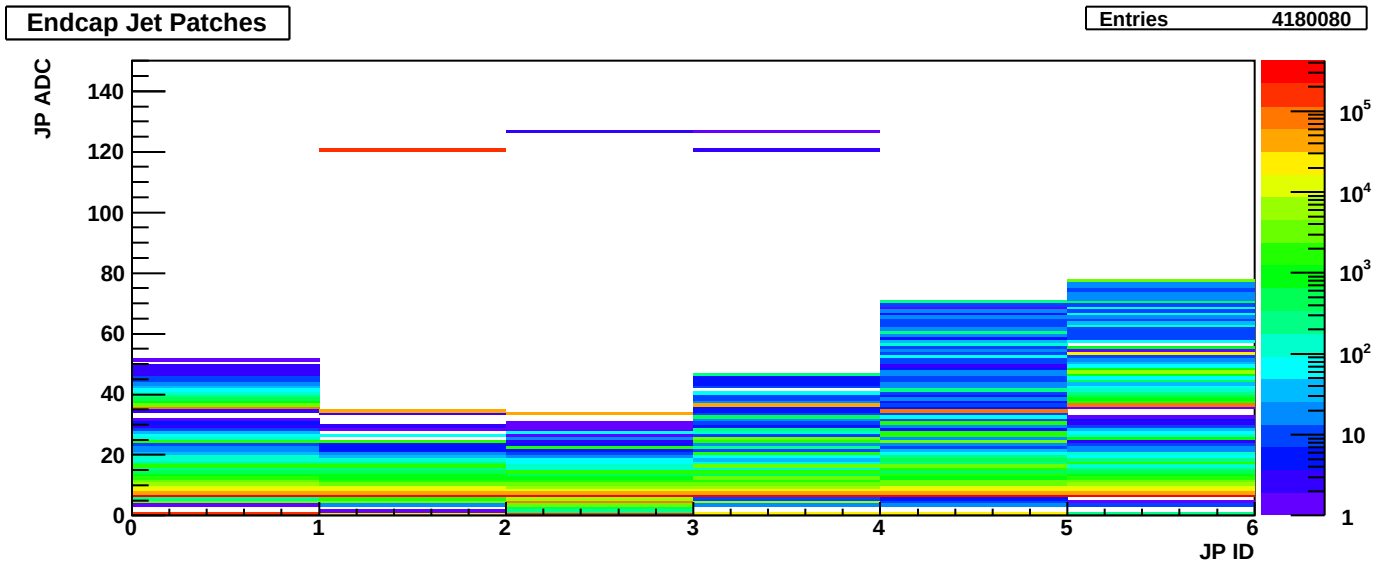

Barrel EMC L0 Input - High Tower
Entries 2.09004e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

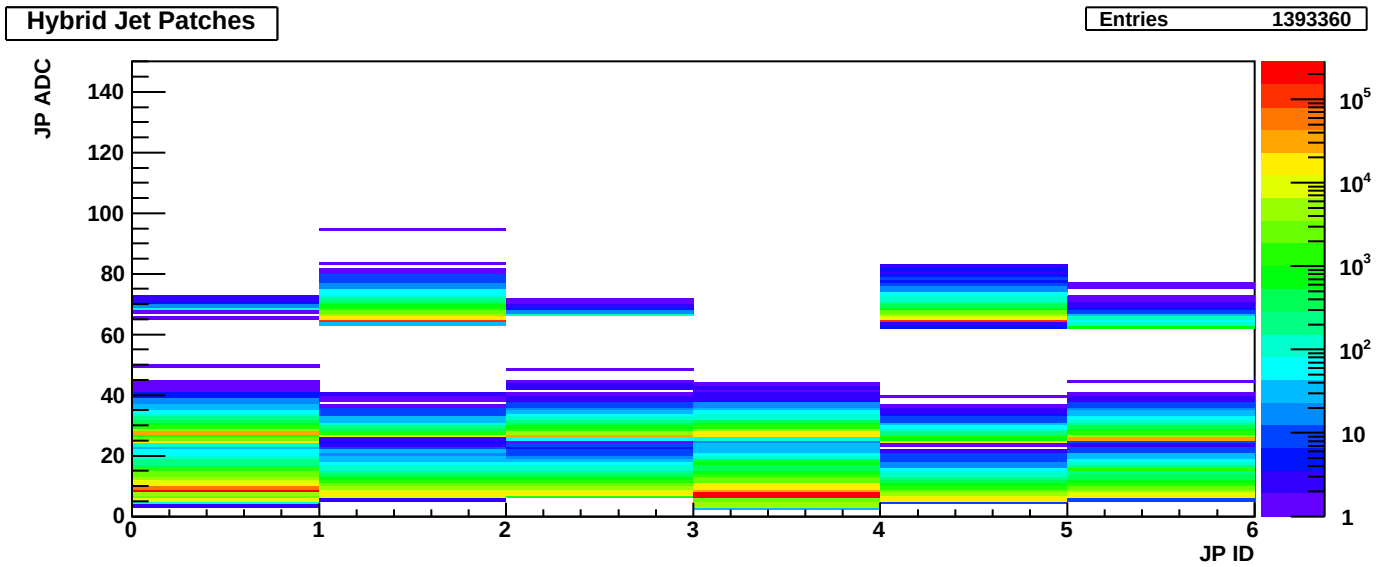

Barrel EMC L0 Input - Patch Sum
Entries 2.09004e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

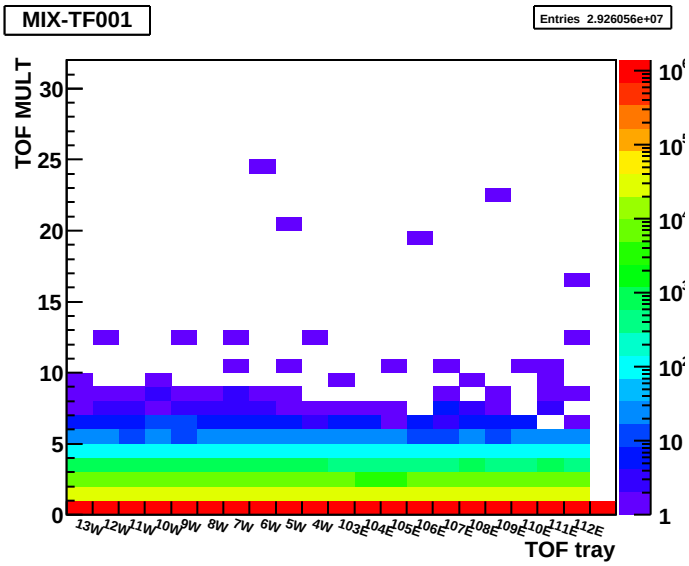

Endcap EMC L0 Input - High Tower
Entries 6.27012e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

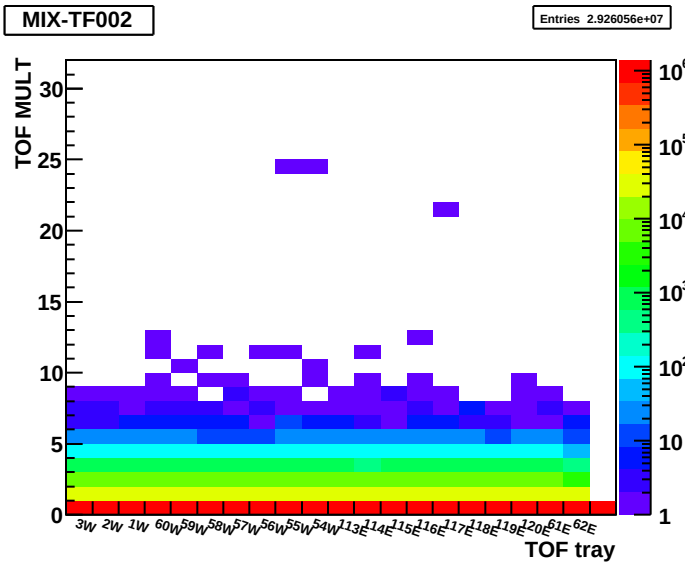

Endcap EMC L0 Input - Patch Sum
Entries 6.27012e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

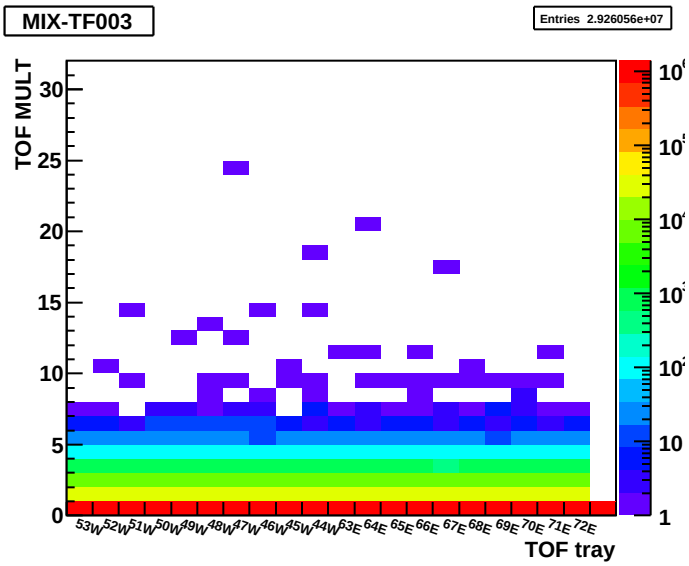

Barrel Jet Patches
Entries 1.254024e+07
JP ADC
JP ID

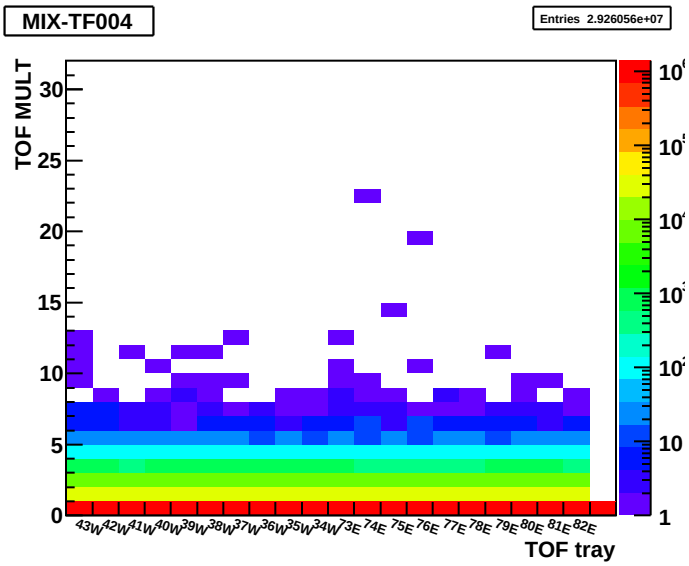

Endcap Jet Patches
Entries 4180080
JP ADC
JP ID

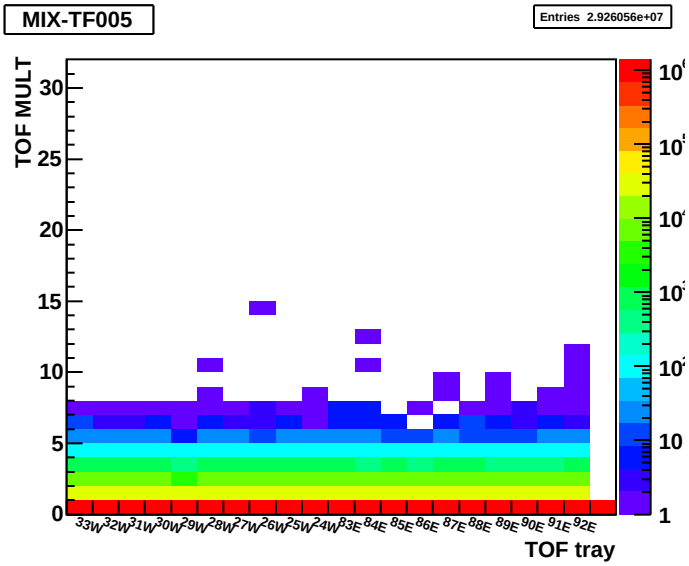

Hybrid Jet Patches
Entries 1393360
JP ADC
JP ID

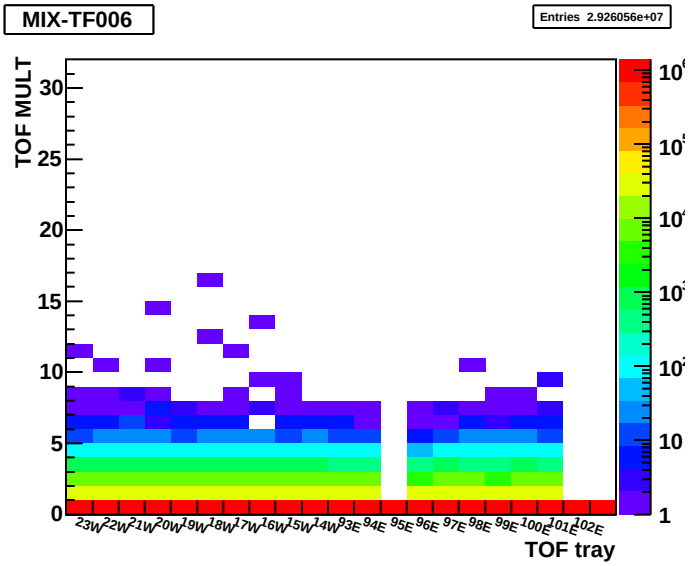

MIX-TF001
Entries 2.926056e+07
TOF MULT
TOF tray

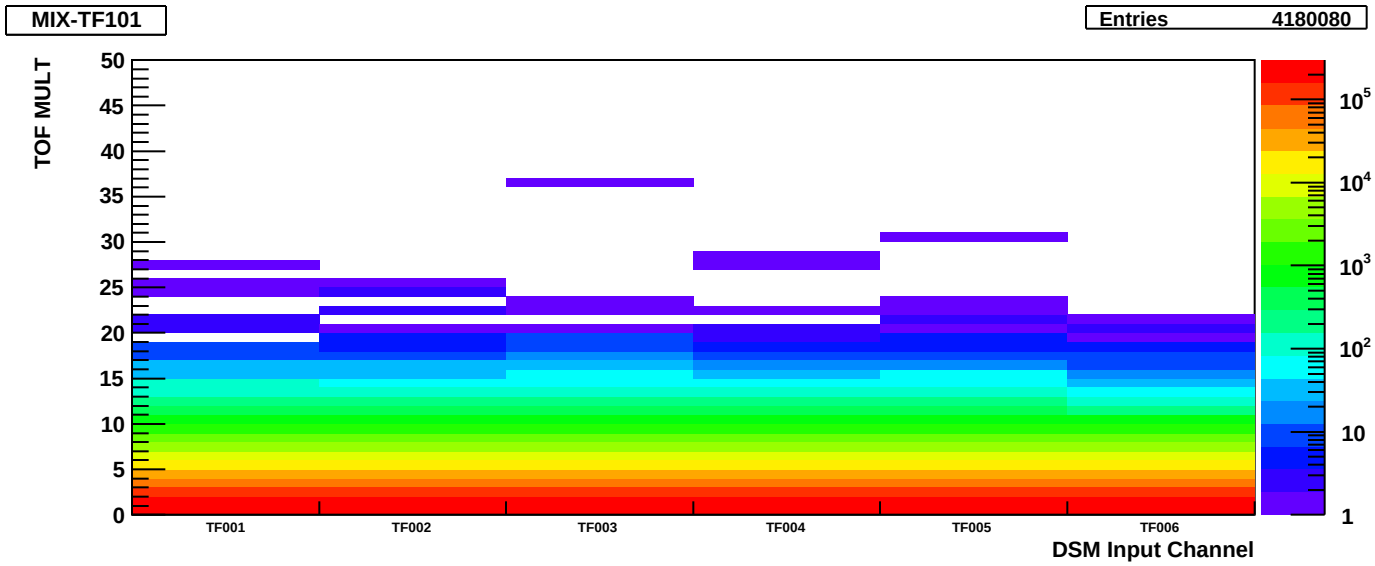

MIX-TF002
Entries 2.926056e+07
TOF MULT
TOF tray

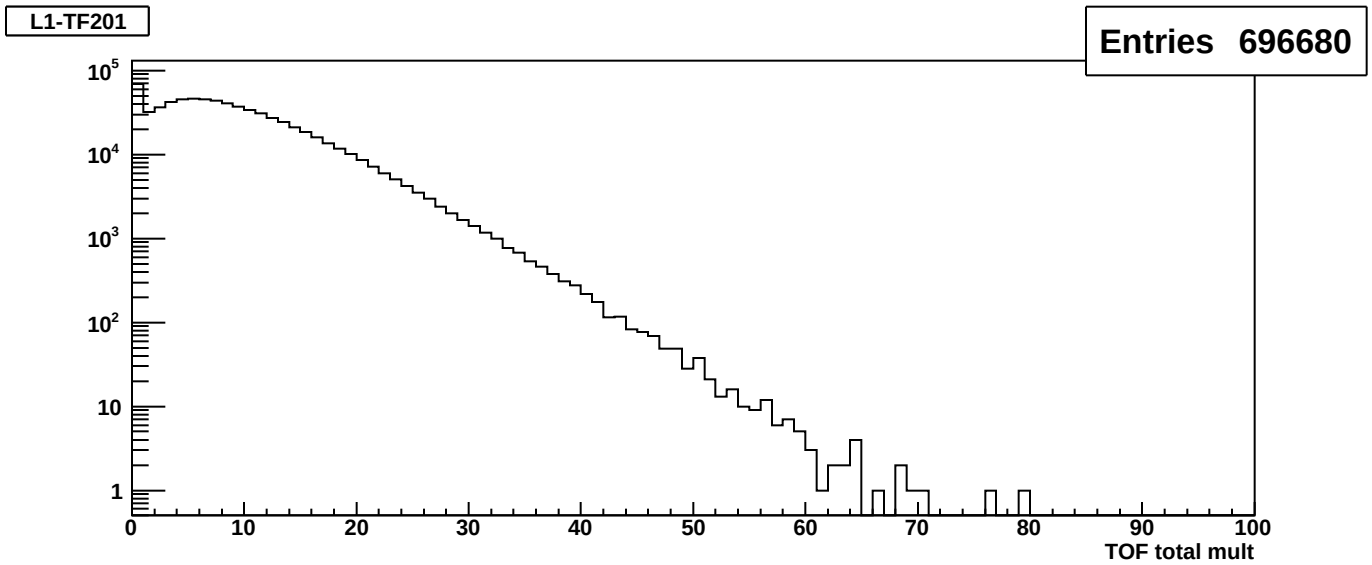

MIX-TF003
Entries 2.926056e+07
TOF MULT
TOF tray

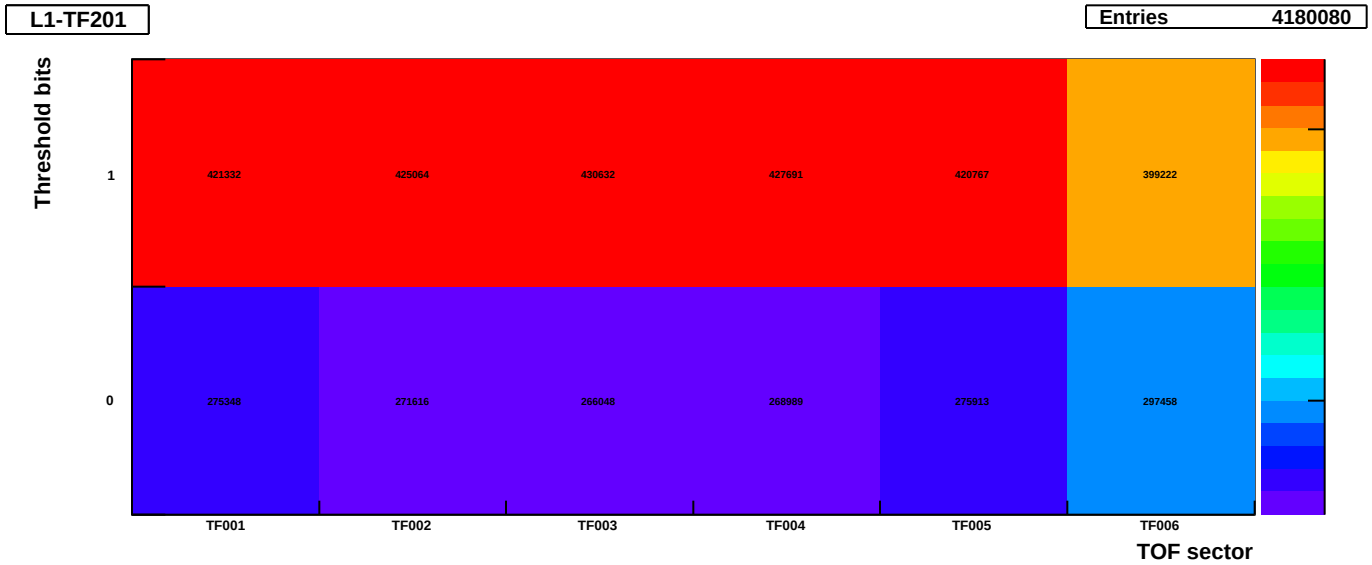

MIX-TF004
Entries 2.926056e+07
TOF MULT
TOF tray

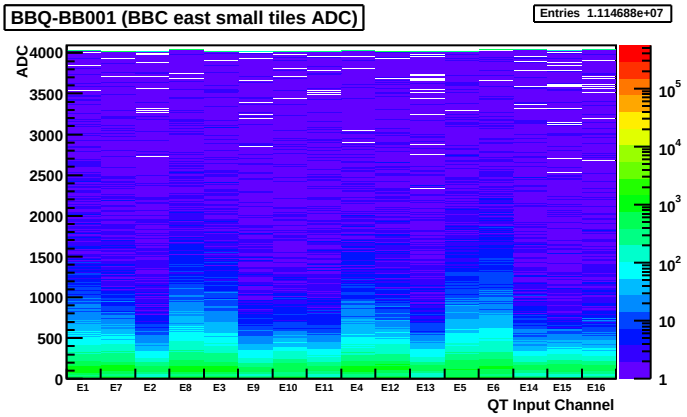

MIX-TF005
Entries 2.926056e+07
TOF MULT
TOF tray

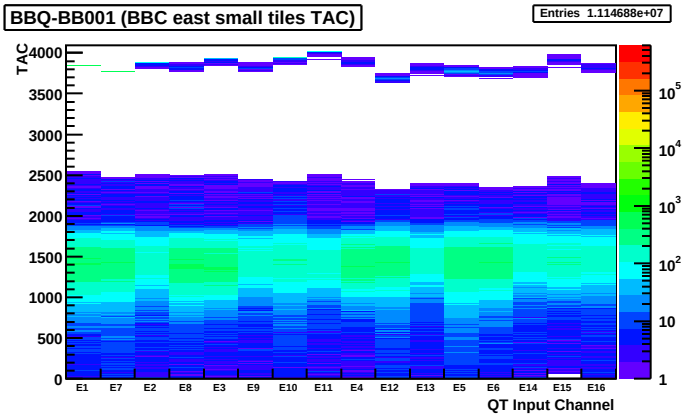

MIX-TF006
Entries 2.926056e+07
TOF MULT
TOF tray

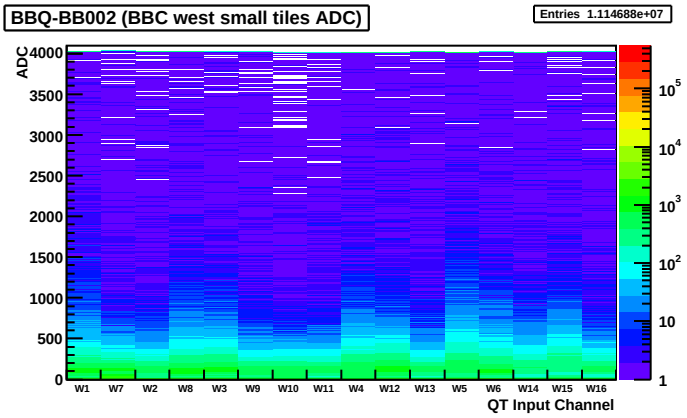

MIX-TF101
Entries 4180080
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

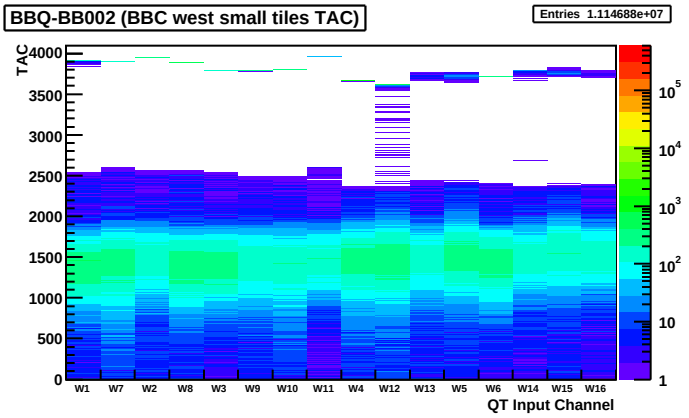

L1-TF201
Entries 696680
TOF total mult

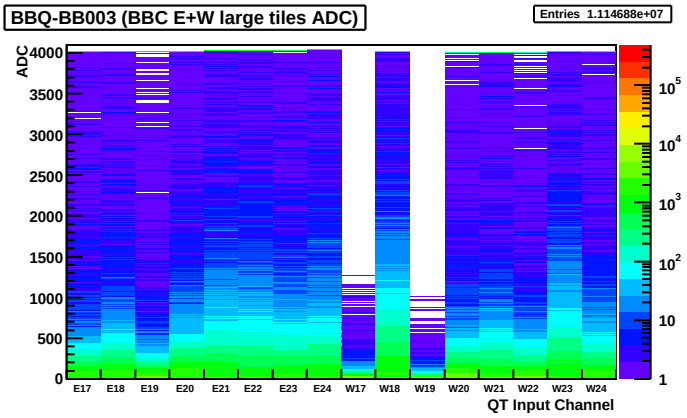

L1-TF201
Entries 4180080
Threshold bits
421332
425064
430632
427691
420767
399222
275348
271616
266048
268989
275913
297458
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

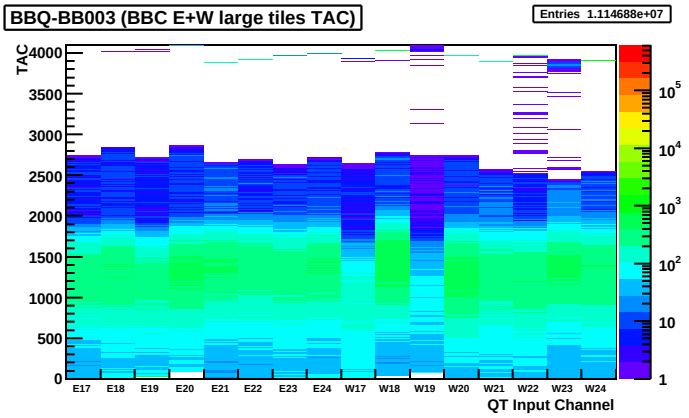

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.114688e+07
ADC
QT Input Channel

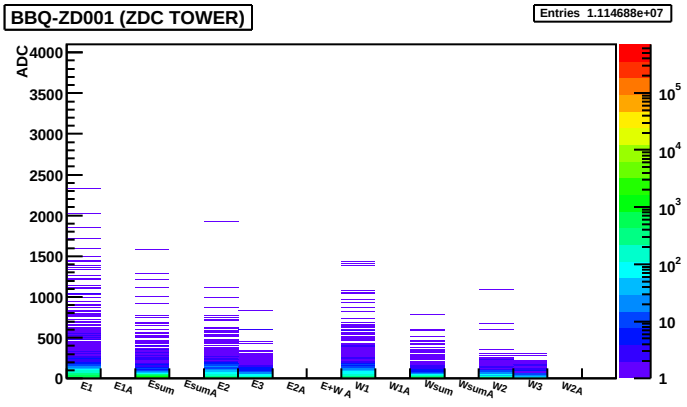

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.114688e+07
TAC
QT Input Channel

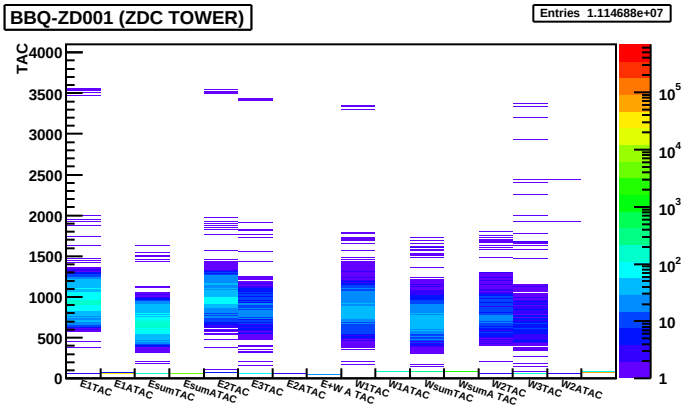

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.114688e+07
ADC
QT Input Channel

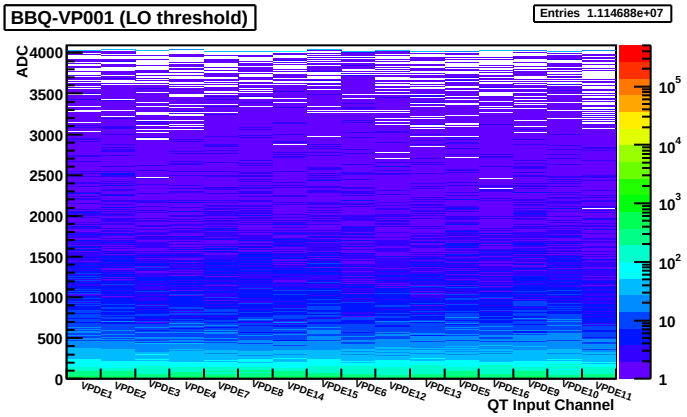

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.114688e+07
TAC
QT Input Channel

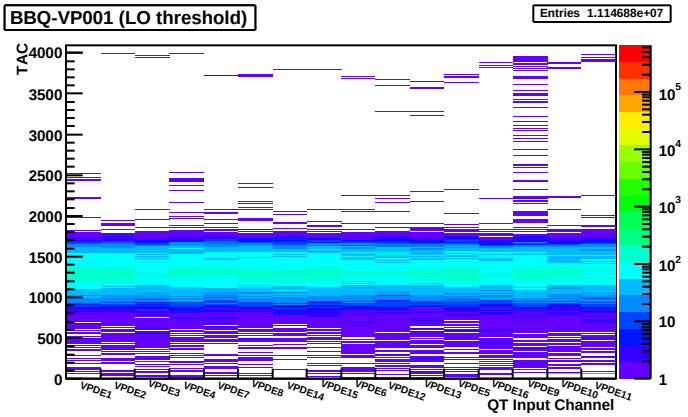

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.114688e+07
ADC
QT Input Channel

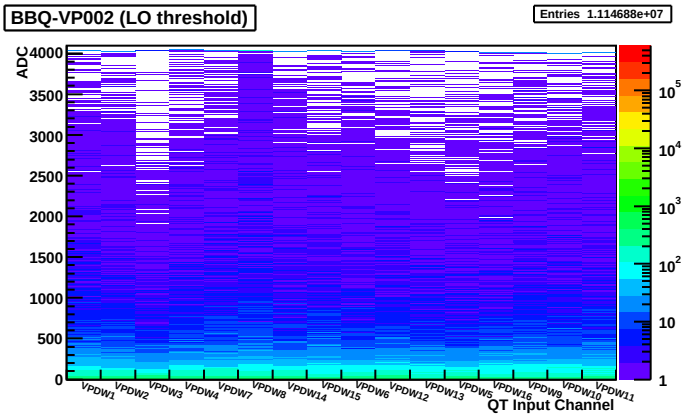

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.114688e+07
TAC
QT Input Channel

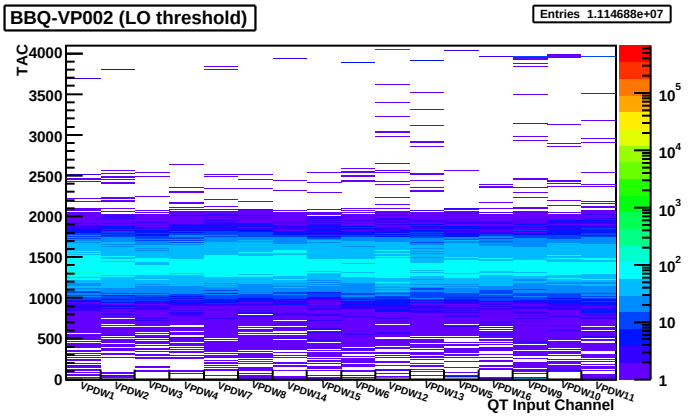

BBQ-ZD001 (ZDC TOWER)
Entries 1.114688e+07
ADC

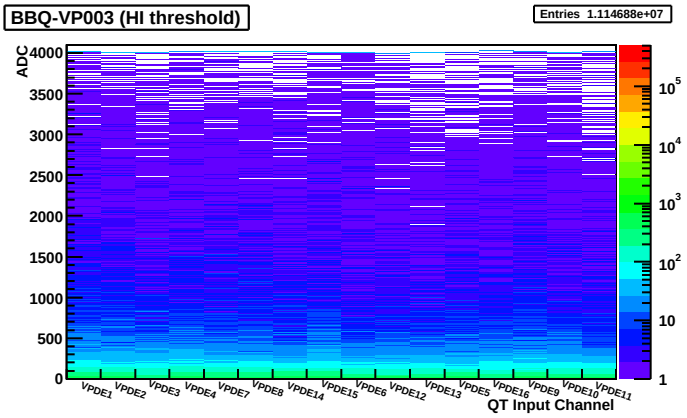

BBQ-ZD001 (ZDC TOWER)
Entries 1.114688e+07
TAC

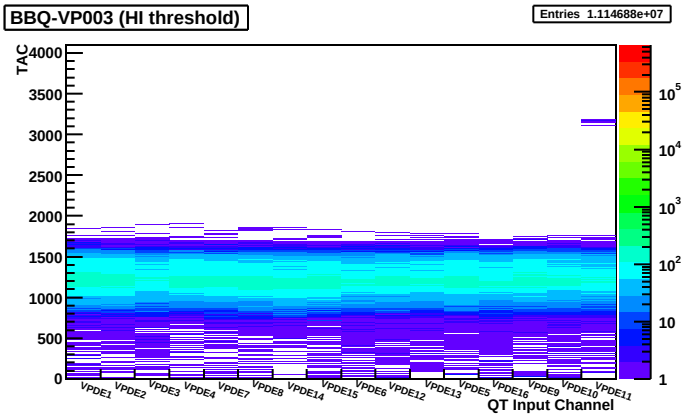

BBQ-VP001 (LO threshold)
Entries 1.114688e+07
ADC
QT Input Channel

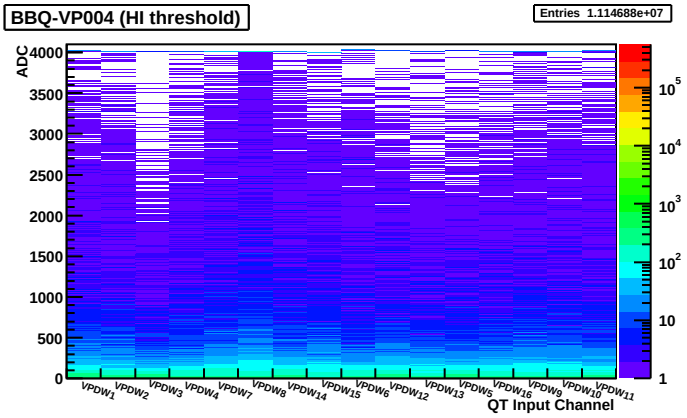

BBQ-VP001 (LO threshold)
Entries 1.114688e+07
TAC
QT Input Channel

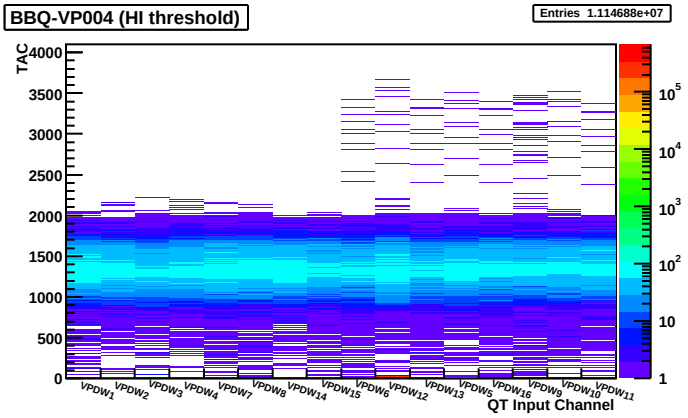

BBQ-VP002 (LO threshold)
Entries 1.114688e+07
ADC
QT Input Channel

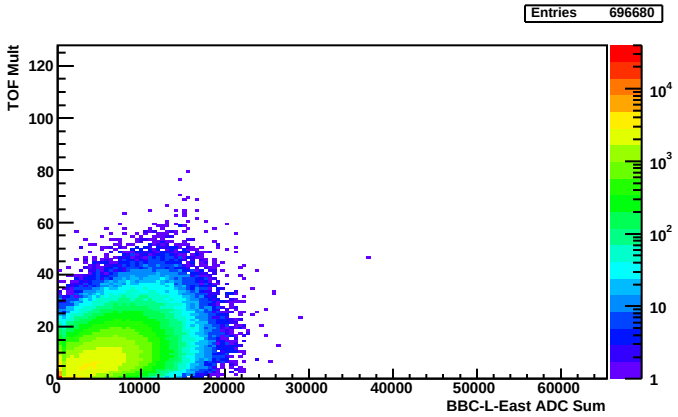

BBQ-VP002 (LO threshold)
Entries 1.114688e+07
TAC
QT Input Channel

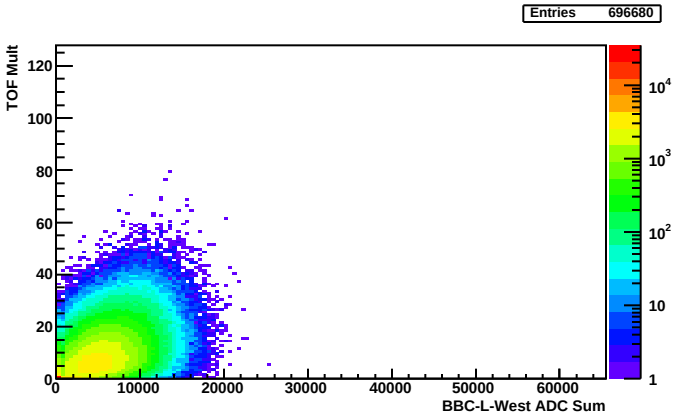

BBQ-VP003 (HI threshold)
Entries 1.114688e+07
ADC
QT Input Channel

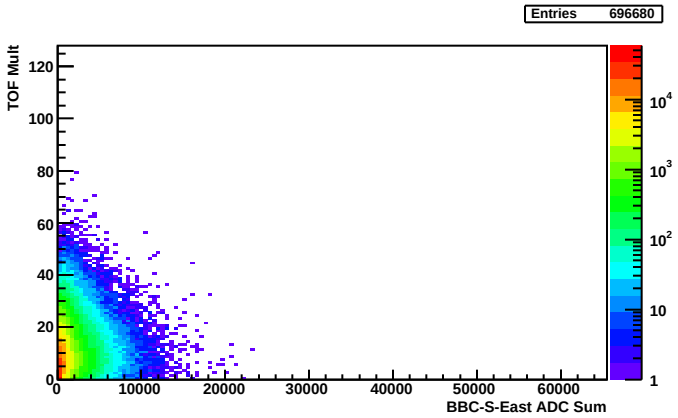

BBQ-VP003 (HI threshold)
Entries 1.114688e+07
TAC
QT Input Channel

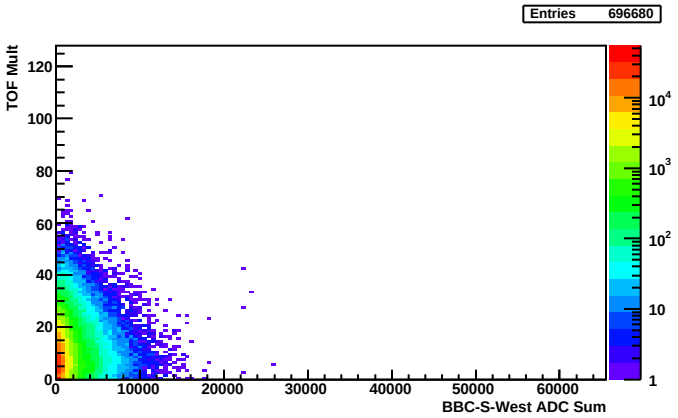

BBQ-VP004 (HI threshold)
Entries 1.114688e+07
ADC
QT Input Channel

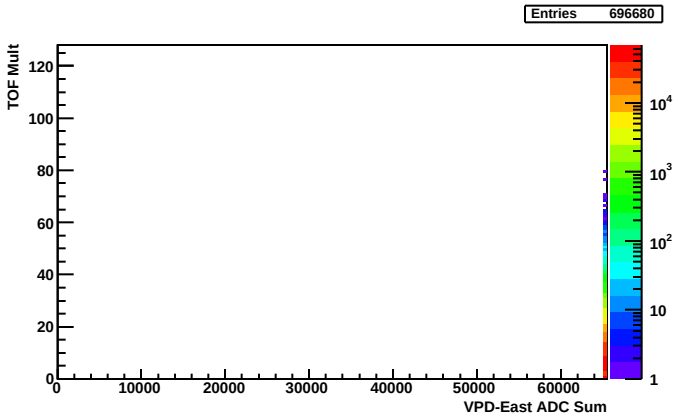

BBQ-VP004 (HI threshold)
Entries 1.114688e+07
TAC
QT Input Channel

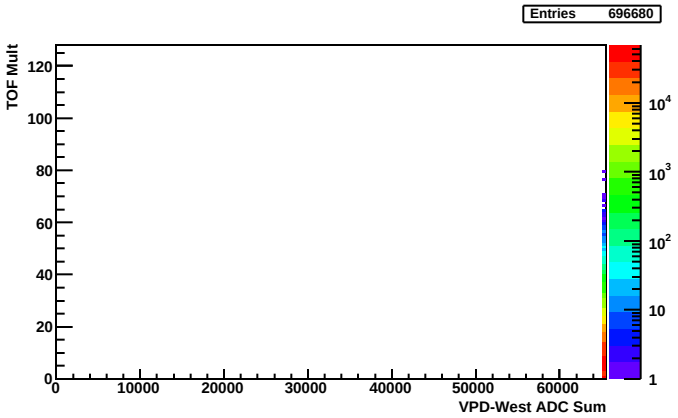

Entries 696680
TOF Mult
BBC-L-East ADC Sum

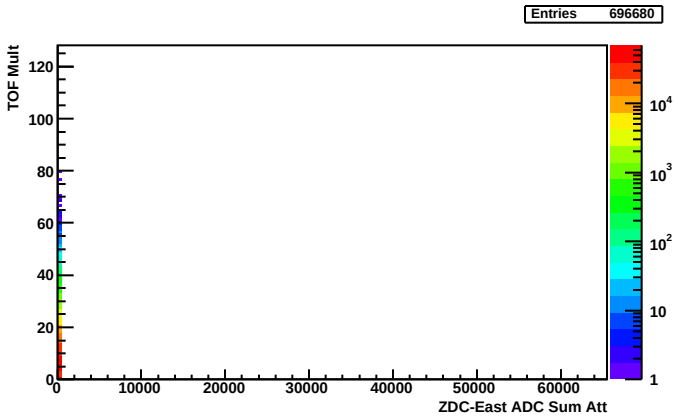

Entries 696680
TOF Mult
BBC-L-West ADC Sum

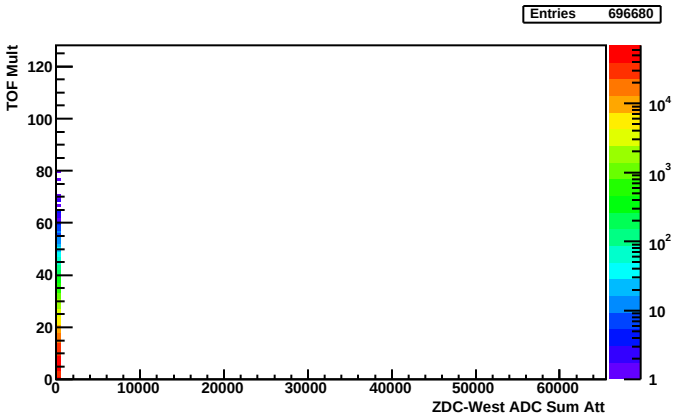

Entries 696680
TOF Mult
BBC-S-East ADC Sum

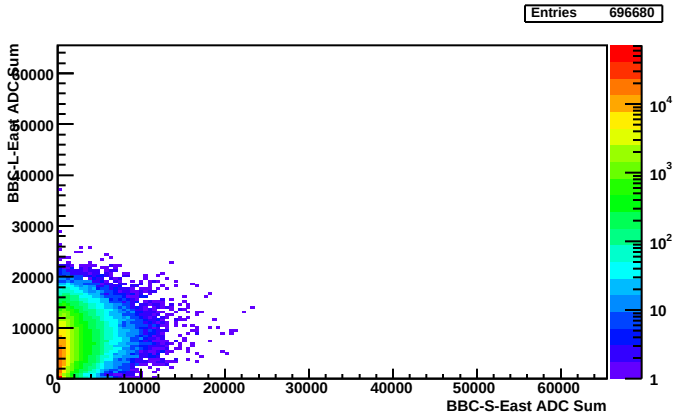

Entries 696680
TOF Mult
BBC-S-West ADC Sum

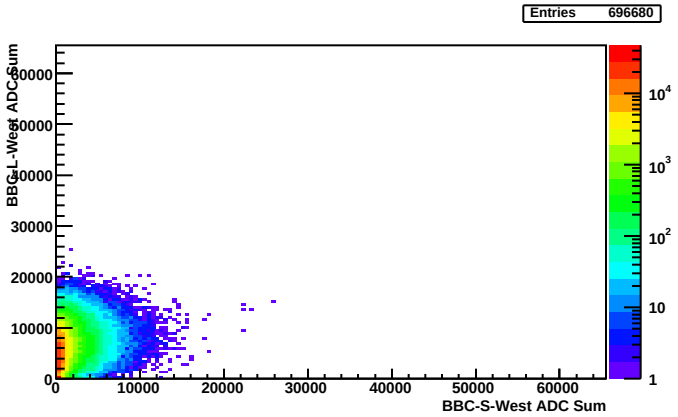

Entries 696680
TOF Mult
VPD-East ADC Sum

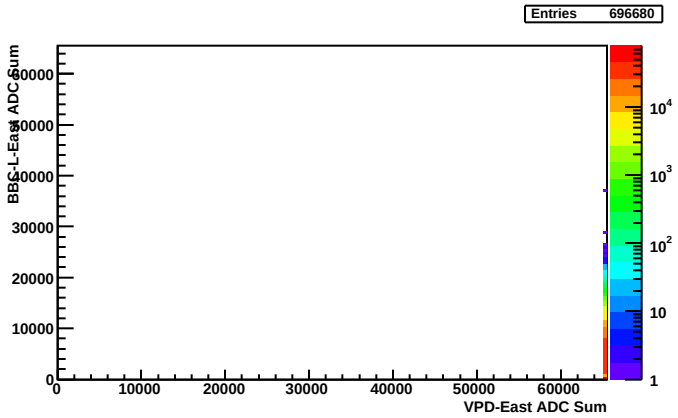

Entries 696680
TOF Mult
VPD-West ADC Sum

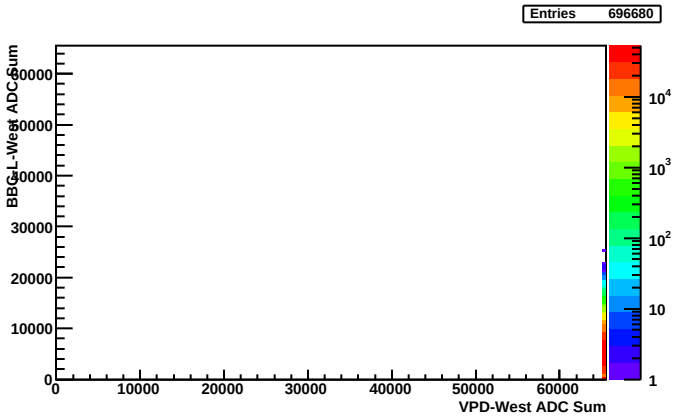

Entries 696680
TOF Mult
ZDC-East ADC Sum Att

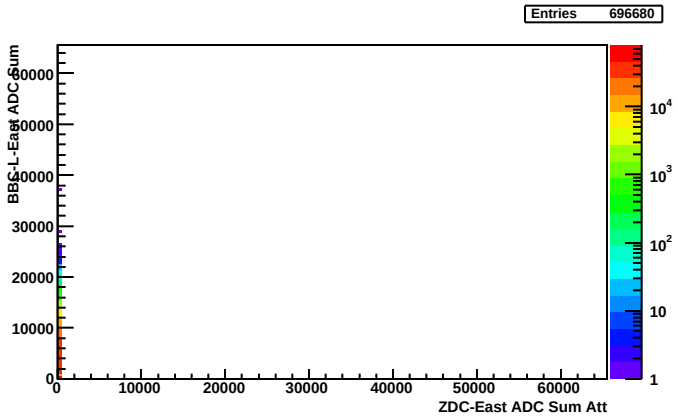

Entries 696680
TOF Mult
ZDC-West ADC Sum Att

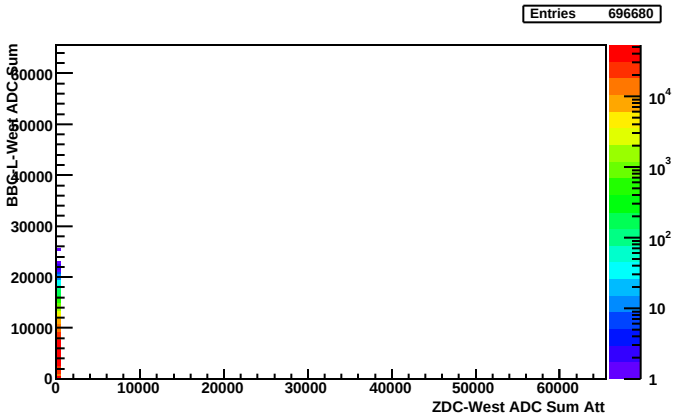

Entries 696680
BBC-L-East ADC Sum
BBC-S-East ADC Sum

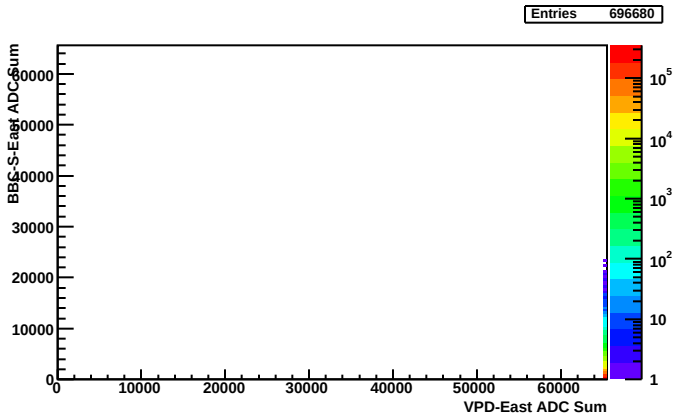

Entries 696680
BBC-L-West ADC Sum
BBC-S-West ADC Sum

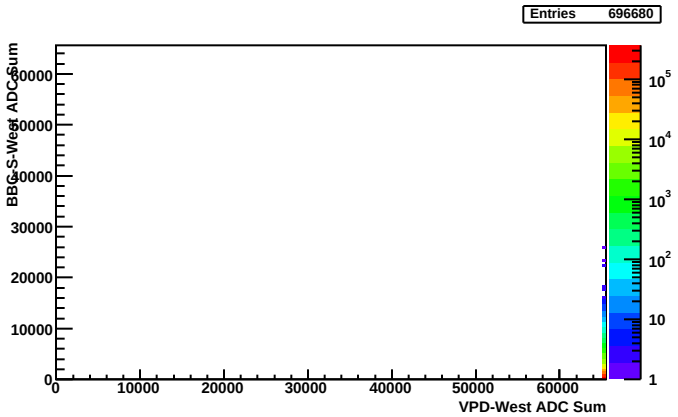

Entries 696680
BBC-L-East ADC Sum
VPD-East ADC Sum

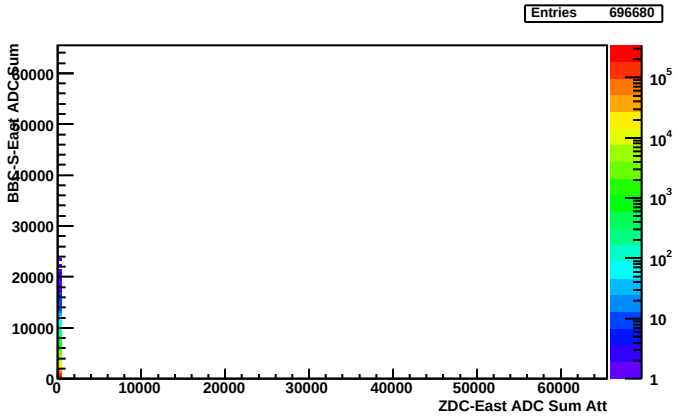

Entries 696680
BBC-L-West ADC Sum
VPD-West ADC Sum

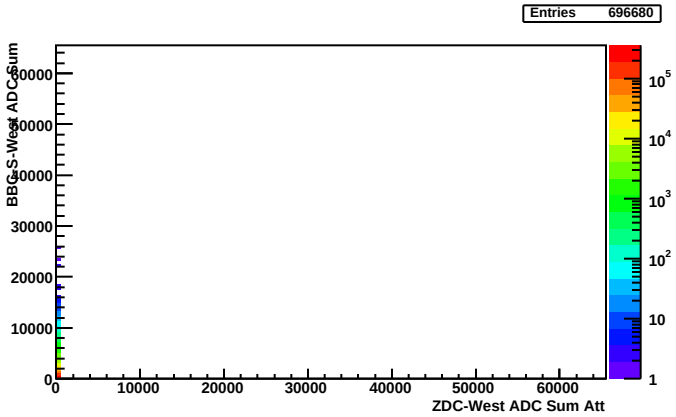

Entries 696680
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

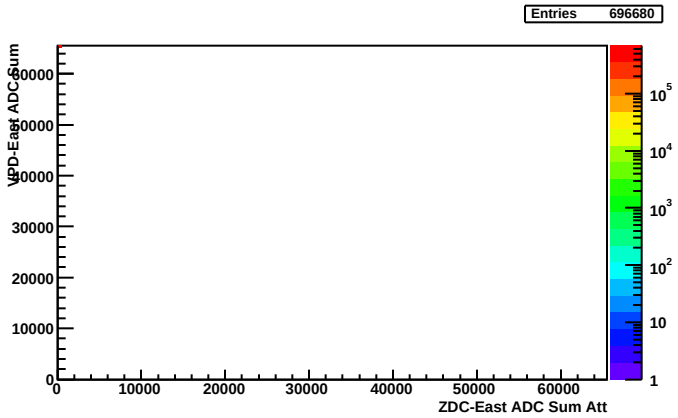

Entries 696680
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

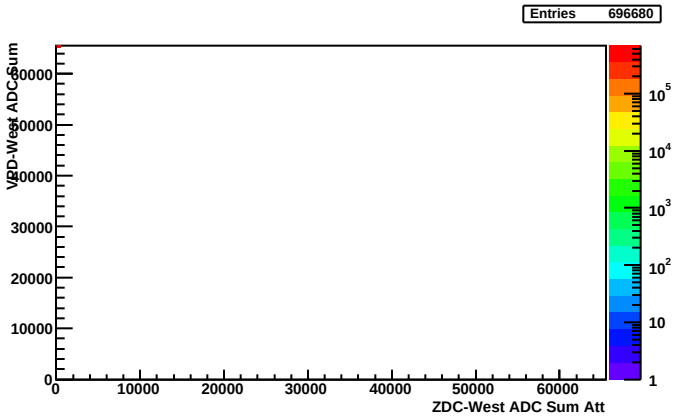

Entries 696680
BBC-S-East ADC Sum
VPD-East ADC Sum

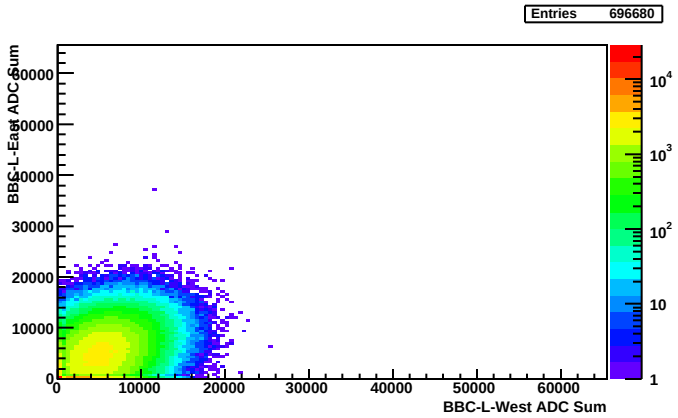

Entries 696680
BBC-S-West ADC Sum
VPD-West ADC Sum

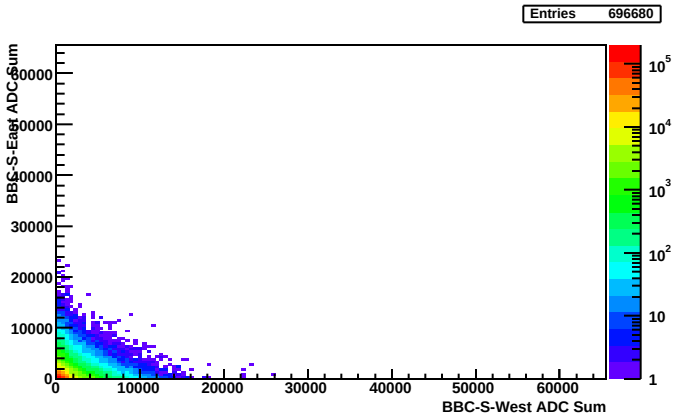

Entries 696680
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

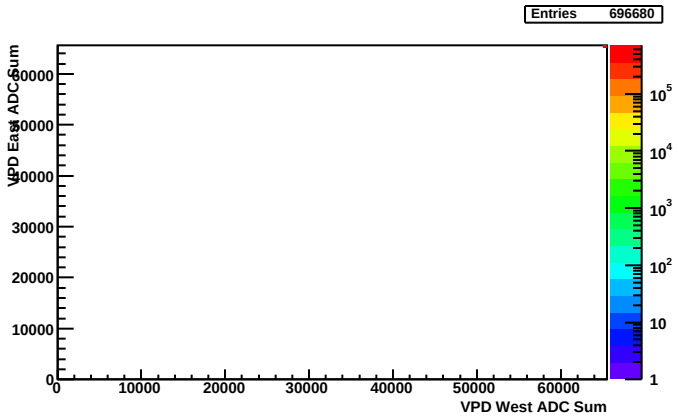

Entries 696680
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

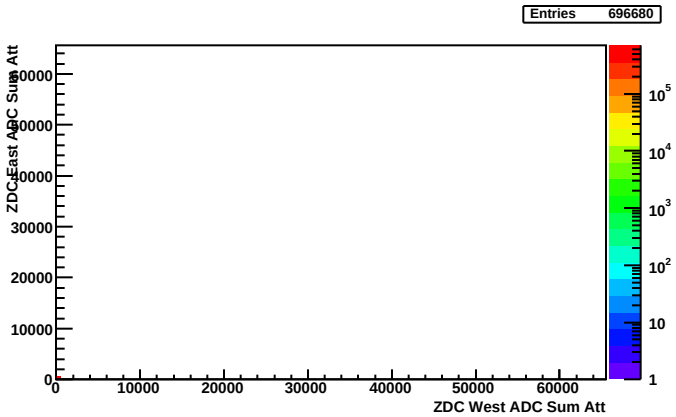

Entries 696680
VPD-East ADC Sum
ZDC-East ADC Sum Att

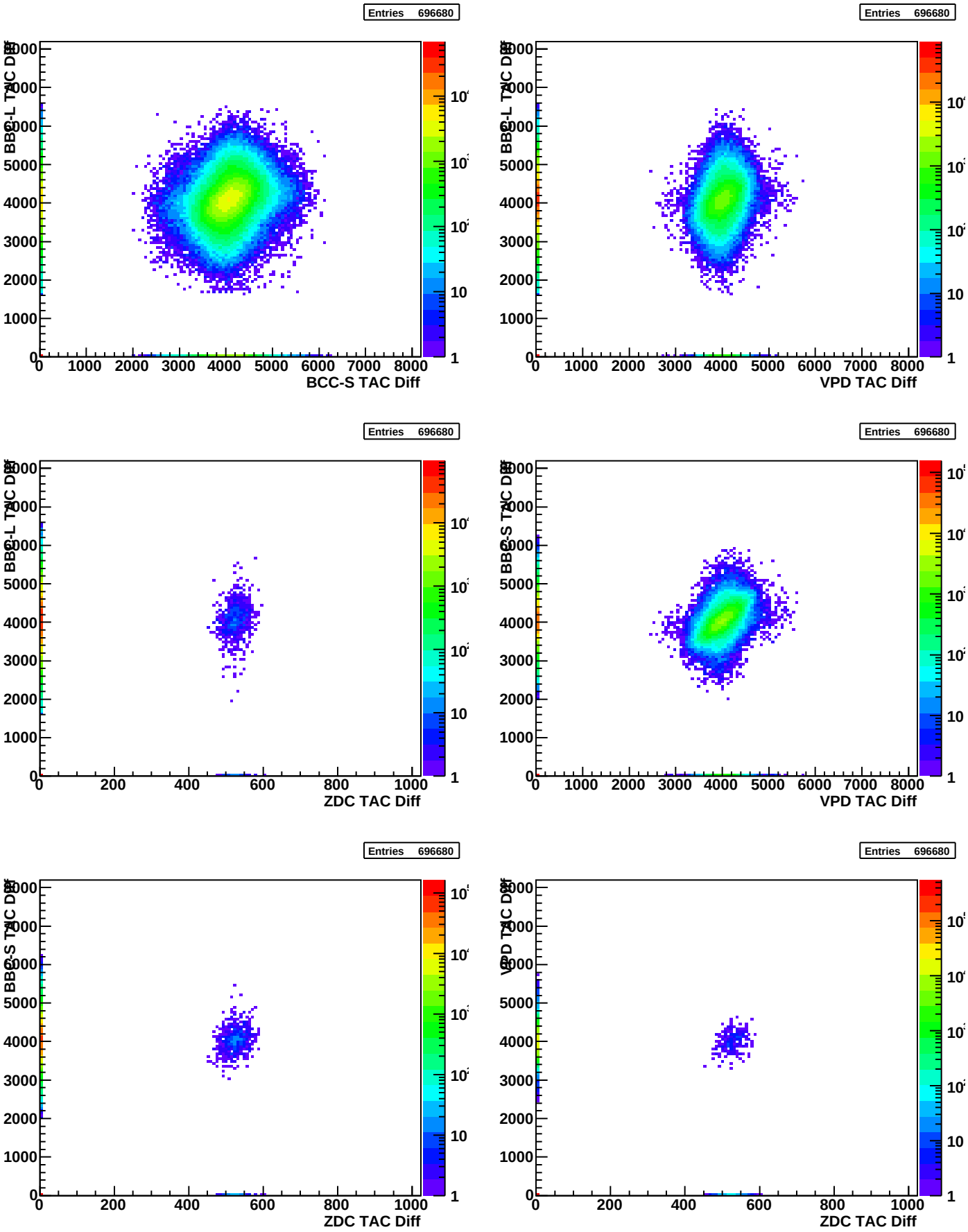

Entries 696680
VPD-West ADC Sum
ZDC-West ADC Sum Att

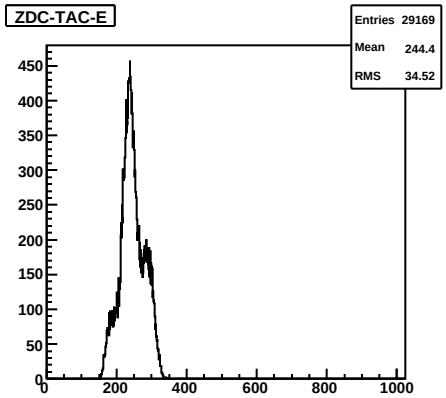

Entries 696680
BBC-L-East ADC Sum
BBC-L-West ADC Sum

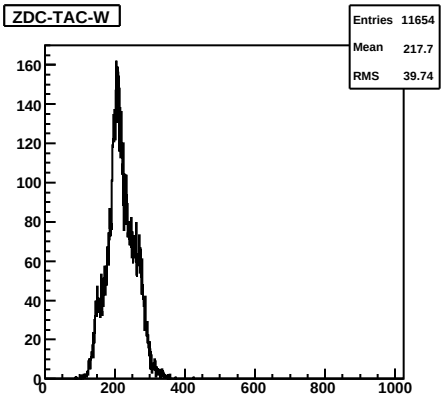

Entries 696680
BBC-S-East ADC Sum
BBC-S-West ADC Sum

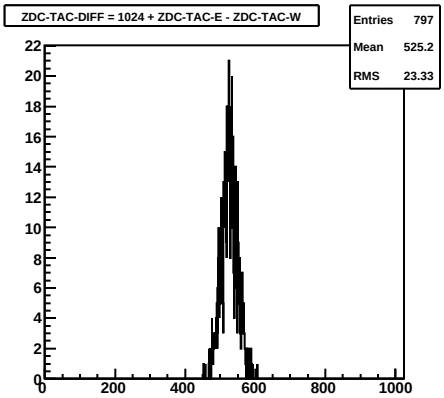

Entries 696680
VPD East ADC Sum
VPD West ADC Sum

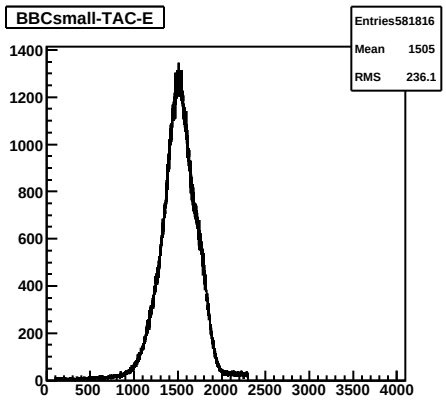

Entries 696680
ZDC East ADC Sum Att
ZDC West ADC Sum Att

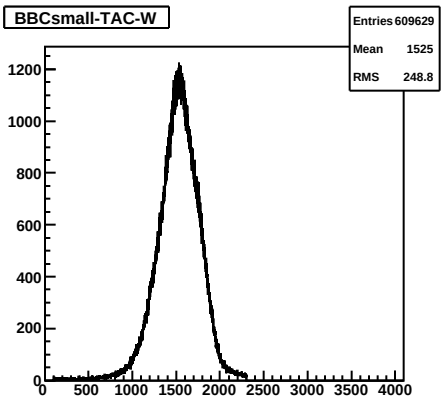

Entries 696680
BBC-L TAC Diff
BCC-S TAC Diff
Entries 696680
BBC-L TAC Diff
VPD TAC Diff
Entries 696680
BBC-L TAC Diff
ZDC TAC Diff
Entries 696680
BBC-S TAC Diff
VPD TAC Diff
Entries 696680
BBC-S TAC Diff
ZDC TAC Diff
Entries 696680
VPD TAC Diff
ZDC TAC Diff

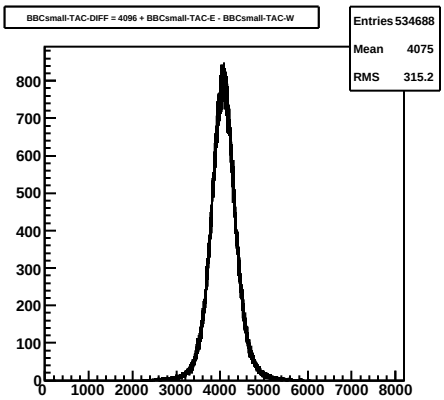

ZDC-TAC-E
Entries 29169
Mean 244.4
RMS 34.52

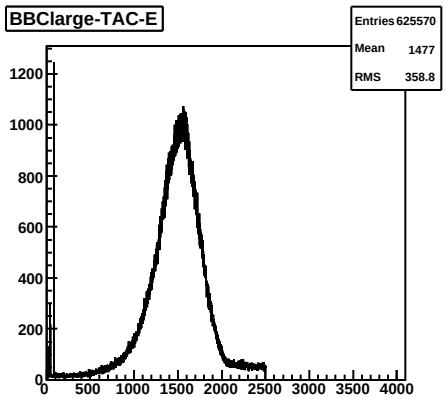

ZDC-TAC-W
Entries 11654
Mean 217.7
RMS 39.74

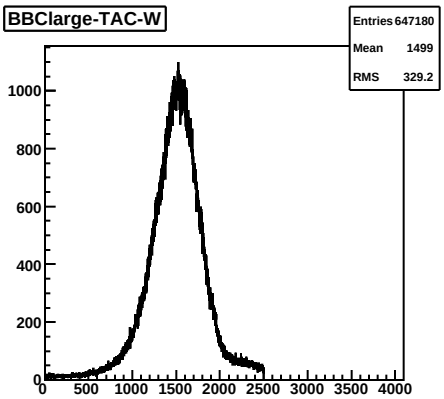

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 797
Mean 525.2
RMS 23.33

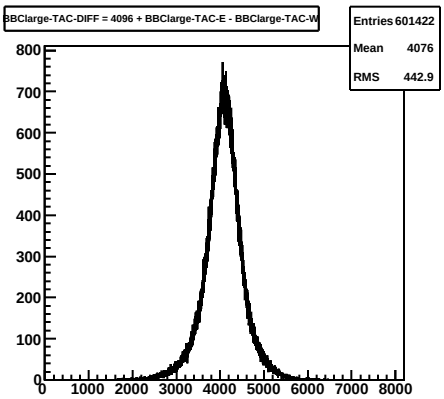

BBCsmall-TAC-E
Entries 581816
Mean 1505
RMS 236.1

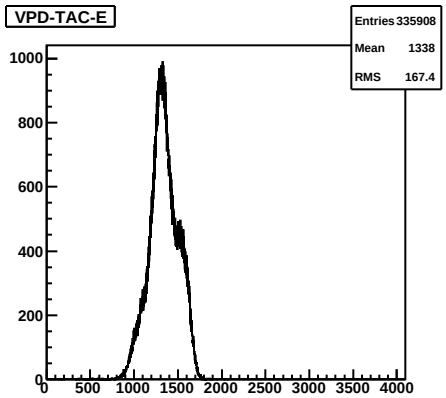

BBCsmall-TAC-W
Entries 609629
Mean 1525
RMS 248.8

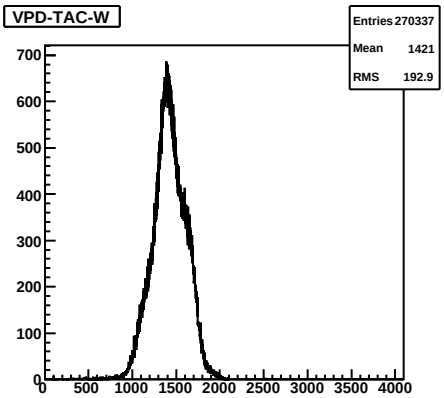

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 534688
Mean 4075
RMS 315.2

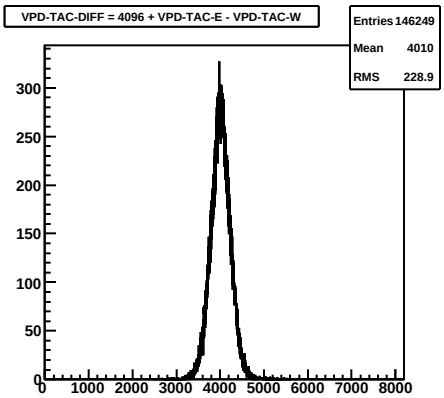

BBClarge-TAC-E
Entries 625570
Mean 1477
RMS 358.8

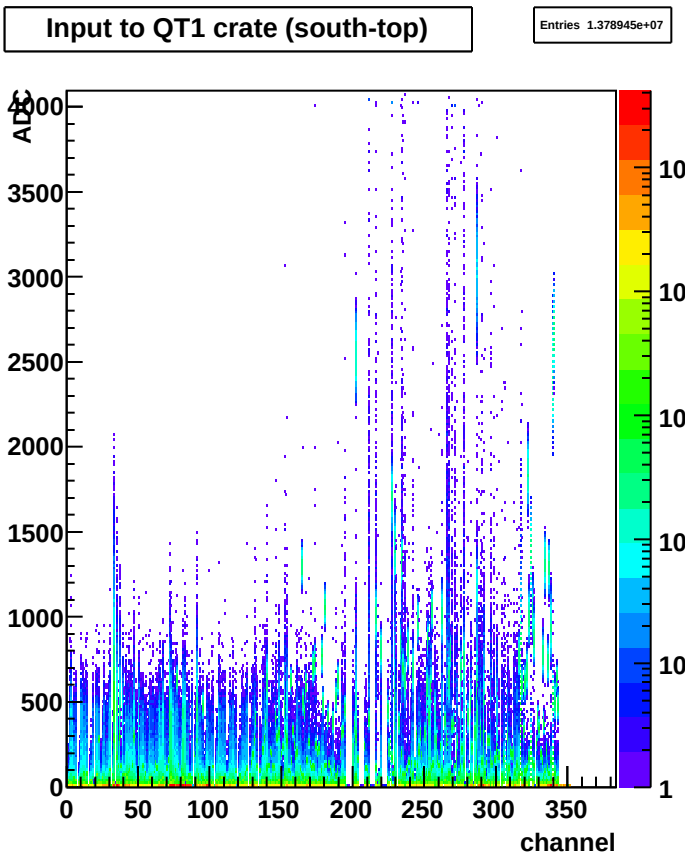

BBClarge-TAC-W
Entries 647180
Mean 1499
RMS 329.2

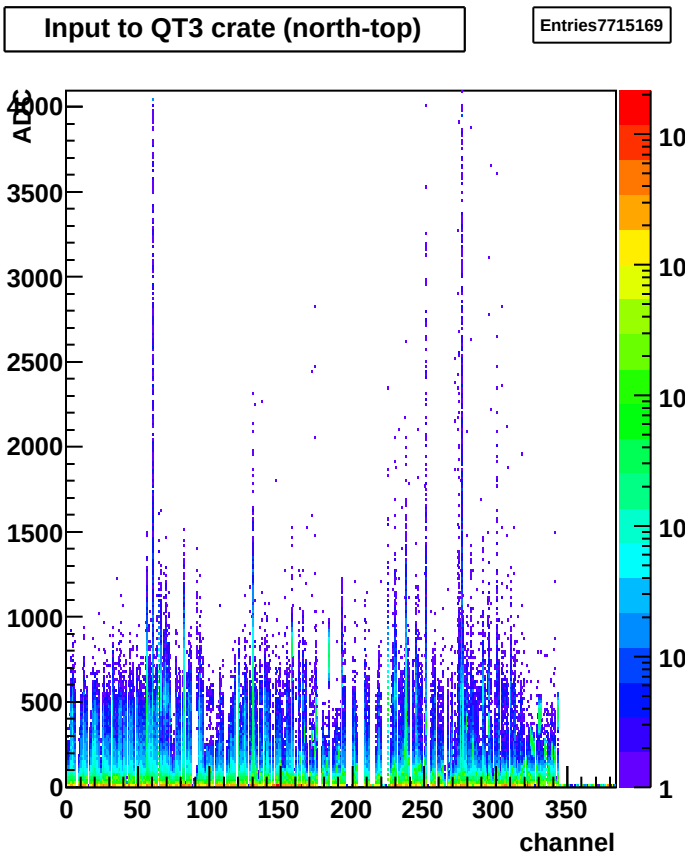

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 601422
Mean 4076
RMS 442.9

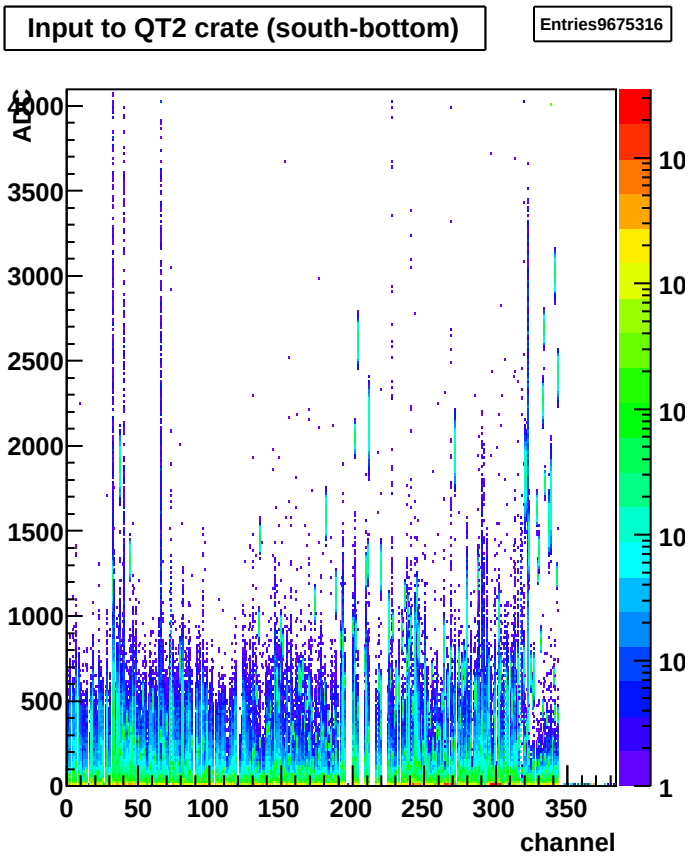

VPD-TAC-E
Entries 335908
Mean 1338
RMS 167.4

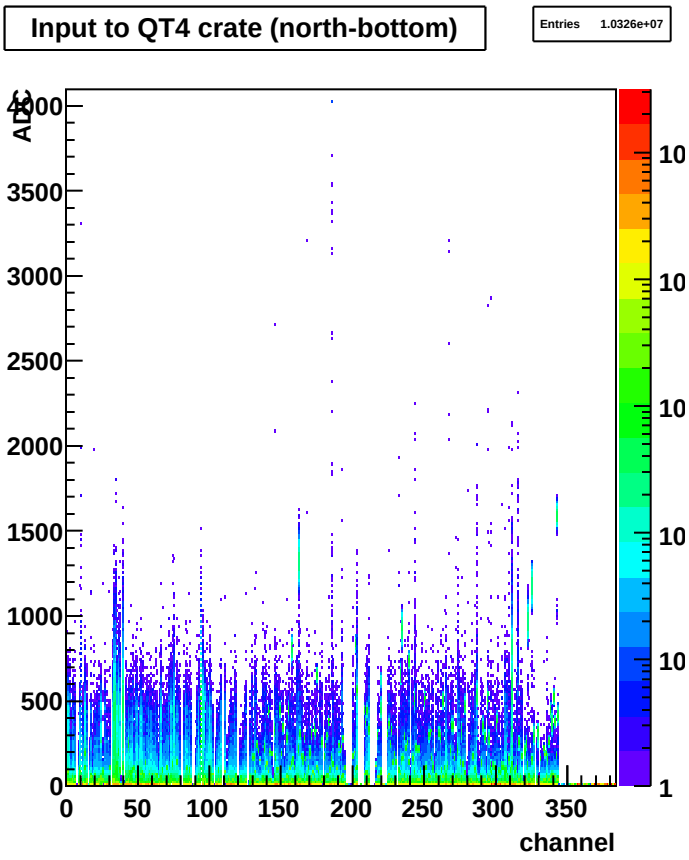

VPD-TAC-W
Entries 270337
Mean 1421
RMS 192.9

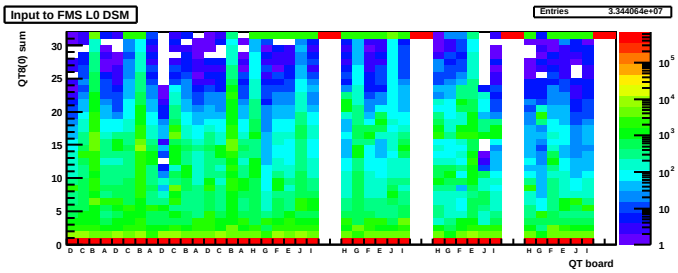

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 146249
Mean 4010
RMS 228.9

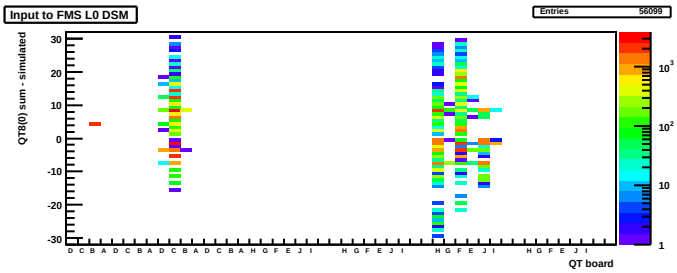

Input to QT1 crate (south-top)
Entries 1.378945e+07
ADC
channel

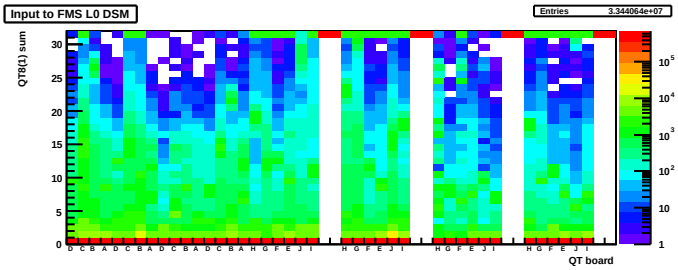

Input to QT3 crate (north-top)
Entries7715169
ADC
channel

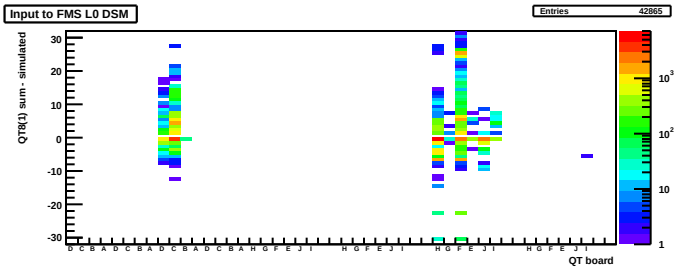

Input to QT2 crate (south-bottom)
Entries9675316
ADC
channel

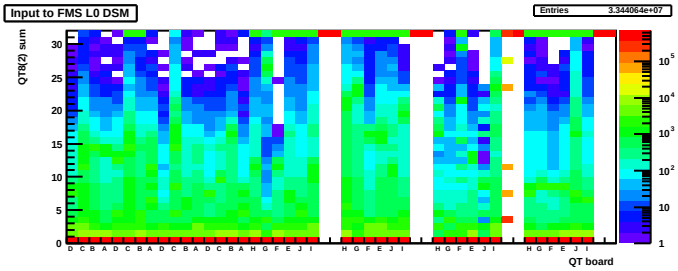

Input to QT4 crate (north-bottom)
Entries 1.0326e+07
ADC
channel

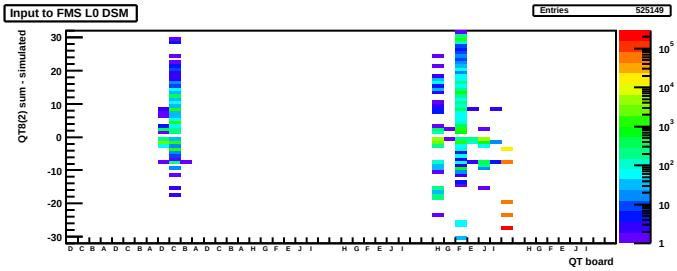

Input to FMS L0 DSM
Entries 3.344064e+07
QT8(0) sum
QT board

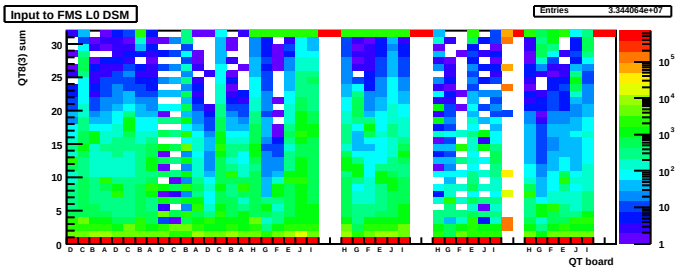

Input to FMS L0 DSM
Entries 56099
QT8(0) sum - simulated
QT board

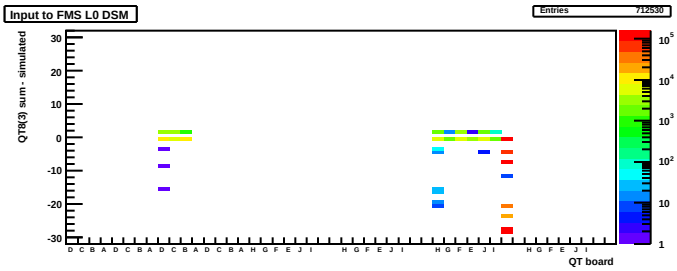

Input to FMS L0 DSM
Entries 3.344064e+07
QT8(1) sum
QT board

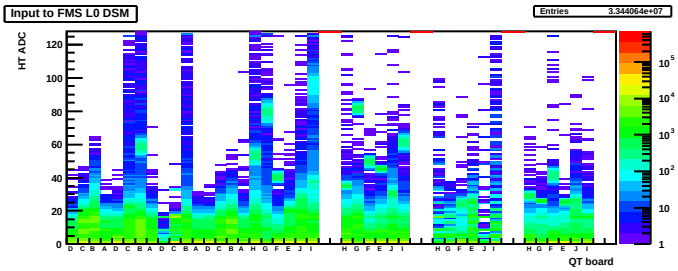

Input to FMS L0 DSM
Entries 42865
QT8(1) sum - simulated
QT board

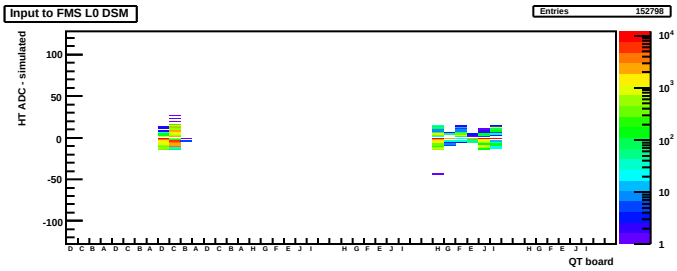

Input to FMS L0 DSM
Entries 3.344064e+07
QT8(2) sum
QT board

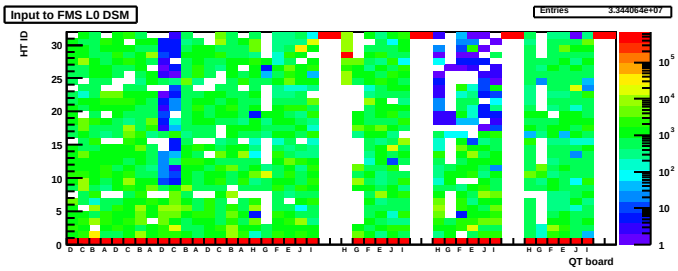

Input to FMS L0 DSM
Entries 525149
QT8(2) sum - simulated
QT board

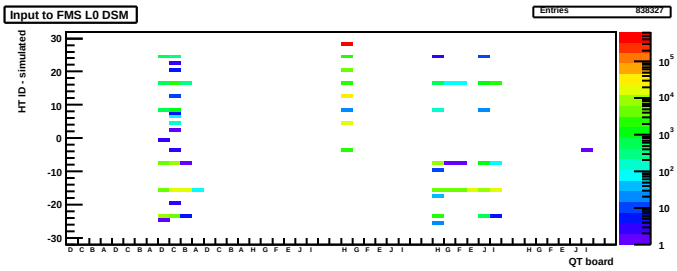

Input to FMS L0 DSM
Entries 3.344064e+07
QT8(3) sum
QT board

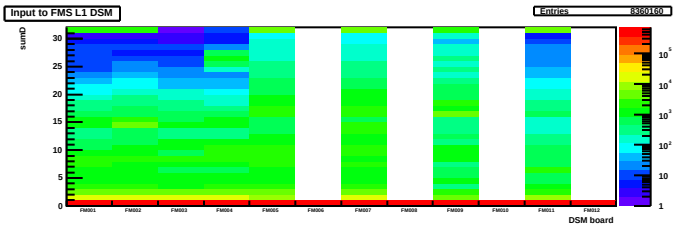

Input to FMS L0 DSM
Entries 712530
QT8(3) sum - simulated
QT board

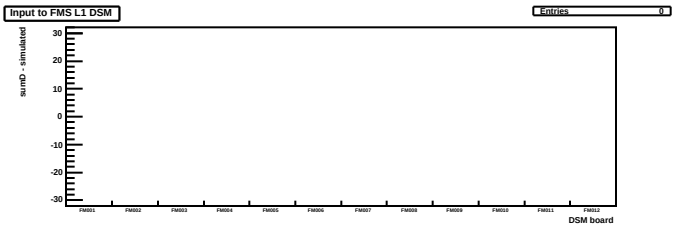

Input to FMS L0 DSM
Entries 3.344064e+07
HT ADC
QT board

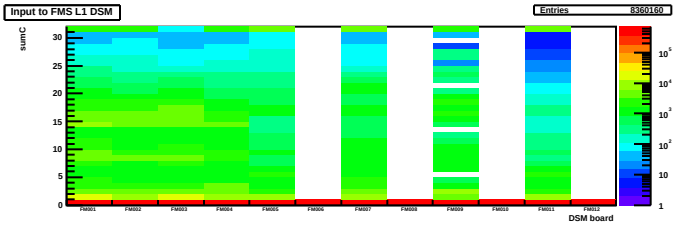

Input to FMS L0 DSM
Entries 152798
HT ADC - simulated
QT board

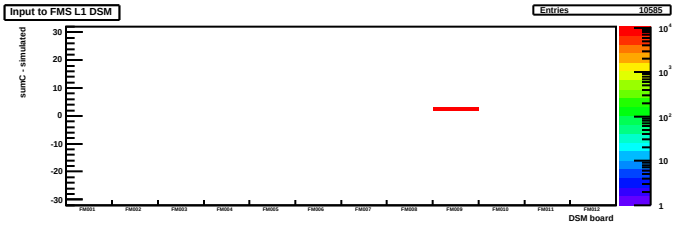

Input to FMS L0 DSM
Entries 3.344064e+07
HT ID
QT board

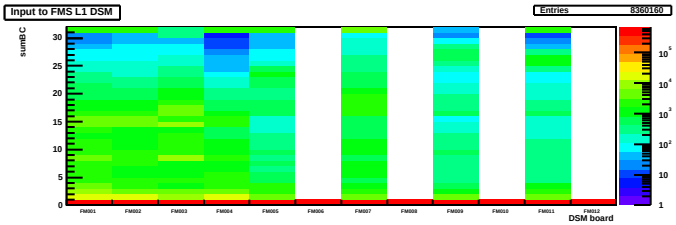

Input to FMS L0 DSM
Entries 838327
HT ID - simulated
QT board

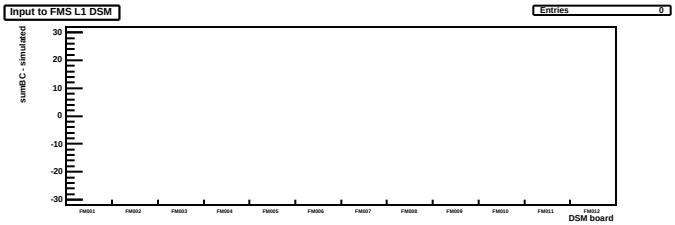

Input to FMS L1 DSM
Entries 8360160
sumD
DSM board

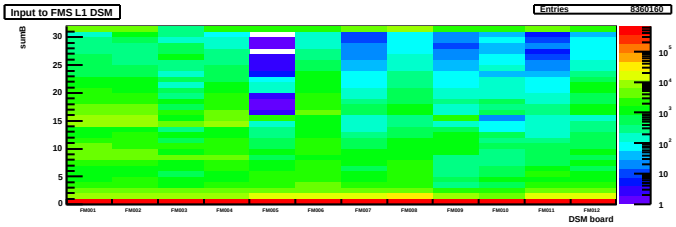

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

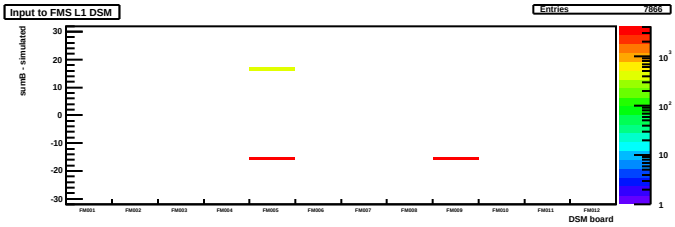

Input to FMS L1 DSM
Entries 8360160
sumC
DSM board

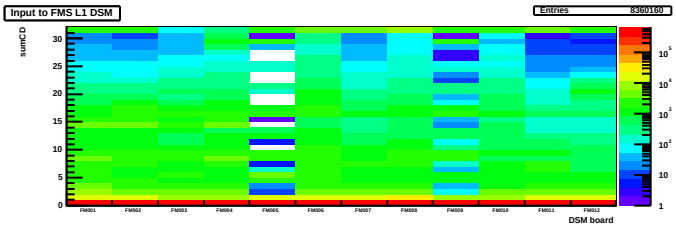

Input to FMS L1 DSM
Entries 10585
sumC - simulated
DSM board

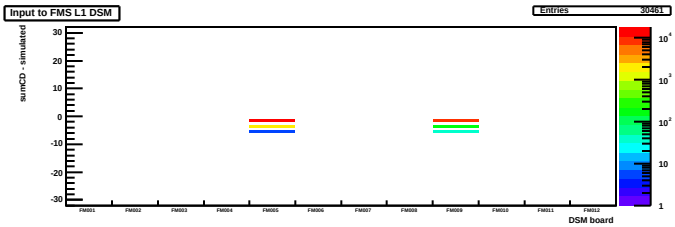

Input to FMS L1 DSM
Entries 8360160
sumBC
DSM board

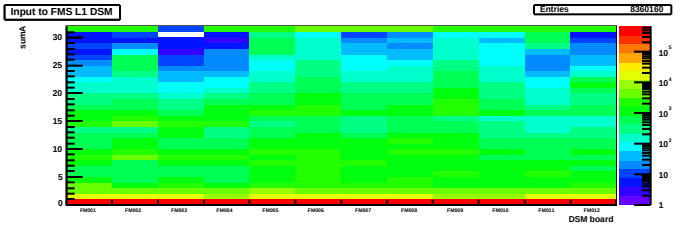

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

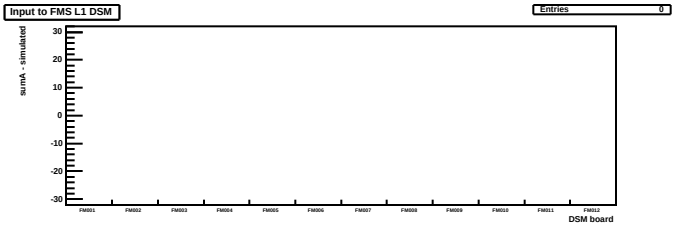

Input to FMS L1 DSM
Entries 8360160
sumB
DSM board

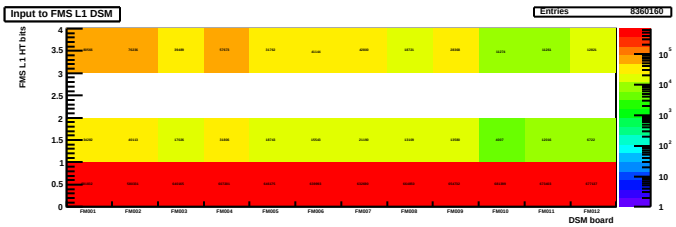

Input to FMS L1 DSM
Entries 7866
sumB - simulated
DSM board

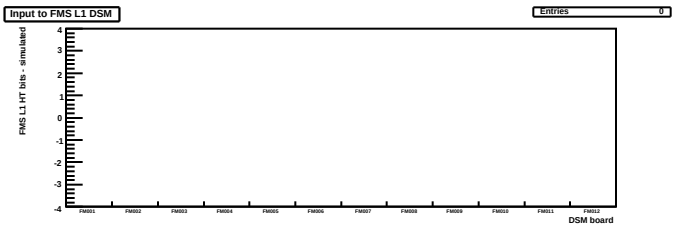

Input to FMS L1 DSM
Entries 8360160
sumCD
DSM board

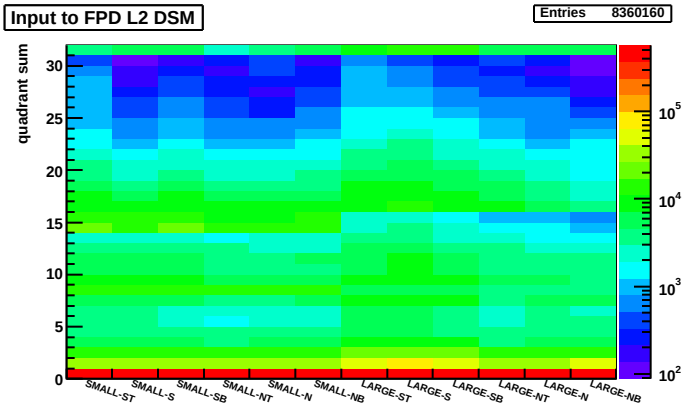

Input to FMS L1 DSM
Entries 30461
sumCD - simulated
DSM board

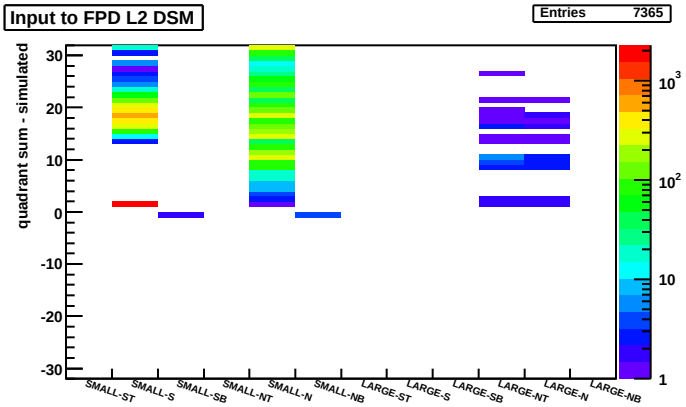

Input to FMS L1 DSM
Entries 8360160
sumA
DSM board

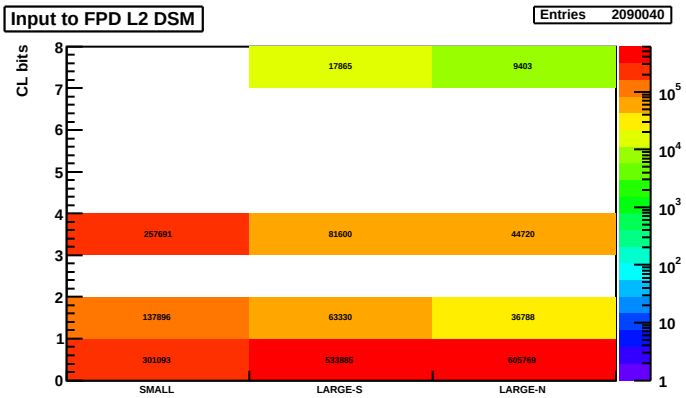

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

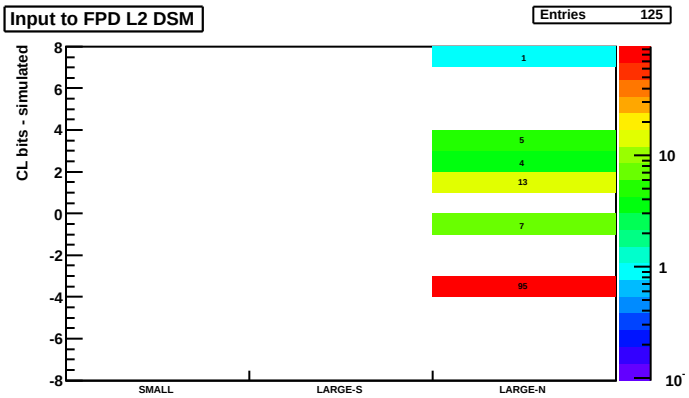

Input to FMS L1 DSM
Entries 8360160
FMS L1 HT bits
DSM board

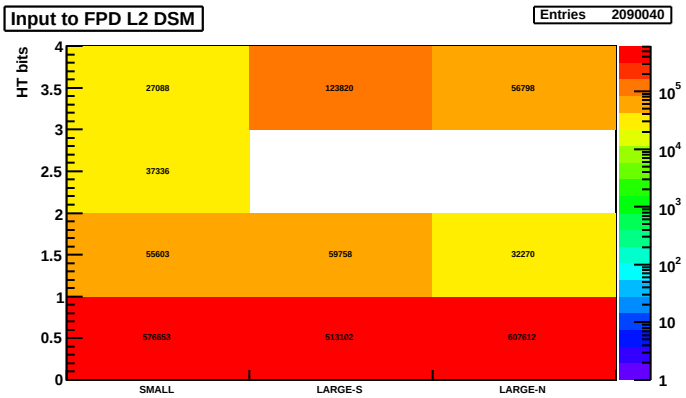

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

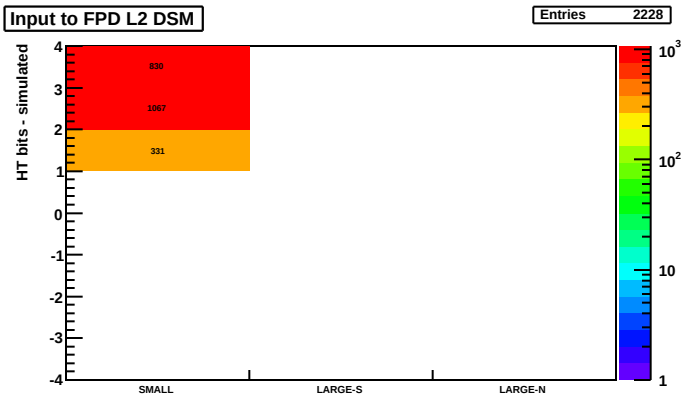

Input to FPD L2 DSM
Entries 8360160
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

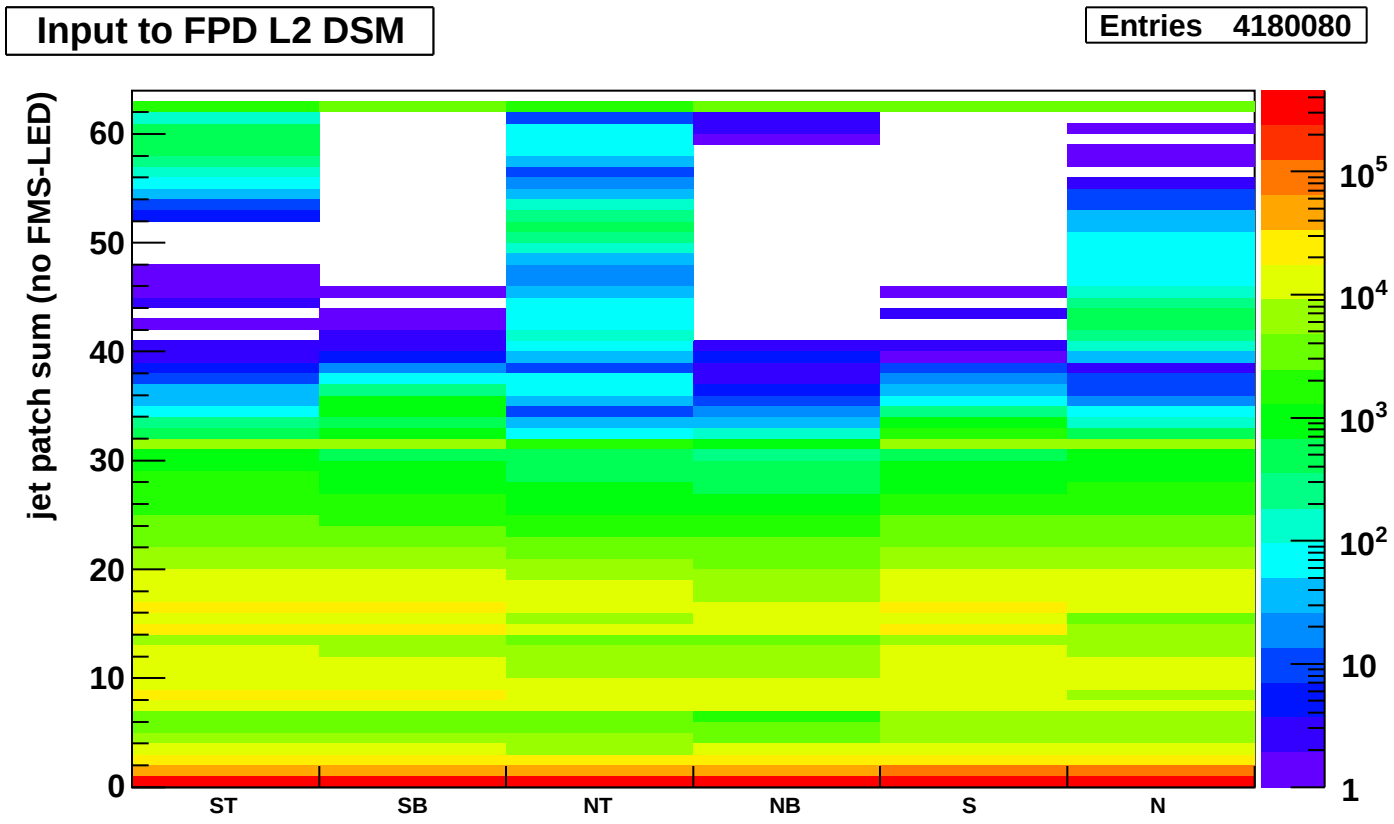

Input to FPD L2 DSM
Entries 7365
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

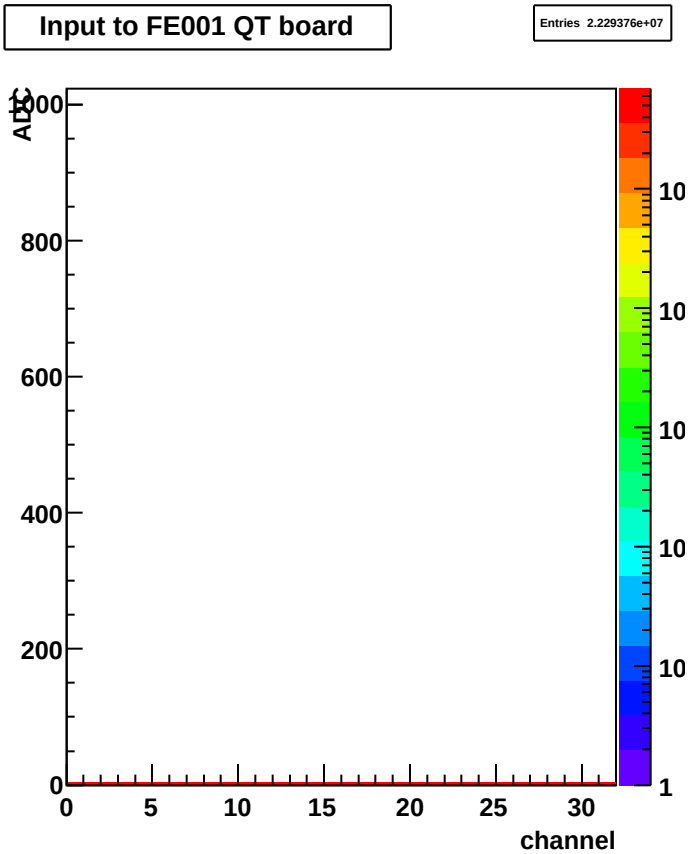

Input to FPD L2 DSM
Entries 2090040
CL bits
17865
9403
257691
81600
44720
137896
63330
36788
301093
533885
605769
SMALL
LARGE-S
LARGE-N

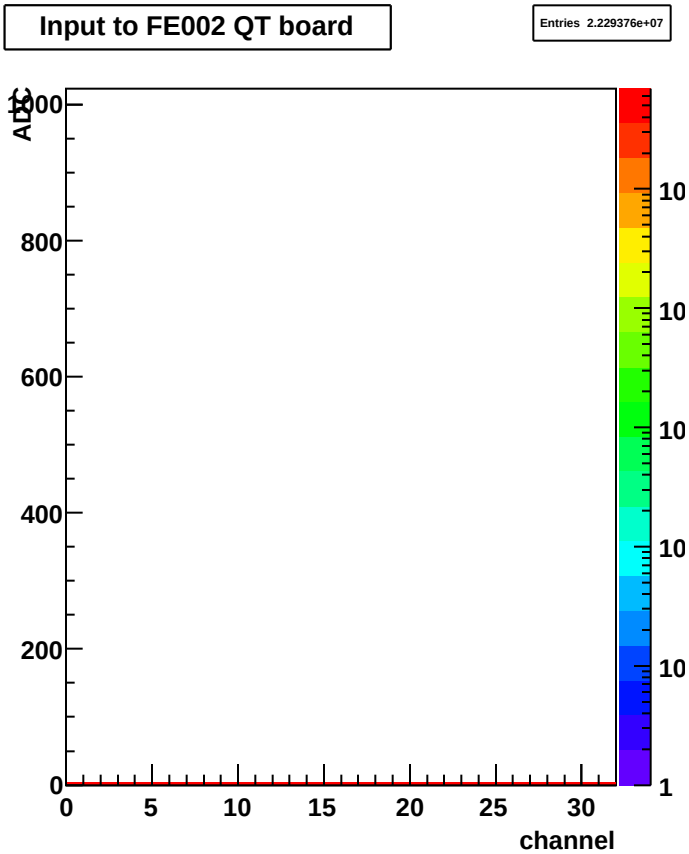

Input to FPD L2 DSM
Entries 125
CL bits - simulated
1
5
4
13
7
95
SMALL
LARGE-S
LARGE-N

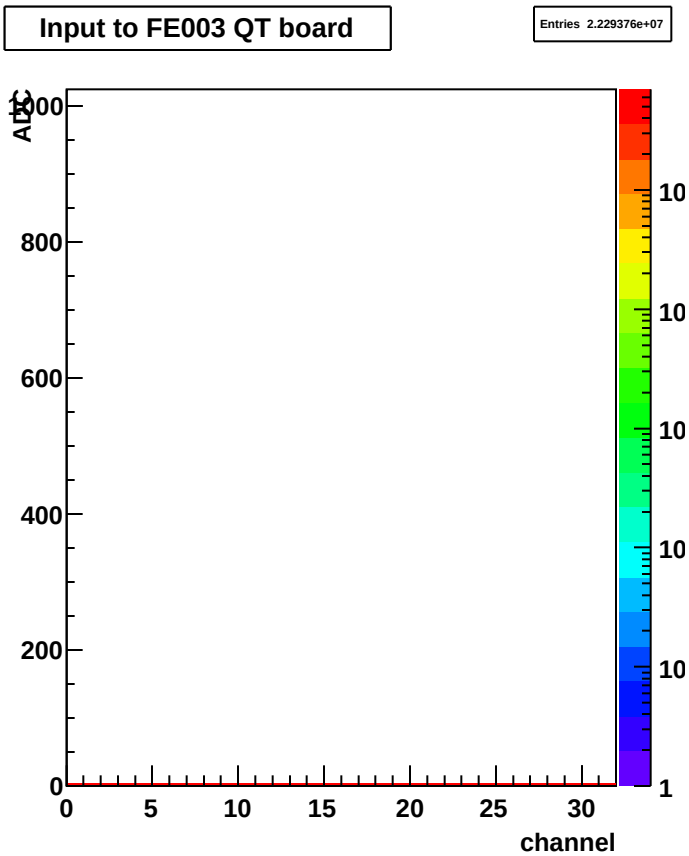

Input to FPD L2 DSM
Entries 2090040
HT bits
27088
123820
56798
37336
55603
59758
32270
576653
513102
607612
SMALL
LARGE-S
LARGE-N

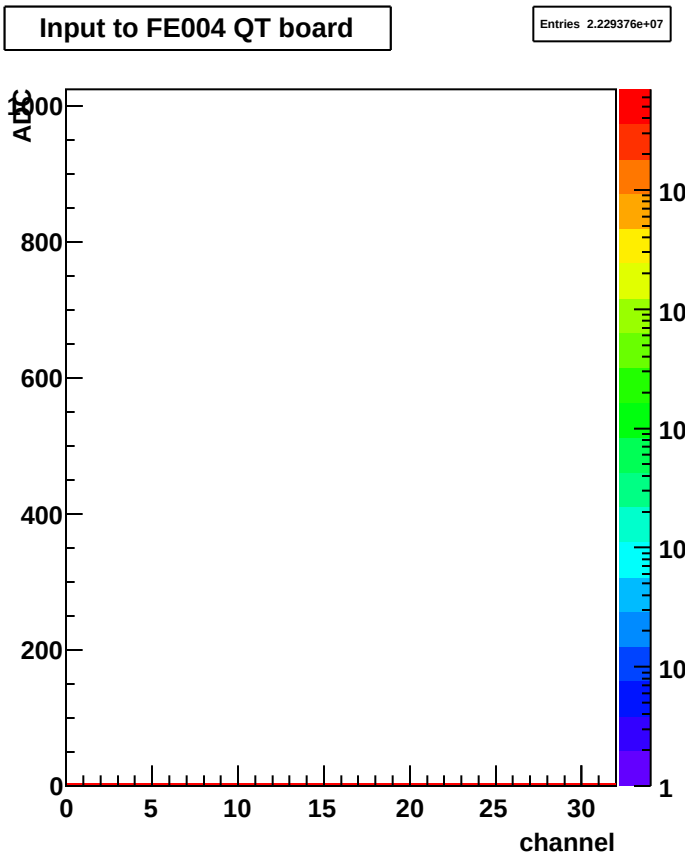

Input to FPD L2 DSM
Entries 2228
HT bits - simulated
830
1067
331
SMALL
LARGE-S
LARGE-N

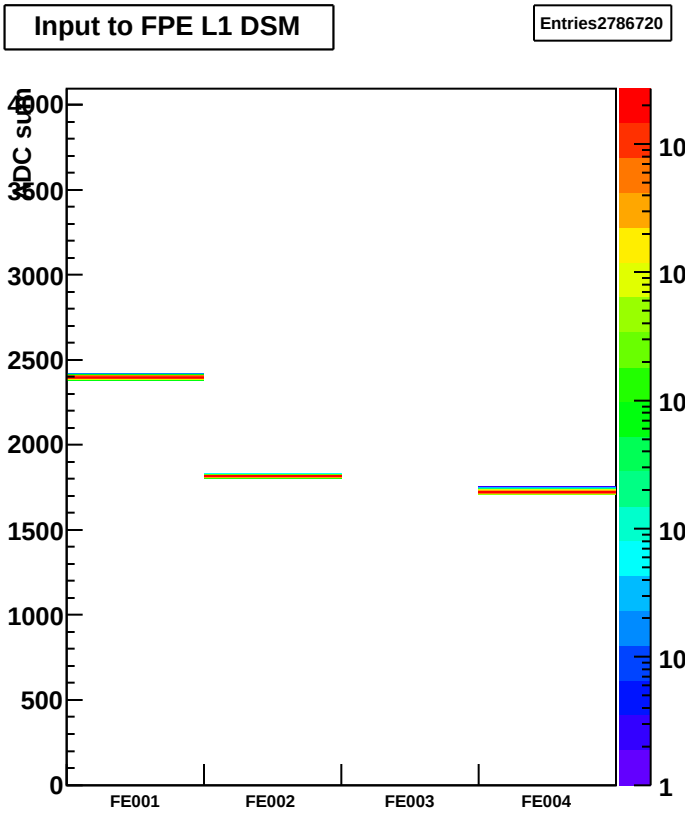

Input to FPD L2 DSM
Entries 4180080
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

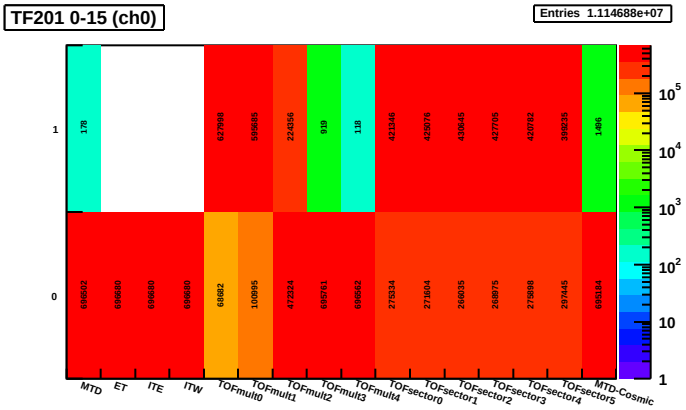

Input to FE001 QT board
Entries 2.229376e+07
ADC
channel

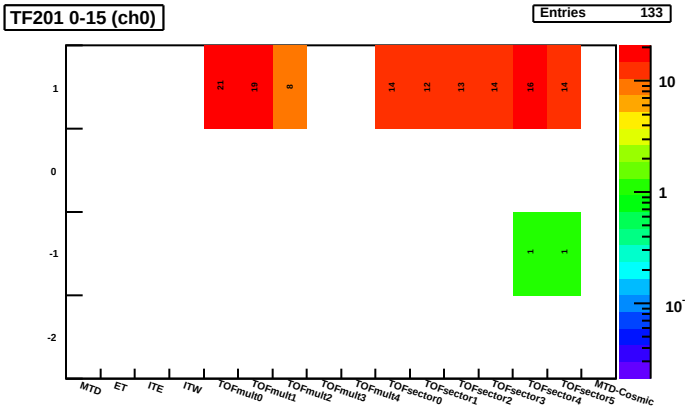

Input to FE002 QT board
Entries 2.229376e+07
ADC
channel

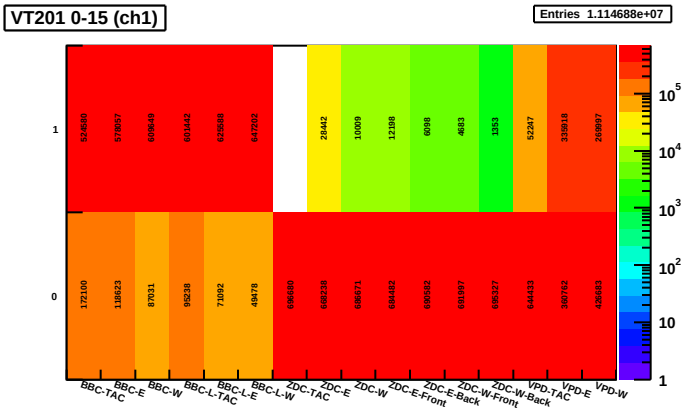

Input to FE003 QT board
Entries 2.229376e+07
ADC
channel


Input to FE004 QT board
Entries 2.229376e+07
ADC
channel


Input to FPE L1 DSM
Entries2786720
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.114688e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 133
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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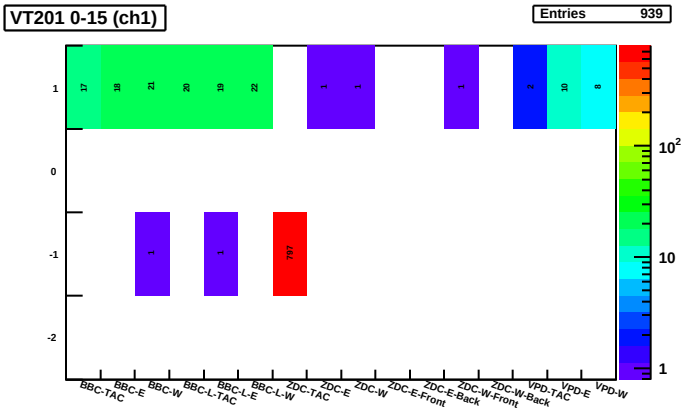
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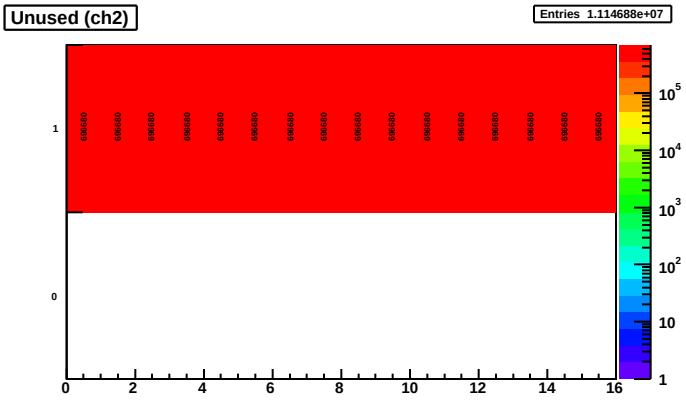
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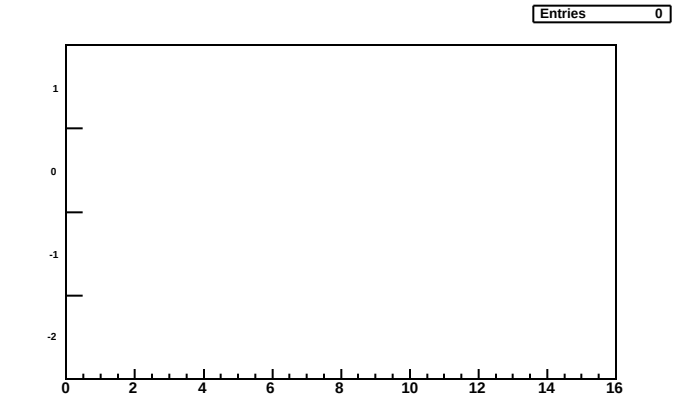
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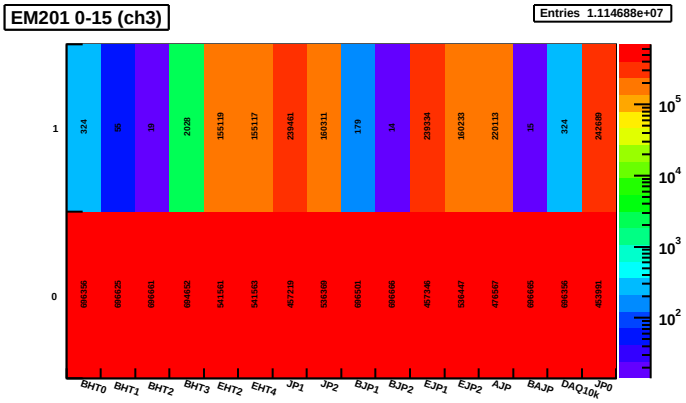
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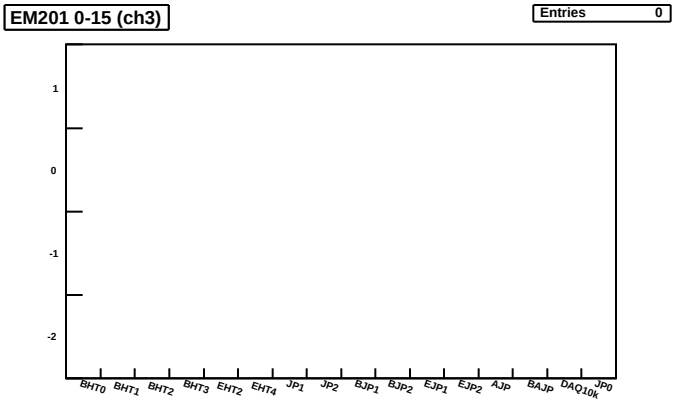
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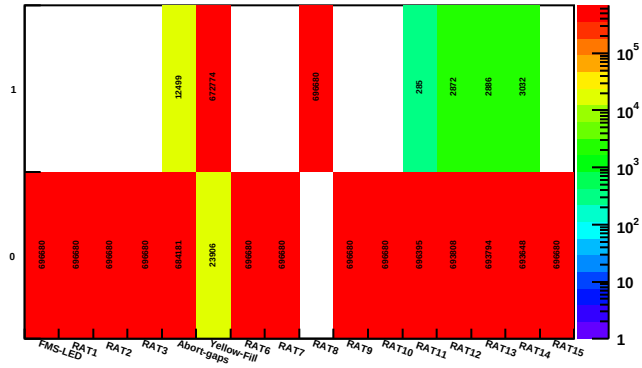
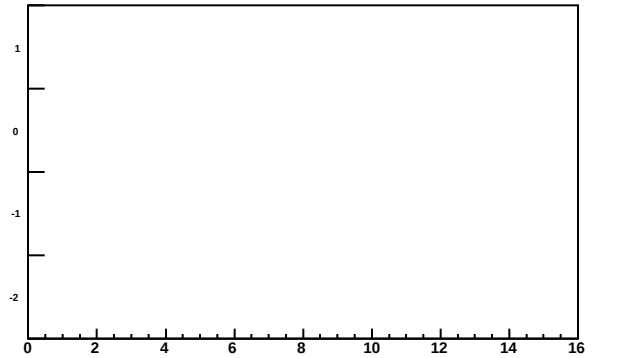
RAT board (ch4)





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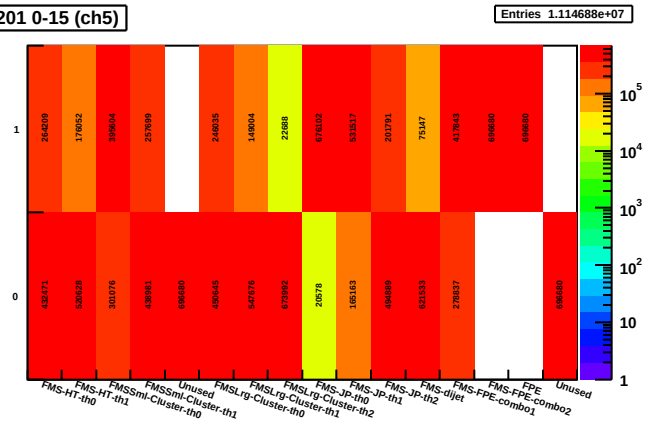

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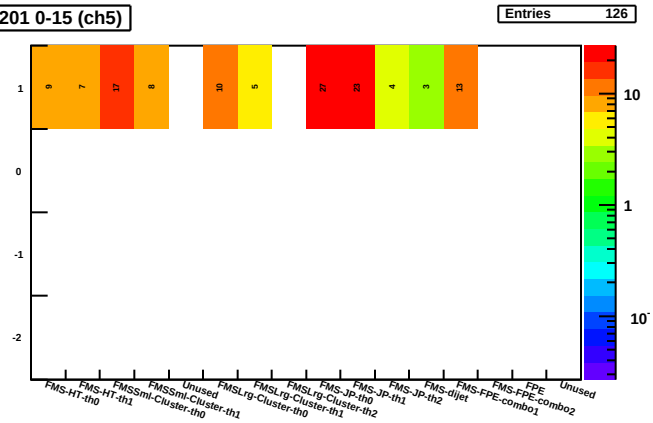
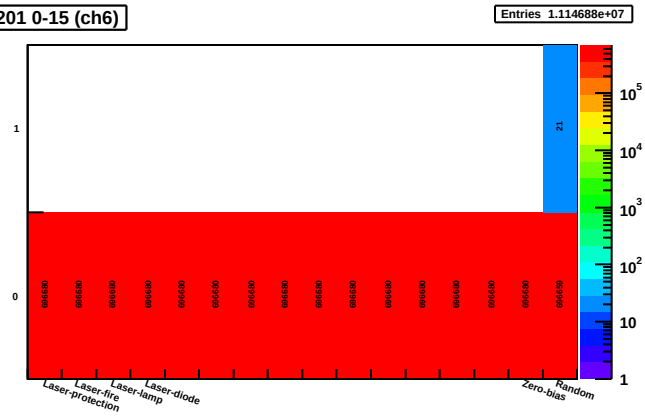
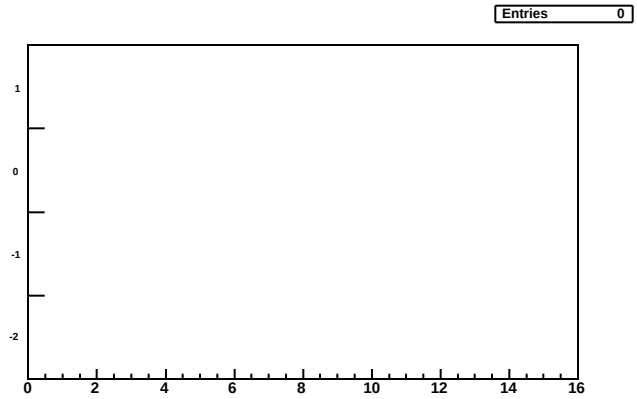
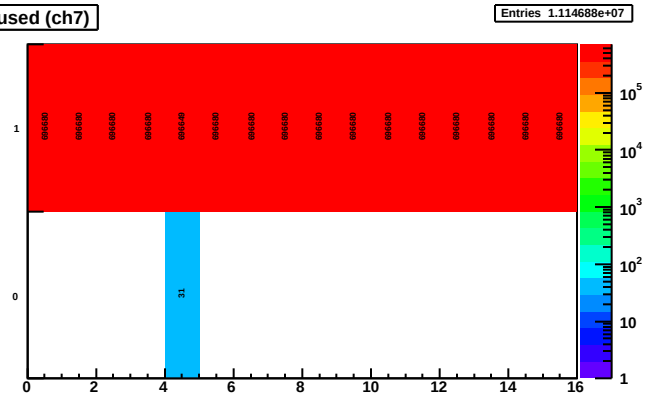
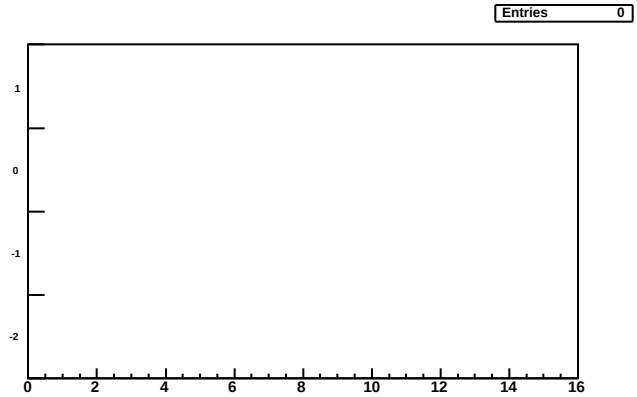


ST201 0-15 (ch6)





Unused (ch7)