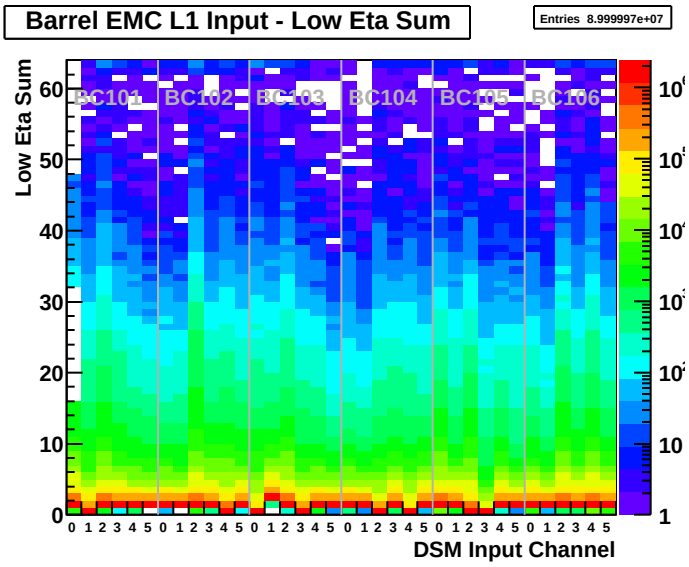
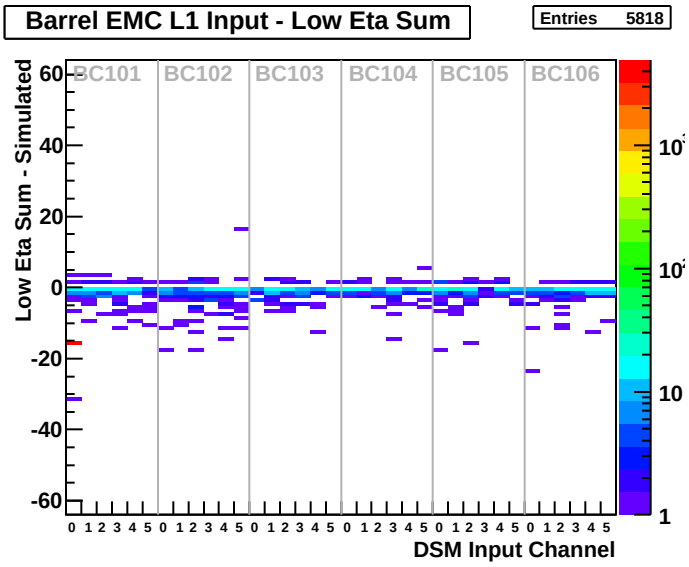
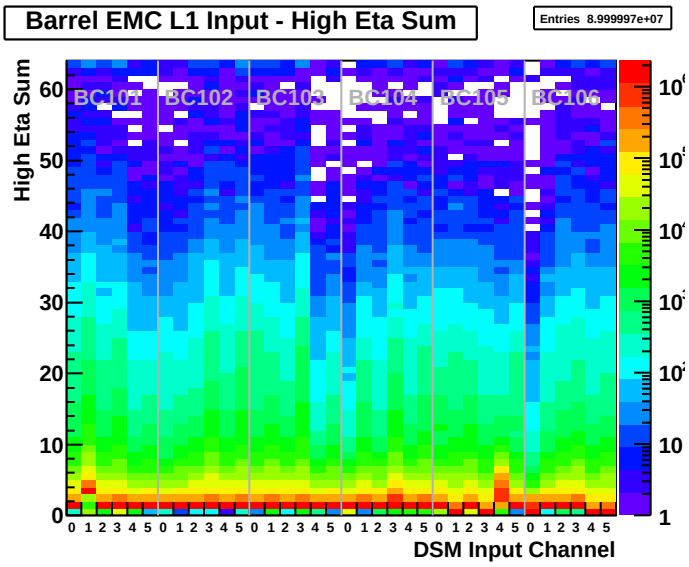
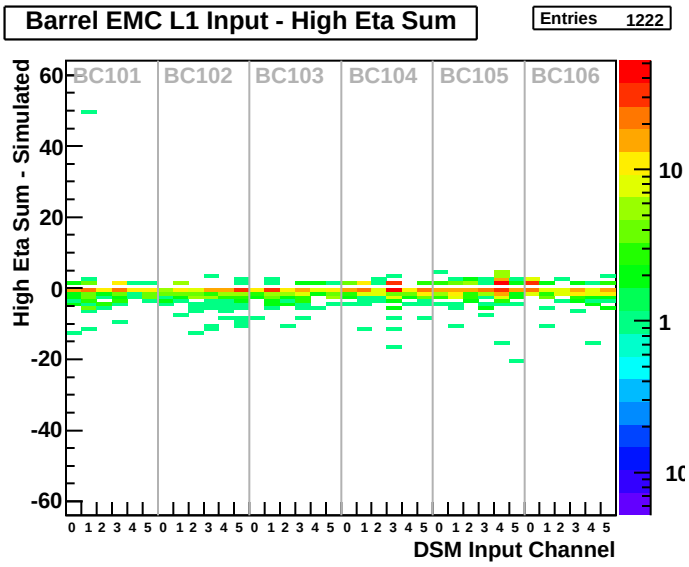
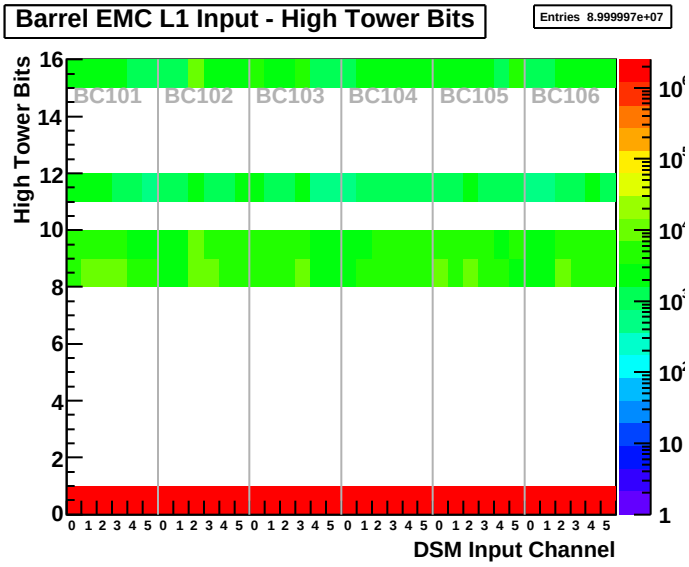
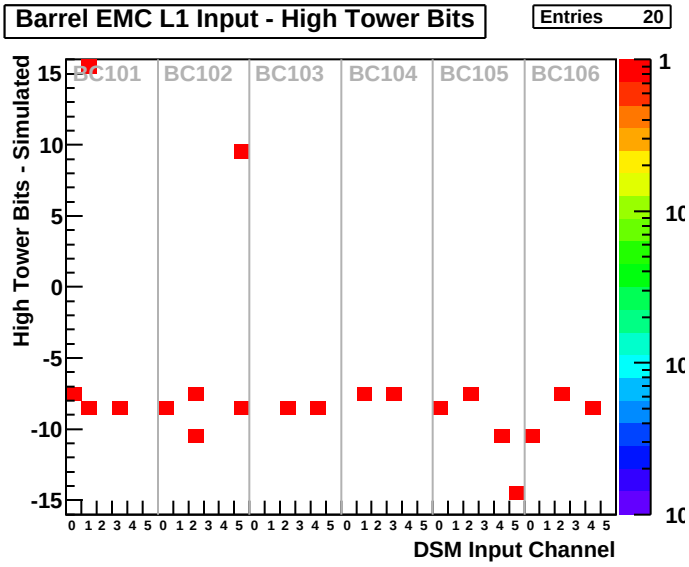

Barrel EMC L1 Input - Low Eta Sum
Entries 8.999997e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

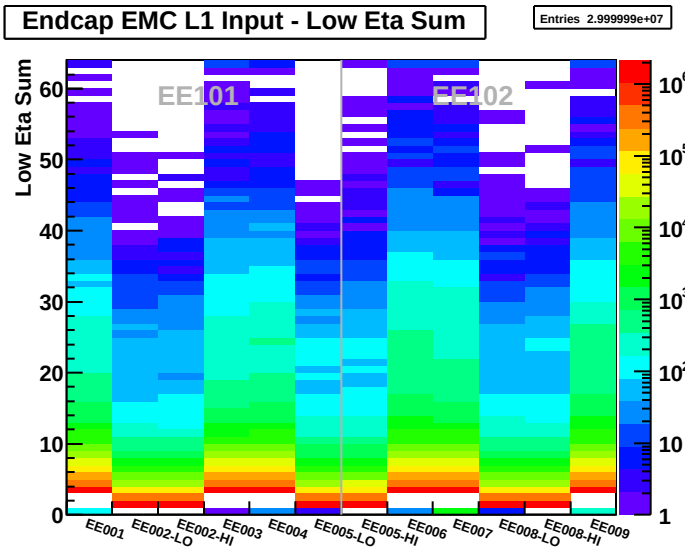

Barrel EMC L1 Input - Low Eta Sum
Entries 5818
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

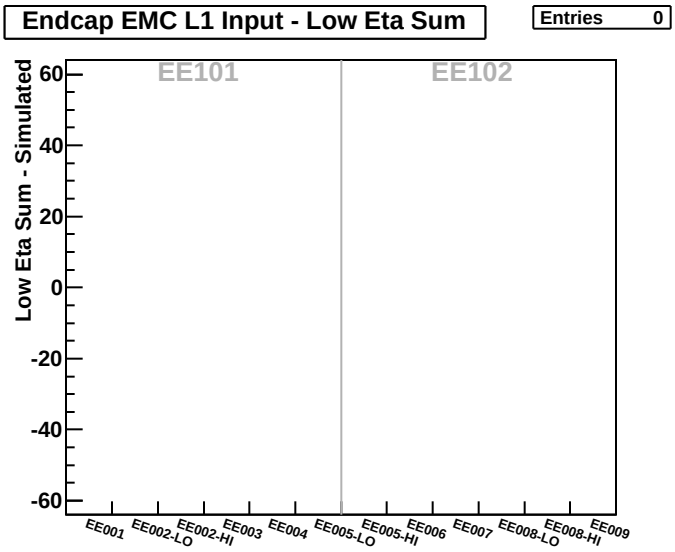

Barrel EMC L1 Input - High Eta Sum
Entries 8.999997e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

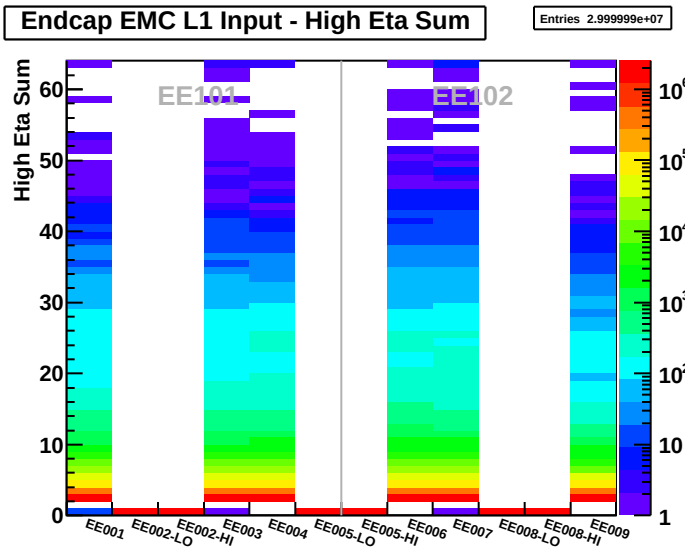

Barrel EMC L1 Input - High Eta Sum
Entries 1222
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

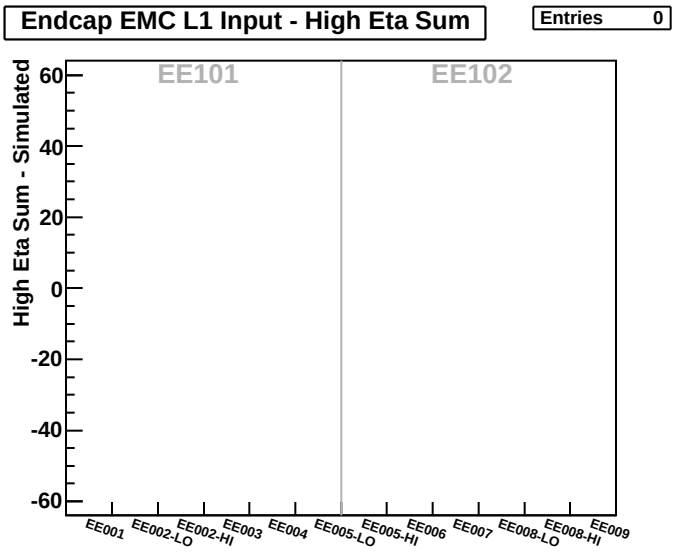

Barrel EMC L1 Input - High Tower Bits
Entries 8.999997e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

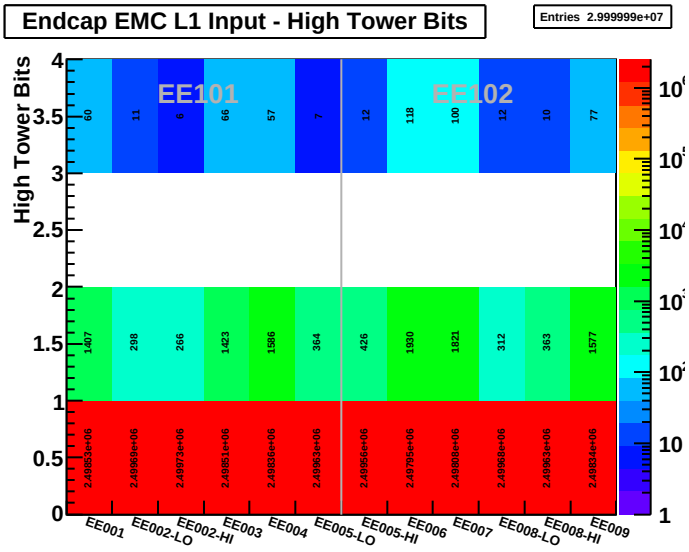

Barrel EMC L1 Input - High Tower Bits
Entries 20
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

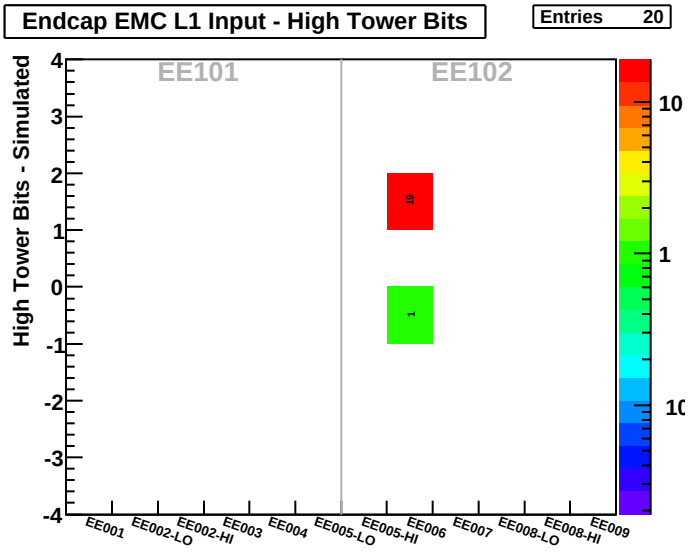

Endcap EMC L1 Input - Low Eta Sum
Entries 2.999999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

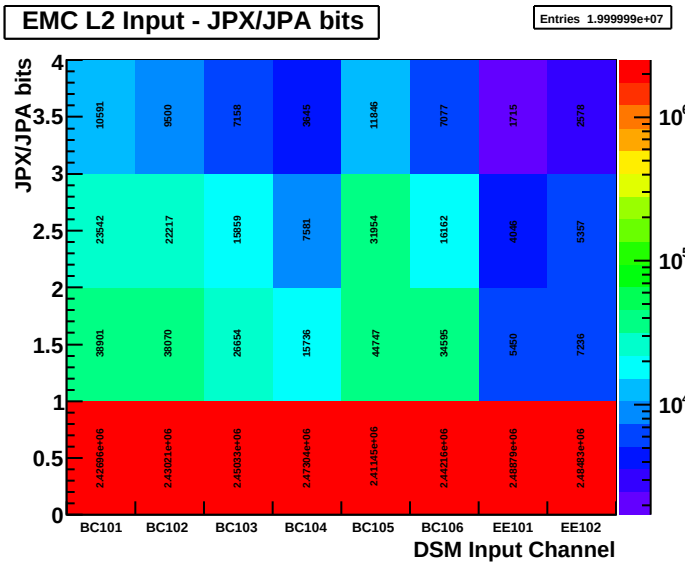

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

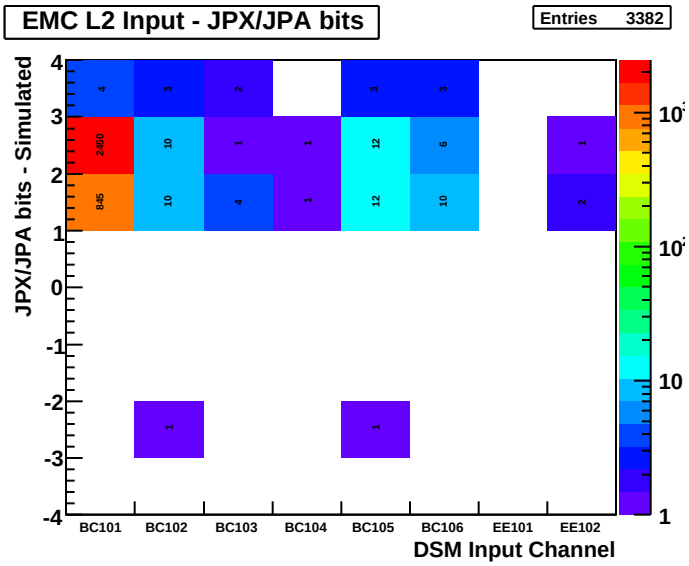

Endcap EMC L1 Input - High Eta Sum
Entries 2.999999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

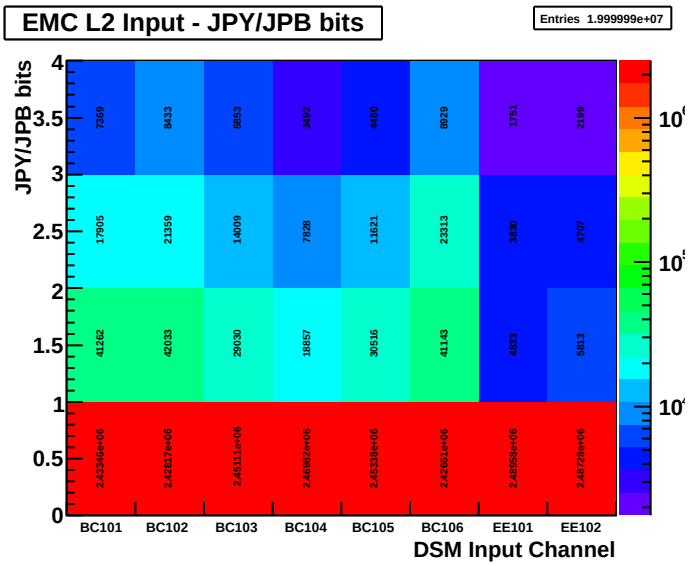

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

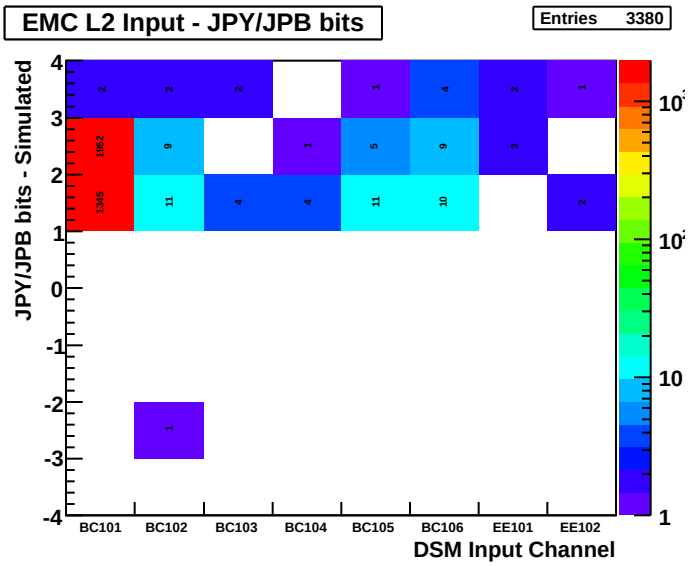

Endcap EMC L1 Input - High Tower Bits
Entries 2.999999e+07
High Tower Bits
EE101
EE102
60 11 6 66 57 7 12 118 100 12 10 77
1407 298 266 1423 1586 364 426 1930 1821 312 363 1577
2.49853e+06 2.49969e+06 2.49973e+06 2.49851e+06 2.49838e+06 2.49963e+06 2.49956e+06 2.49795e+06 2.49808e+06 2.49968e+06 2.49963e+06 2.49834e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

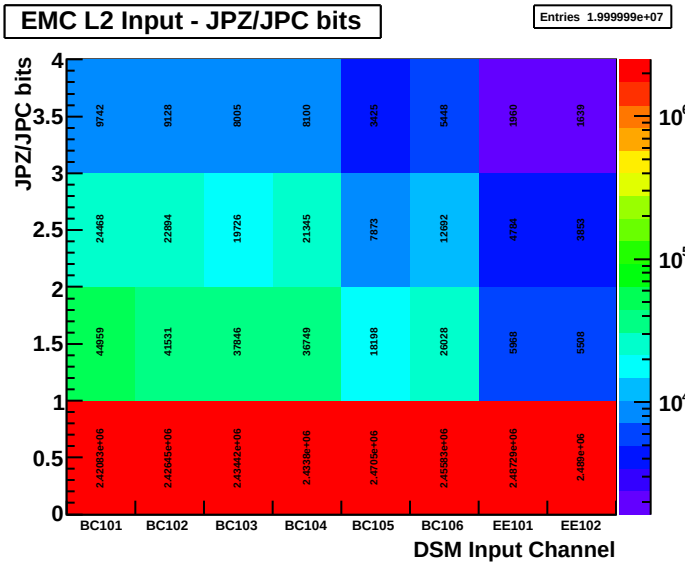

Endcap EMC L1 Input - High Tower Bits
Entries 20
High Tower Bits - Simulated
EE101
EE102
19
1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

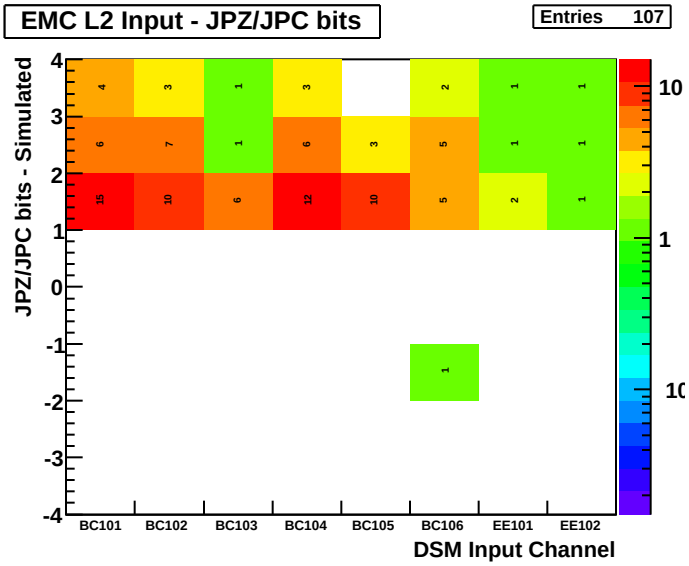

EMC L2 Input - JPX/JPA bits
Entries 1.999999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10591 9500 7158 3845 11846 7077 1715 2578
23542 22217 15859 7581 31954 16162 4046 5357
38901 38070 26654 15736 44747 34595 5450 7236
2.42896e+06 2.43021e+06 2.45033e+06 2.47304e+06 2.41145e+06 2.44216e+06 2.48879e+06 2.48483e+06

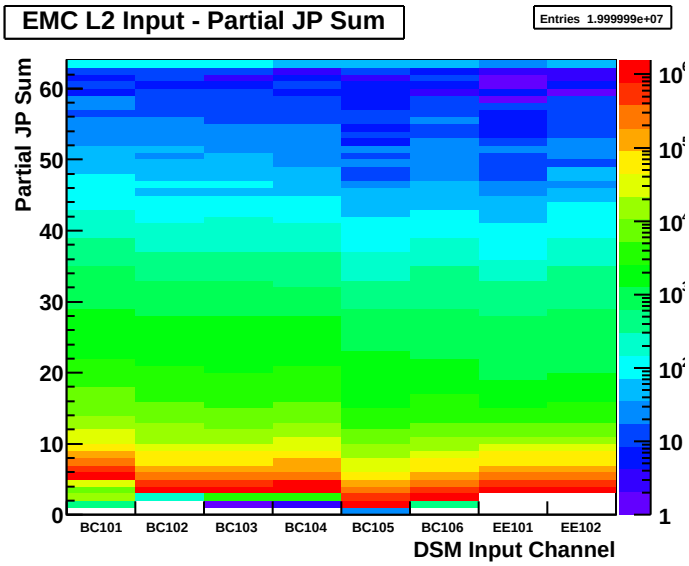

EMC L2 Input - JPX/JPA bits
Entries 3382
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4 3 2 3 3
2450 10 1 1 12 6 1
845 10 4 1 12 10 2
1 1

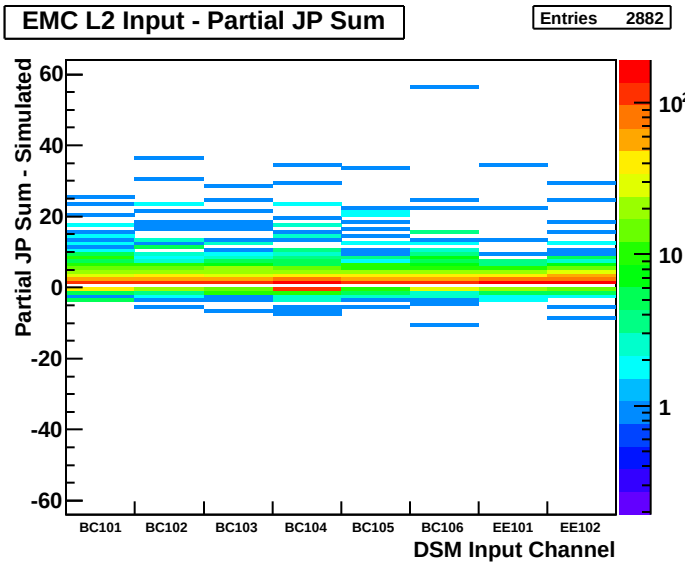

EMC L2 Input - JPY/JPB bits
Entries 1.999999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7369 8433 5853 3492 4480 8929 1751 2199
17905 21359 14009 7828 11621 23313 3830 4707
41262 42033 29030 18857 30516 41143 4833 5813
2.43346e+06 2.42817e+06 2.45111e+06 2.46982e+06 2.45338e+06 2.42061e+06 2.48958e+06 2.48728e+06

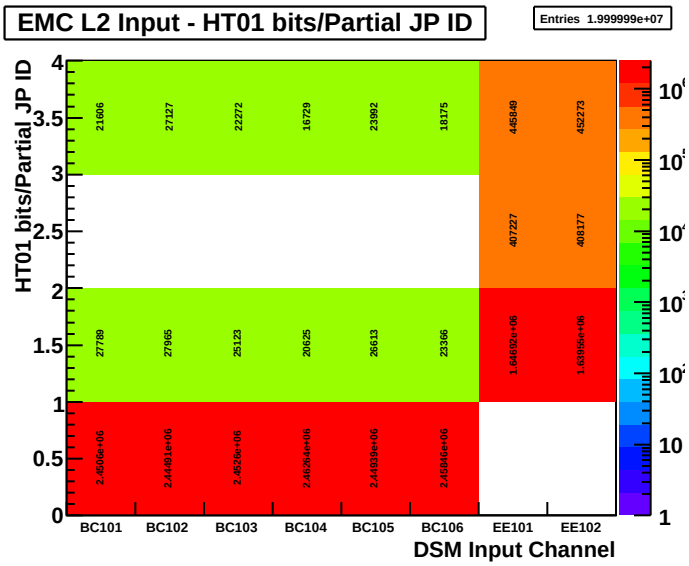

EMC L2 Input - JPY/JPB bits
Entries 3380
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 2 2 1 4 2 1
1952 9 1 5 9 2
1345 11 4 4 11 10 2
1

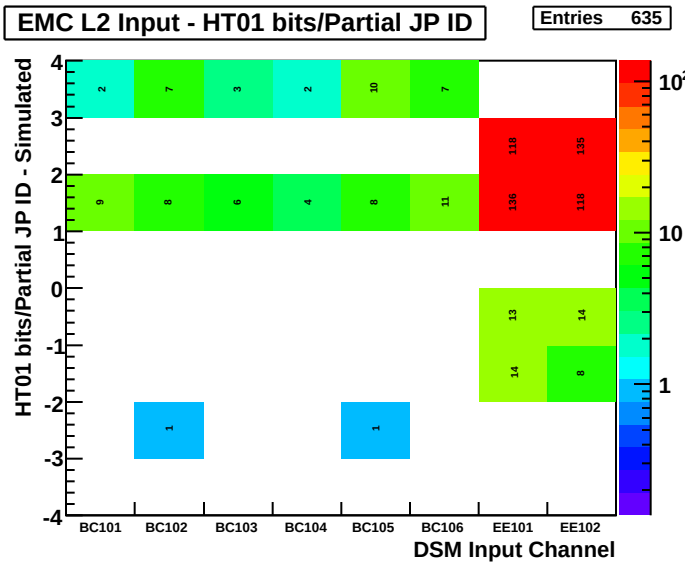

EMC L2 Input - JPZ/JPC bits
Entries 1.999999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9742 9128 8005 8100 3425 5448 1960 1639
24468 22894 19726 21345 7873 12692 4784 3853
48959 41531 37846 36749 18198 26028 5968 5508
2.42083e+06 2.42645e+06 2.43442e+06 2.4338e+06 2.4705e+06 2.45583e+06 2.48729e+06 2.489e+06

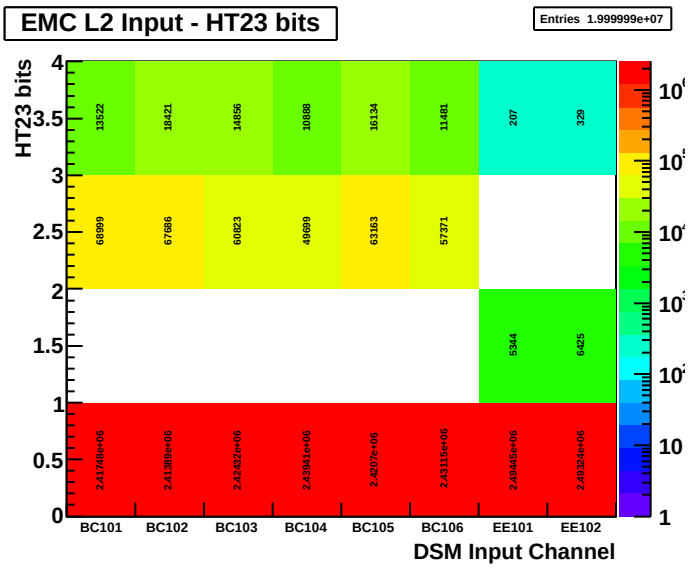

EMC L2 Input - JPZ/JPC bits
Entries 107
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4 3 1 3 2 1 1
6 7 1 6 3 5 1 1
15 10 6 12 10 5 2 1
1

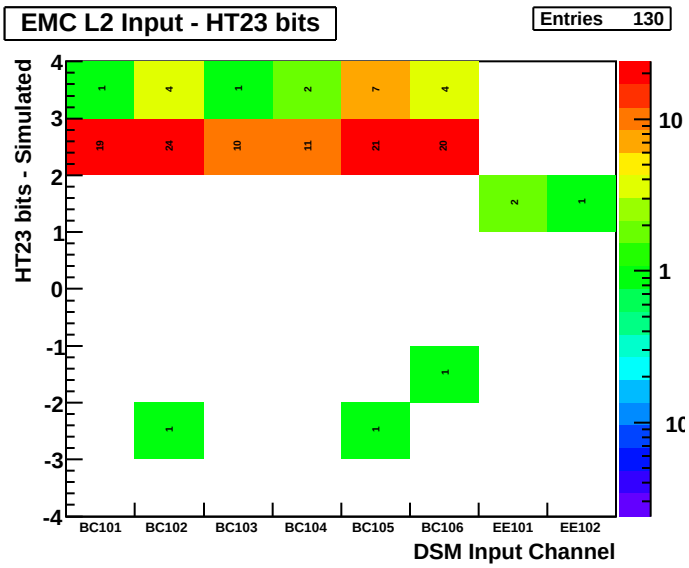

EMC L2 Input - Partial JP Sum
Entries 1.999999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

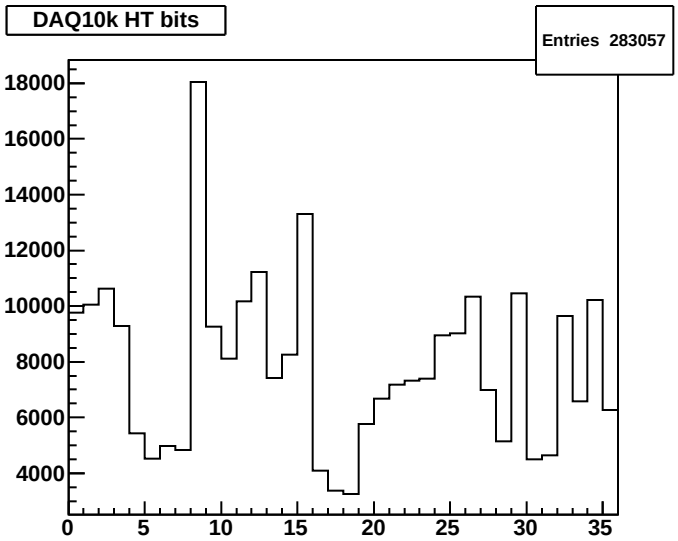

EMC L2 Input - Partial JP Sum
Entries 2882
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

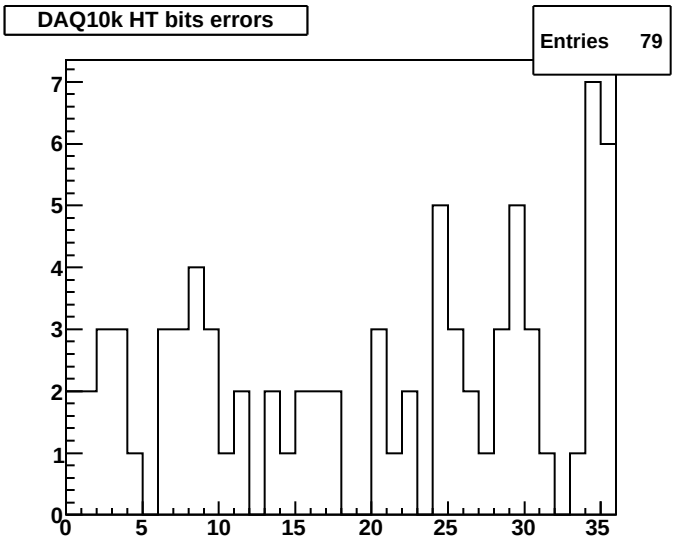

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.999999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

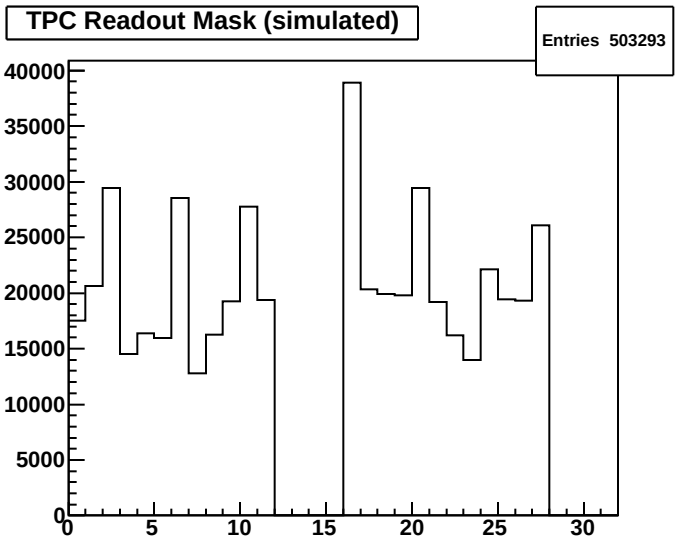

EMC L2 Input - HT01 bits/Partial JP ID
Entries 635
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

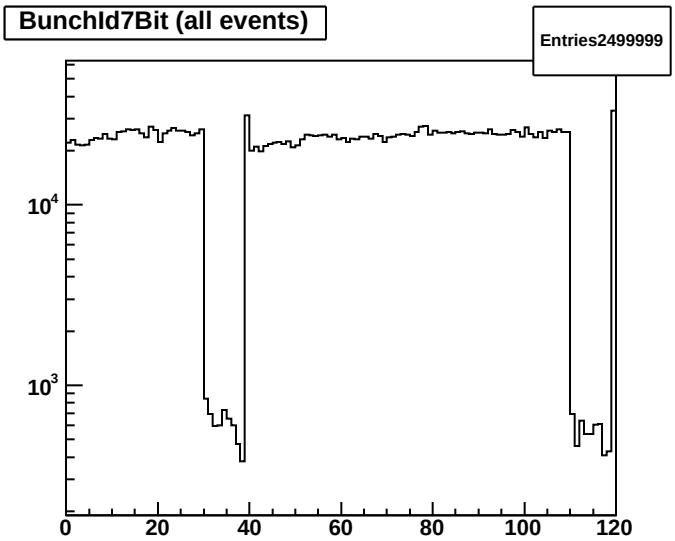

EMC L2 Input - HT23 bits
Entries 1.999999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

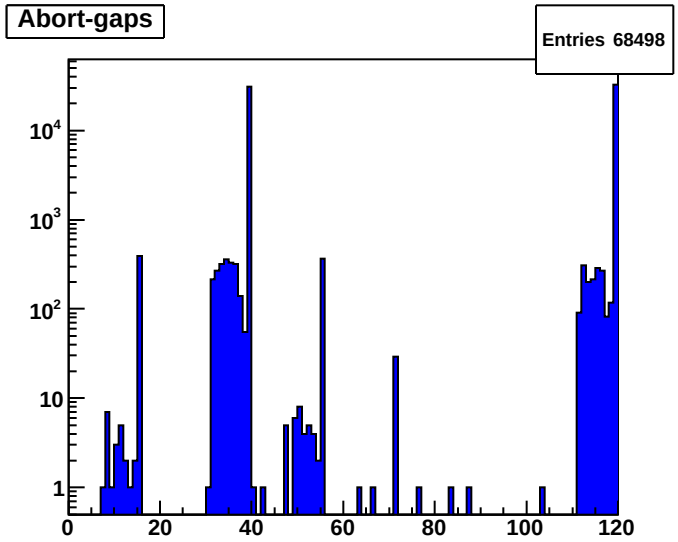

EMC L2 Input - HT23 bits
Entries 130
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

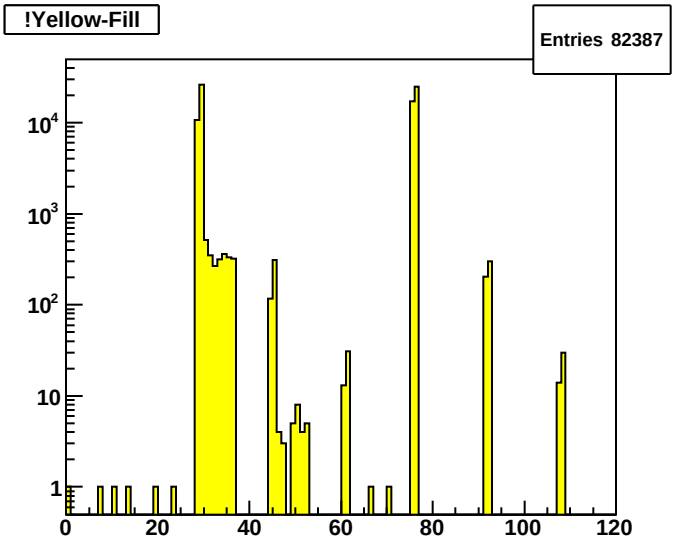

DAQ10k HT bits
Entries 283057

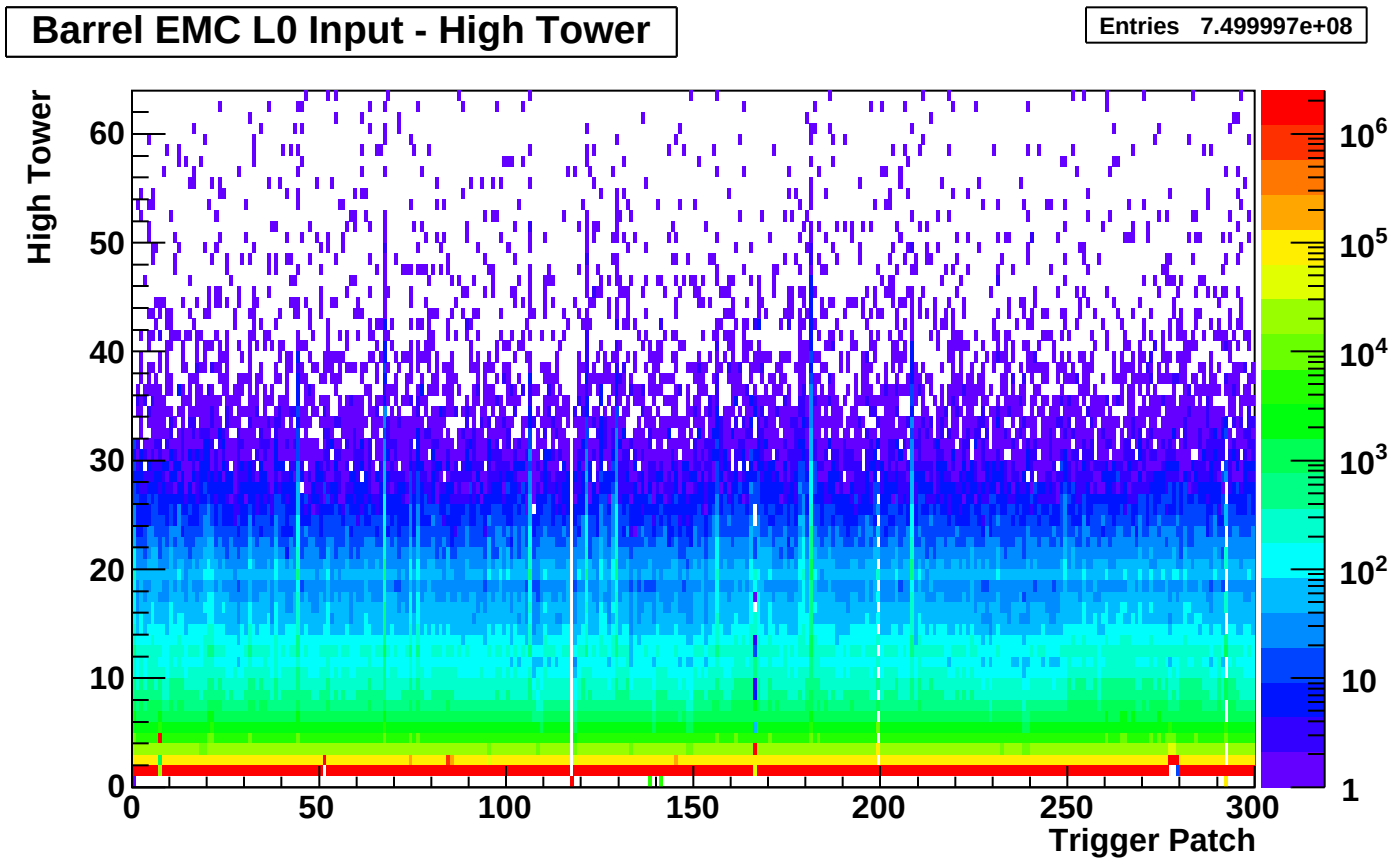

DAQ10k HT bits errors
Entries 79

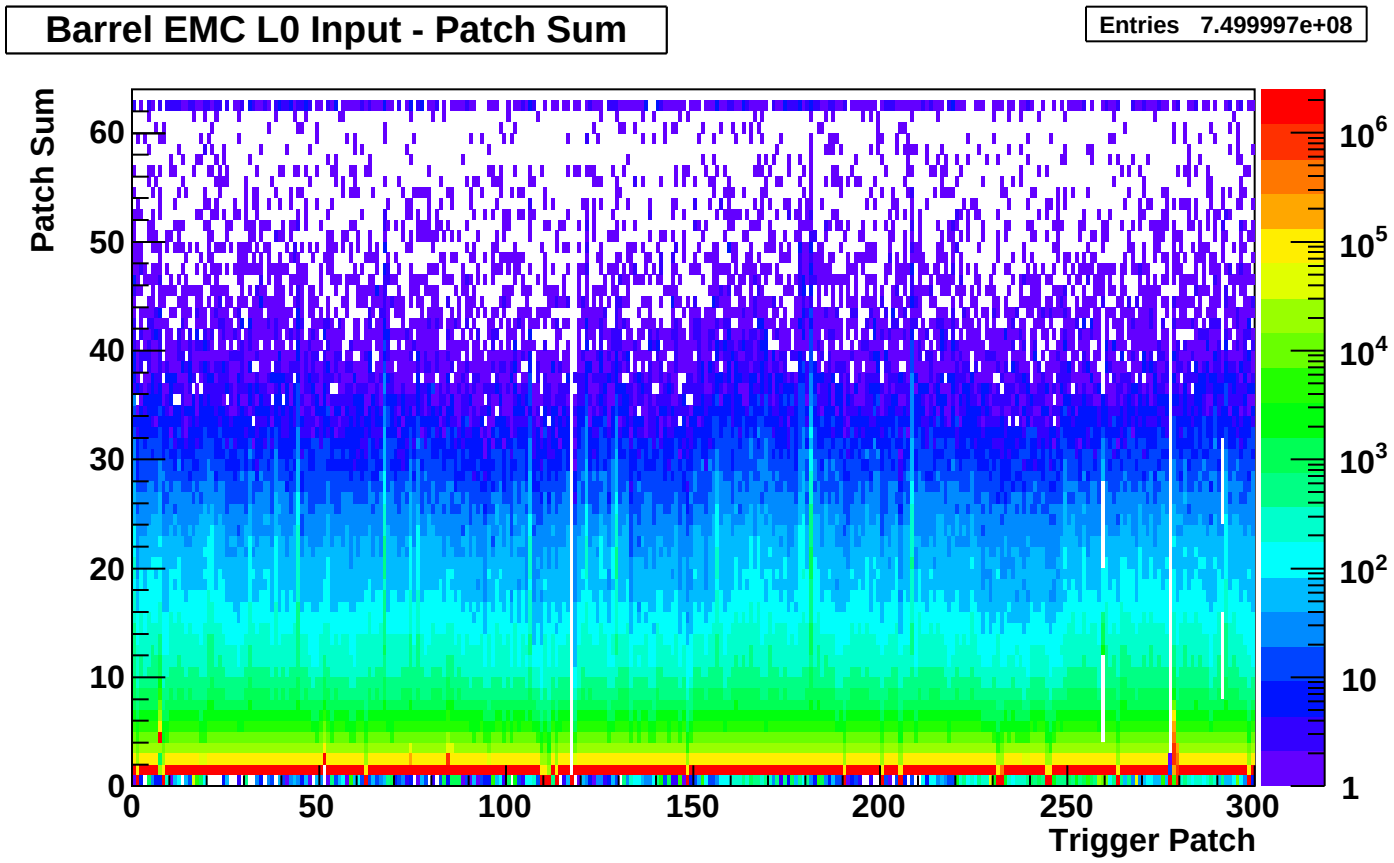

TPC Readout Mask (simulated)
Entries 503293

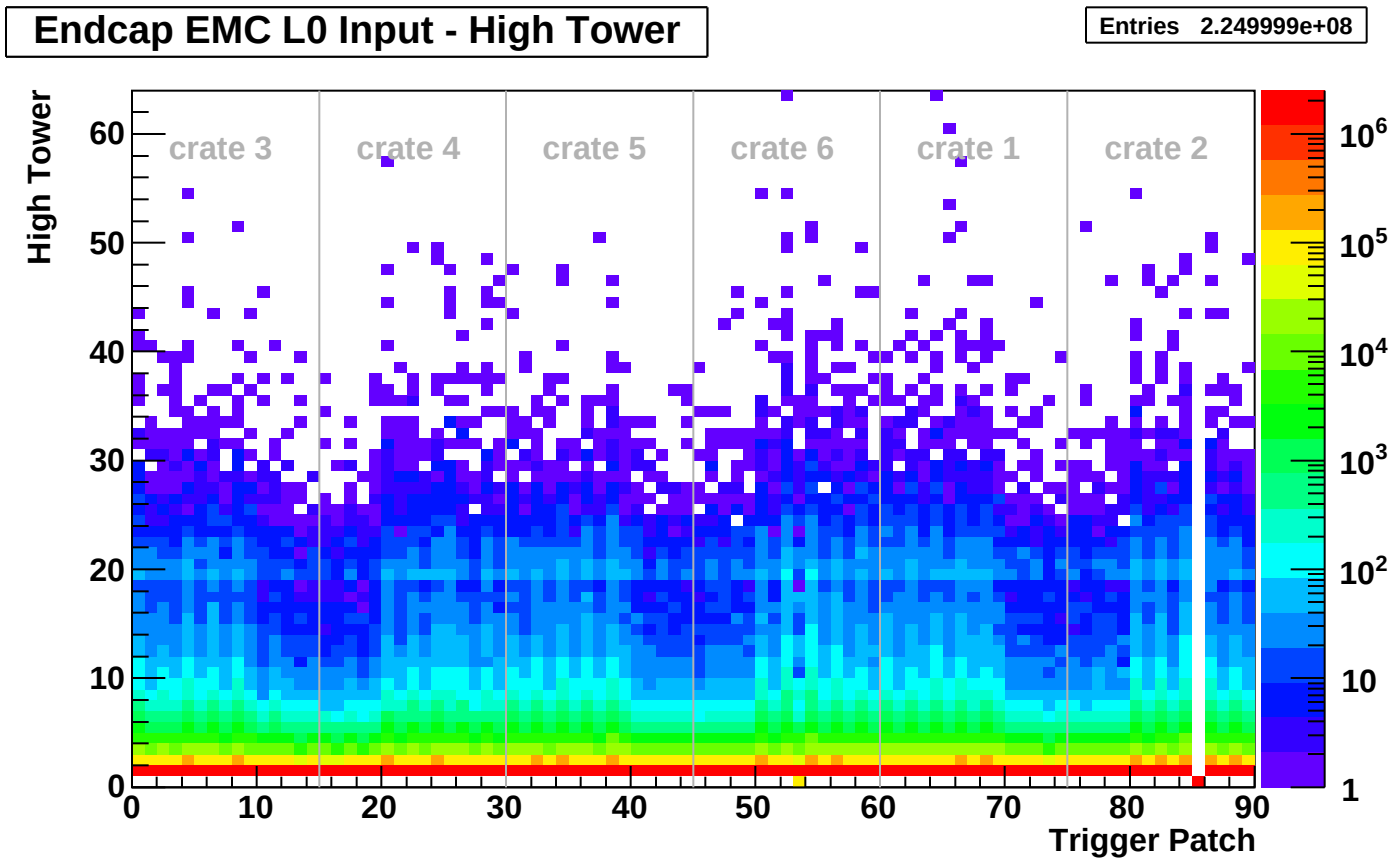

BunchId7Bit (all events)
Entries2499999

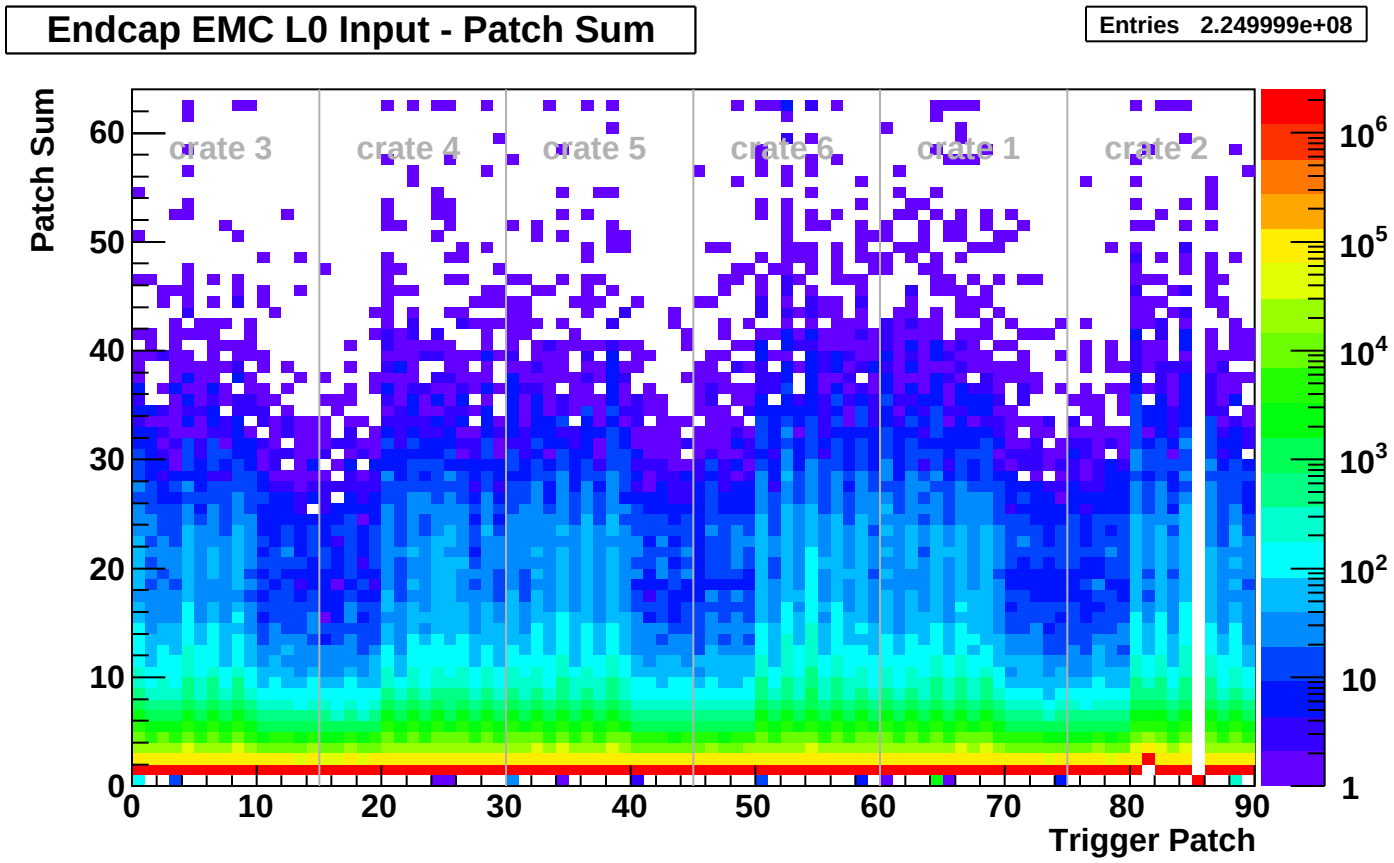

Abort-gaps
Entries 68498

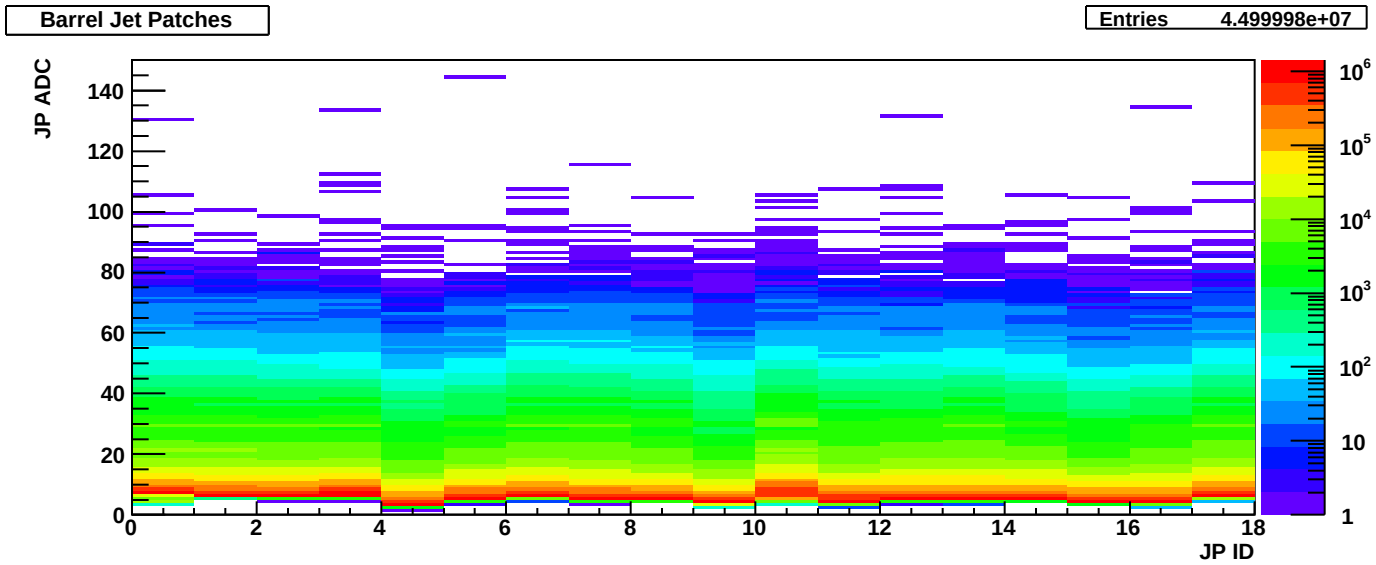

!Yellow-Fill
Entries 82387

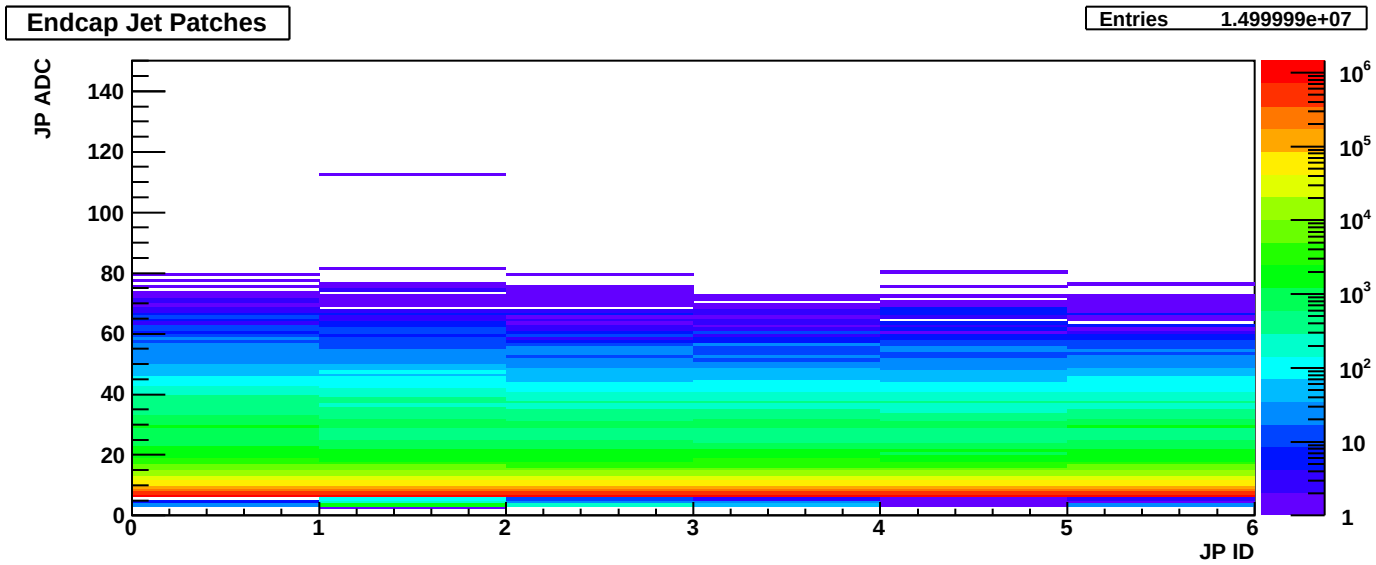

Barrel EMC L0 Input - High Tower
Entries 7.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

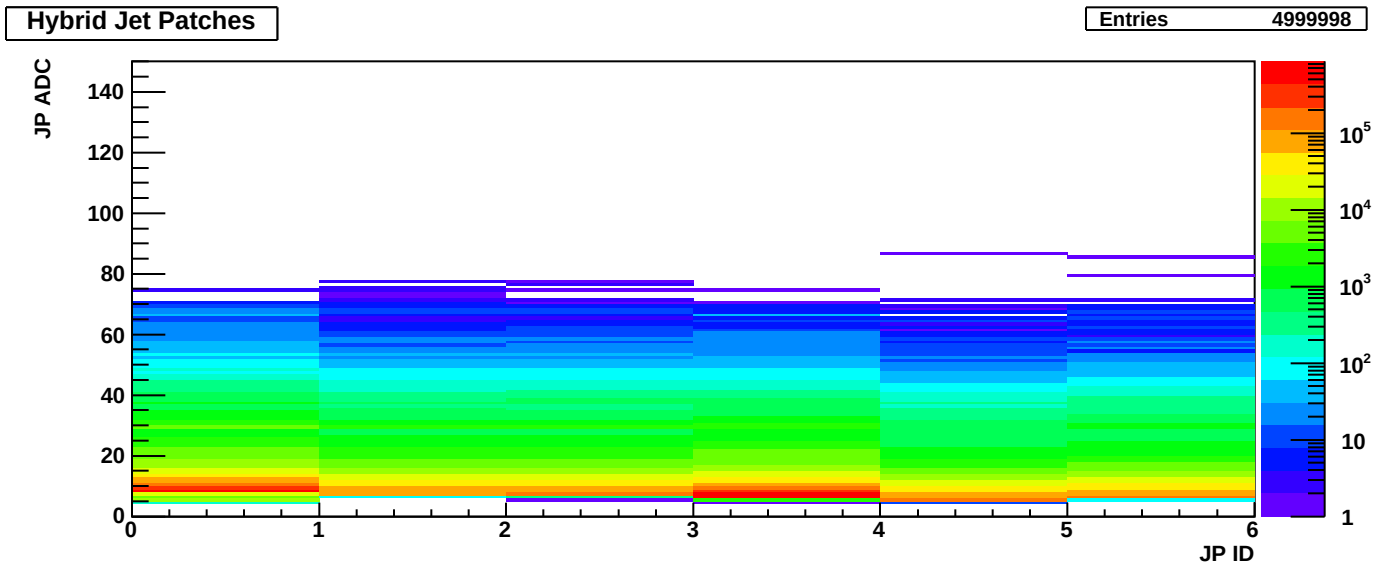

Barrel EMC L0 Input - Patch Sum
Entries 7.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

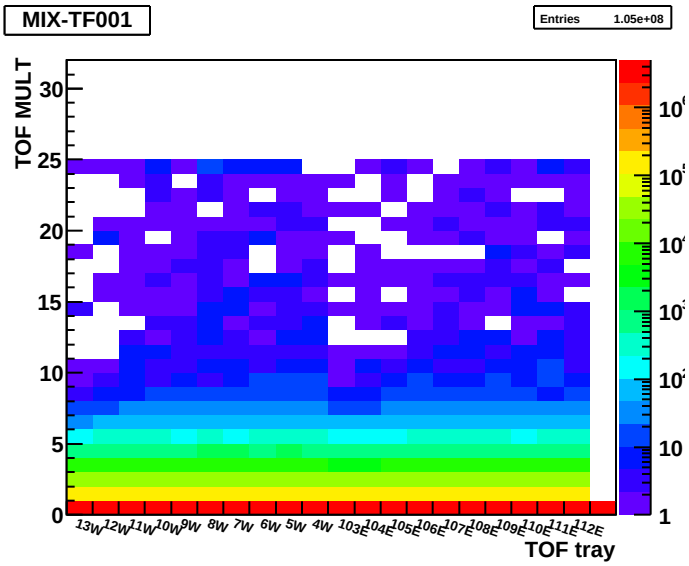

Endcap EMC L0 Input - High Tower
Entries 2.249999e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

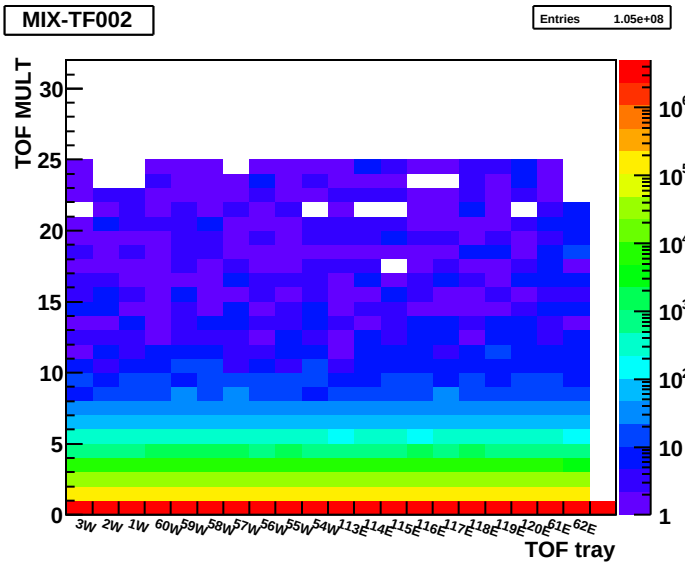

Endcap EMC L0 Input - Patch Sum
Entries 2.249999e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

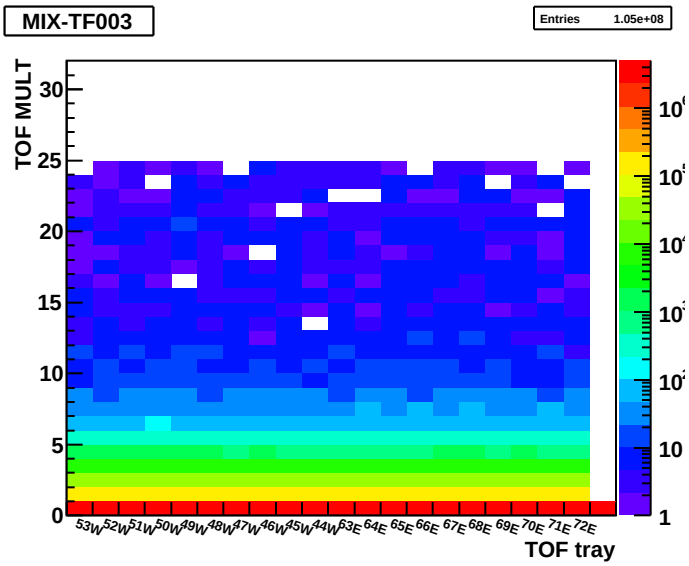

Barrel Jet Patches
Entries 4.499998e+07
JP ADC
JP ID

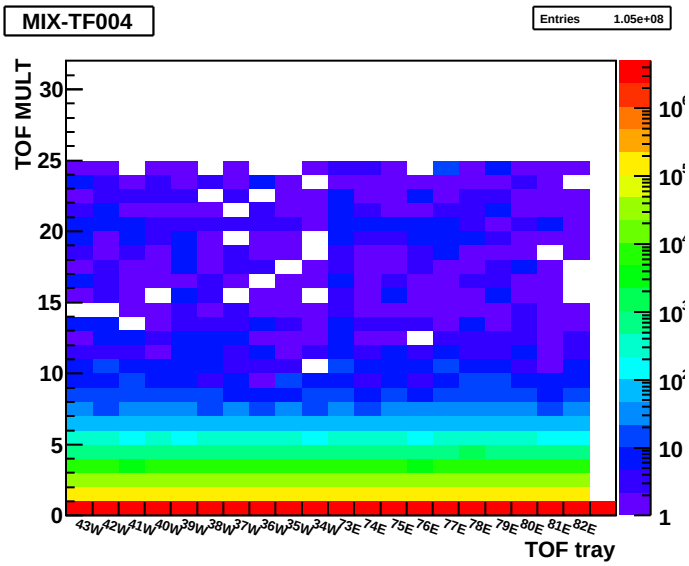

Endcap Jet Patches
Entries 1.499999e+07
JP ADC
JP ID

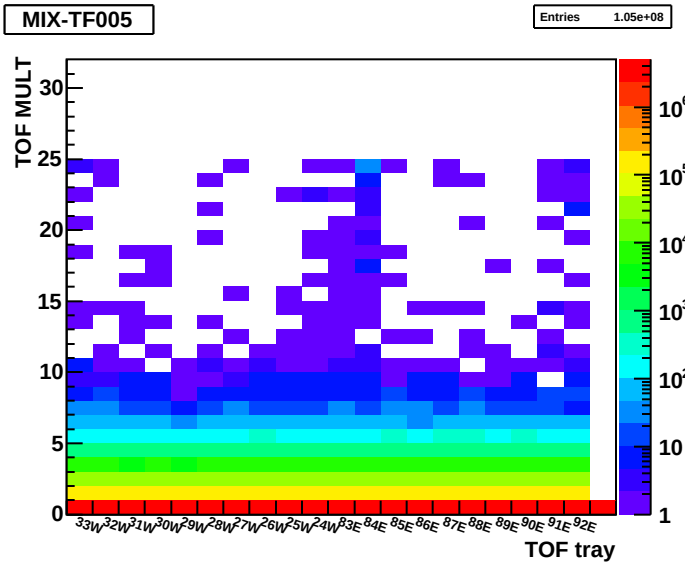

Hybrid Jet Patches
Entries 4999998
JP ADC
JP ID

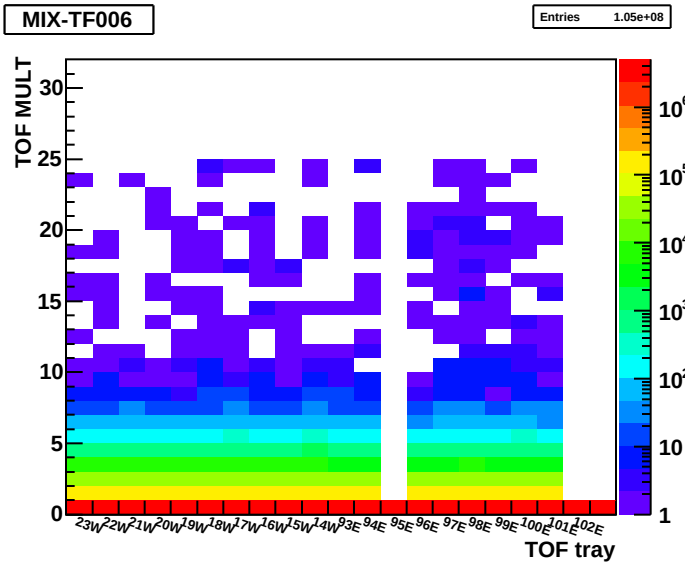

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

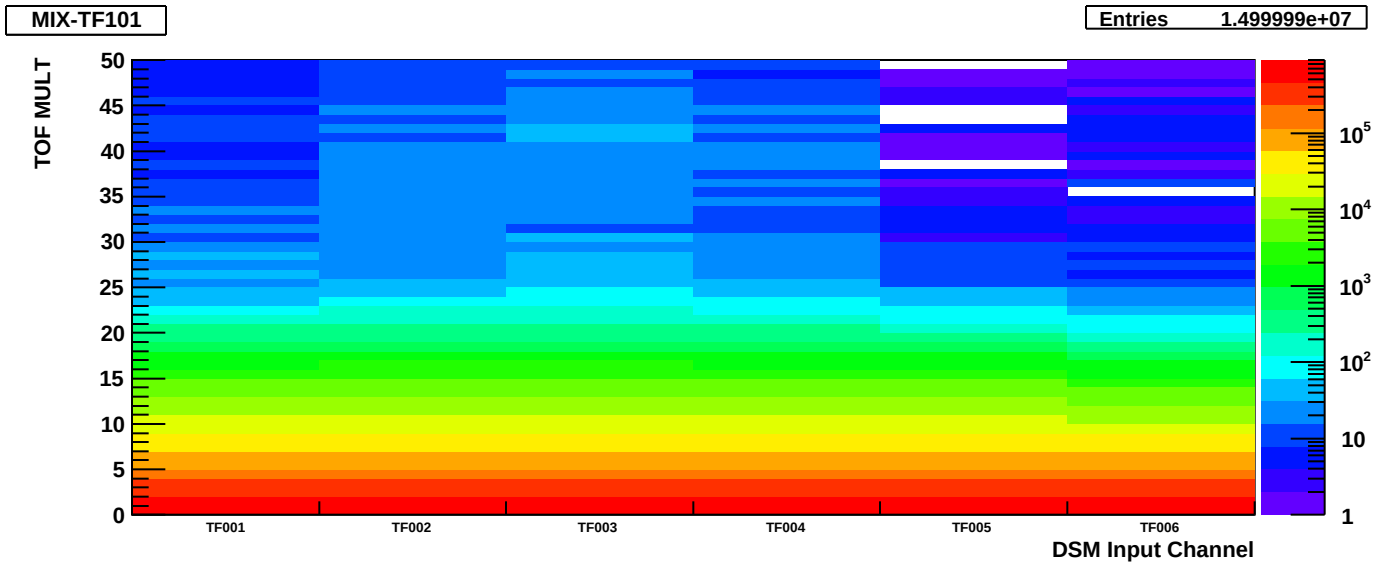

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

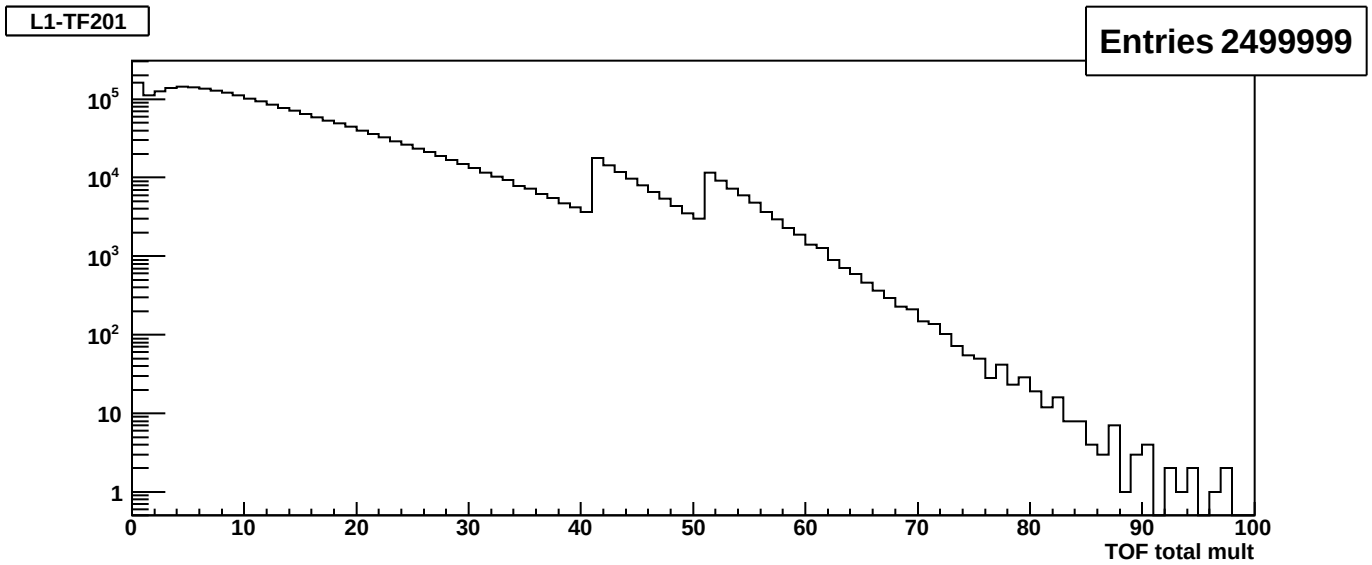

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

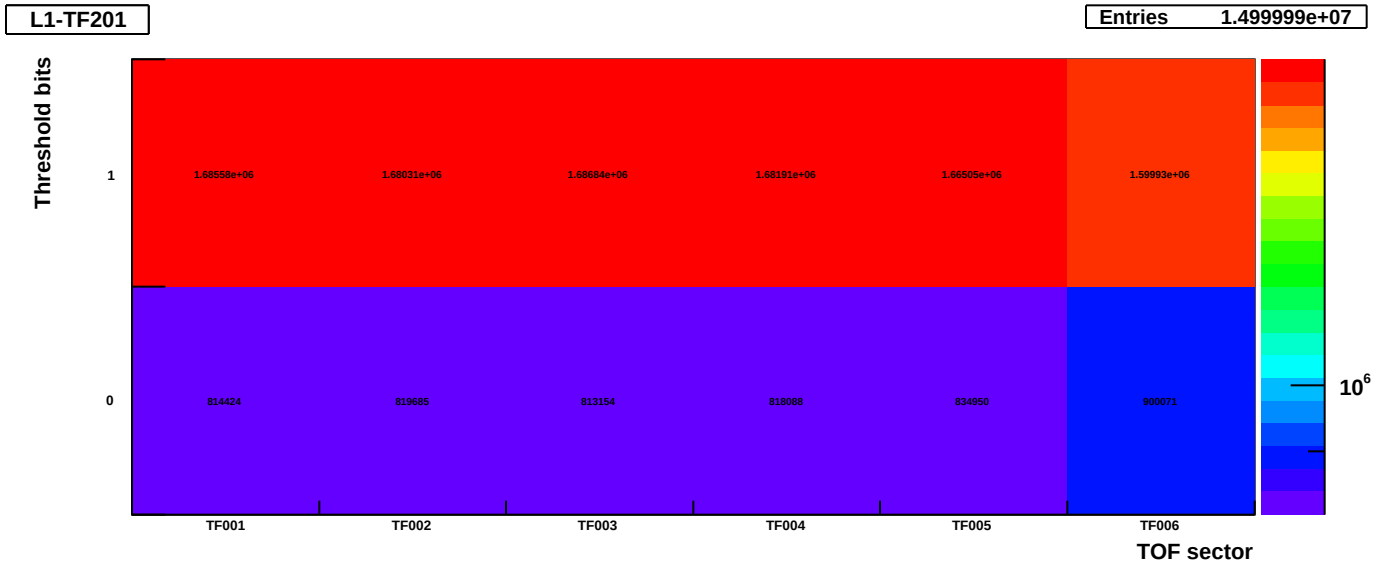

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

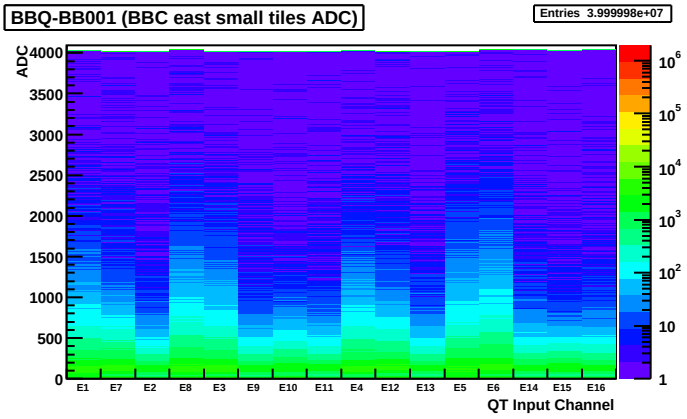

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

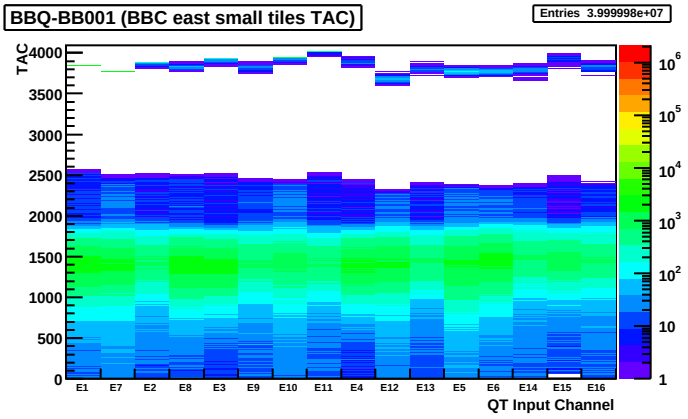

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

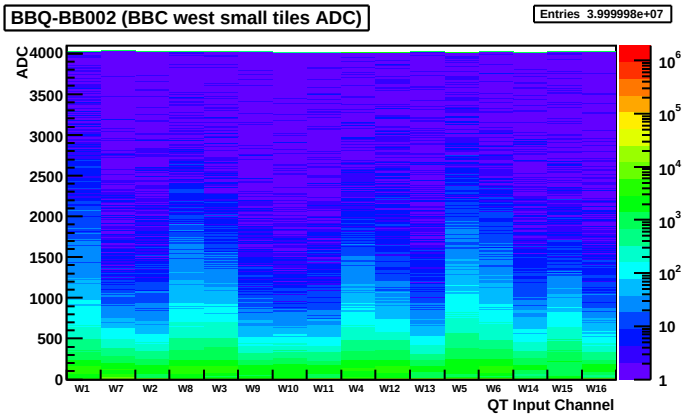

MIX-TF101
Entries 1.499999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

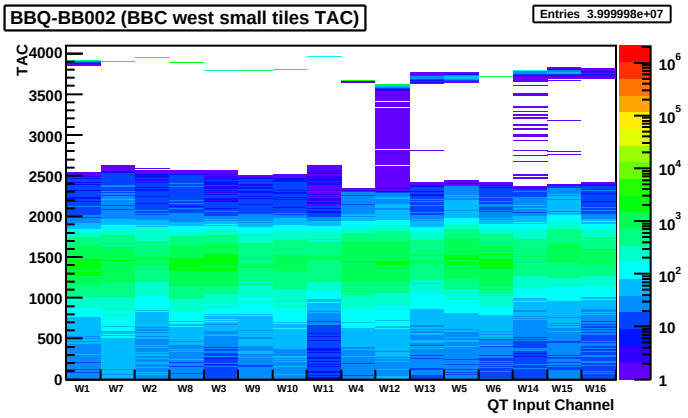

L1-TF201
Entries 2499999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

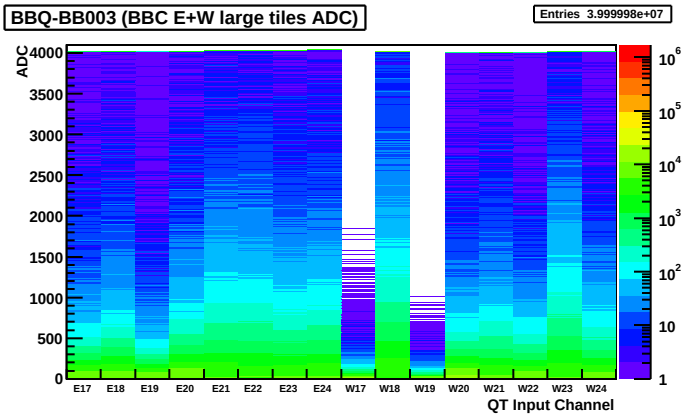

L1-TF201
Entries 1.499999e+07
Threshold bits
1
0
1.68558e+06
1.68031e+06
1.68684e+06
1.68191e+06
1.66505e+06
1.59993e+06
814424
819685
813154
818088
834950
900071
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

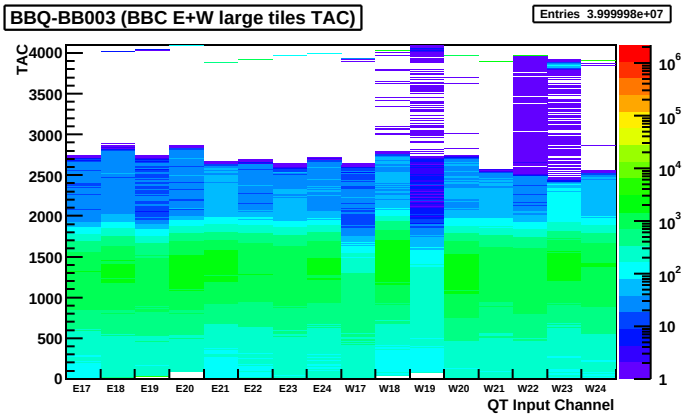

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

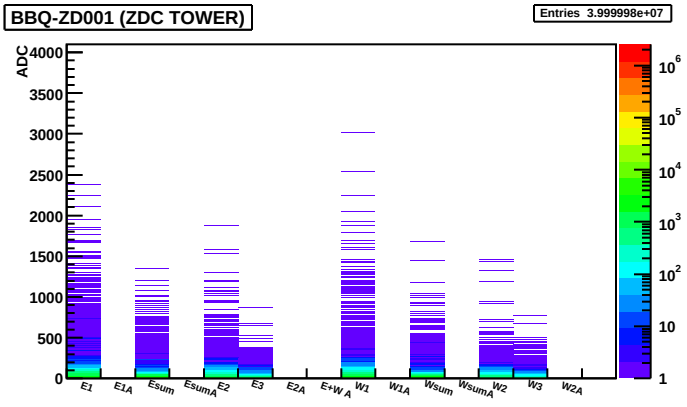

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

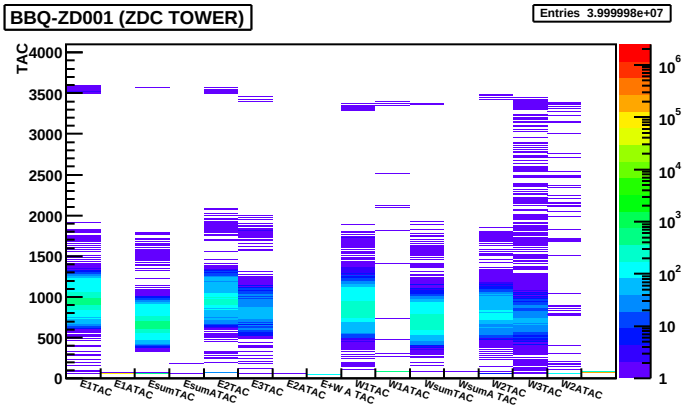

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

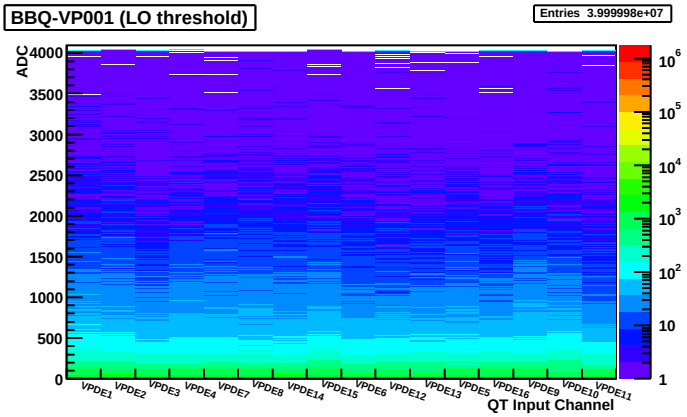

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

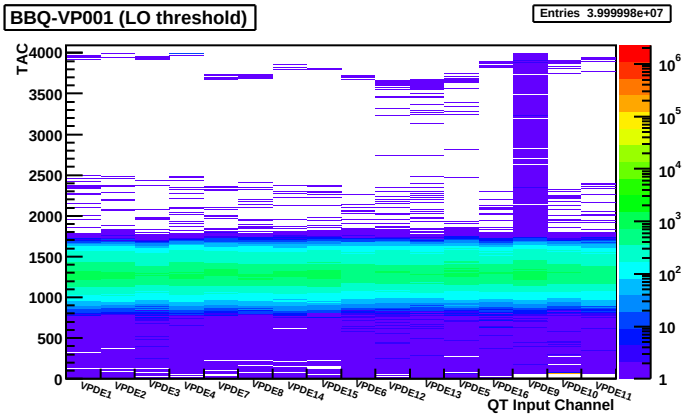

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

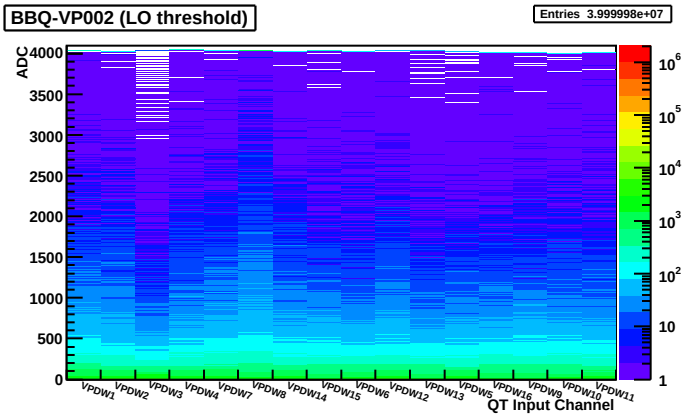

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

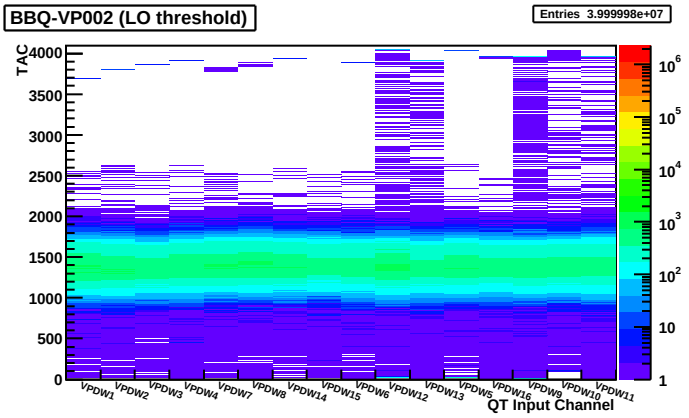

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
ADC

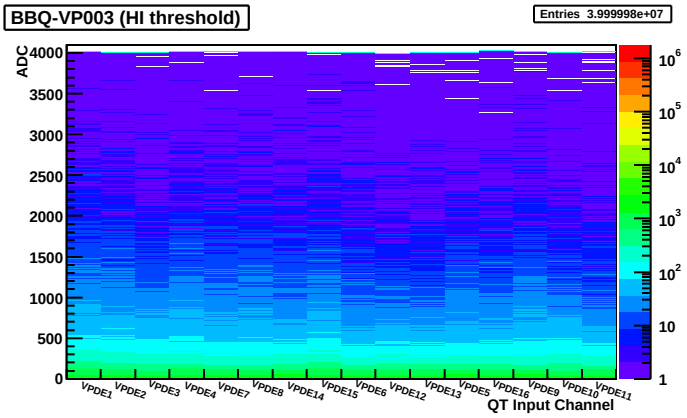

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
TAC

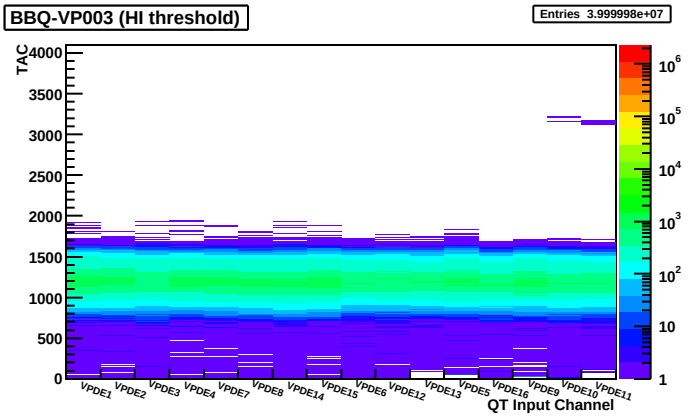

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

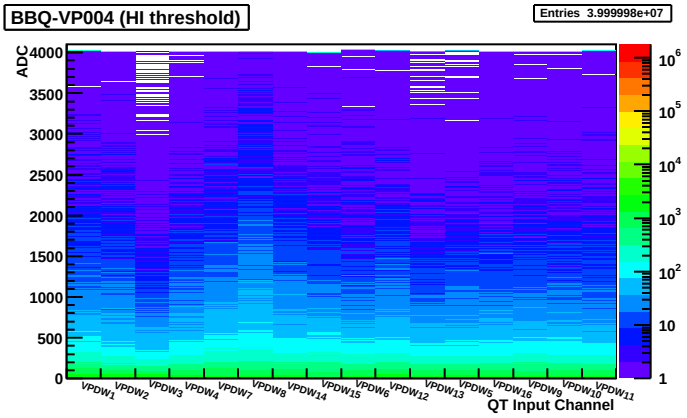

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

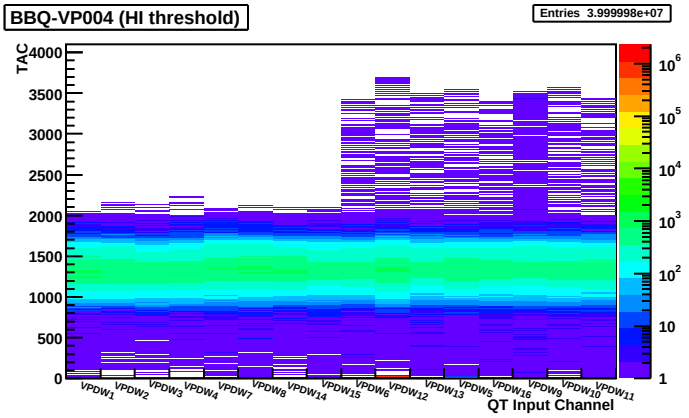

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

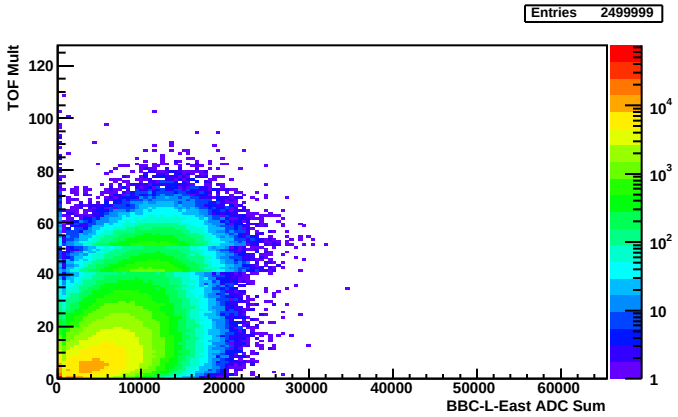

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

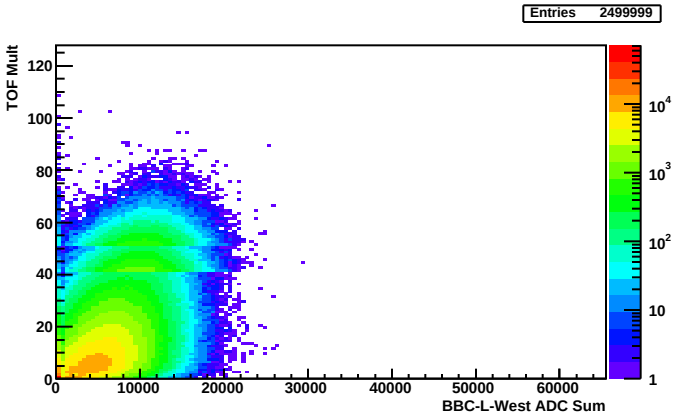

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

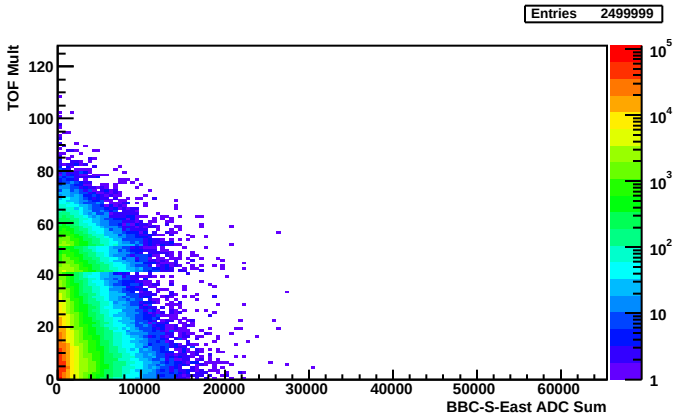

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

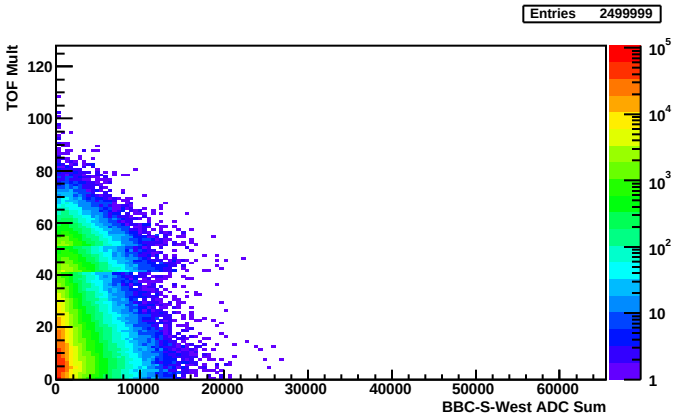

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

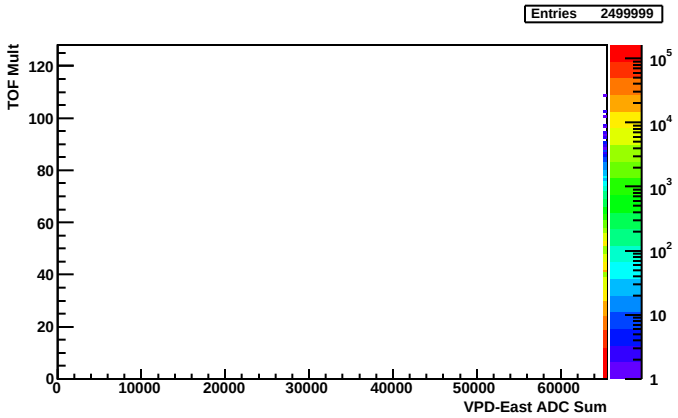

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

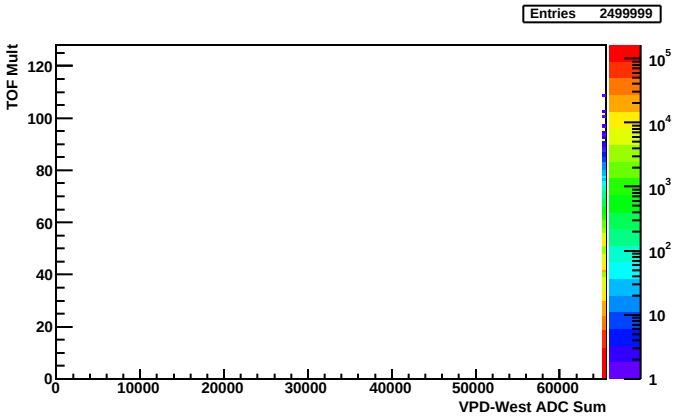

Entries 2499999
TOF Mult
BBC-L-East ADC Sum

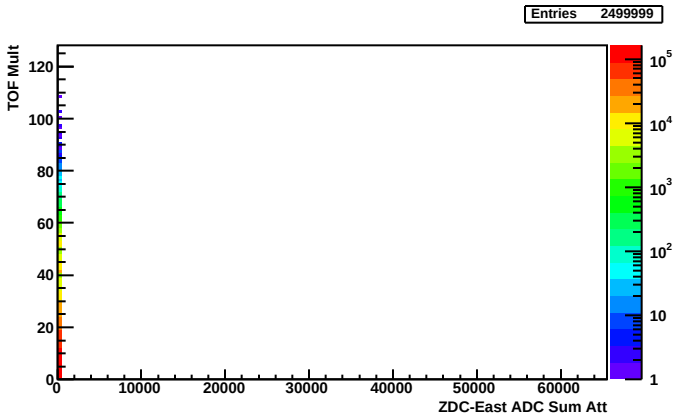

Entries 2499999
TOF Mult
BBC-L-West ADC Sum

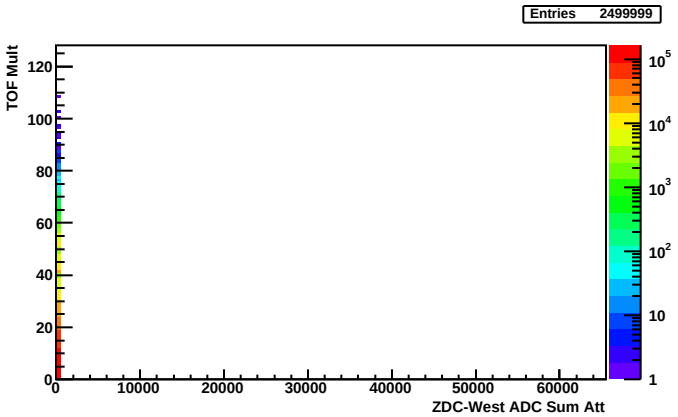

Entries 2499999
TOF Mult
BBC-S-East ADC Sum

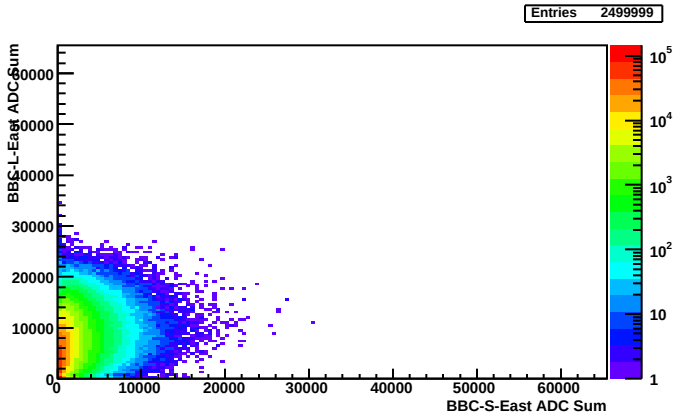

Entries 2499999
TOF Mult
BBC-S-West ADC Sum

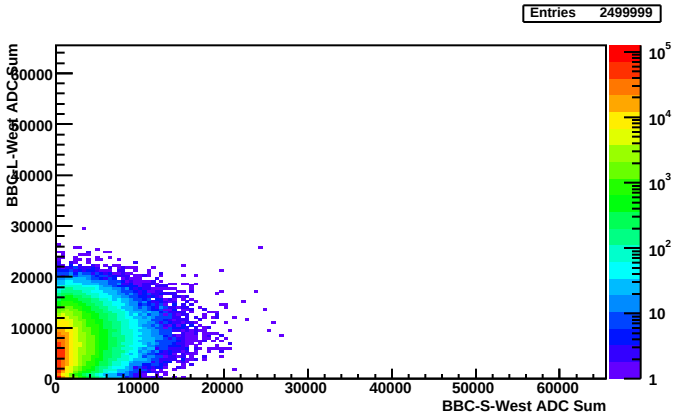

Entries 2499999
TOF Mult
VPD-East ADC Sum

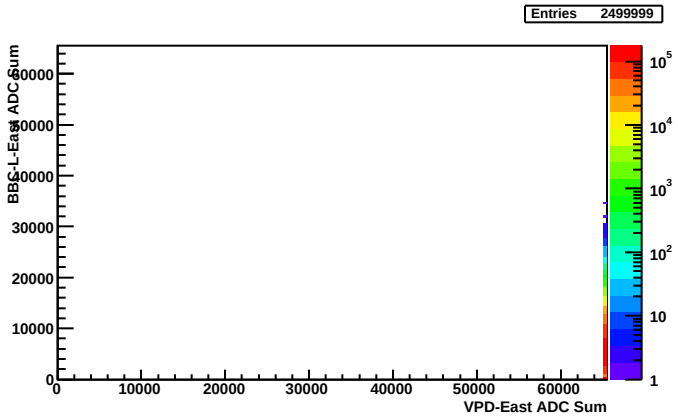

Entries 2499999
TOF Mult
VPD-West ADC Sum

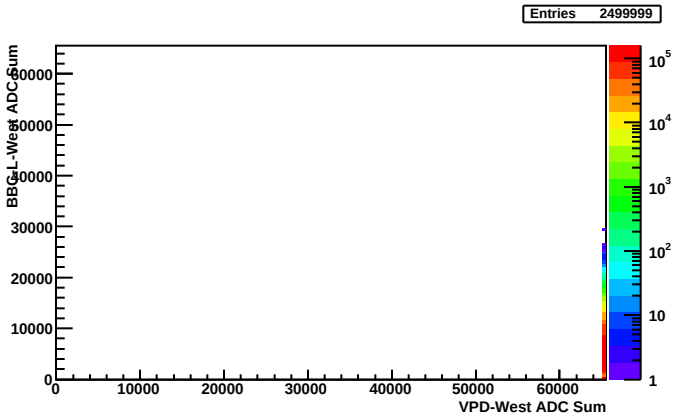

Entries 2499999
TOF Mult
ZDC-East ADC Sum Att

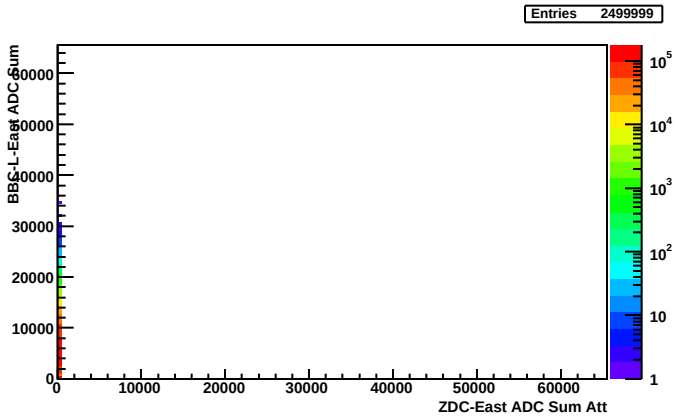

Entries 2499999
TOF Mult
ZDC-West ADC Sum Att

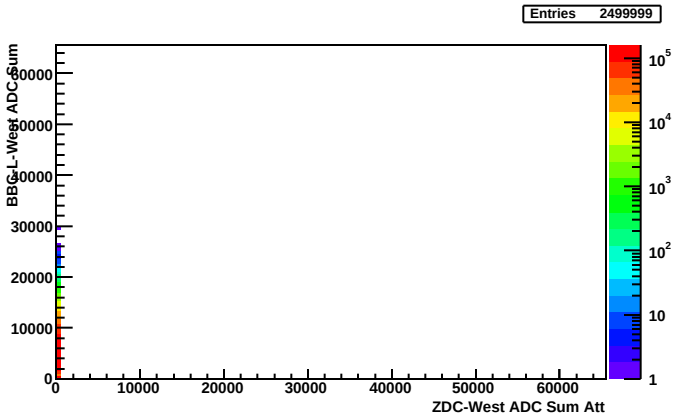

Entries 2499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum

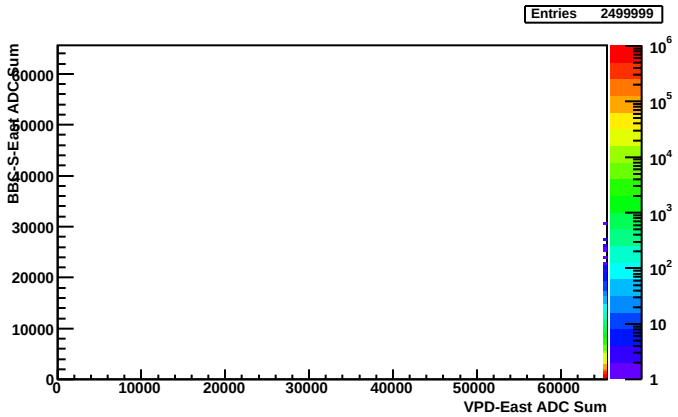

Entries 2499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum

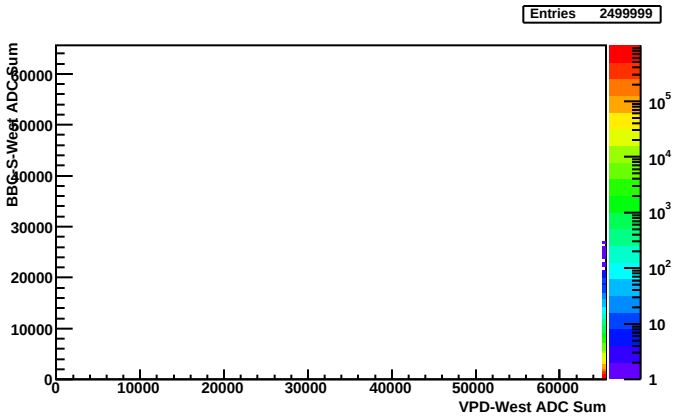

Entries 2499999
BBC-L-East ADC Sum
VPD-East ADC Sum

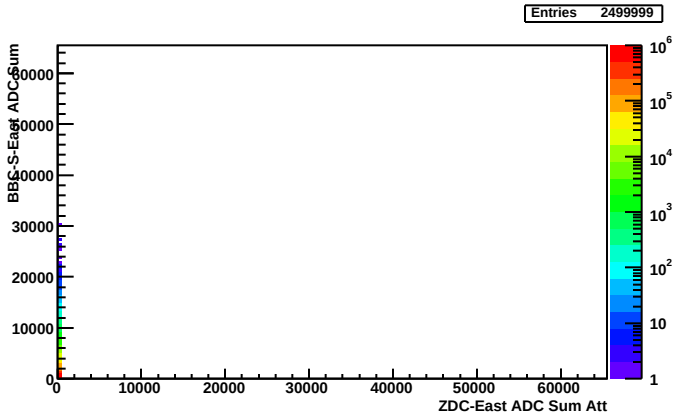

Entries 2499999
BBC-L-West ADC Sum
VPD-West ADC Sum

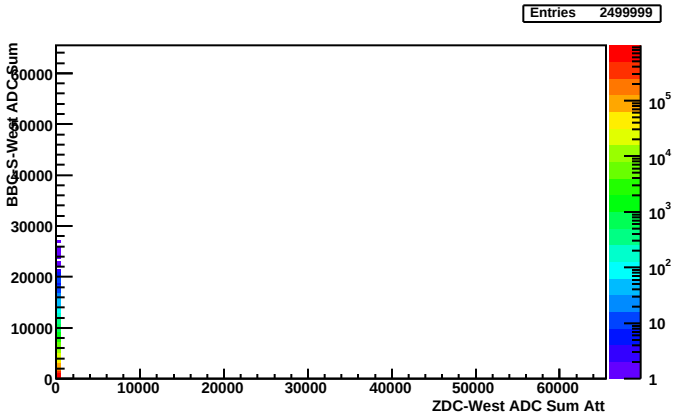

Entries 2499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

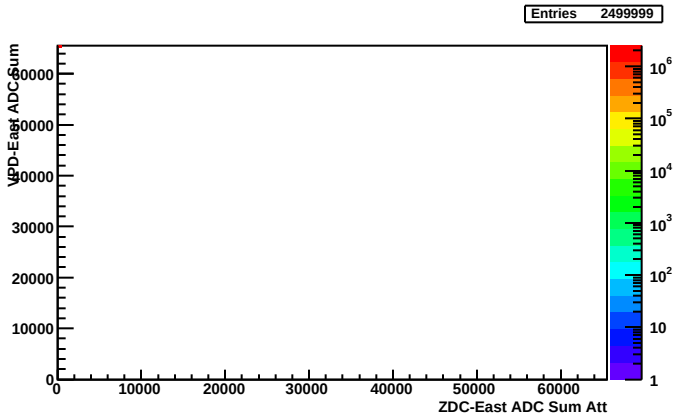

Entries 2499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

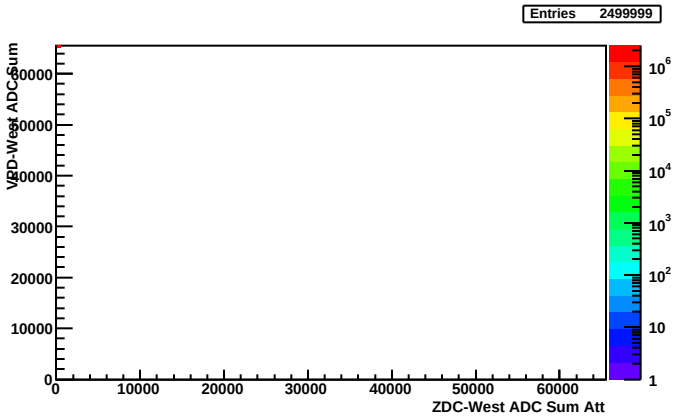

Entries 2499999
BBC-S-East ADC Sum
VPD-East ADC Sum

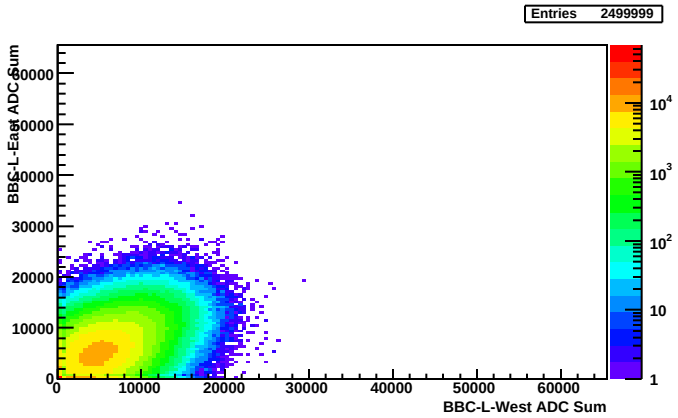

Entries 2499999
BBC-S-West ADC Sum
VPD-West ADC Sum

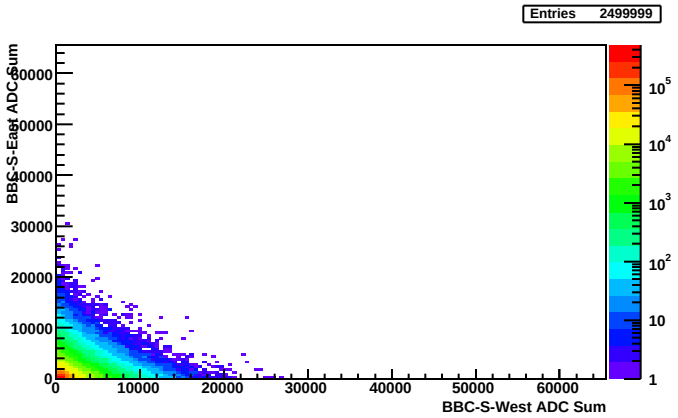

Entries 2499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

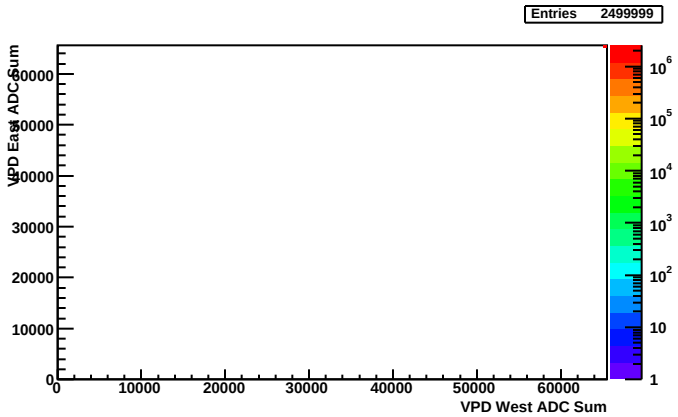

Entries 2499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

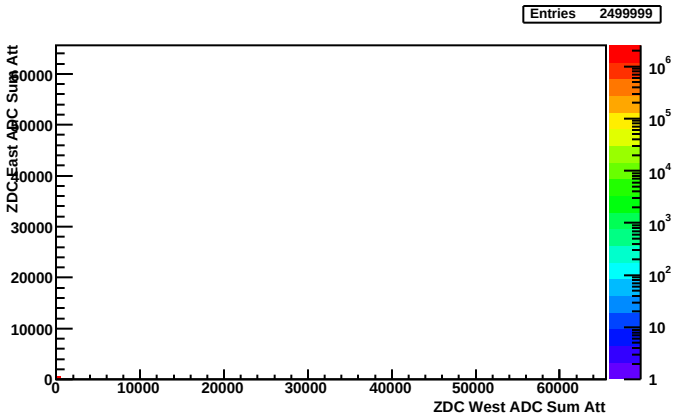

Entries 2499999
VPD-East ADC Sum
ZDC-East ADC Sum Att

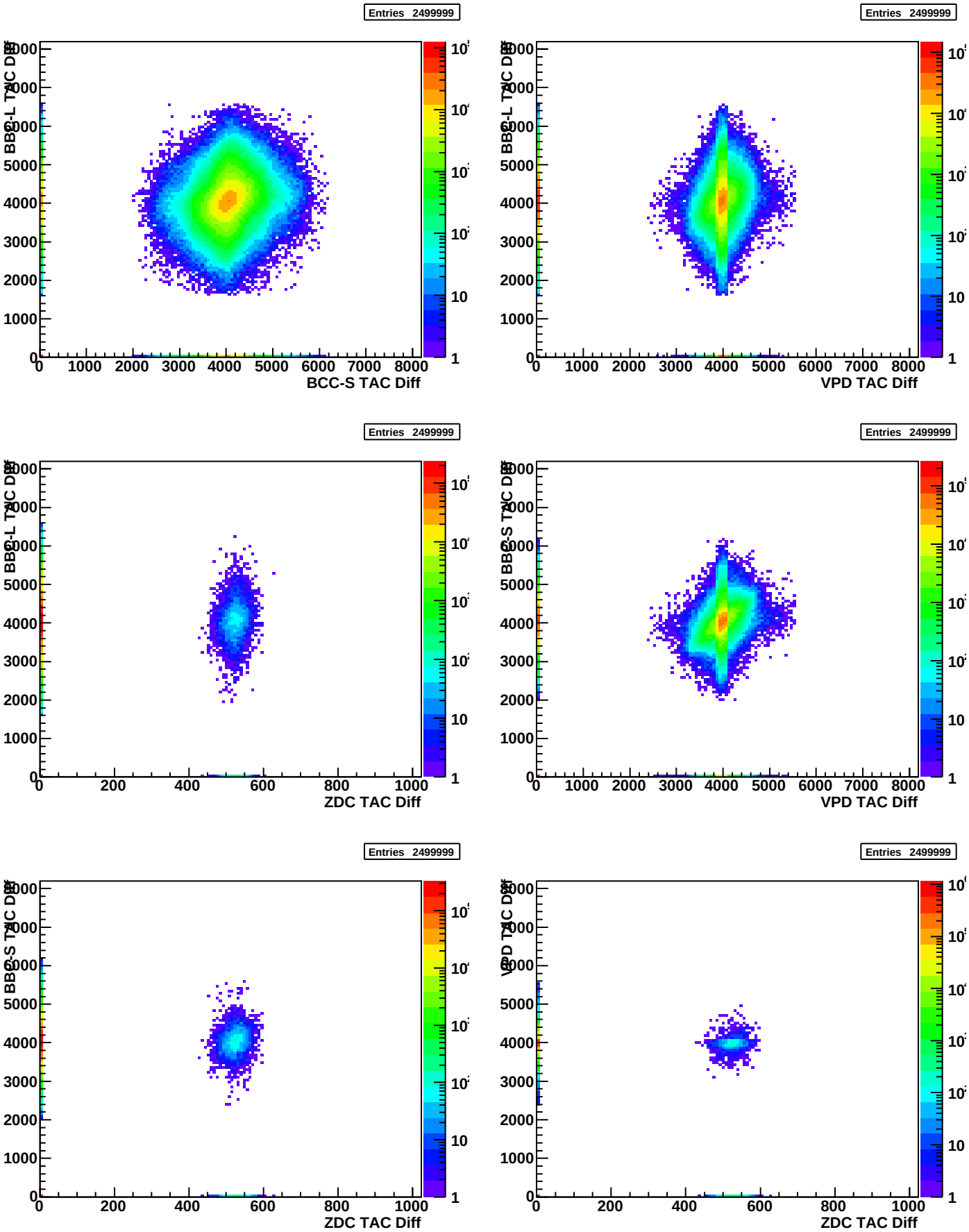

Entries 2499999
VPD-West ADC Sum
ZDC-West ADC Sum Att

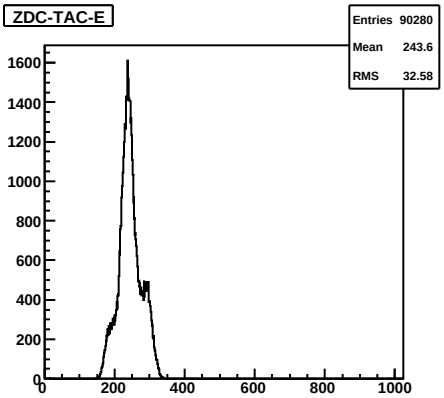

Entries 2499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

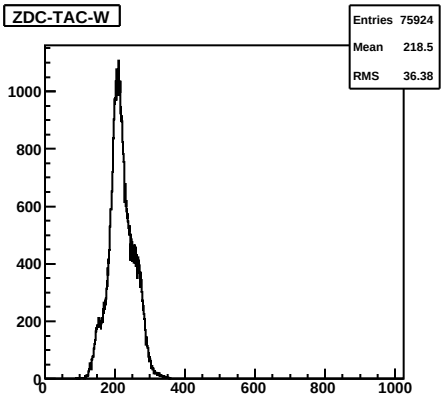

Entries 2499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

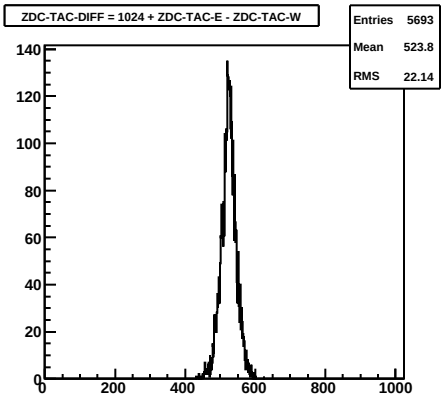

Entries 2499999
VPD East ADC Sum
VPD West ADC Sum

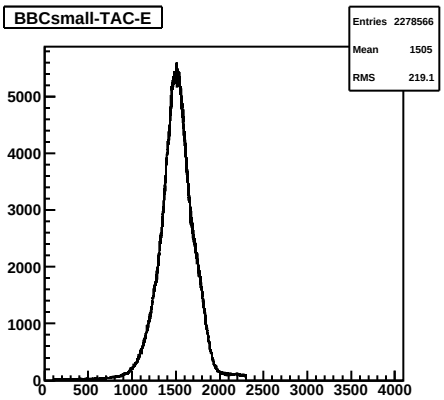

Entries 2499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

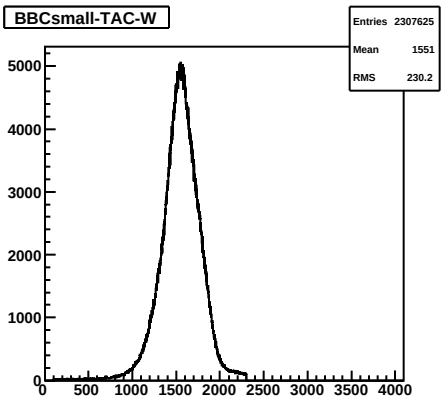

Entries 2499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2499999
BBC-L TAC Diff
VPD TAC Diff
Entries 2499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2499999
BBC-S TAC Diff
VPD TAC Diff
Entries 2499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2499999
VPD TAC Diff
ZDC TAC Diff

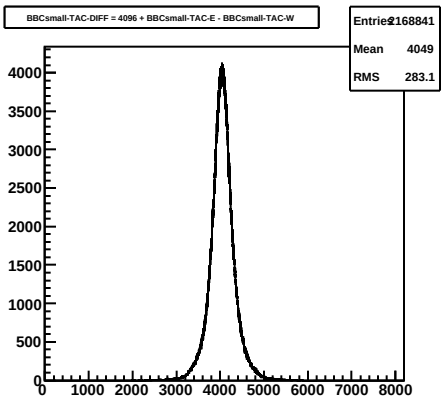

ZDC-TAC-E
Entries 90280
Mean 243.6
RMS 32.58

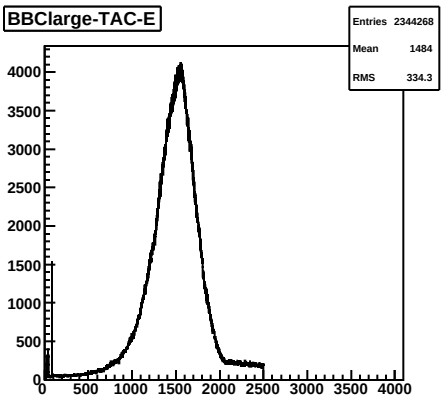

ZDC-TAC-W
Entries 75924
Mean 218.5
RMS 36.38

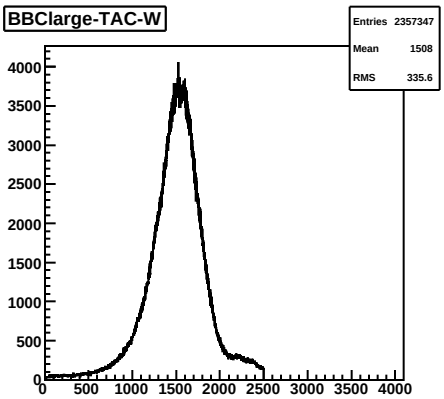

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5693
Mean 523.8
RMS 22.14

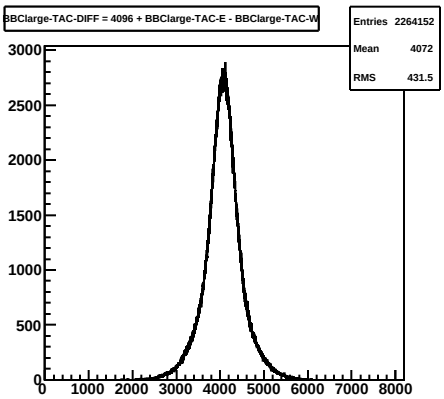

BBCsmall-TAC-E
Entries 2278566
Mean 1505
RMS 219.1

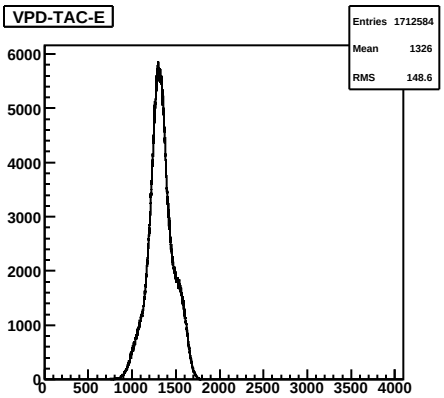

BBCsmall-TAC-W
Entries 2307625
Mean 1551
RMS 230.2

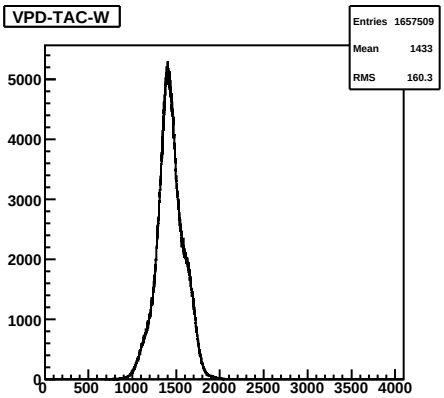

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2168841
Mean 4049
RMS 283.1

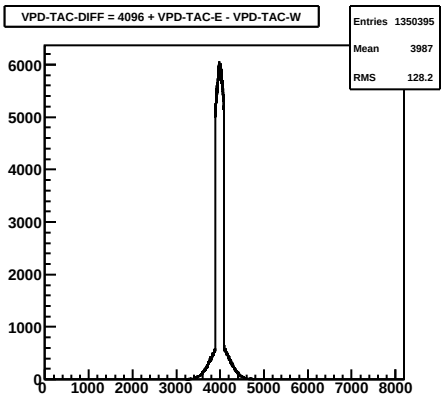

BBClarge-TAC-E
Entries 2344268
Mean 1484
RMS 334.3

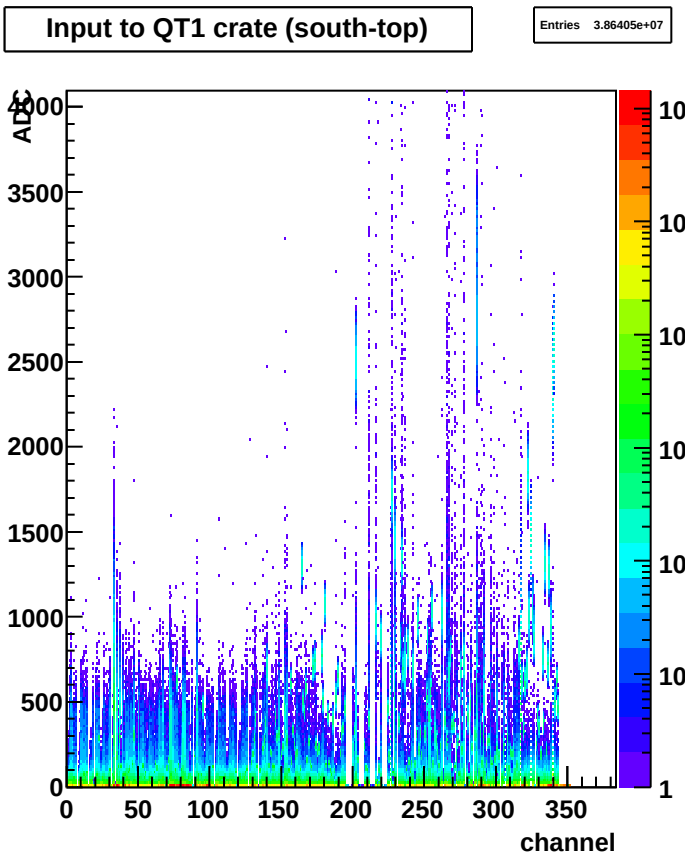

BBClarge-TAC-W
Entries 2357347
Mean 1508
RMS 335.6

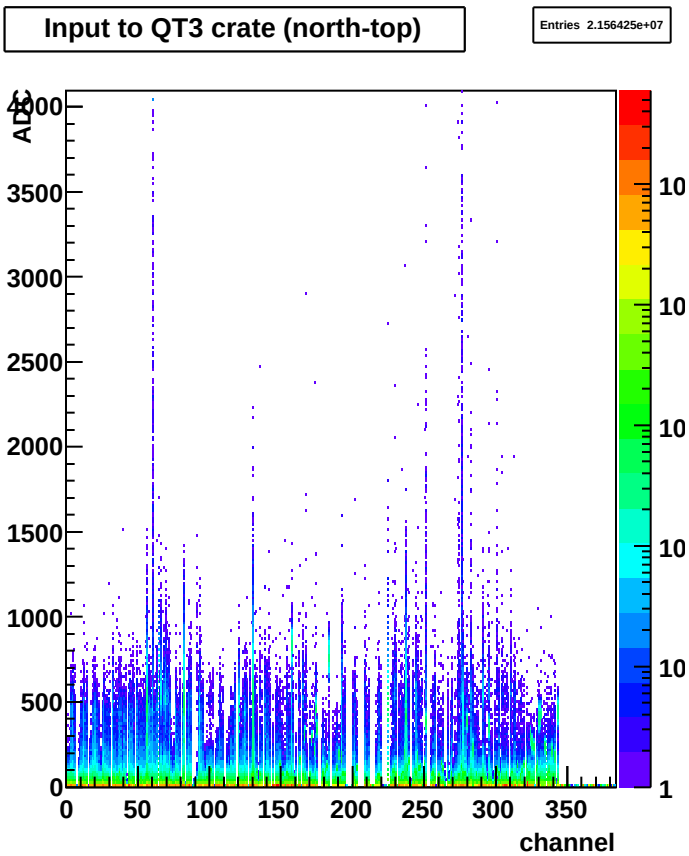

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2264152
Mean 4072
RMS 431.5

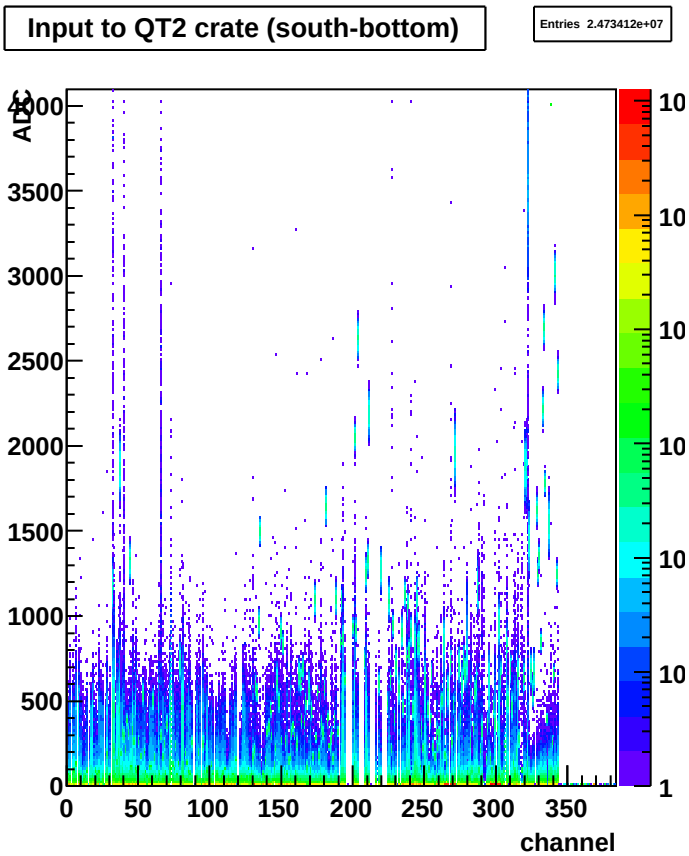

VPD-TAC-E
Entries 1712584
Mean 1326
RMS 148.6

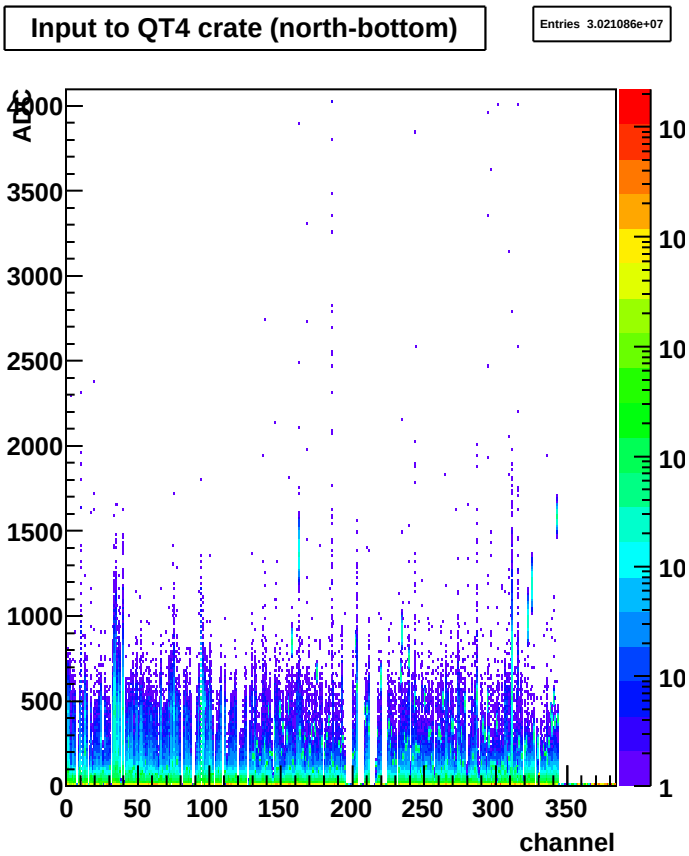

VPD-TAC-W
Entries 1657509
Mean 1433
RMS 160.3

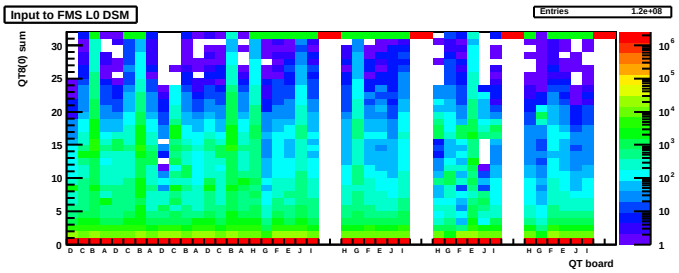

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1350395
Mean 3987
RMS 128.2

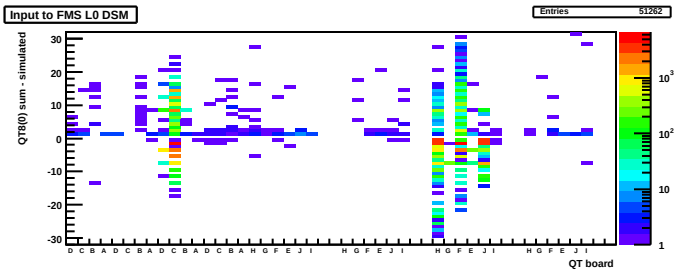

Input to QT1 crate (south-top)
Entries 3.86405e+07
ADC
channel

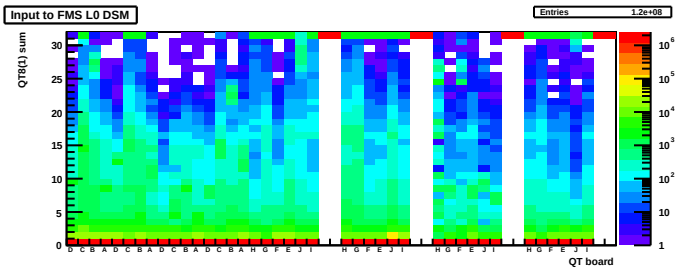

Input to QT3 crate (north-top)
Entries 2.156425e+07
ADC
channel

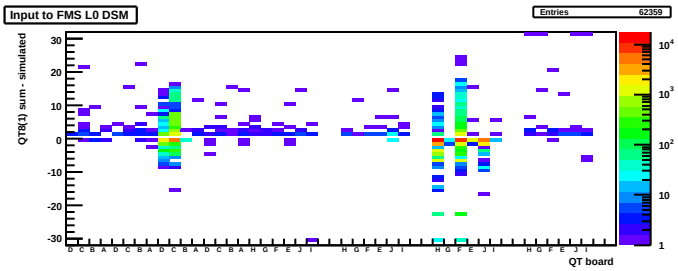

Input to QT2 crate (south-bottom)
Entries 2.473412e+07
ADC
channel

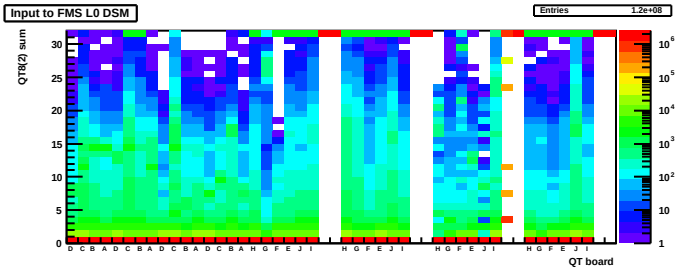

Input to QT4 crate (north-bottom)
Entries 3.021086e+07
ADC
channel

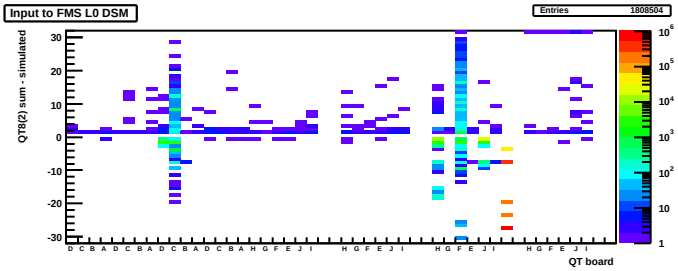

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

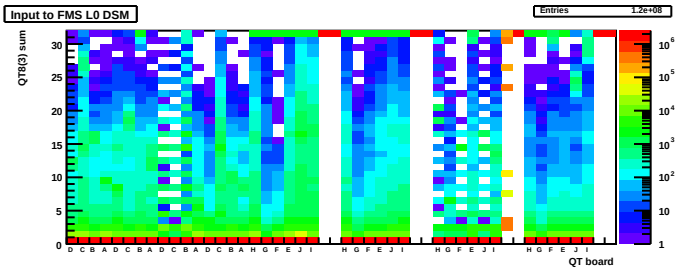

Input to FMS L0 DSM
Entries 51282
QT8(0) sum - simulated
QT board

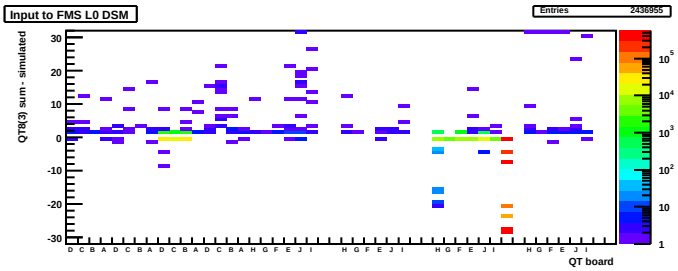

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

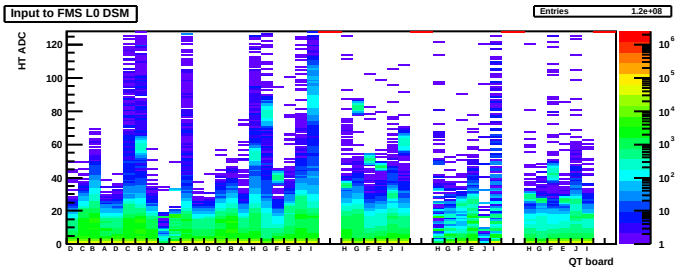

Input to FMS L0 DSM
Entries 62359
QT8(1) sum - simulated
QT board

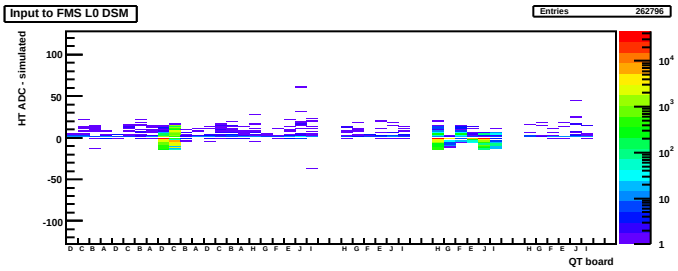

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

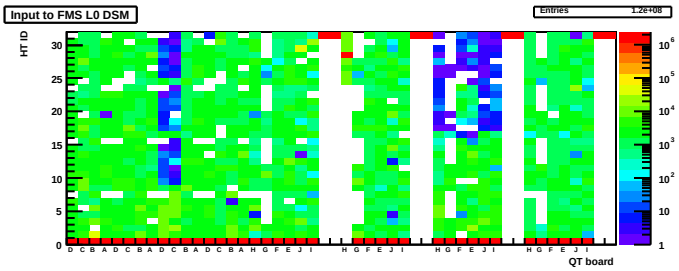

Input to FMS L0 DSM
Entries 1808504
QT8(2) sum - simulated
QT board

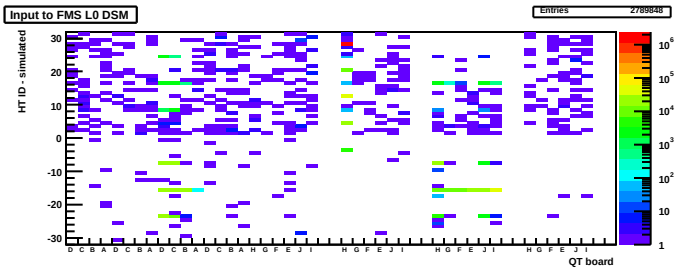

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

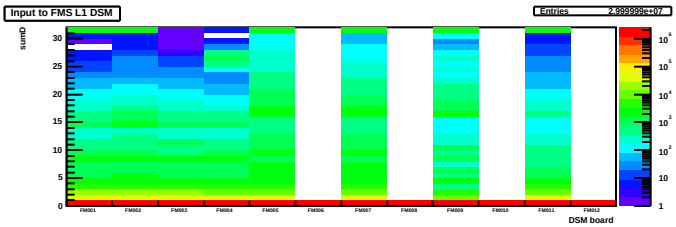

Input to FMS L0 DSM
Entries 2436955
QT8(3) sum - simulated
QT board

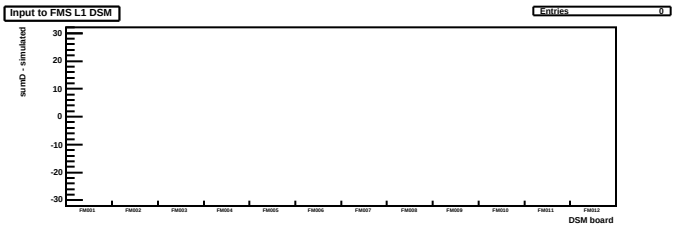

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

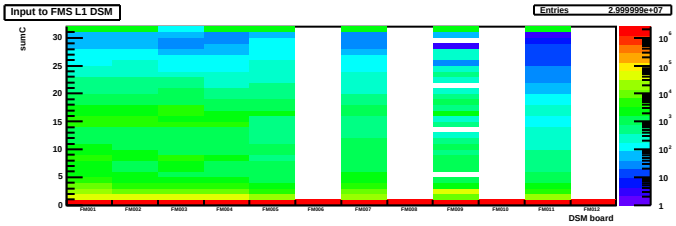

Input to FMS L0 DSM
Entries 262796
HT ADC - simulated
QT board

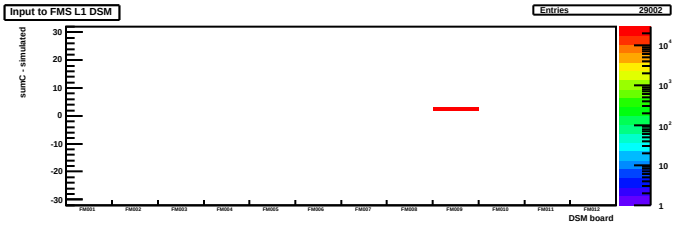

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

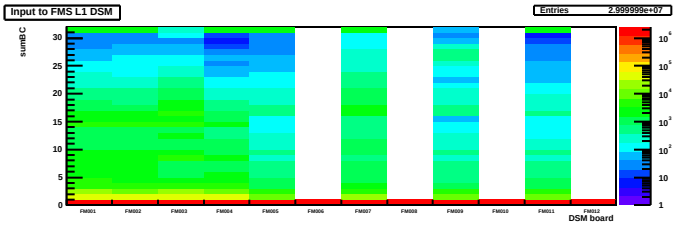

Input to FMS L0 DSM
Entries 2789848
HT ID - simulated
QT board

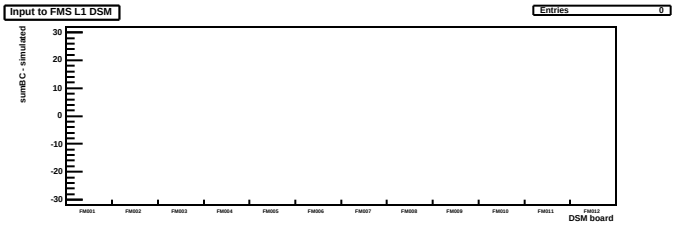

Input to FMS L1 DSM
Entries 2.999999e+07
sumD
DSM board

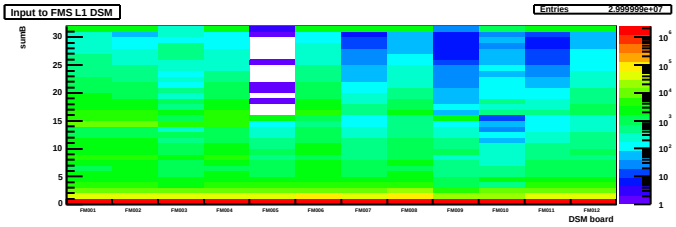

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

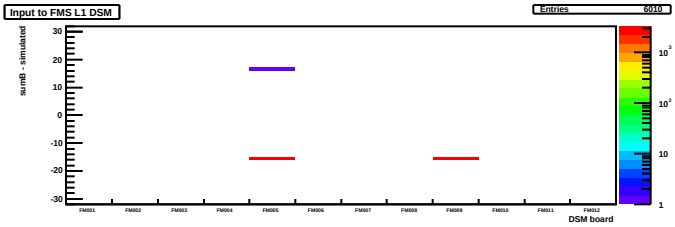

Input to FMS L1 DSM
Entries 2.999999e+07
sumC
DSM board

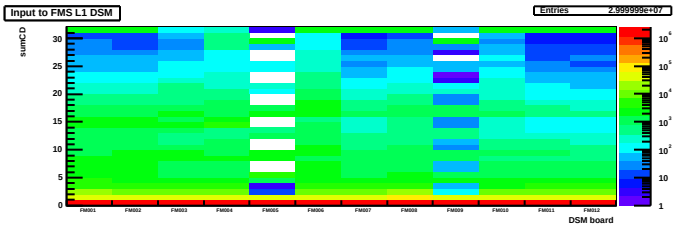

Input to FMS L1 DSM
Entries 29002
sumC - simulated
DSM board

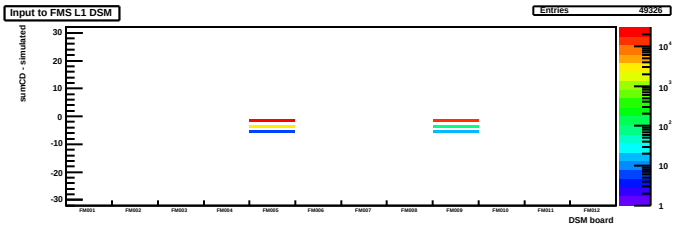

Input to FMS L1 DSM
Entries 2.999999e+07
sumBC
DSM board

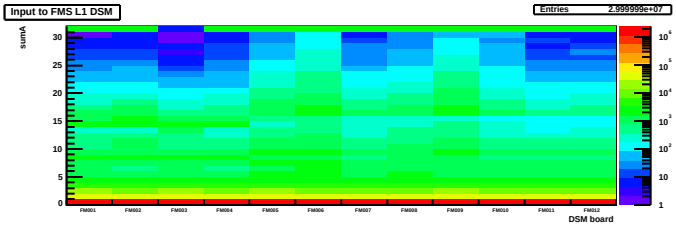

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

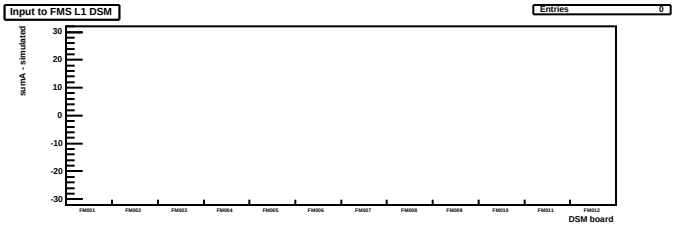

Input to FMS L1 DSM
Entries 2.999999e+07
sumB
DSM board

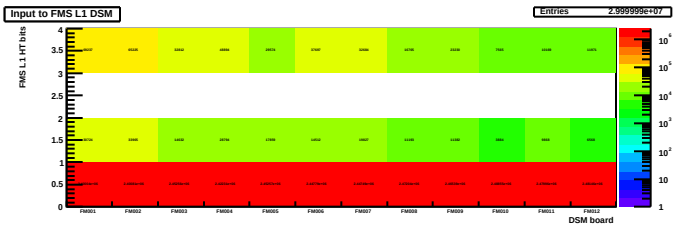

Input to FMS L1 DSM
Entries 6010
sumB - simulated
DSM board

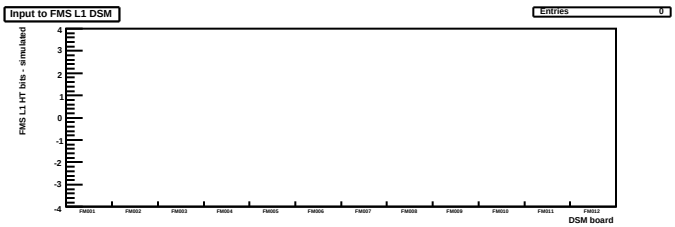

Input to FMS L1 DSM
Entries 2.999999e+07
sumCD
DSM board

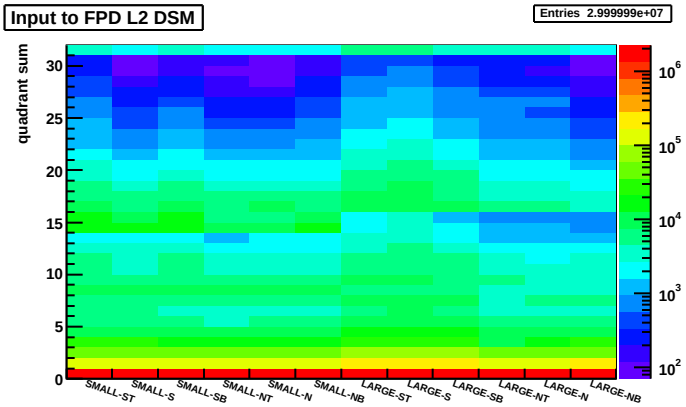

Input to FMS L1 DSM
Entries 49326
sumCD - simulated
DSM board

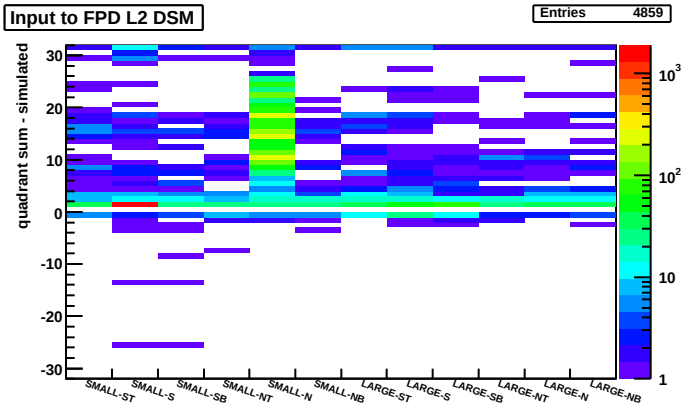

Input to FMS L1 DSM
Entries 2.999999e+07
sumA
DSM board

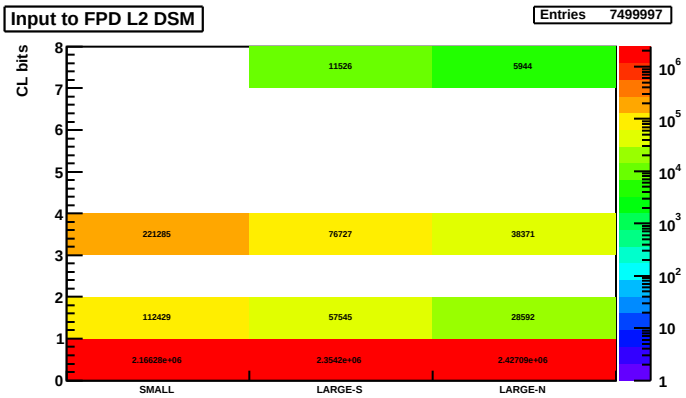

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

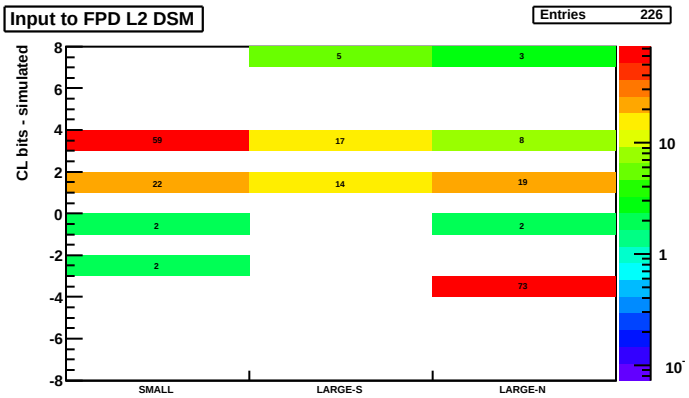

Input to FMS L1 DSM
Entries 2.999999e+07
FMS L1 HT bits
DSM board

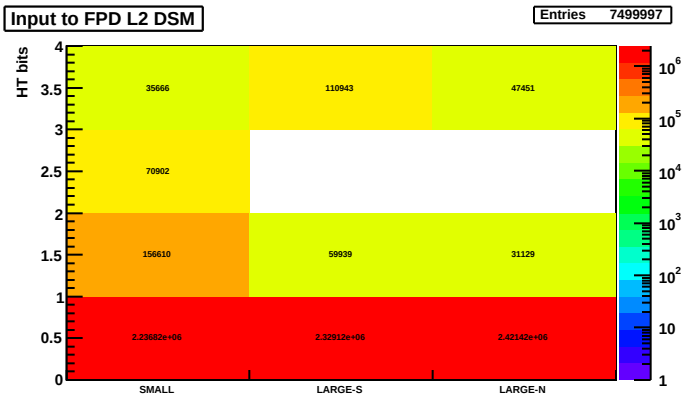

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

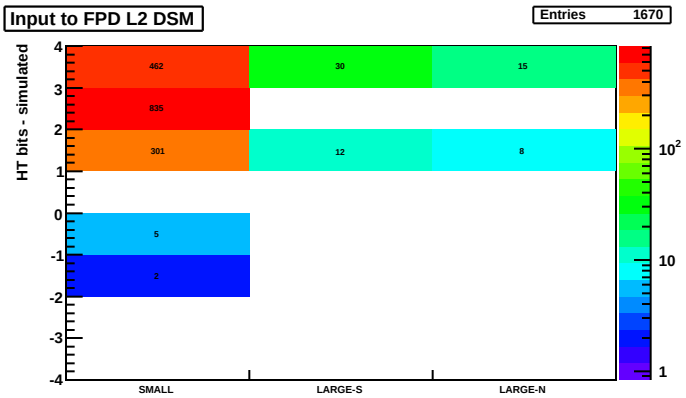

Input to FPD L2 DSM
Entries 2.999999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

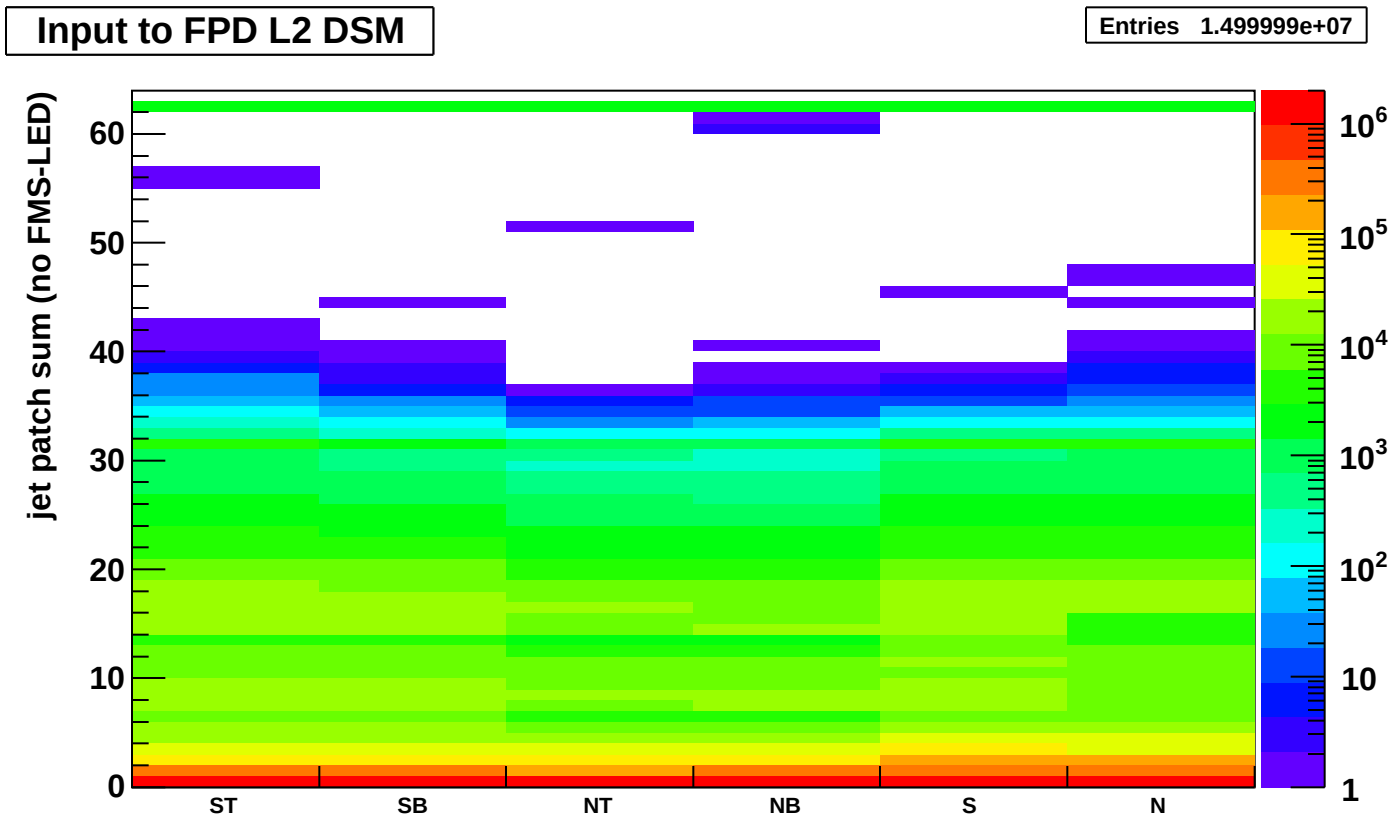

Input to FPD L2 DSM
Entries 4859
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

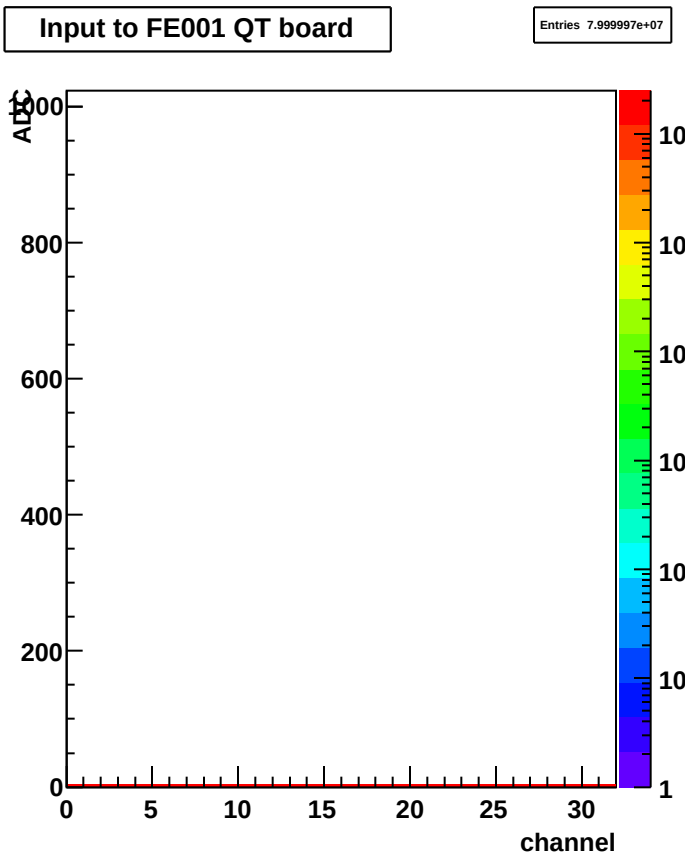

Input to FPD L2 DSM
Entries 7499997
CL bits
11526
5944
221285
76727
38371
112429
57545
28592
2.16628e+06
2.3542e+06
2.42709e+06
SMALL
LARGE-S
LARGE-N

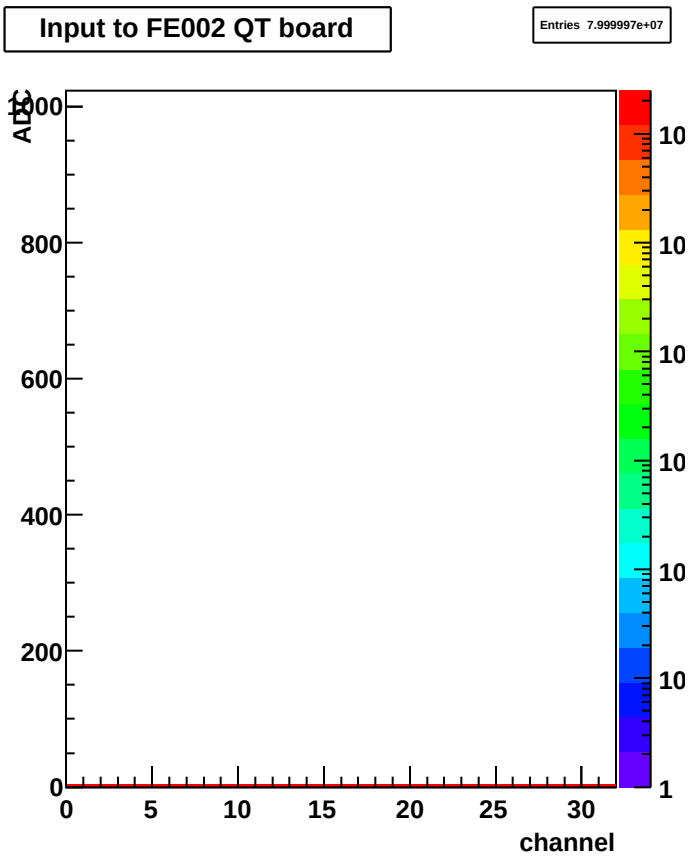

Input to FPD L2 DSM
Entries 226
CL bits - simulated
5
3
59
17
8
22
14
19
2
2
2
73
SMALL
LARGE-S
LARGE-N

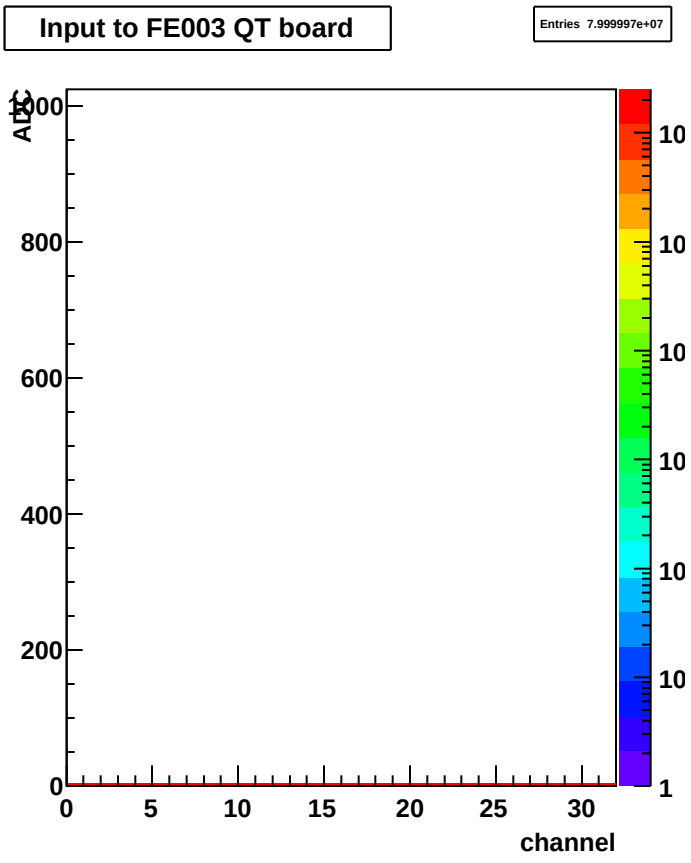

Input to FPD L2 DSM
Entries 7499997
HT bits
35666
110943
47451
70902
156610
59939
31129
2.23682e+06
2.32912e+06
2.42142e+06
SMALL
LARGE-S
LARGE-N

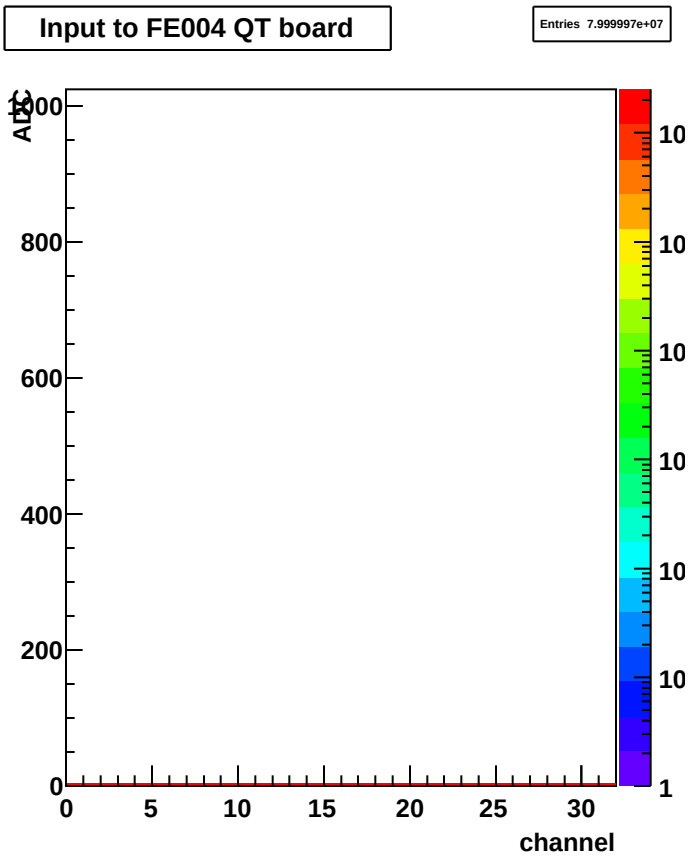

Input to FPD L2 DSM
Entries 1670
HT bits - simulated
462
30
15
835
301
12
8
5
2
SMALL
LARGE-S
LARGE-N

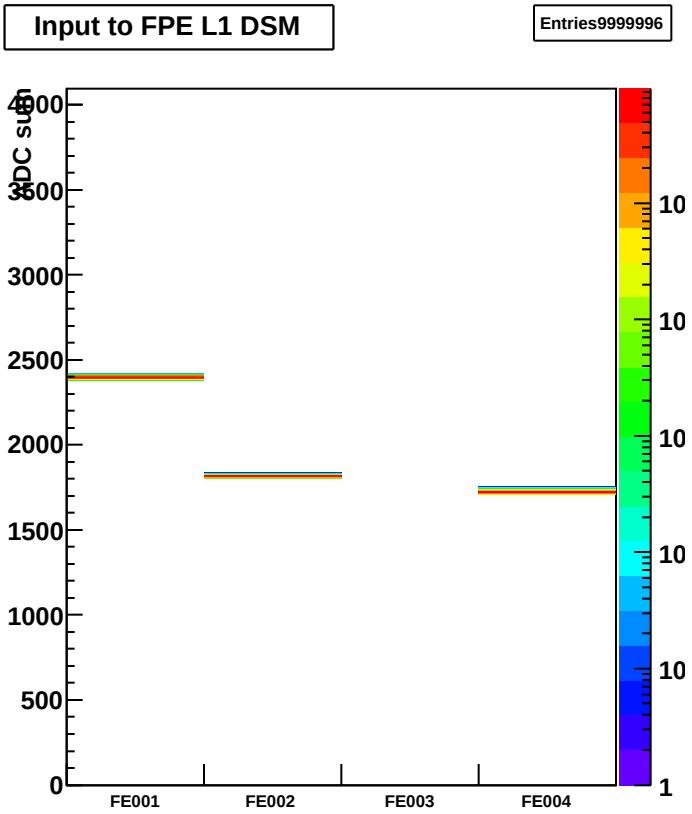

Input to FPD L2 DSM
Entries 1.499999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.999997e+07
ADC
channel

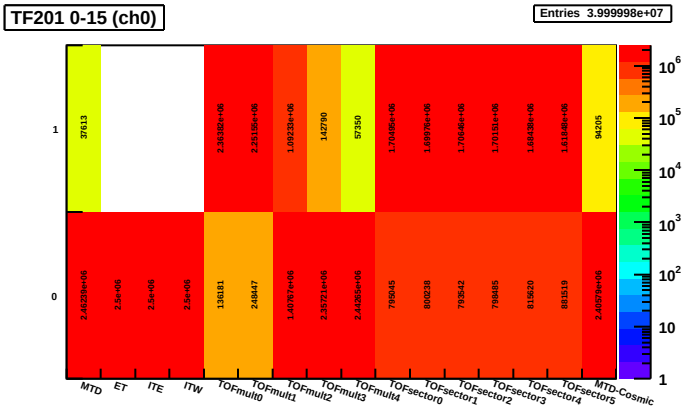

Input to FE002 QT board
Entries 7.999997e+07
ADC
channel


Input to FE003 QT board
Entries 7.999997e+07
ADC
channel


Input to FE004 QT board
Entries 7.999997e+07
ADC
channel


Input to FPE L1 DSM
Entries9999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

TF201 0-15 (ch0)



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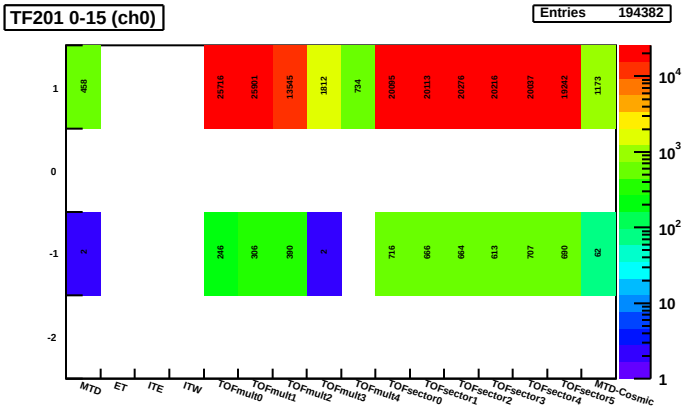

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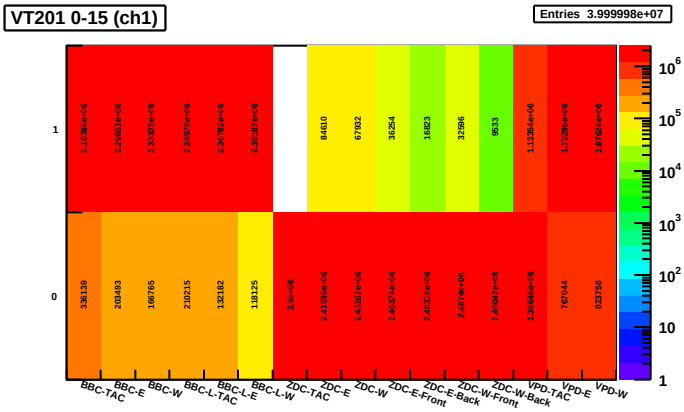

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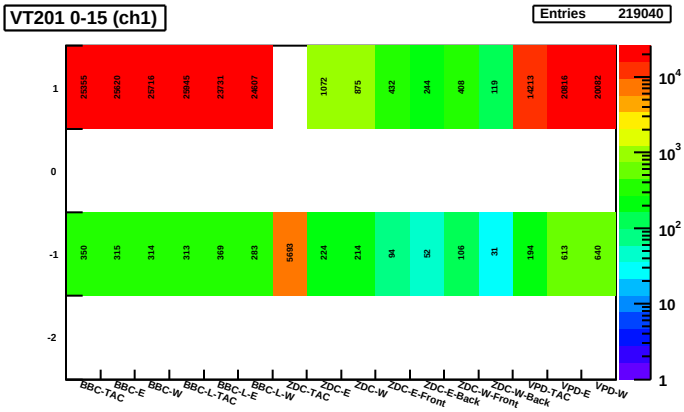

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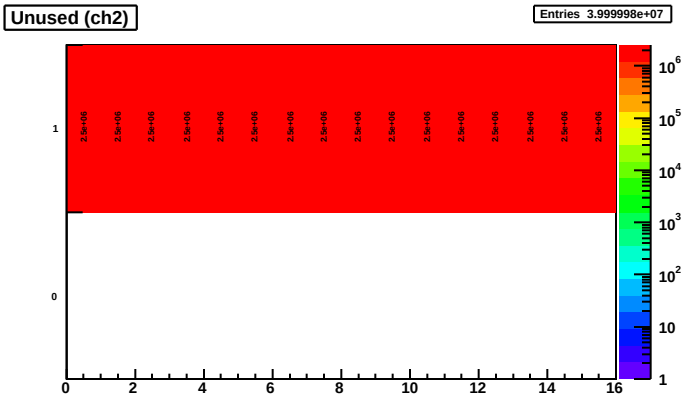

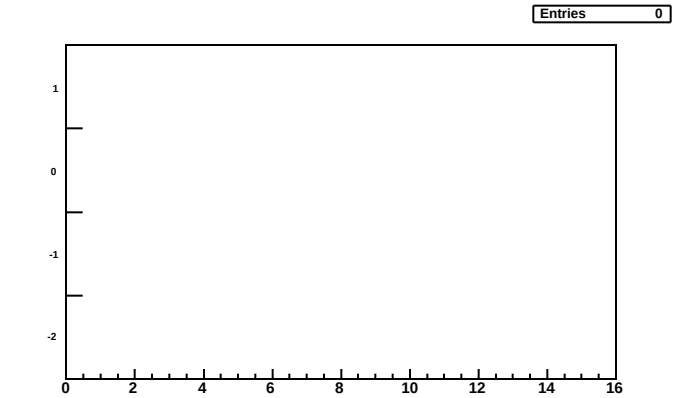

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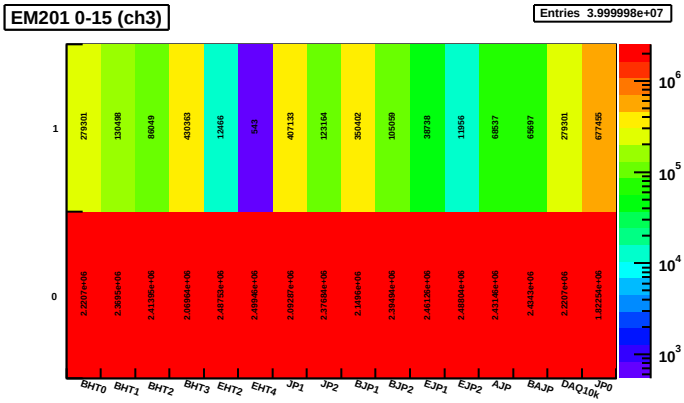

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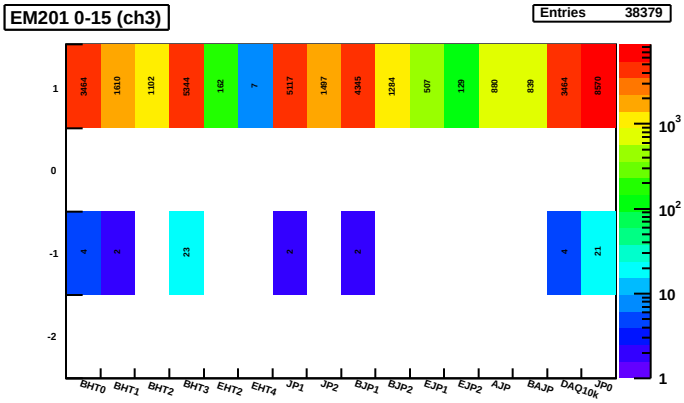
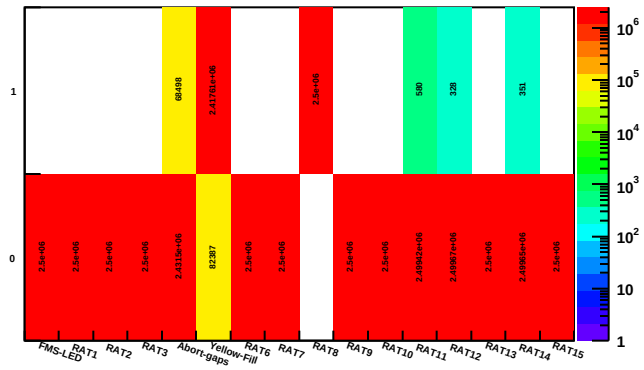
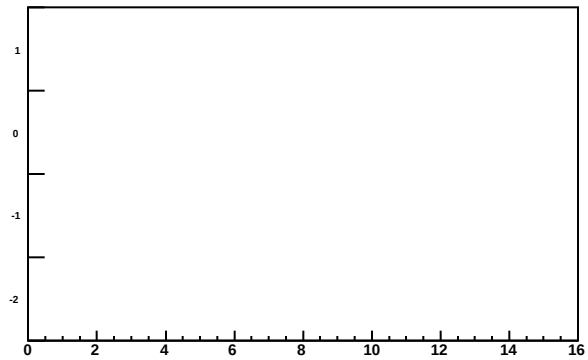


RAT board (ch4)





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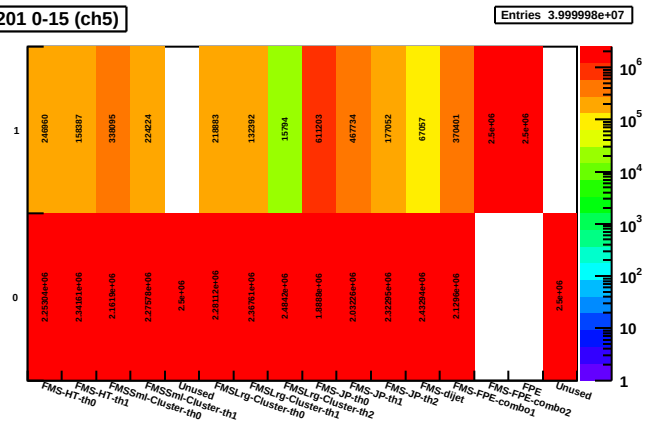
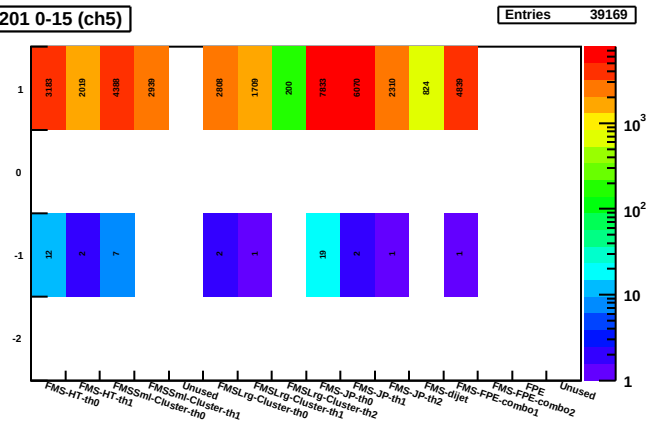


FP201 0-15 (ch5)



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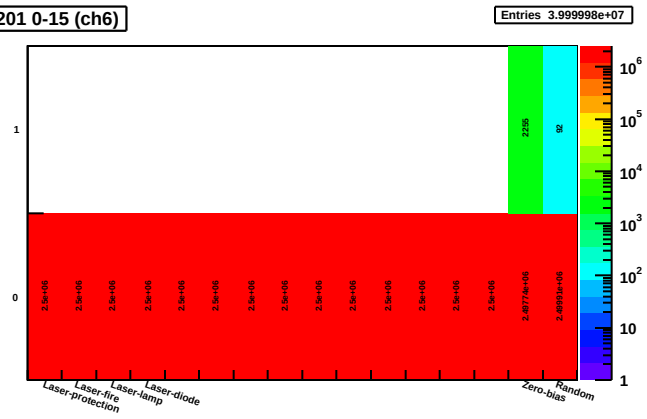

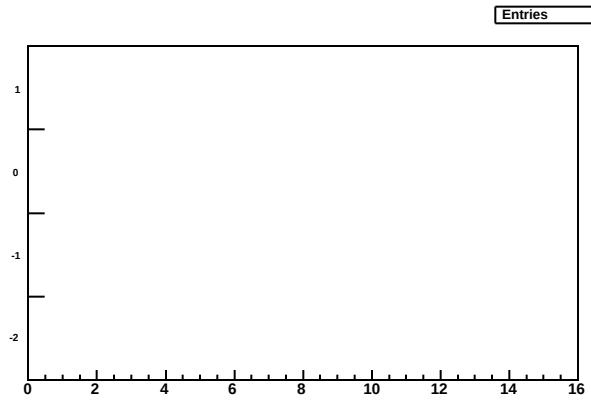
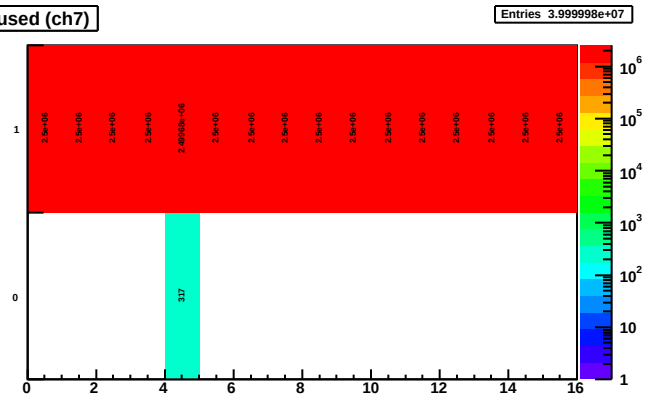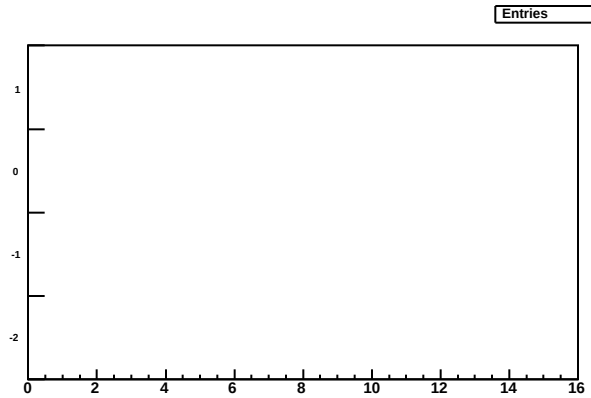
Unused (ch7)