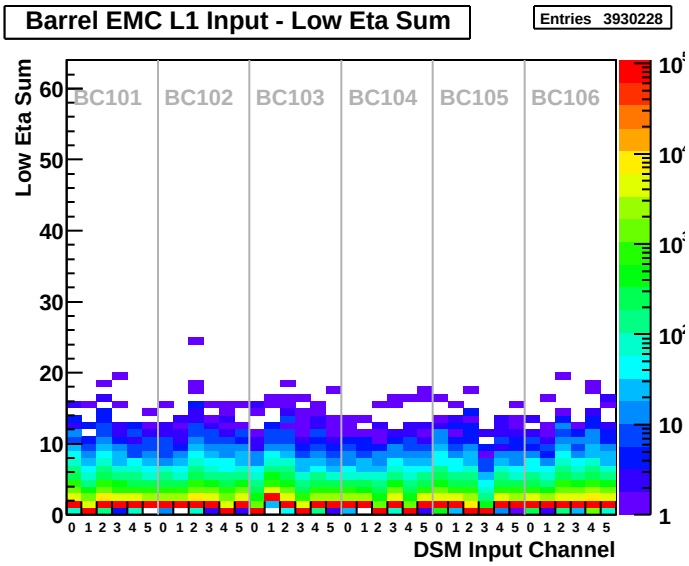
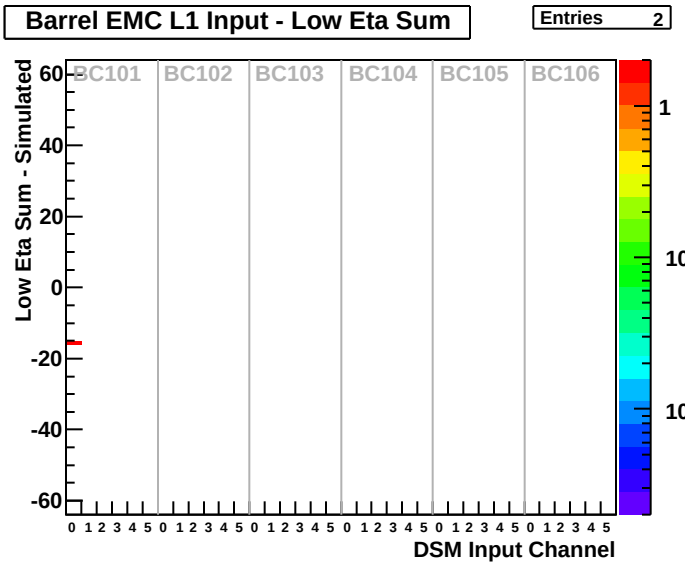
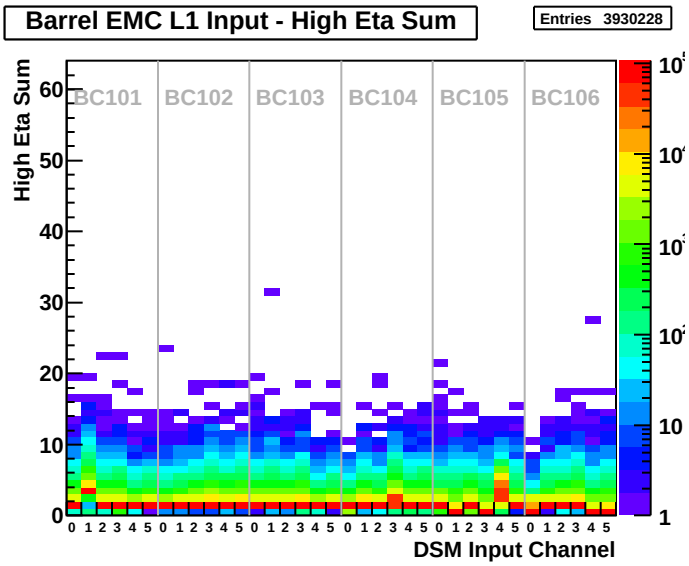
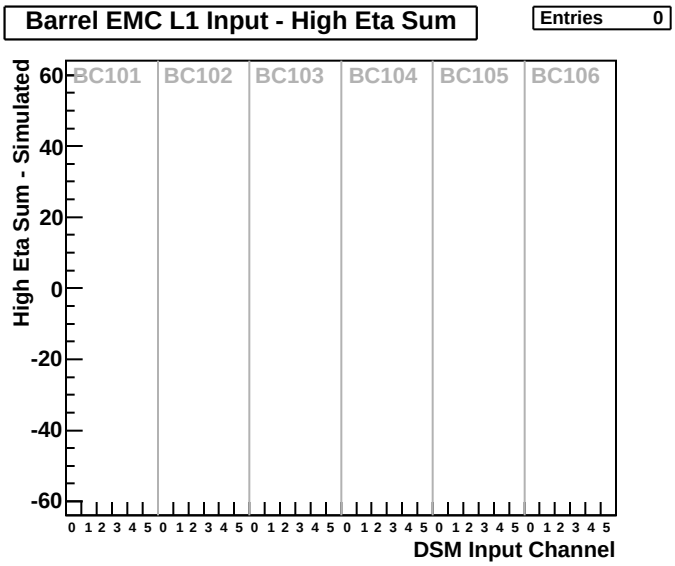
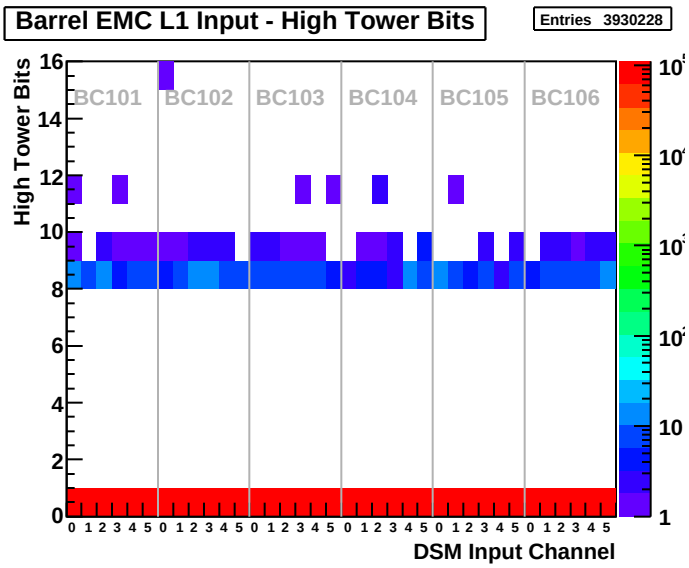
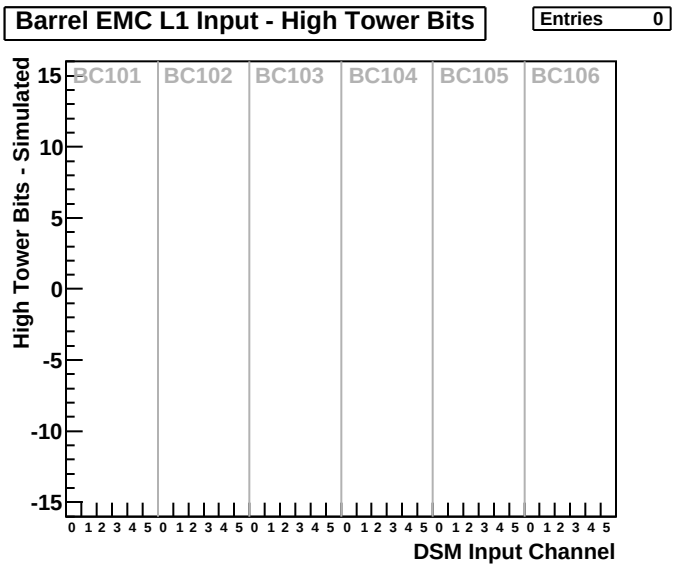

Barrel EMC L1 Input - Low Eta Sum
Entries 3930228
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

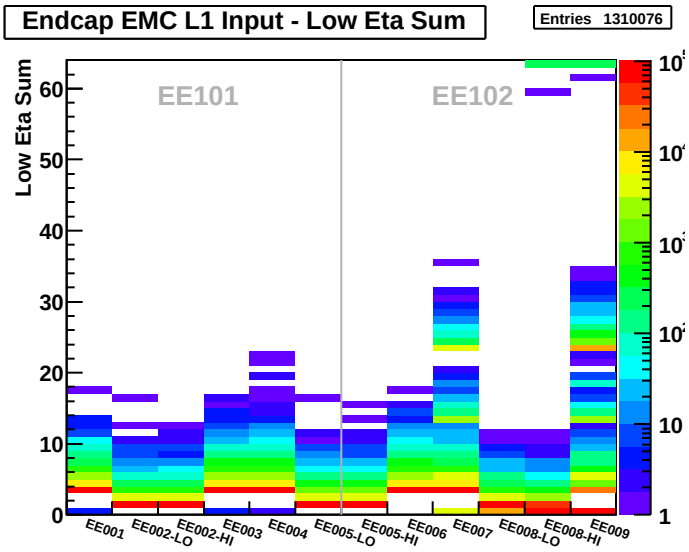

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

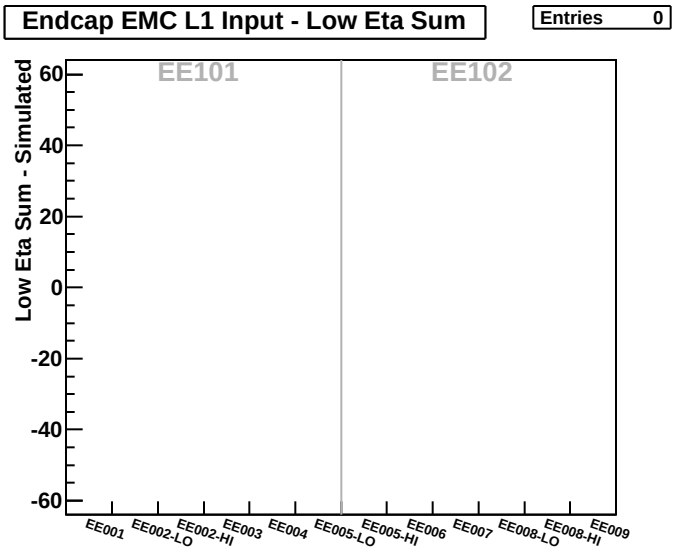

Barrel EMC L1 Input - High Eta Sum
Entries 3930228
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

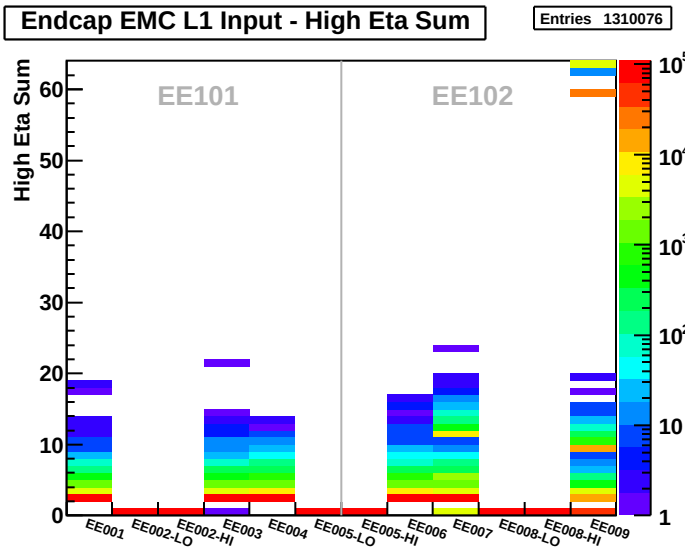

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

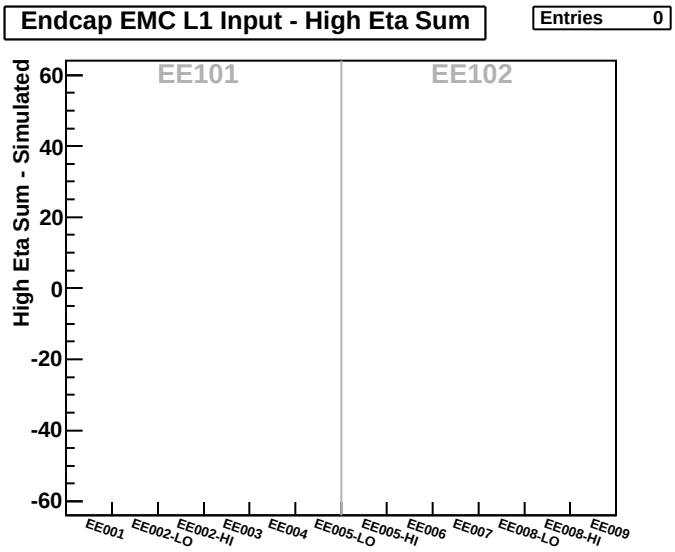

Barrel EMC L1 Input - High Tower Bits
Entries 3930228
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

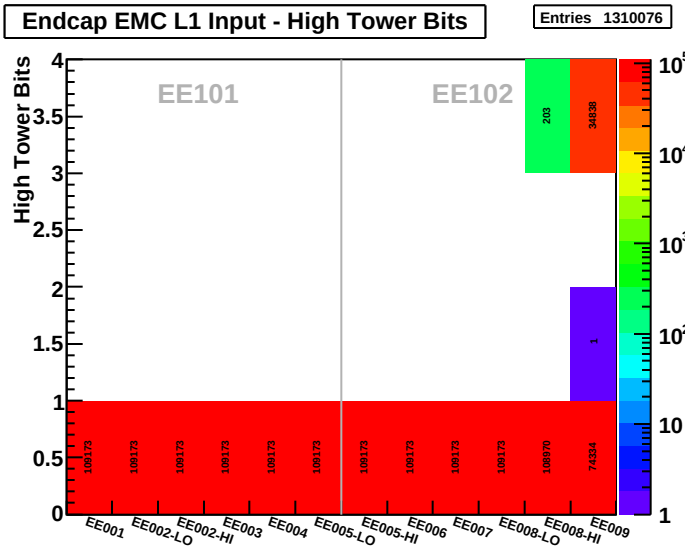

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

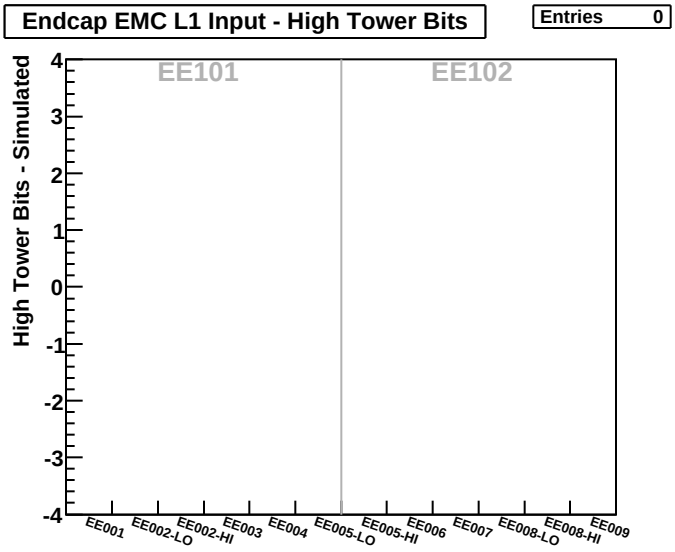

Endcap EMC L1 Input - Low Eta Sum
Entries 1310076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

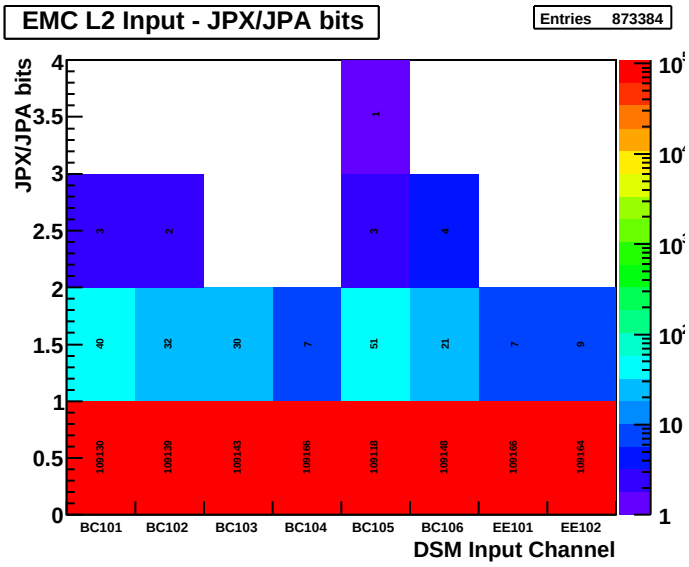

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

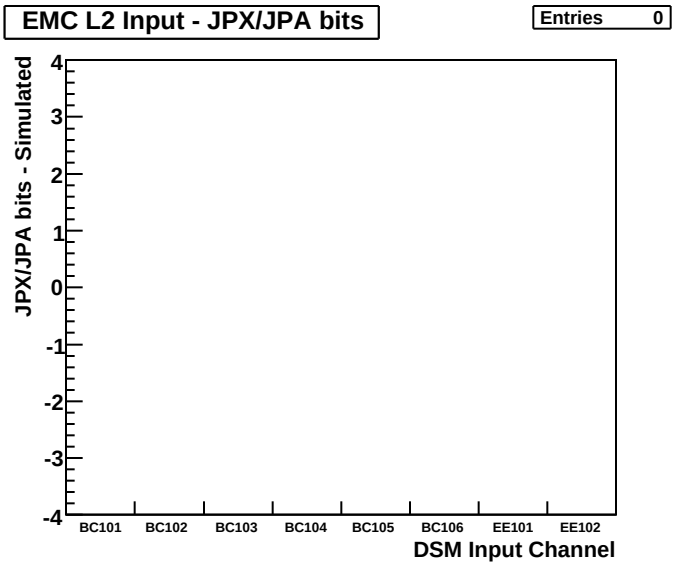

Endcap EMC L1 Input - High Eta Sum
Entries 1310076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

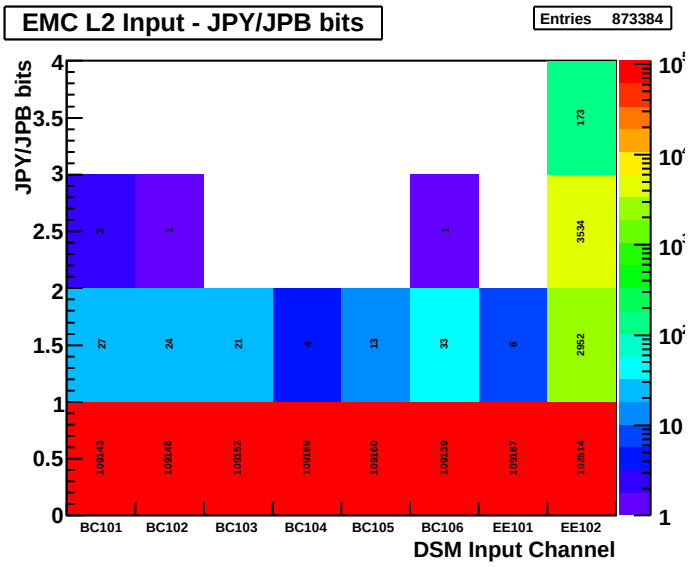

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

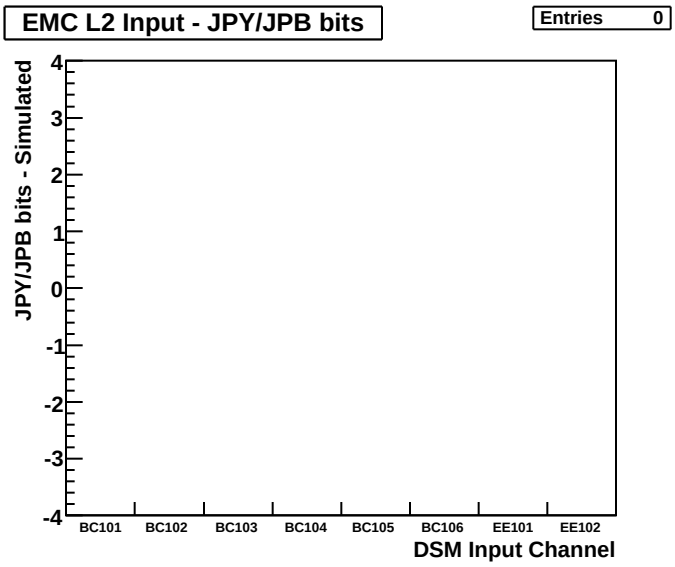

Endcap EMC L1 Input - High Tower Bits
Entries 1310076
High Tower Bits
EE101
EE102
203
34838
1
109173
109173
109173
109173
109173
109173
109173
109173
109173
109173
108970
74334
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

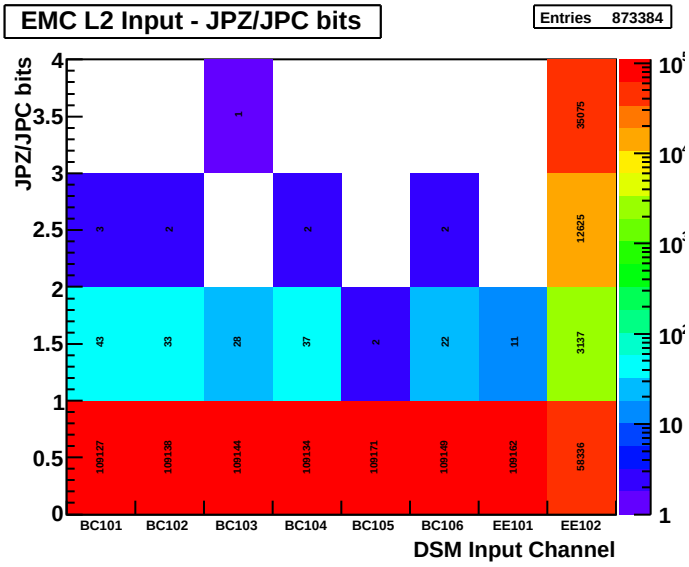

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

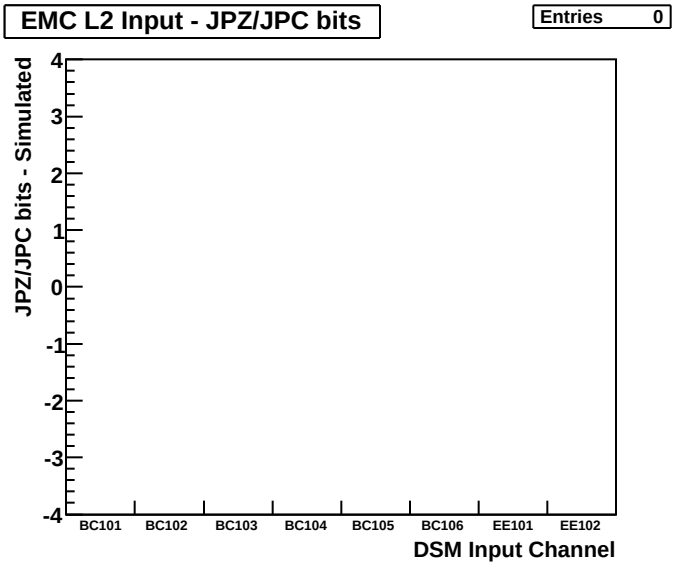

EMC L2 Input - JPX/JPA bits
Entries 873384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

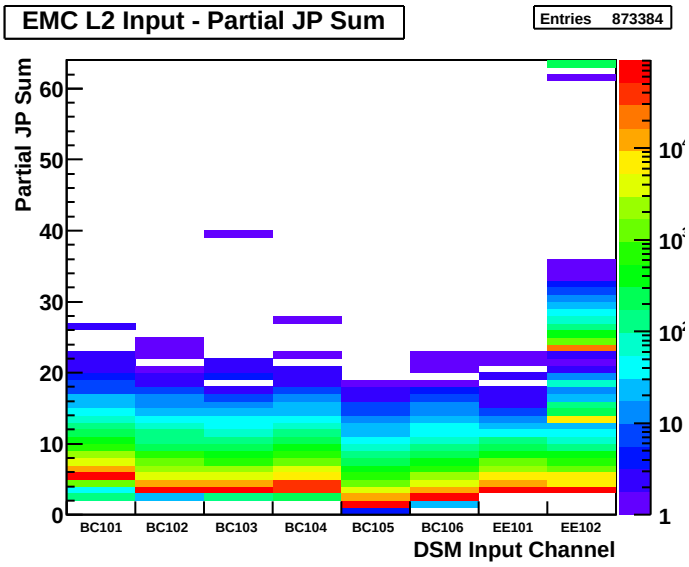

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

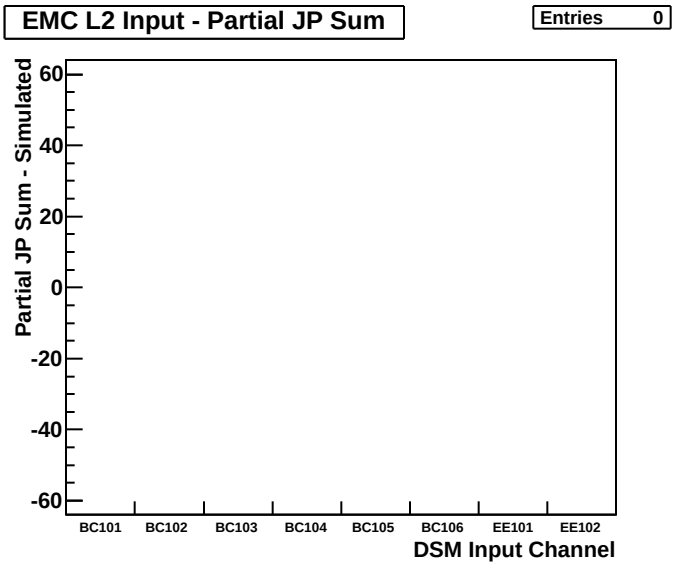

EMC L2 Input - JPY/JPB bits
Entries 873384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

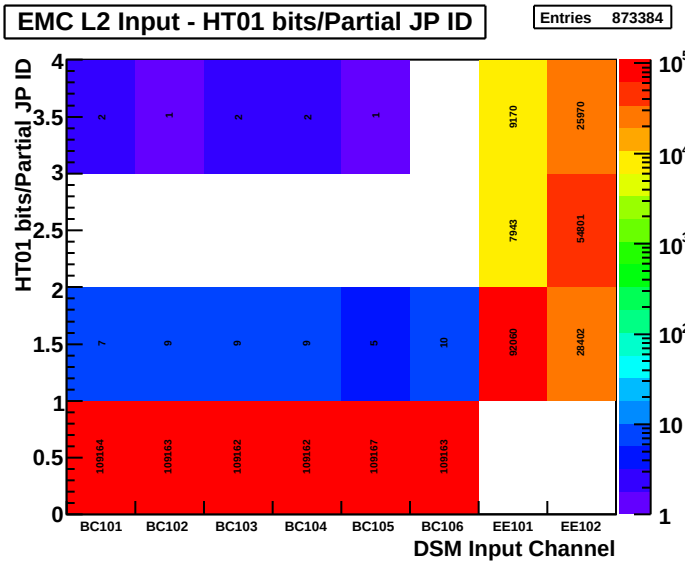

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

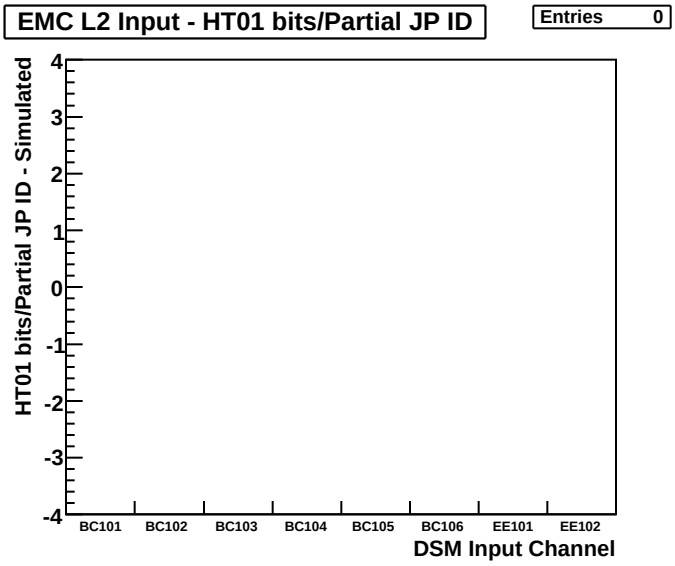

EMC L2 Input - JPZ/JPC bits
Entries 873384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

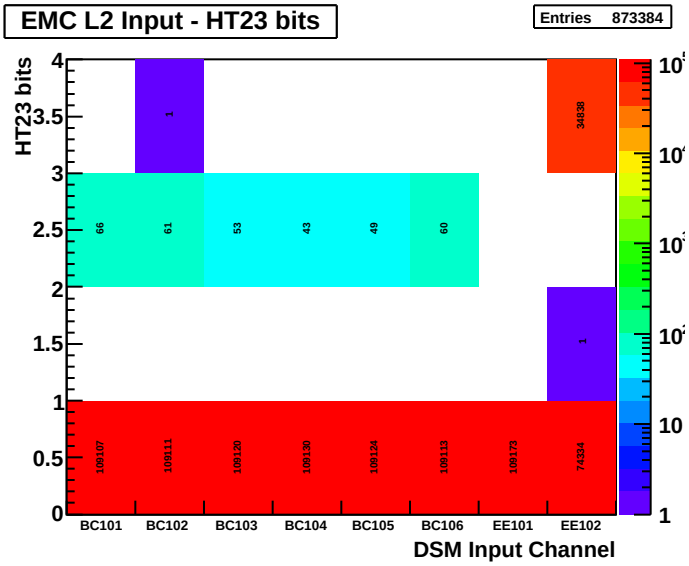

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

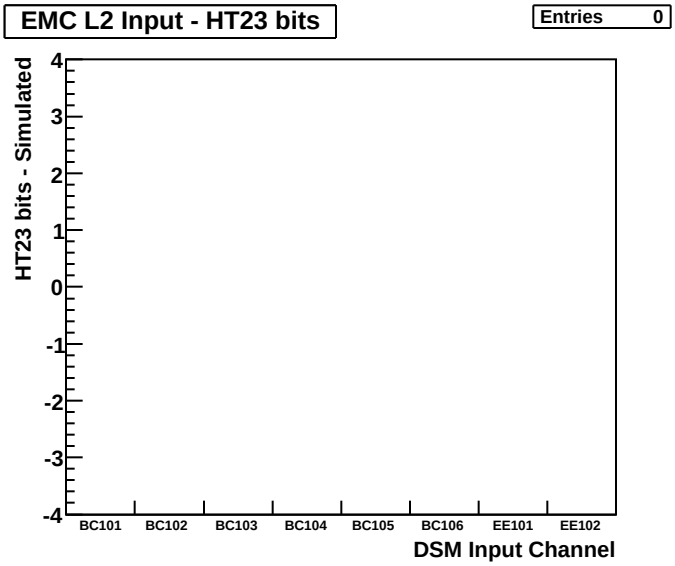

EMC L2 Input - Partial JP Sum
Entries 873384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

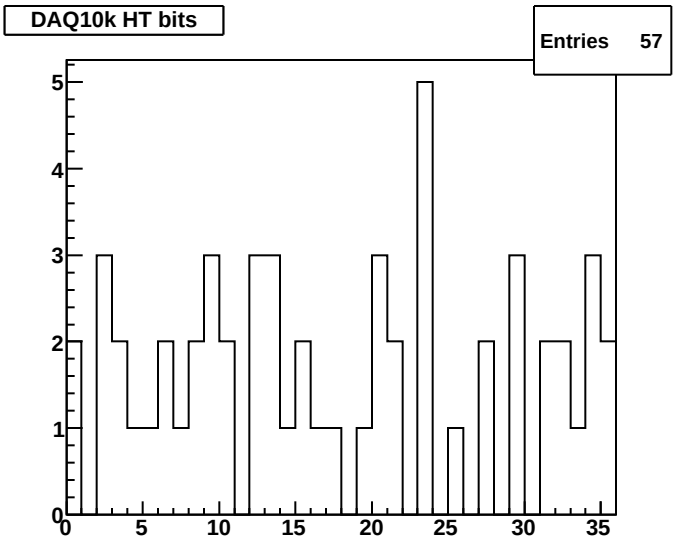

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

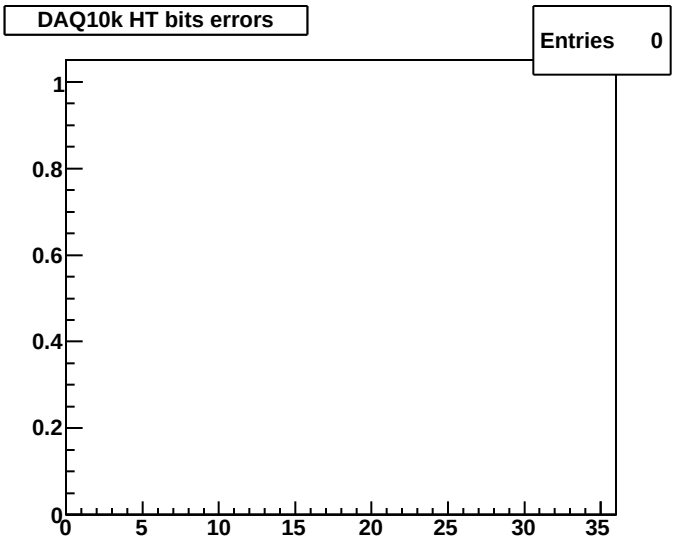

EMC L2 Input - HT01 bits/Partial JP ID
Entries 873384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

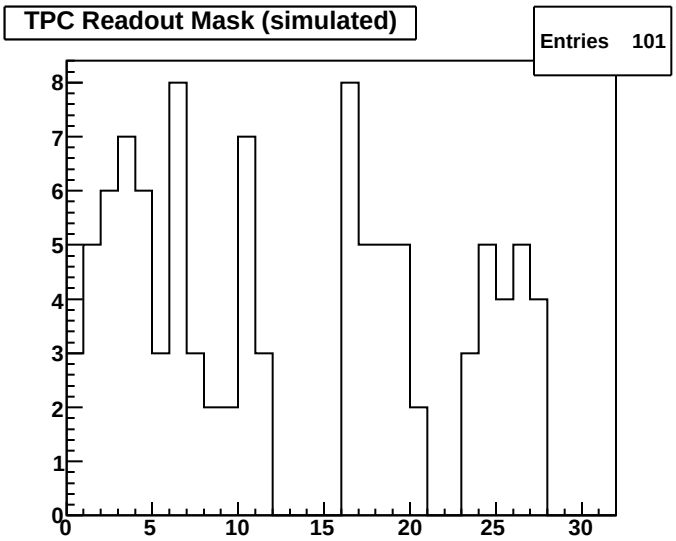

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

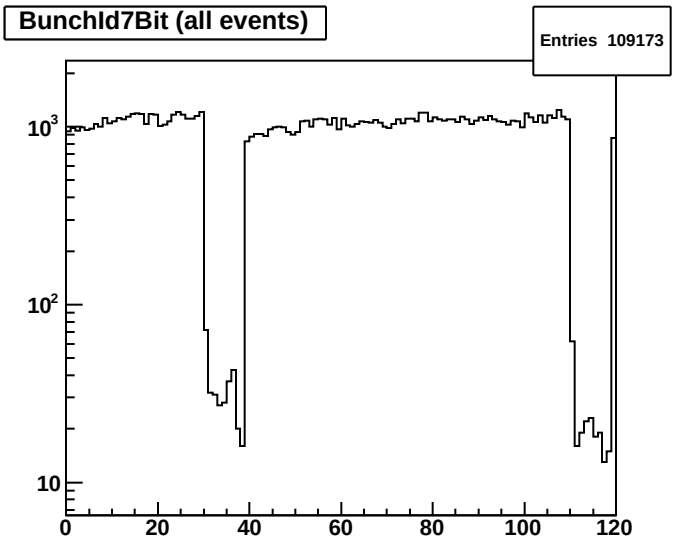

EMC L2 Input - HT23 bits
Entries 873384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

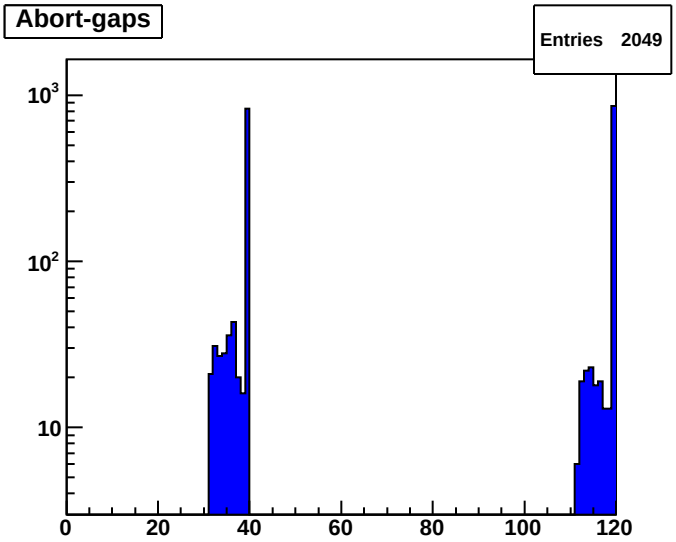

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

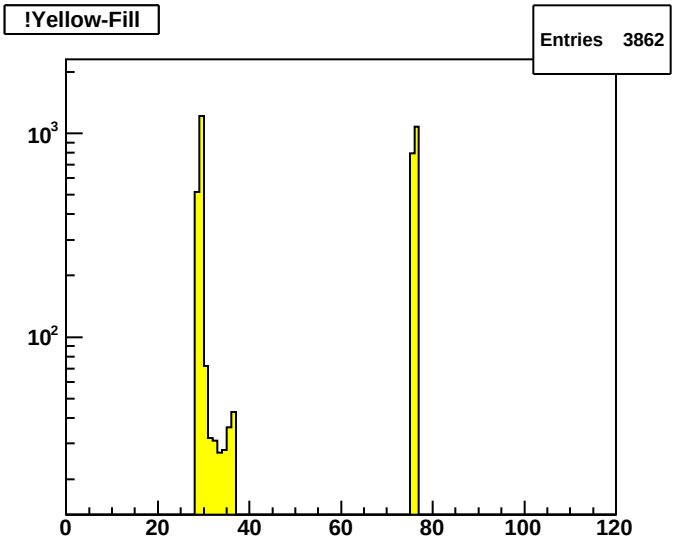

DAQ10k HT bits
Entries 57

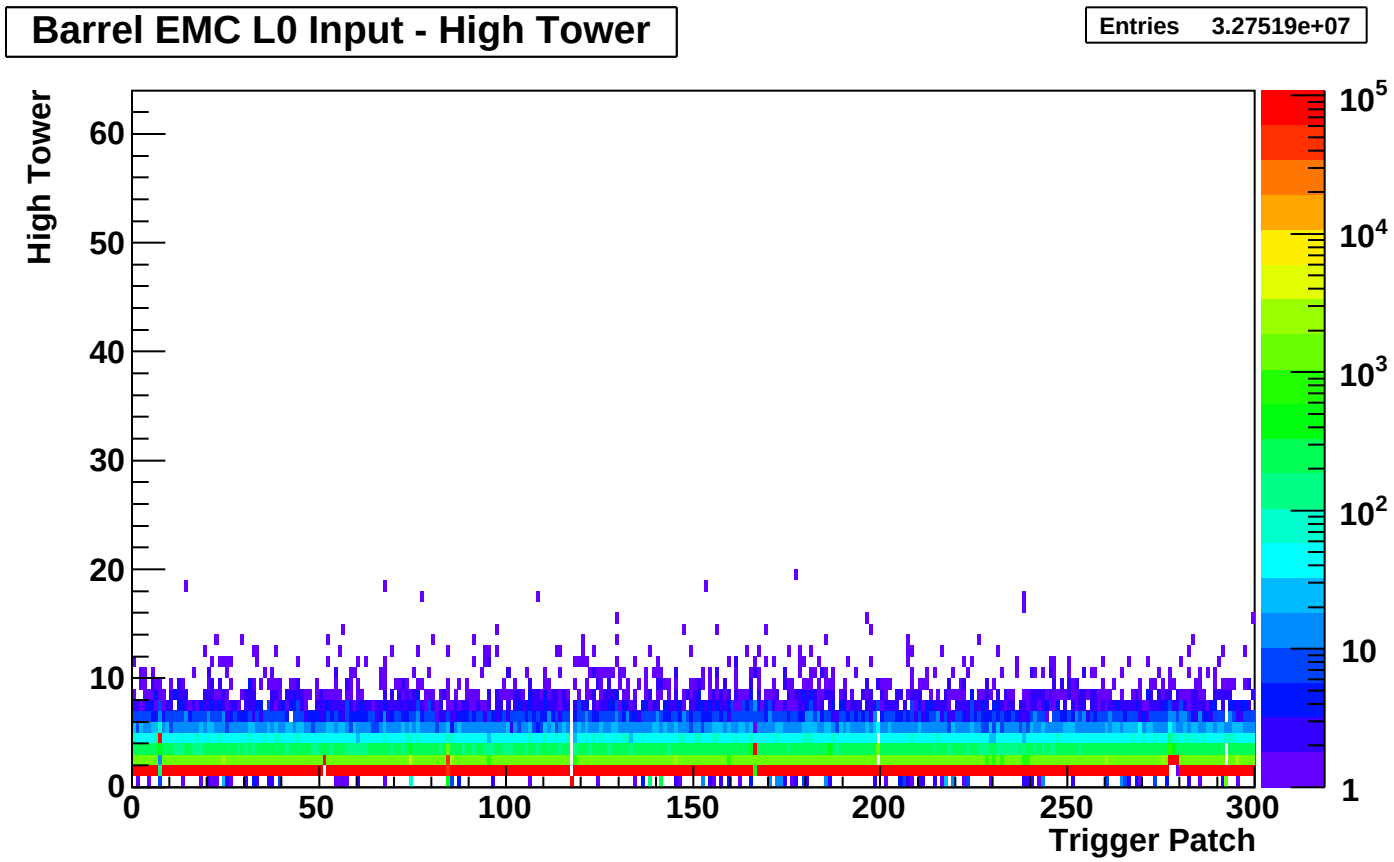

DAQ10k HT bits errors
Entries 0

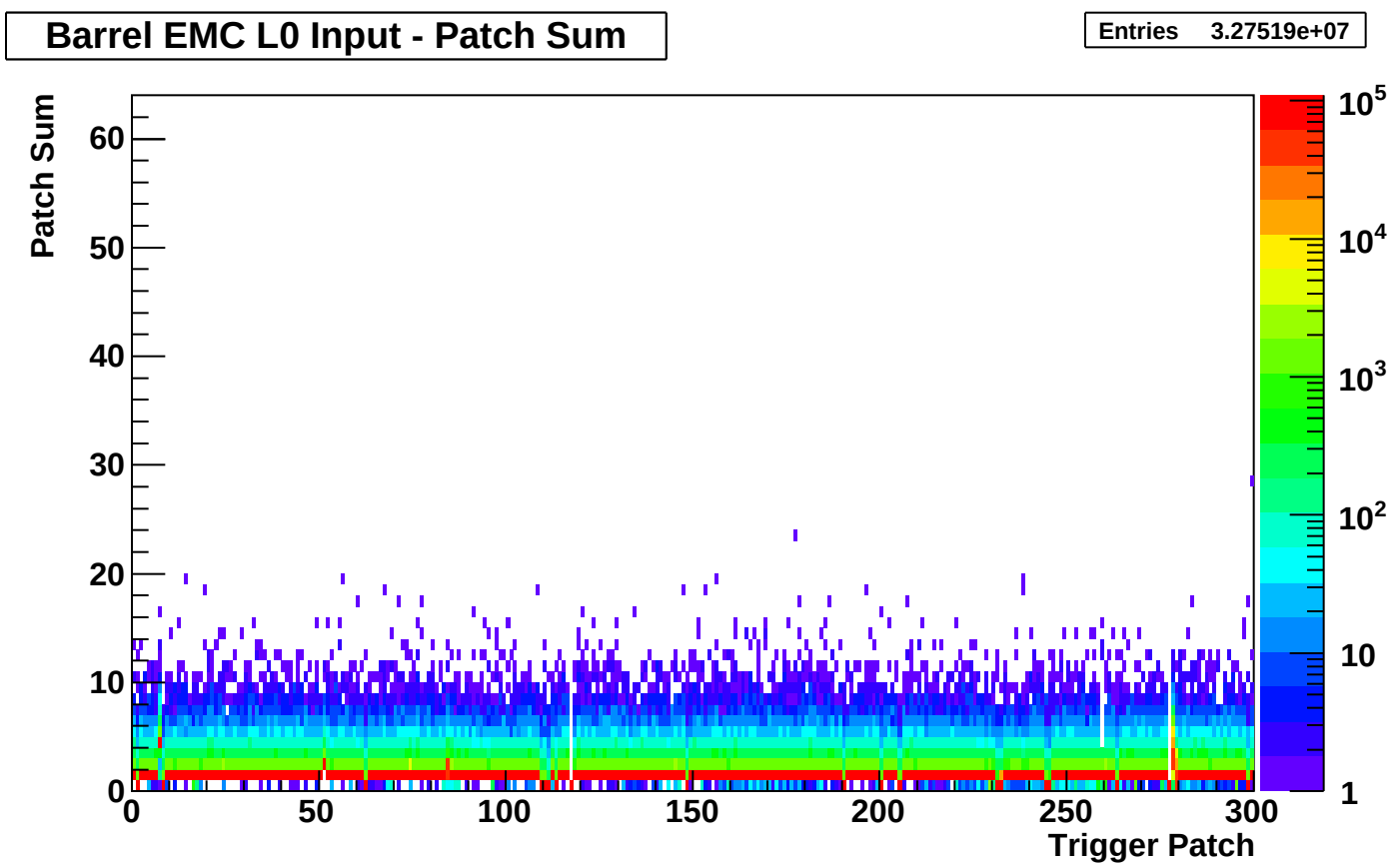

TPC Readout Mask (simulated)
Entries 101

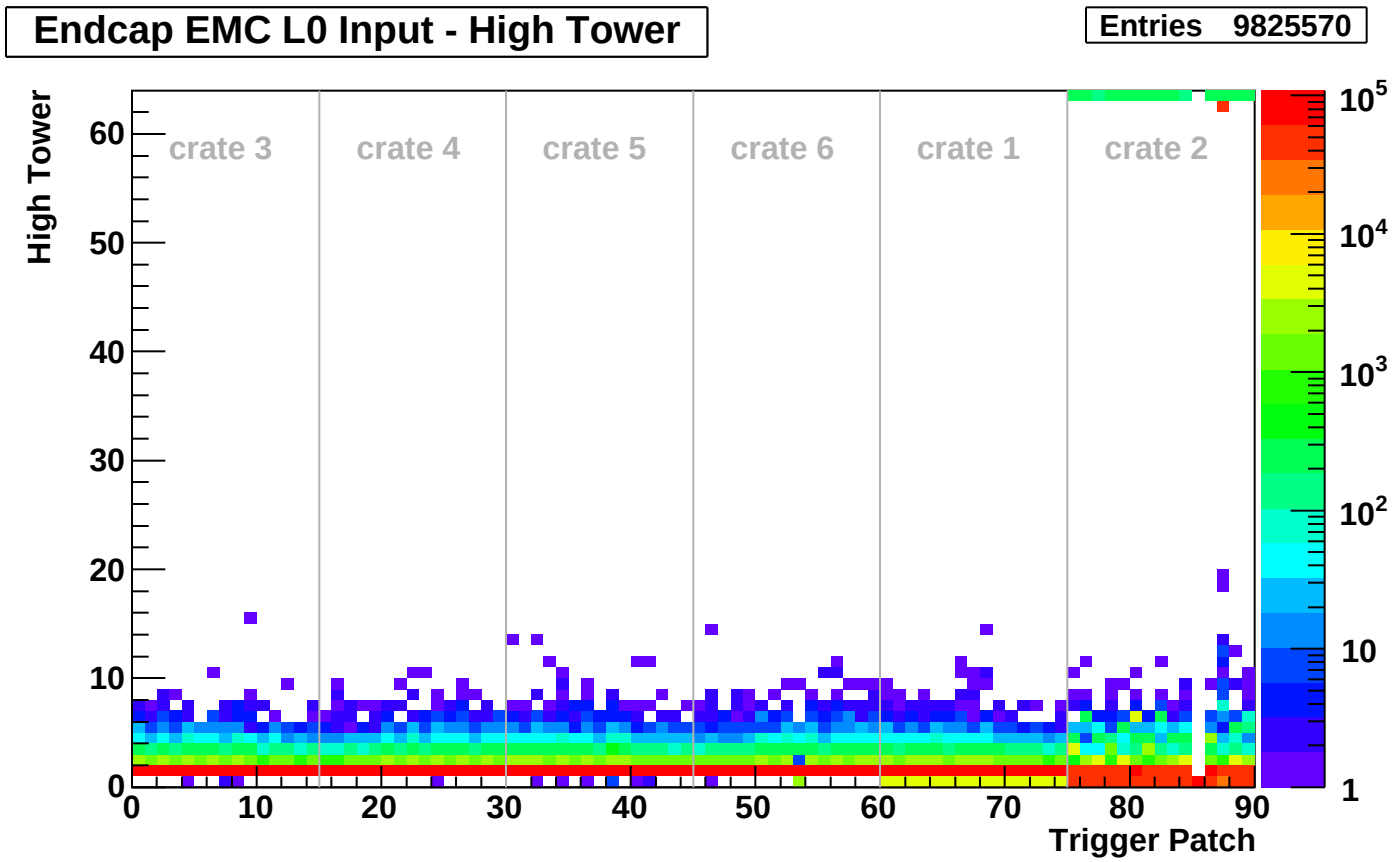

BunchId7Bit (all events)
Entries 109173

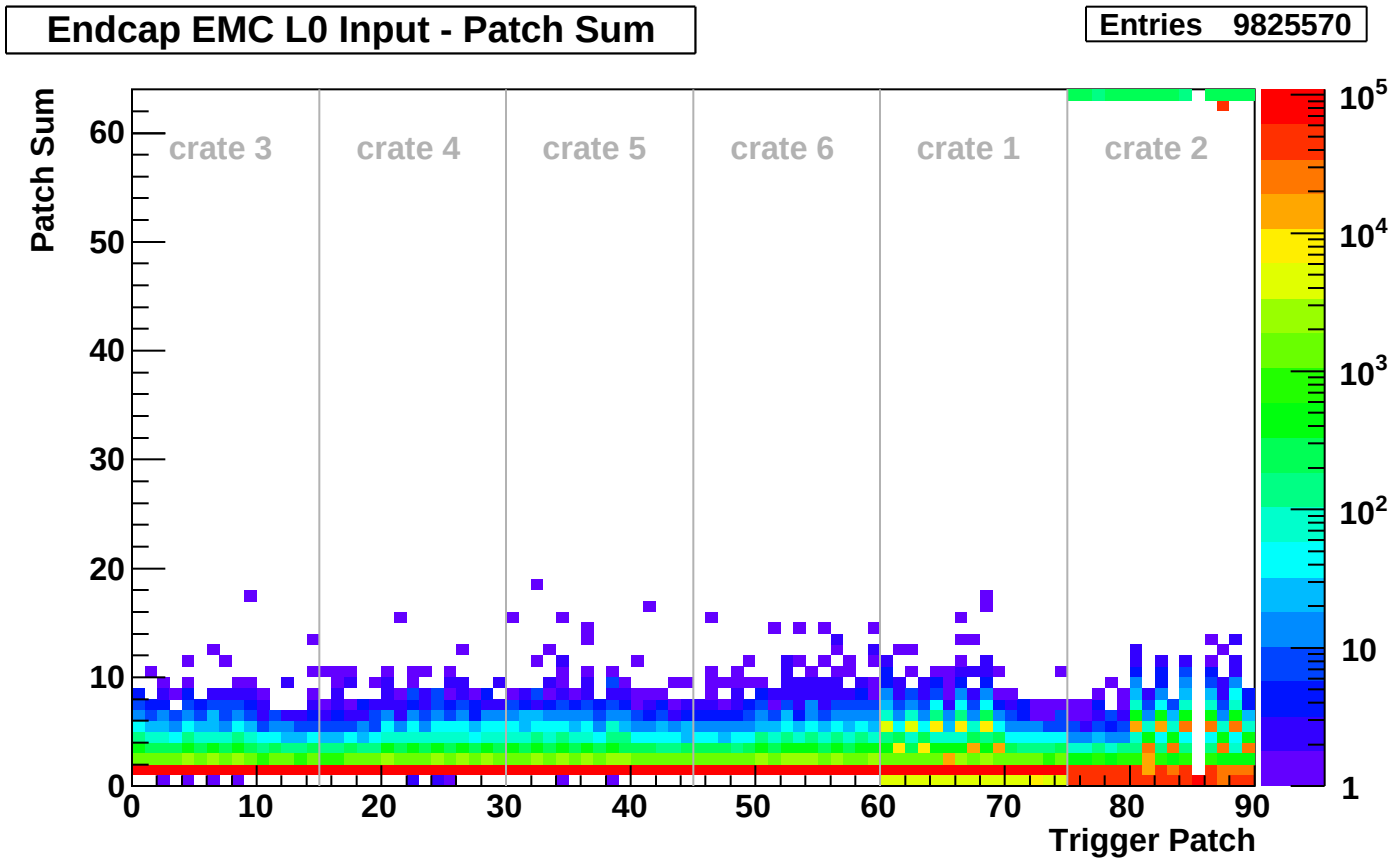

Abort-gaps
Entries 2049

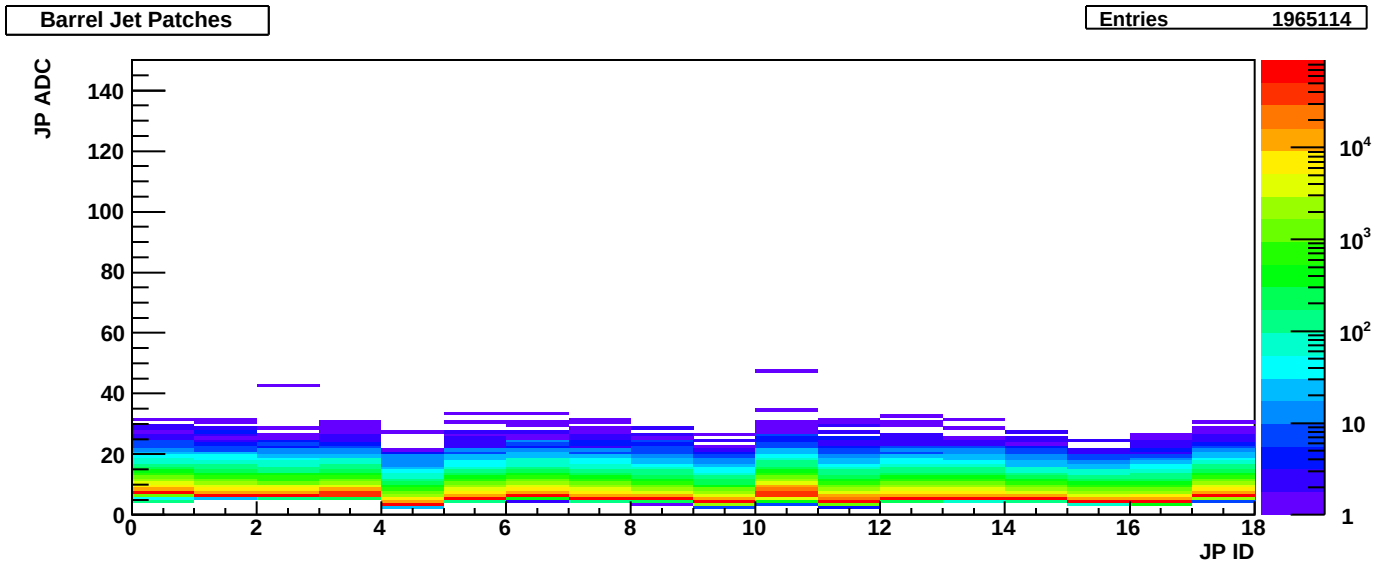

!Yellow-Fill
Entries 3862

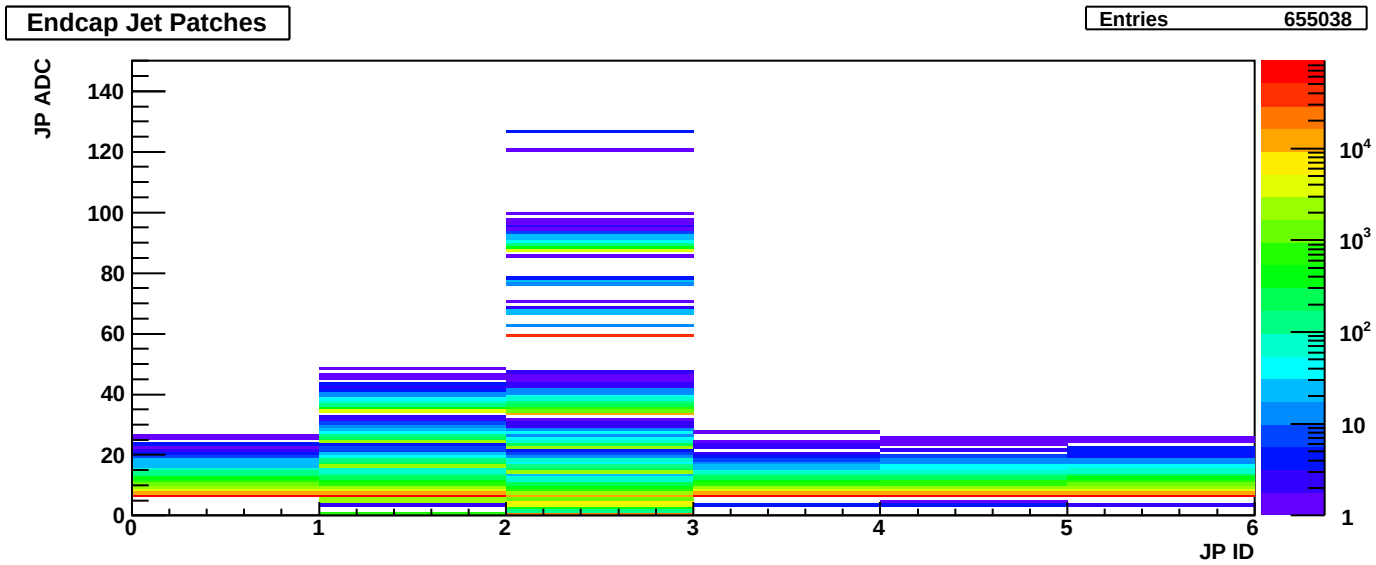

Barrel EMC L0 Input - High Tower
Entries 3.27519e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

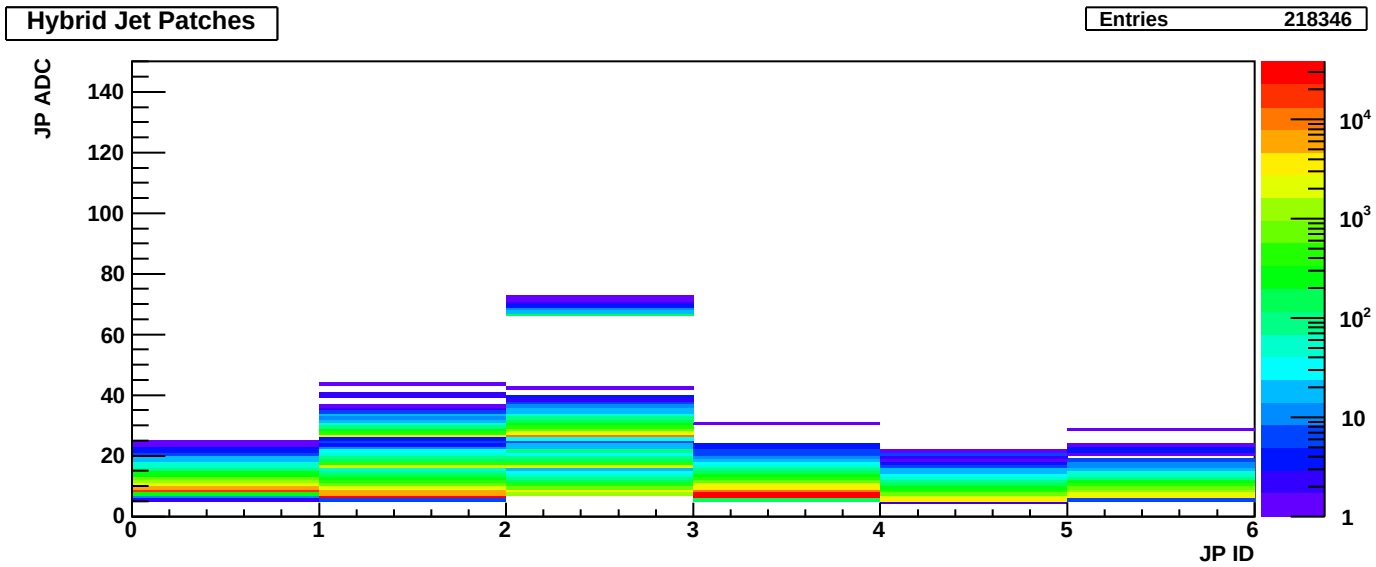

Barrel EMC L0 Input - Patch Sum
Entries 3.27519e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

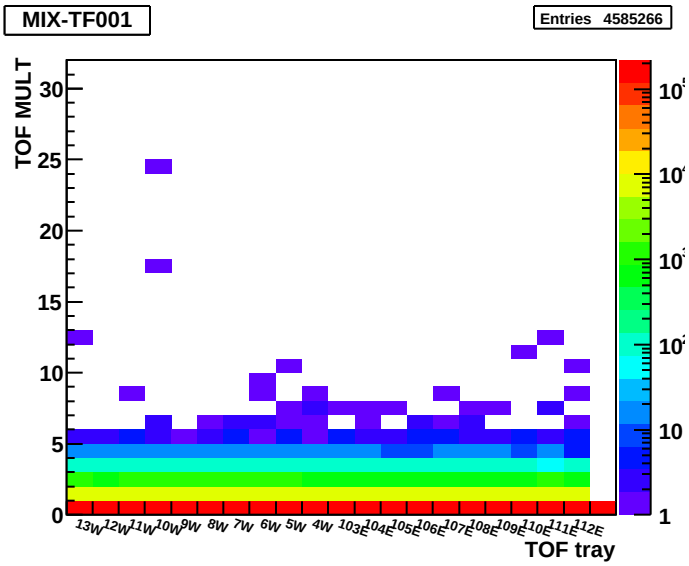

Endcap EMC L0 Input - High Tower
Entries 9825570
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

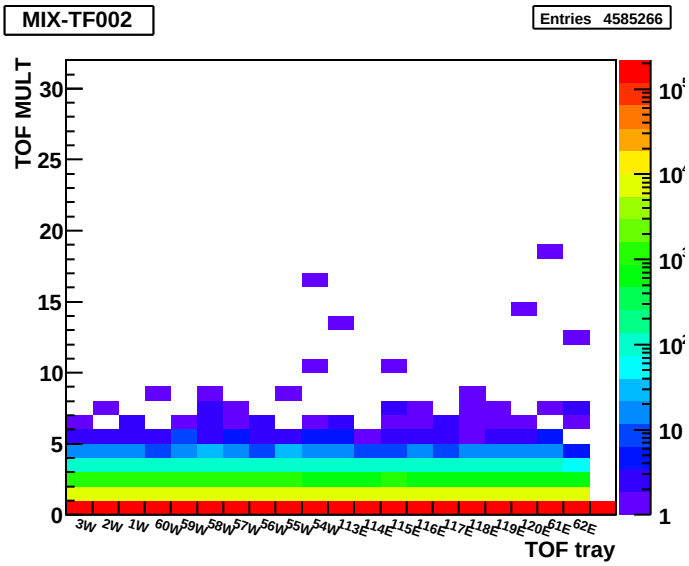

Endcap EMC L0 Input - Patch Sum
Entries 9825570
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

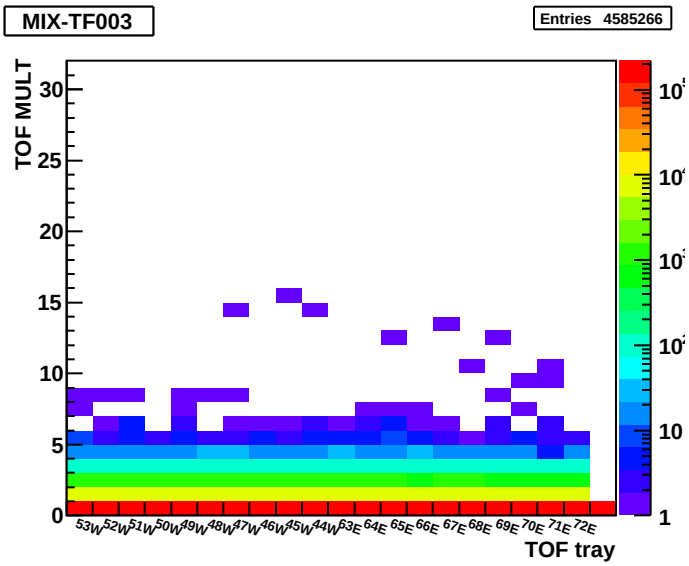

Barrel Jet Patches
Entries 1965114
JP ADC
JP ID

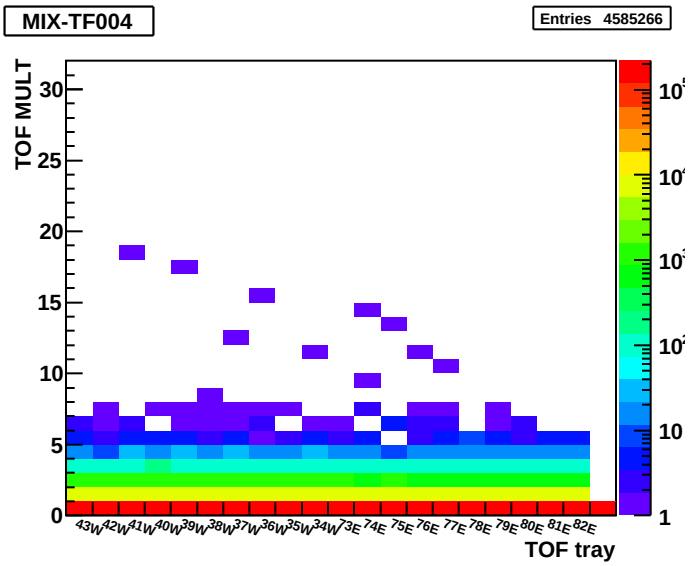

Endcap Jet Patches
Entries 655038
JP ADC
JP ID

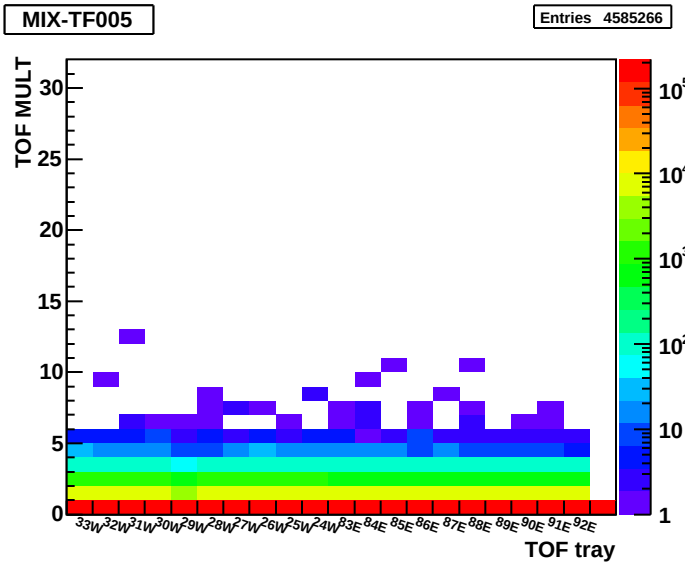

Hybrid Jet Patches
Entries 218346
JP ADC
JP ID

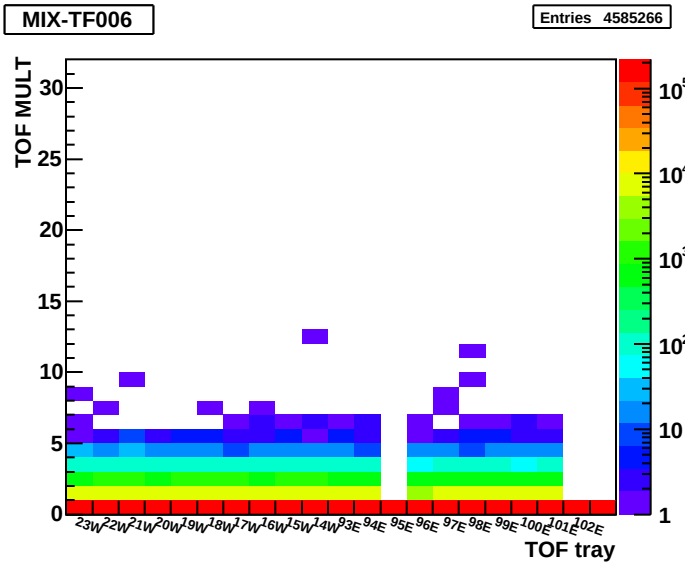

MIX-TF001
Entries 4585266
TOF MULT
TOF tray

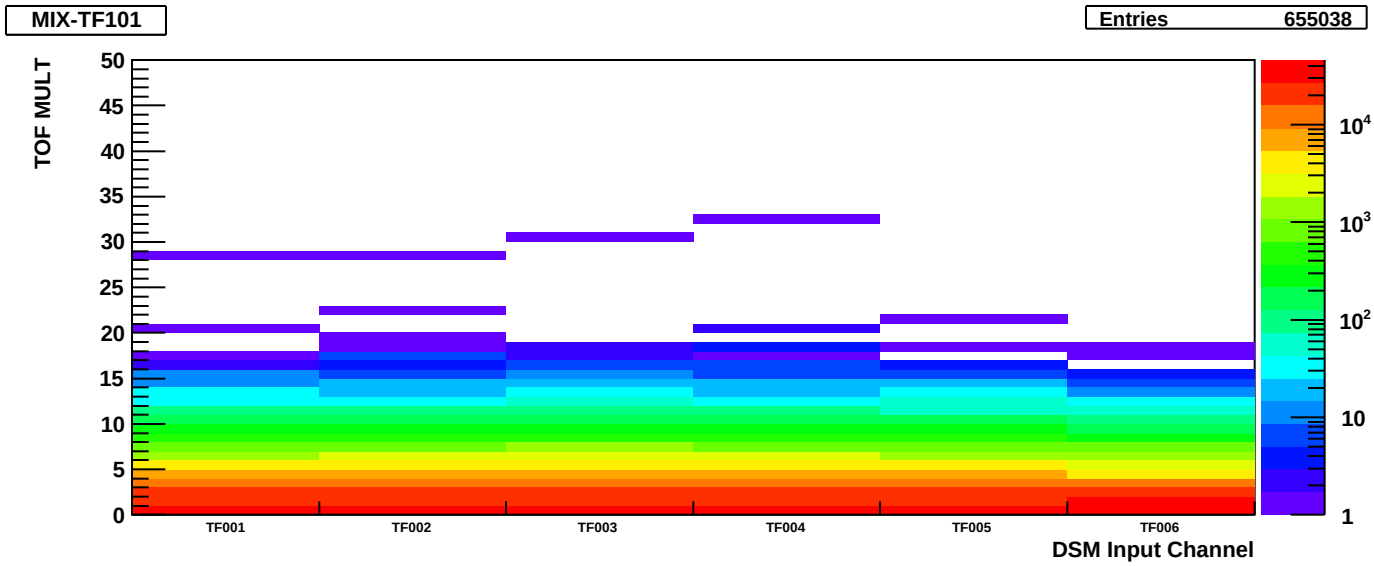

MIX-TF002
Entries 4585266
TOF MULT
TOF tray

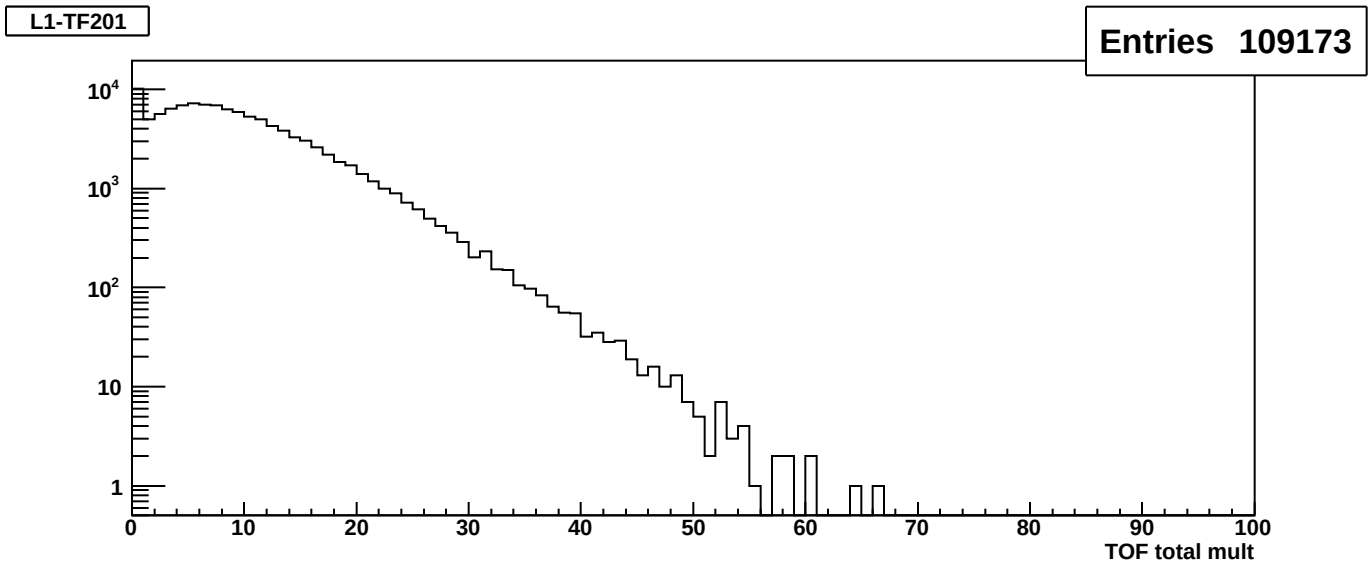

MIX-TF003
Entries 4585266
TOF MULT
TOF tray

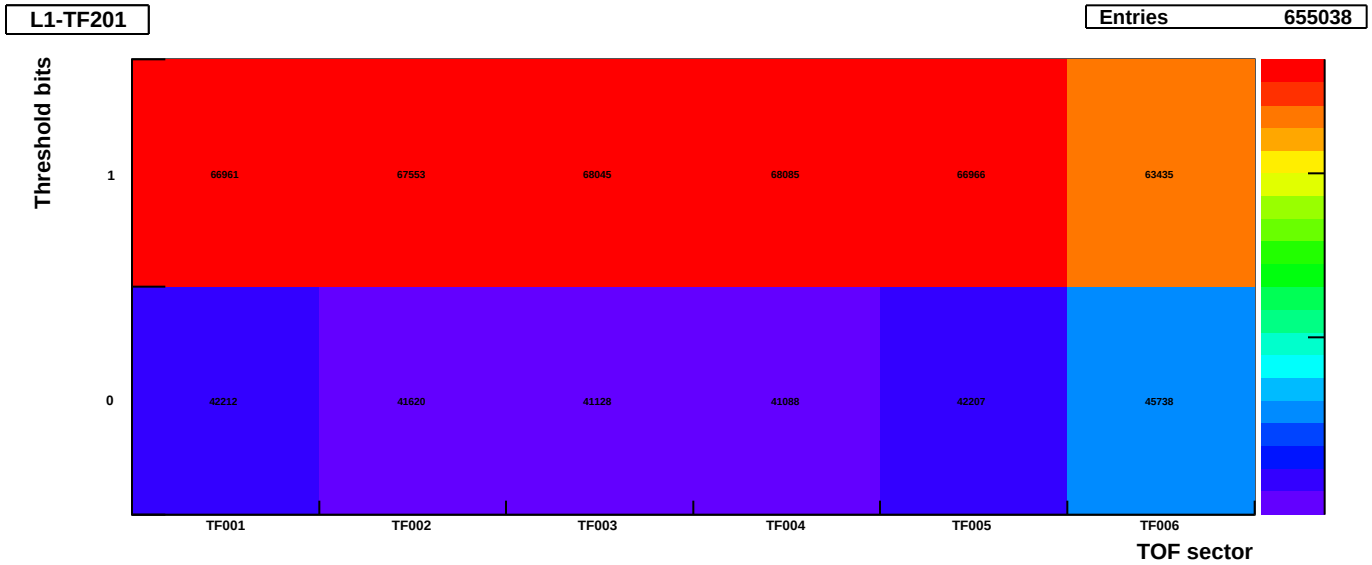

MIX-TF004
Entries 4585266
TOF MULT
TOF tray

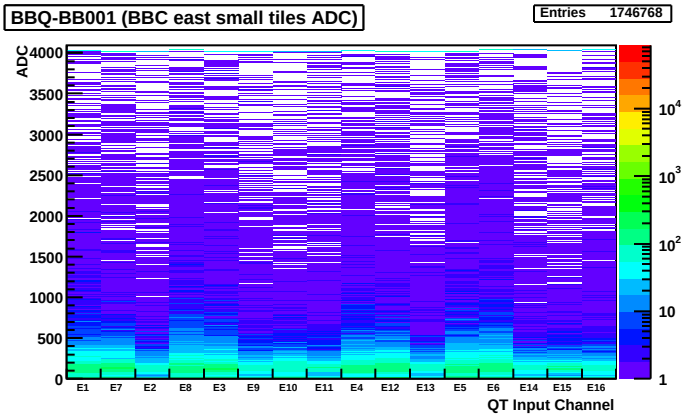

MIX-TF005
Entries 4585266
TOF MULT
TOF tray

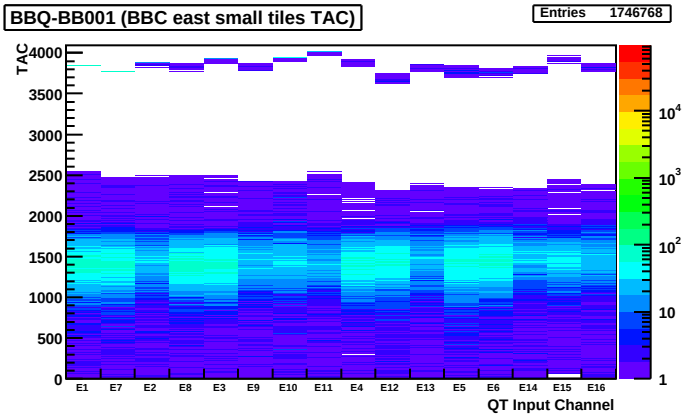

MIX-TF006
Entries 4585266
TOF MULT
TOF tray

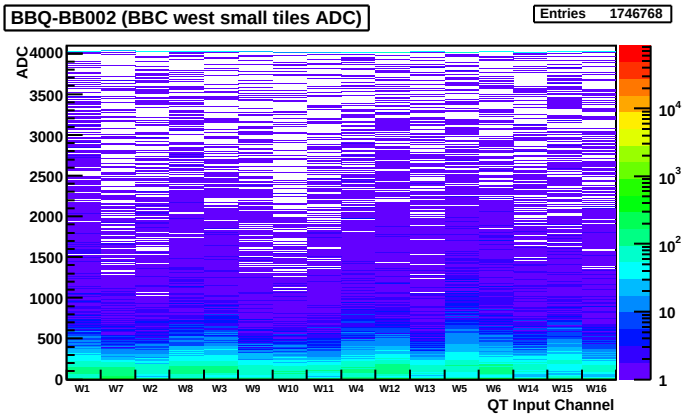

MIX-TF101
Entries 655038
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

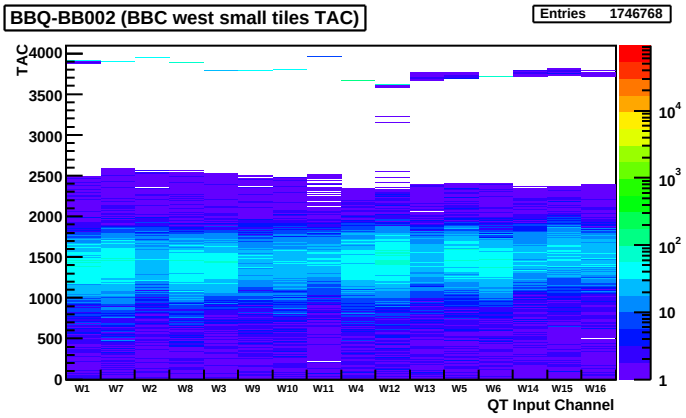

L1-TF201
Entries 109173
TOF total mult

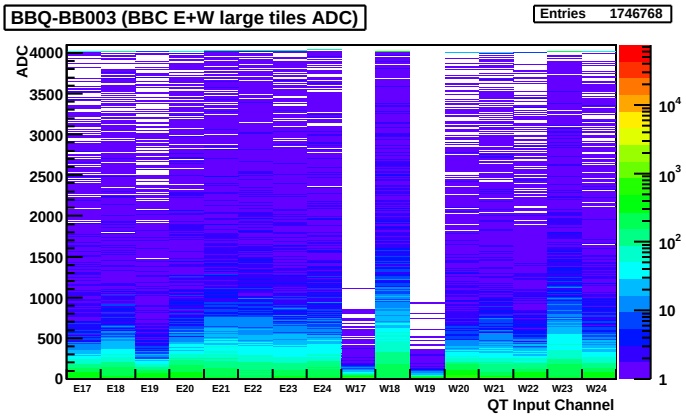

L1-TF201
Entries 655038
Threshold bits
66961
67553
68045
68085
66966
63435
42212
41620
41128
41088
42207
45738
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

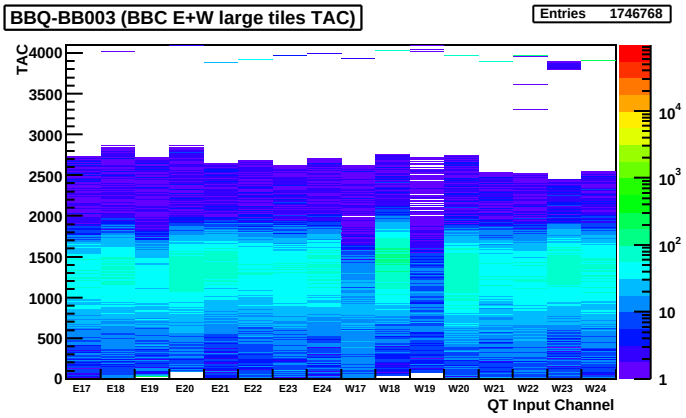

BBQ-BB001 (BBC east small tiles ADC)
Entries 1746768
ADC
QT Input Channel

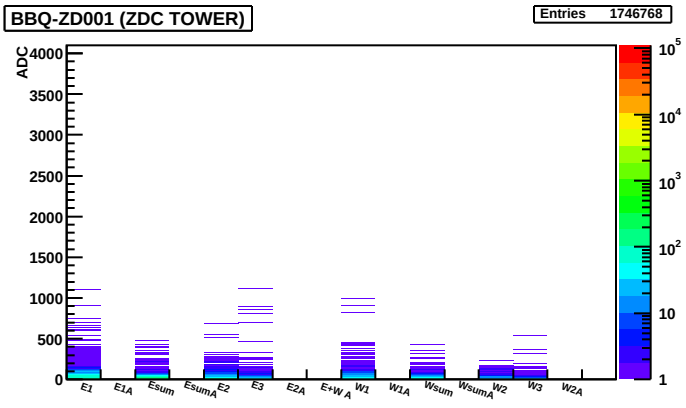

BBQ-BB001 (BBC east small tiles TAC)
Entries 1746768
TAC
QT Input Channel

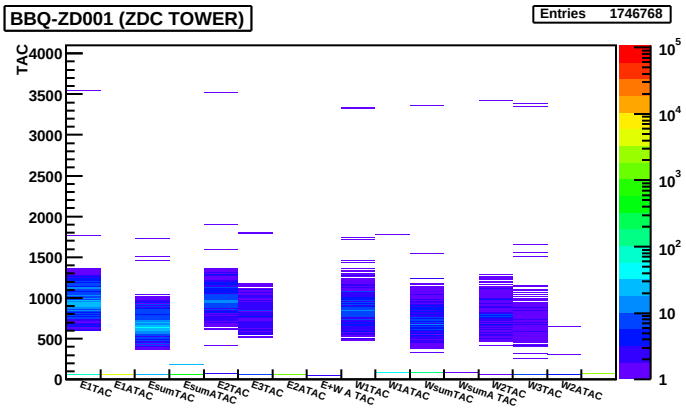

BBQ-BB002 (BBC west small tiles ADC)
Entries 1746768
ADC
QT Input Channel

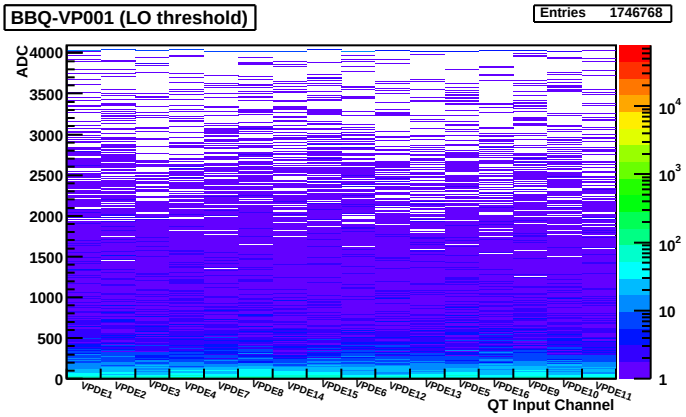

BBQ-BB002 (BBC west small tiles TAC)
Entries 1746768
TAC
QT Input Channel

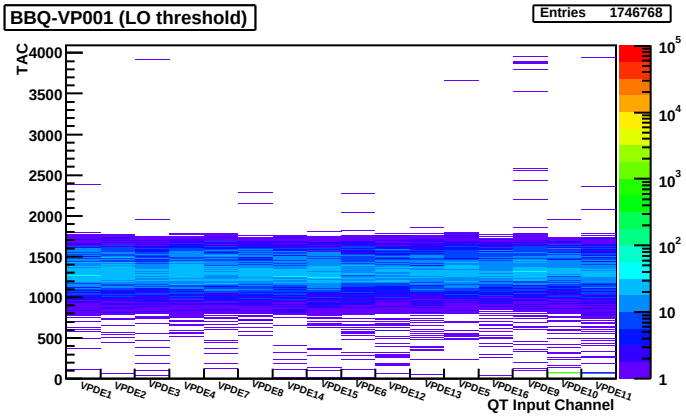

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1746768
ADC
QT Input Channel

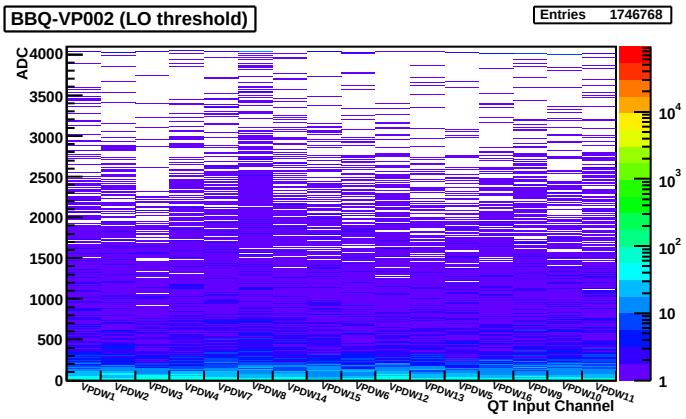

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1746768
TAC
QT Input Channel

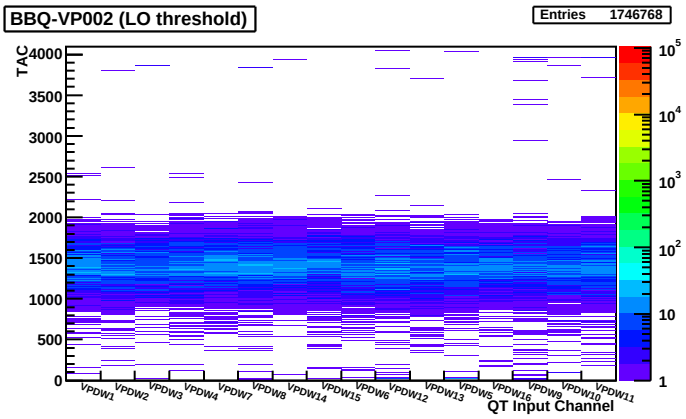

BBQ-ZD001 (ZDC TOWER)
Entries 1746768
ADC

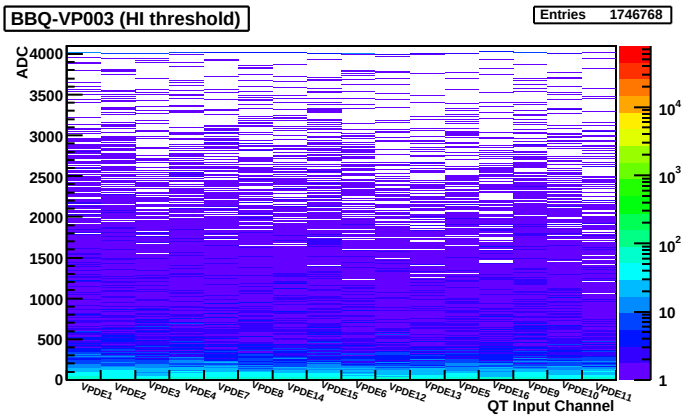

BBQ-ZD001 (ZDC TOWER)
Entries 1746768
TAC

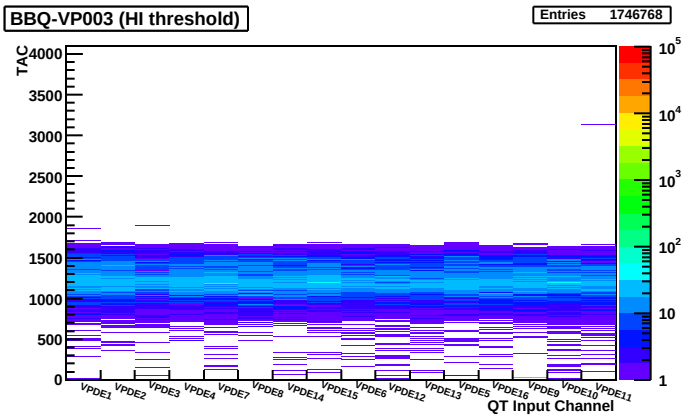

BBQ-VP001 (LO threshold)
Entries 1746768
ADC
QT Input Channel

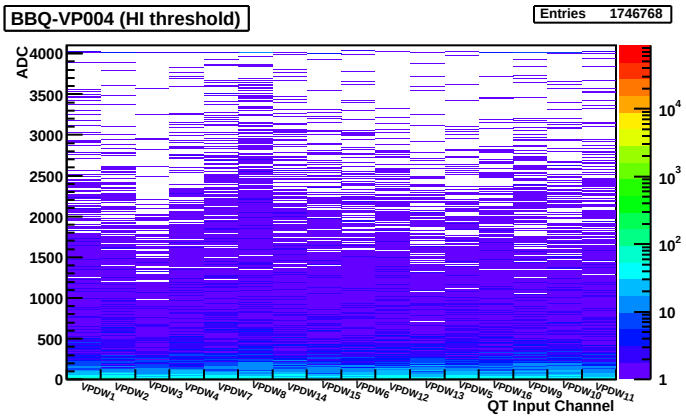

BBQ-VP001 (LO threshold)
Entries 1746768
TAC
QT Input Channel

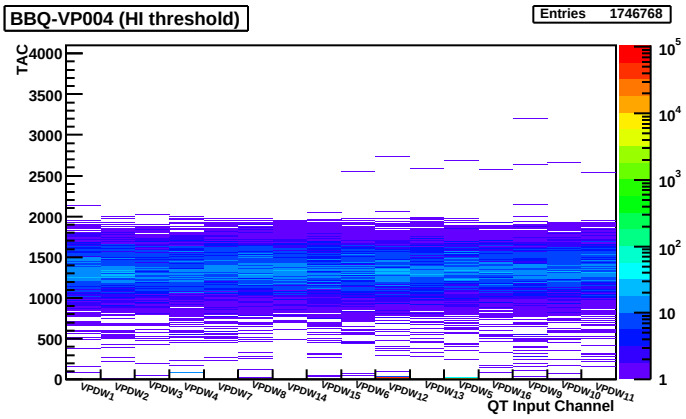

BBQ-VP002 (LO threshold)
Entries 1746768
ADC
QT Input Channel

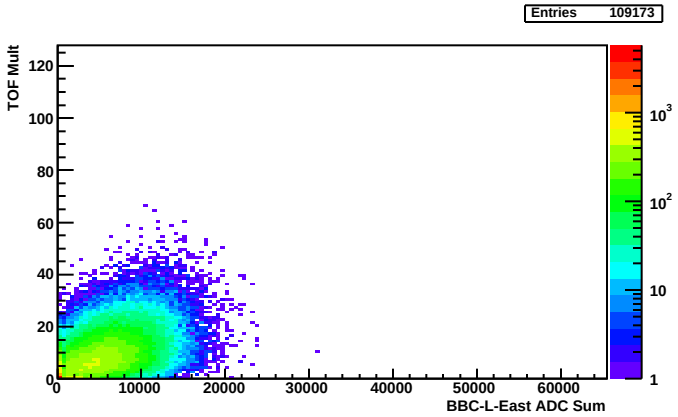

BBQ-VP002 (LO threshold)
Entries 1746768
TAC
QT Input Channel

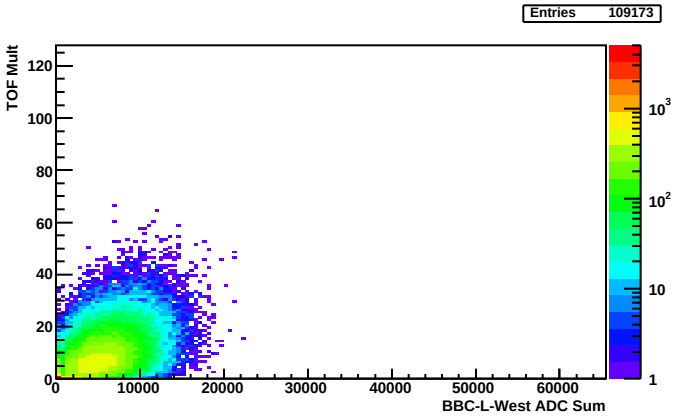

BBQ-VP003 (HI threshold)
Entries 1746768
ADC
QT Input Channel

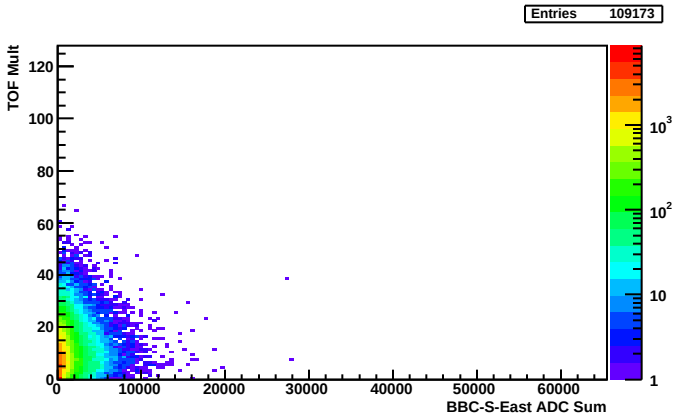

BBQ-VP003 (HI threshold)
Entries 1746768
TAC
QT Input Channel

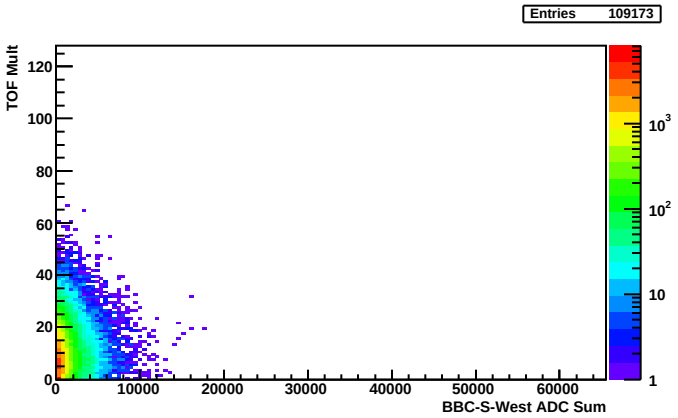

BBQ-VP004 (HI threshold)
Entries 1746768
ADC
QT Input Channel

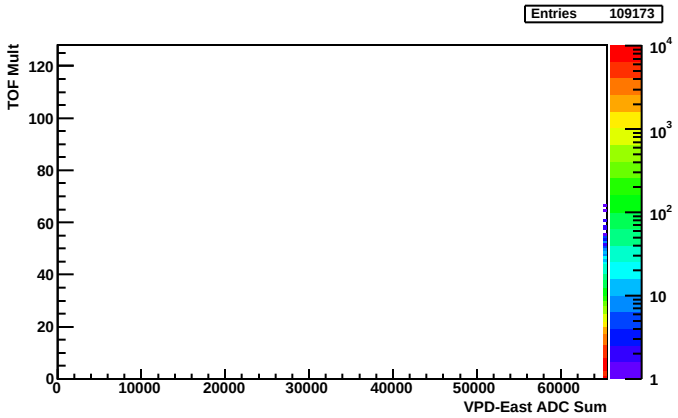

BBQ-VP004 (HI threshold)
Entries 1746768
TAC
QT Input Channel

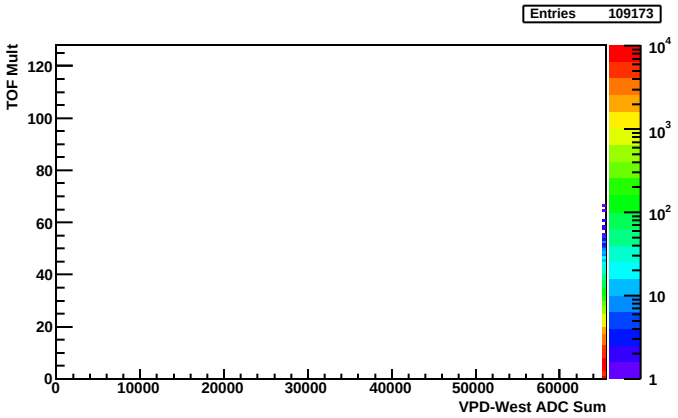

Entries 109173
TOF Mult
BBC-L-East ADC Sum

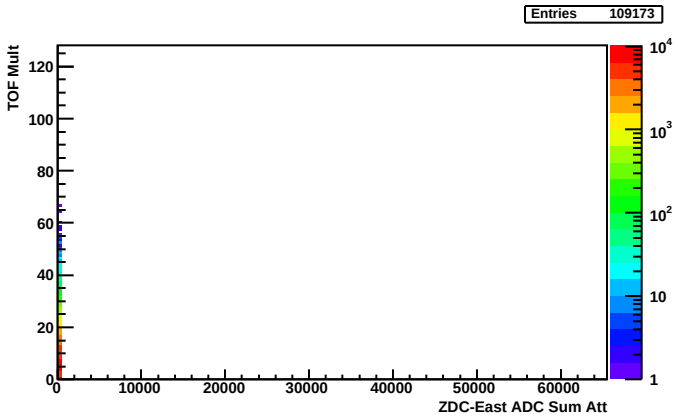

Entries 109173
TOF Mult
BBC-L-West ADC Sum

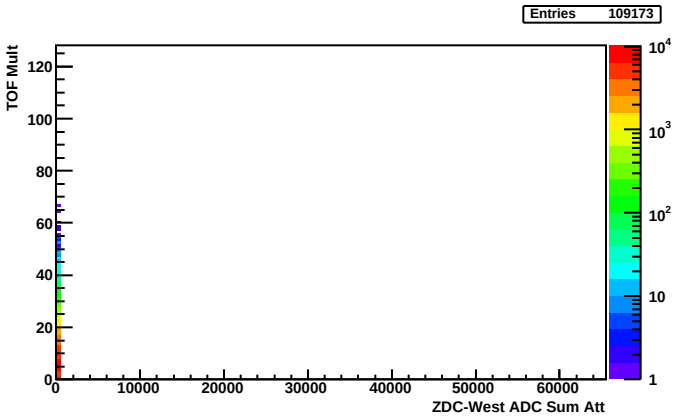

Entries 109173
TOF Mult
BBC-S-East ADC Sum

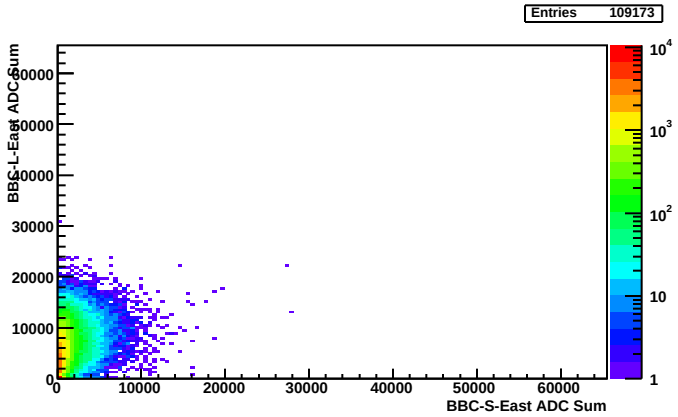

Entries 109173
TOF Mult
BBC-S-West ADC Sum

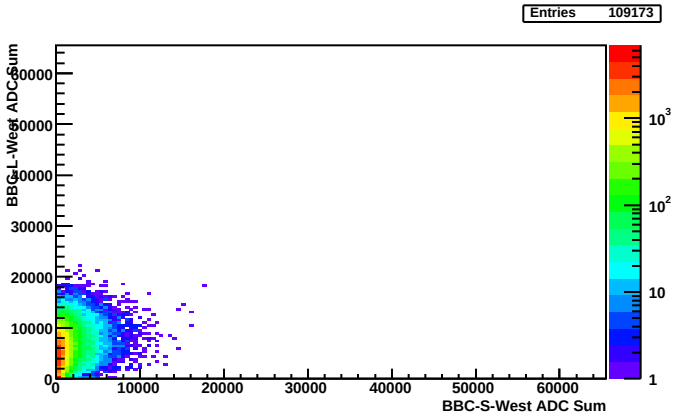

Entries 109173
TOF Mult
VPD-East ADC Sum

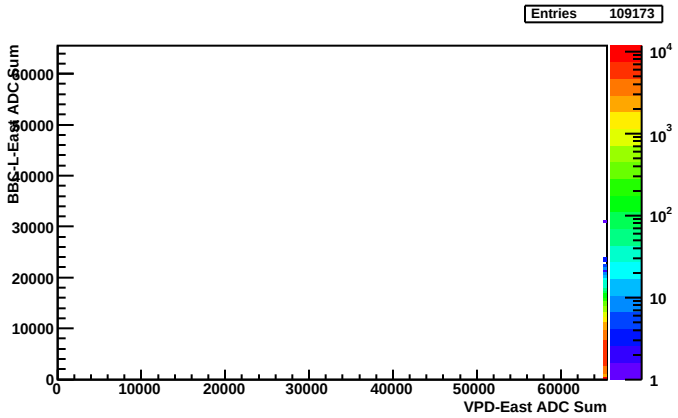

Entries 109173
TOF Mult
VPD-West ADC Sum


Entries 109173
TOF Mult
ZDC-East ADC Sum Att


Entries 109173
TOF Mult
ZDC-West ADC Sum Att


Entries 109173
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 109173
BBC-L-West ADC Sum
BBC-S-West ADC Sum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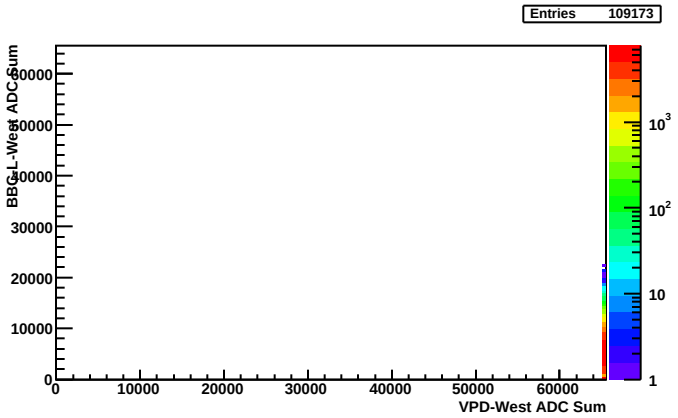
BBC-L-East ADC Sum
VPD-East ADC Sum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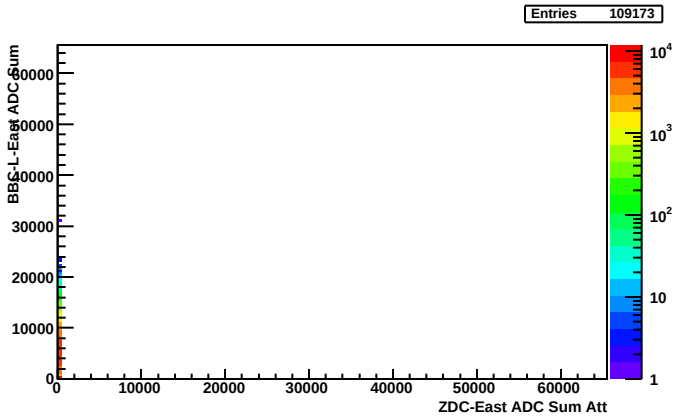
BBC-L-West ADC Sum
VPD-West ADC Sum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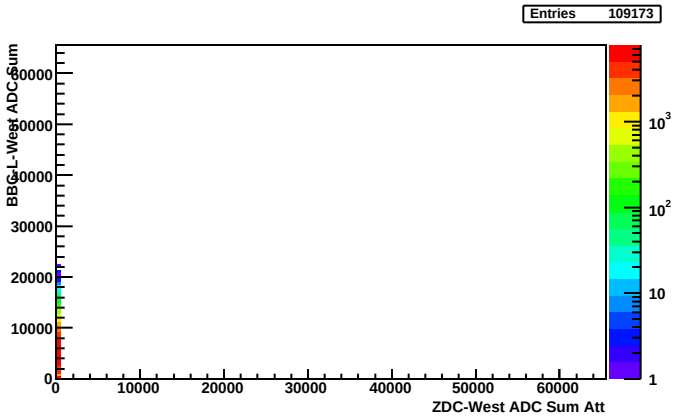
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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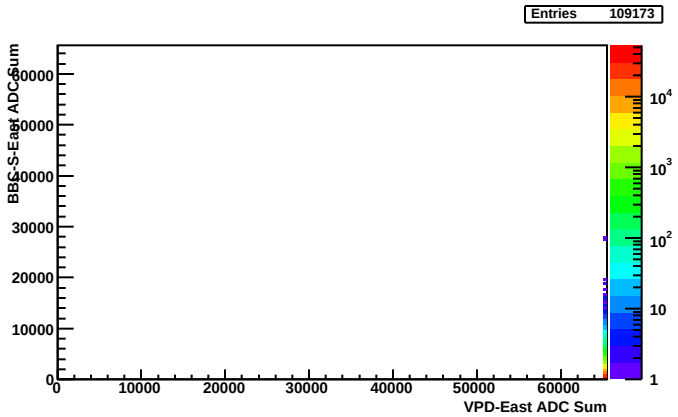
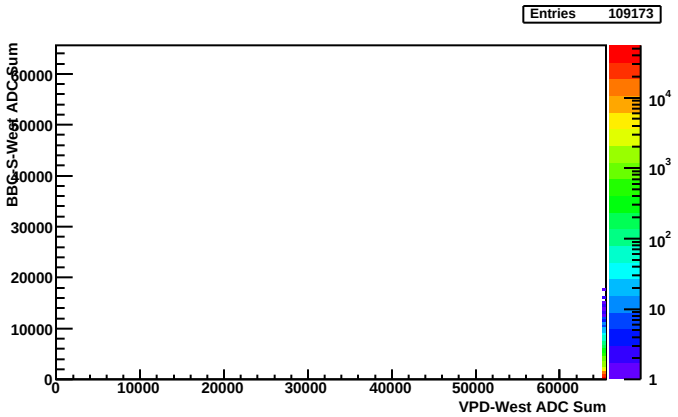
ZDC-West ADC Sum Att


Entries 109173
BBC-S-East ADC Sum
VPD-East ADC Sum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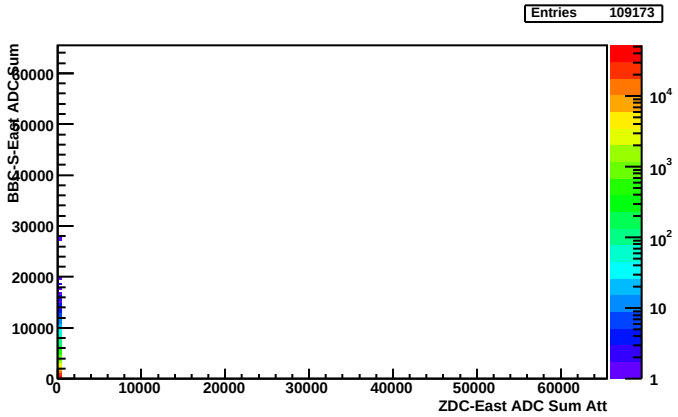
BBC-S-West ADC Sum
VPD-West ADC Sum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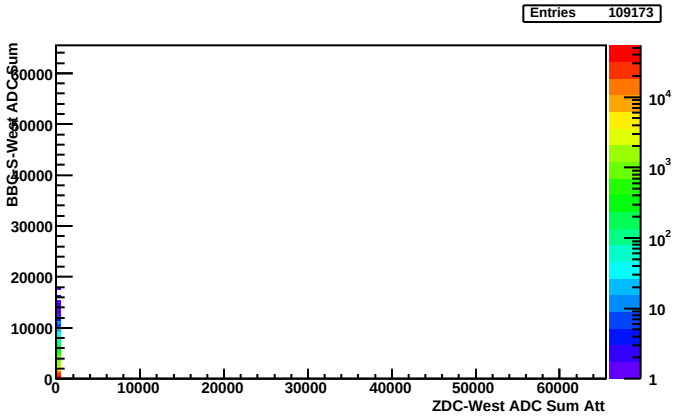
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 109173
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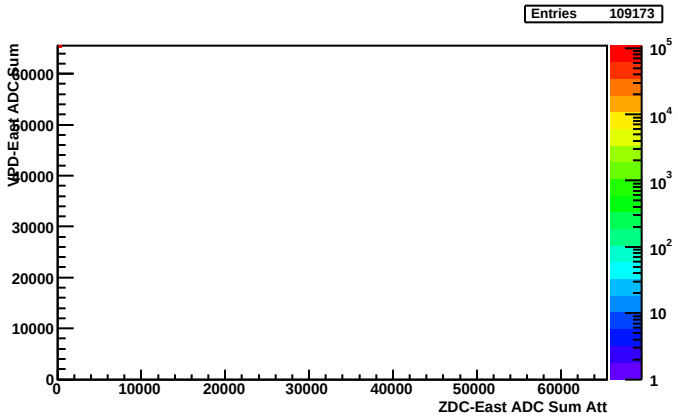
ZDC-West ADC Sum Att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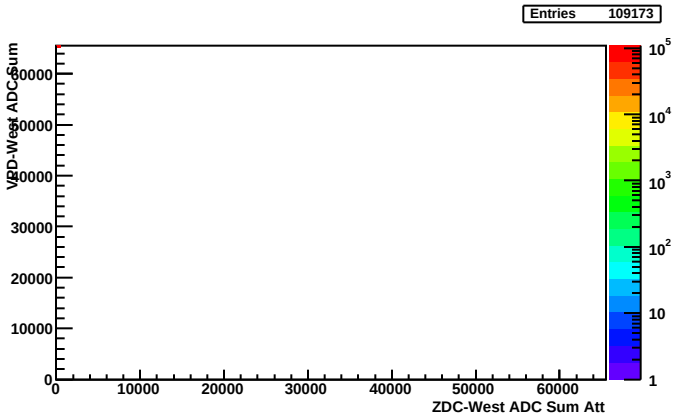
VPD-East ADC Sum
ZDC-East ADC Sum Att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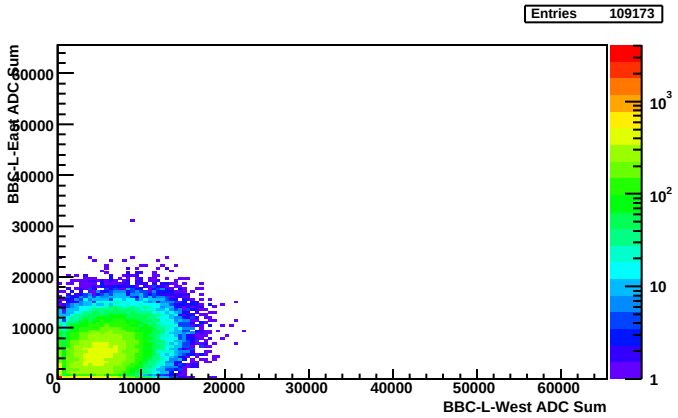
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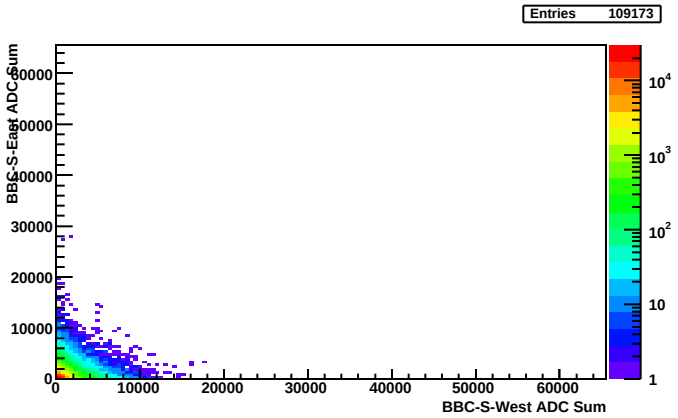
BBC-L-East ADC Sum
BBC-L-West ADC Sum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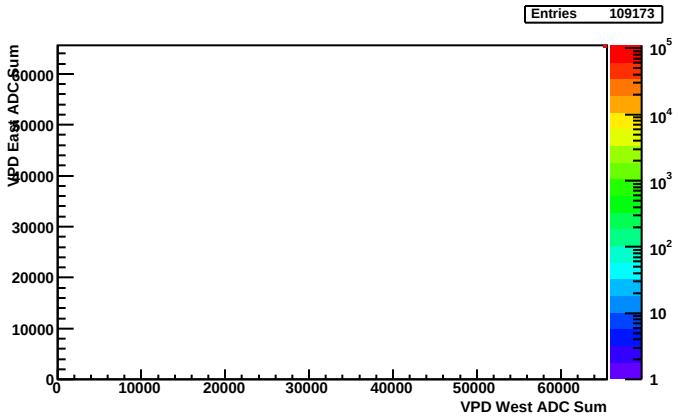
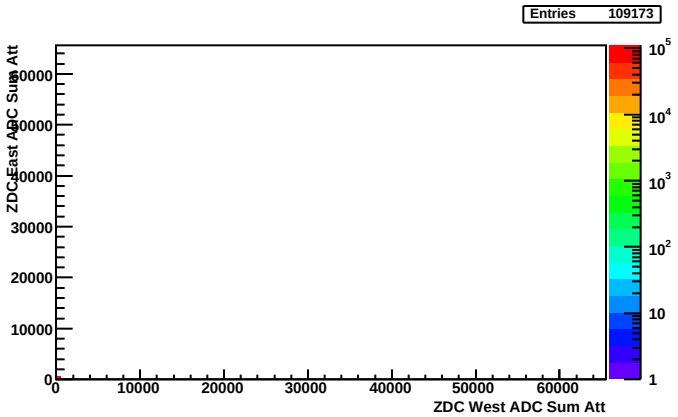
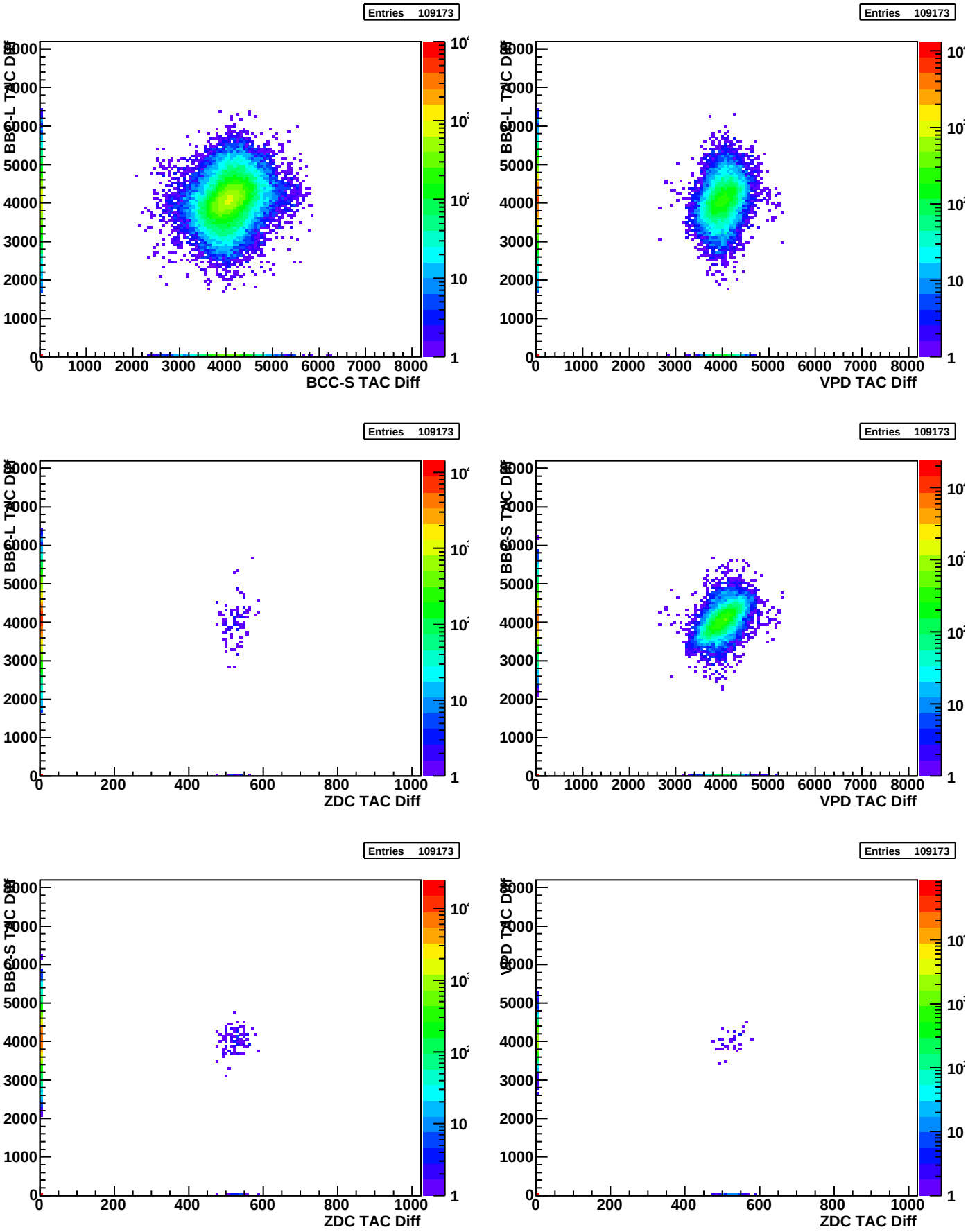

Entries 109173
VPD East ADC Sum
VPD West ADC Sum


Entries 109173
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 109173
BBC-L TAC Diff
BCC-S TAC Diff
Entries 109173
BBC-L TAC Diff
VPD TAC Diff
Entries 109173
BBC-L TAC Diff
ZDC TAC Diff
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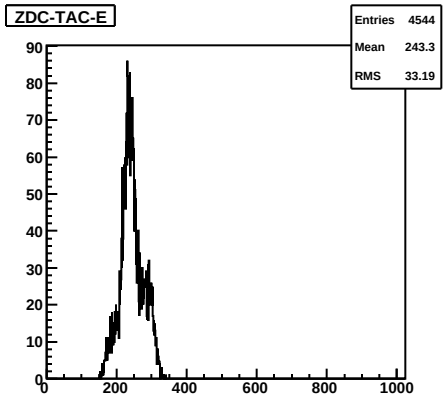
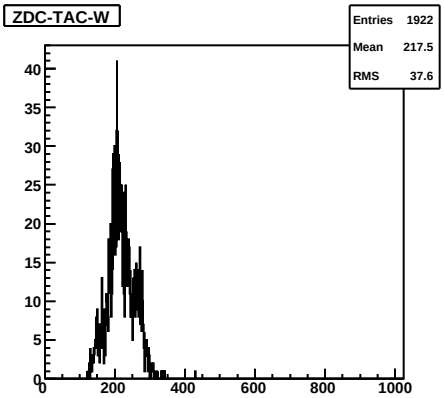
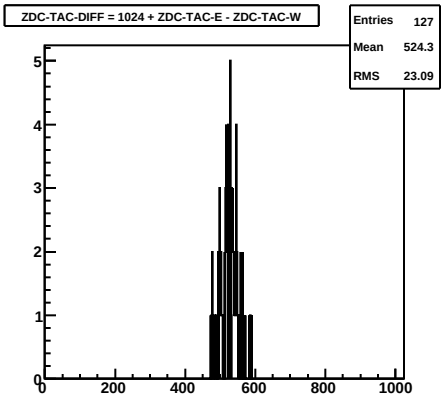
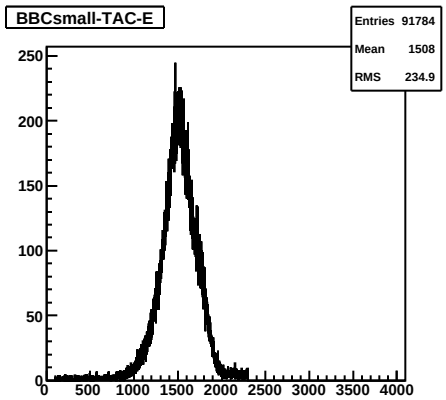
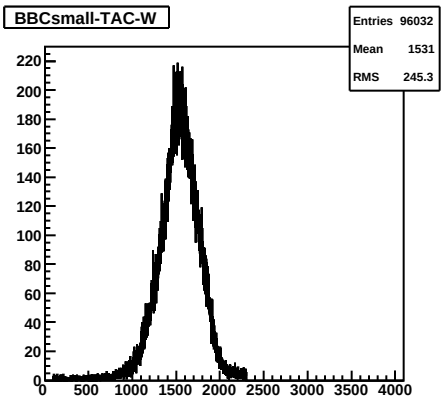
BBC-S TAC Diff
VPD TAC Diff
Entries 109173
BBC-S TAC Diff
ZDC TAC Diff
Entries 109173
VPD TAC Diff
ZDC TAC Diff

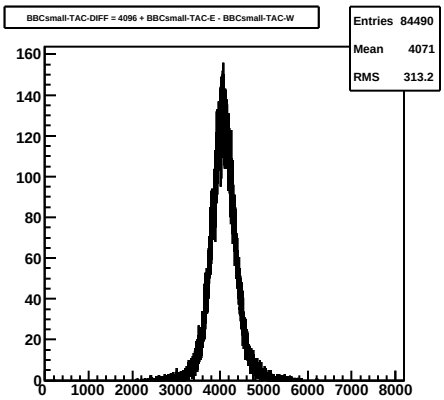

ZDC-TAC-E
Entries 4544
Mean 243.3
RMS 33.19

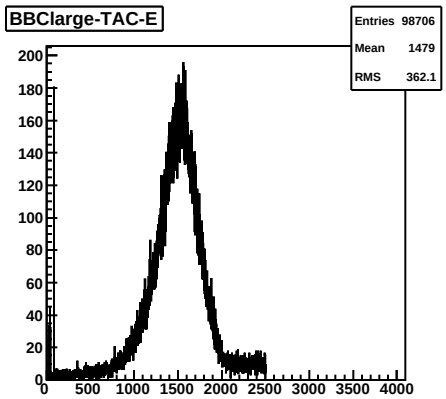

ZDC-TAC-W
Entries 1922
Mean 217.5
RMS 37.6

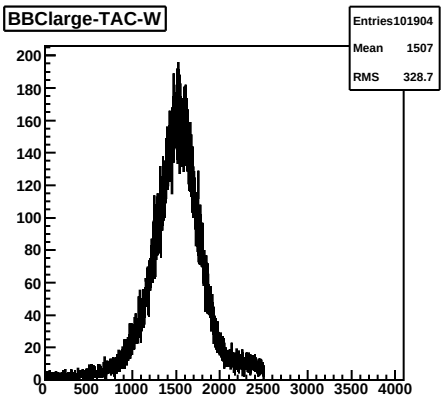

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 127
Mean 524.3
RMS 23.09

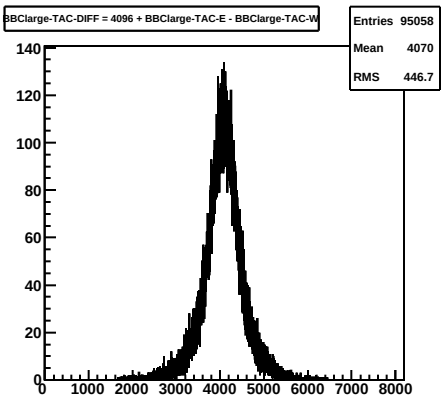

BBCsmall-TAC-E
Entries 91784
Mean 1508
RMS 234.9

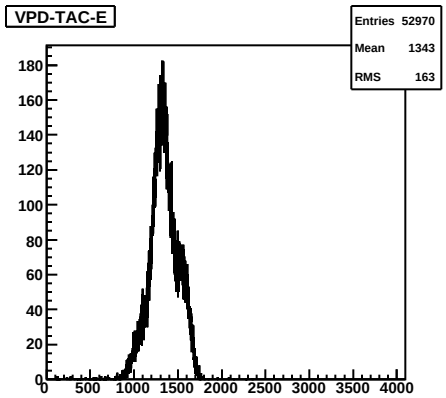

BBCsmall-TAC-W
Entries 96032
Mean 1531
RMS 245.3

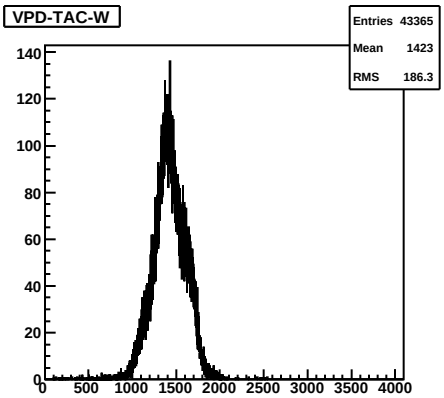

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 84490
Mean 4071
RMS 313.2

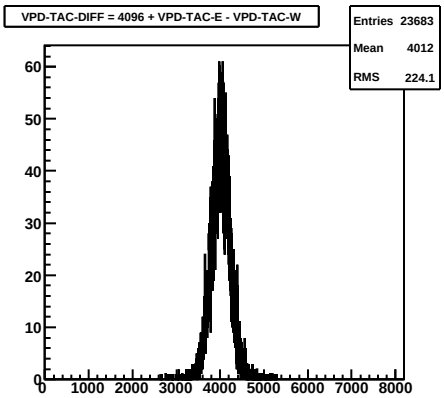

BBClarge-TAC-E
Entries 98706
Mean 1479
RMS 362.1

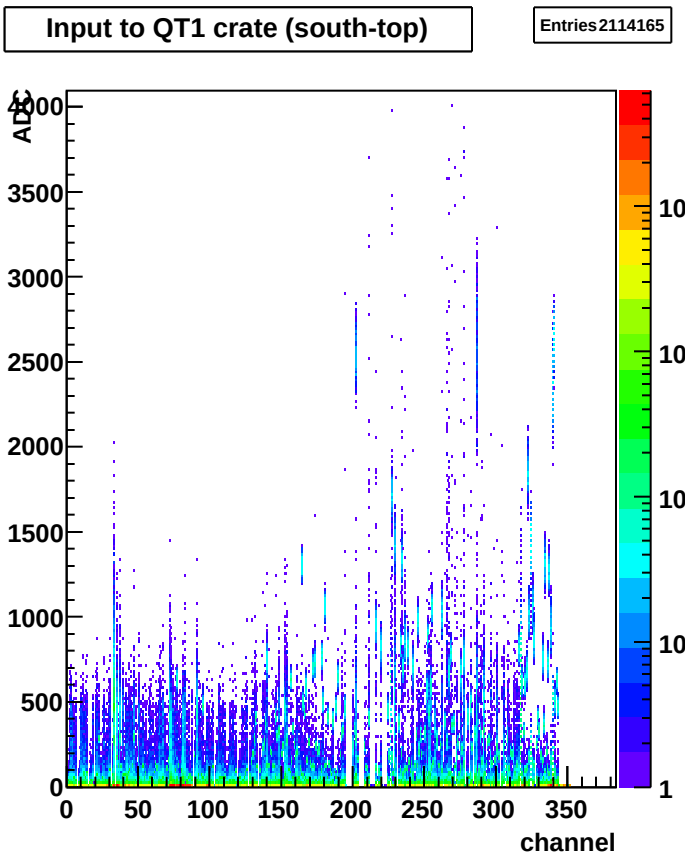

BBClarge-TAC-W
Entries101904
Mean 1507
RMS 328.7

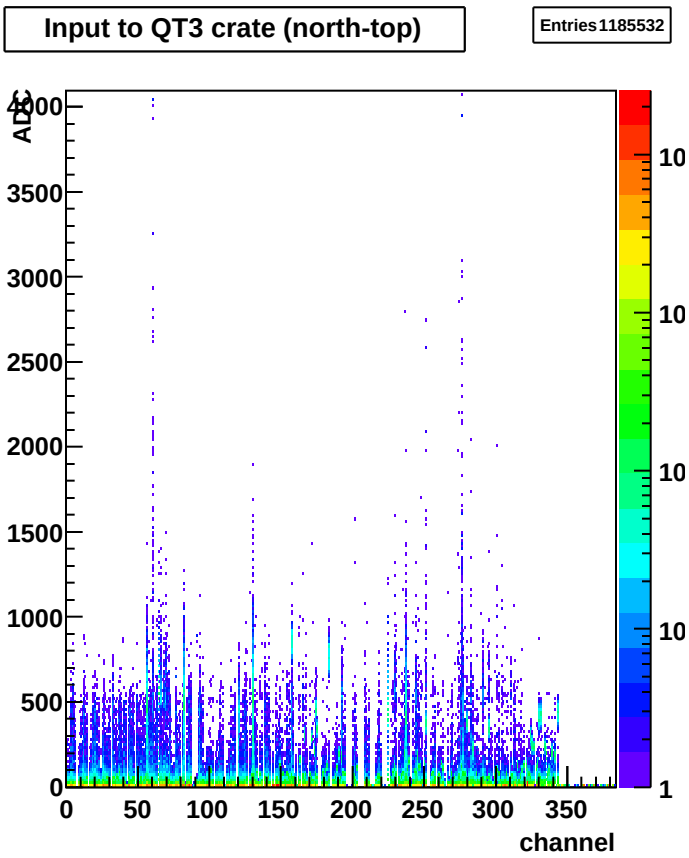

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 95058
Mean 4070
RMS 446.7

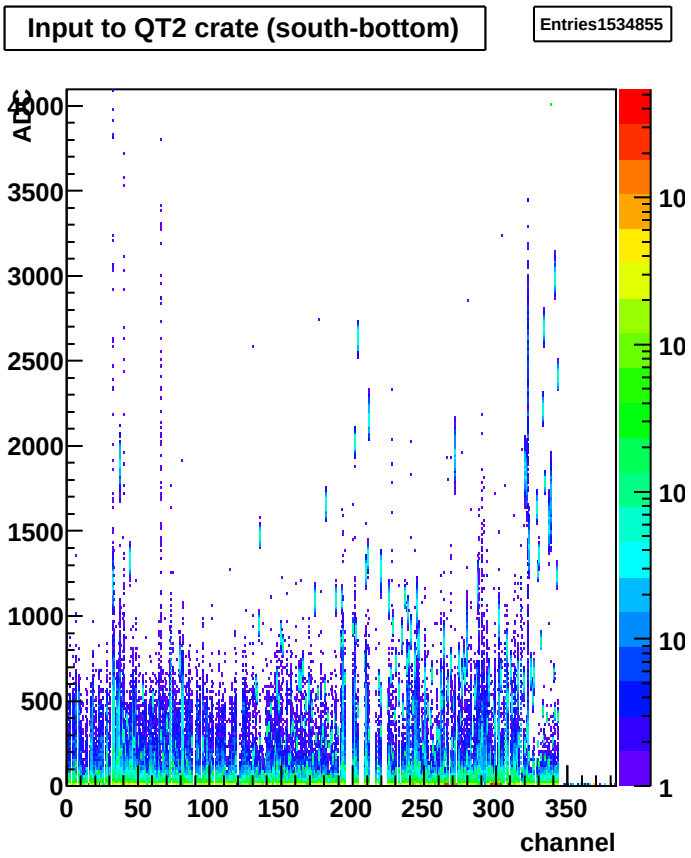

VPD-TAC-E
Entries 52970
Mean 1343
RMS 163

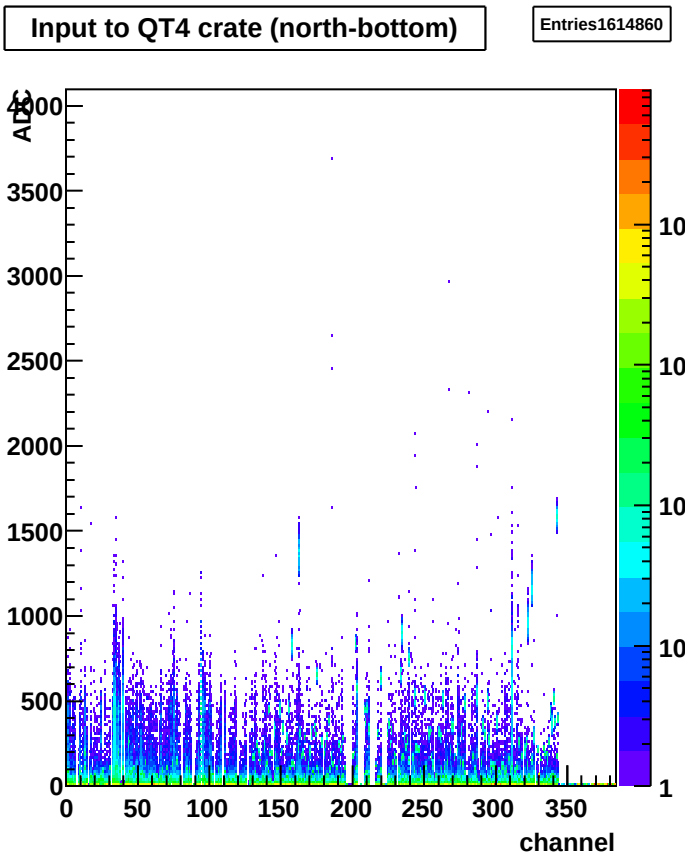

VPD-TAC-W
Entries 43365
Mean 1423
RMS 186.3

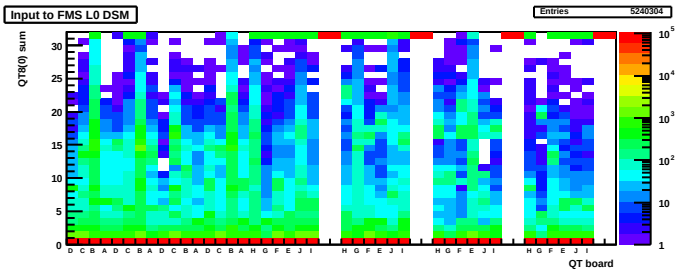

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 23683
Mean 4012
RMS 224.1

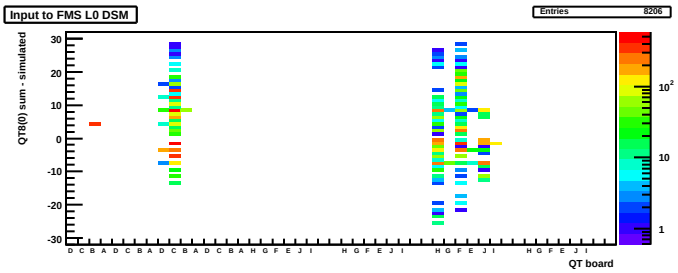

Input to QT1 crate (south-top)
Entries 2114165
ADC
channel

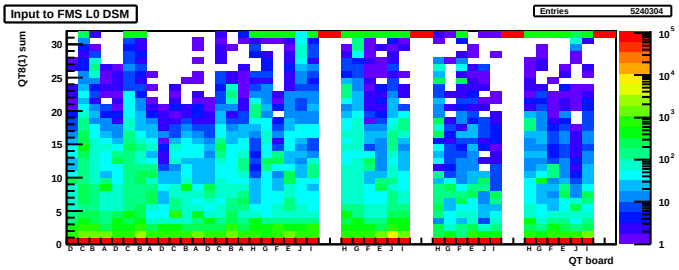

Input to QT3 crate (north-top)
Entries 1185532
ADC
channel

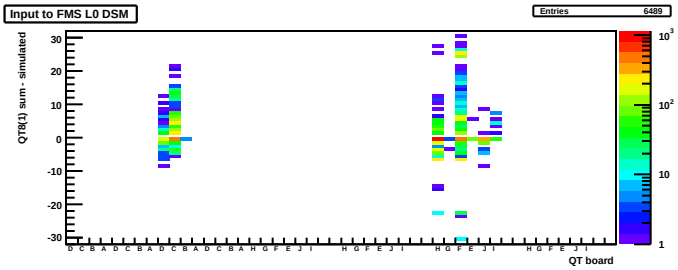

Input to QT2 crate (south-bottom)
Entries 1534855
ADC
channel

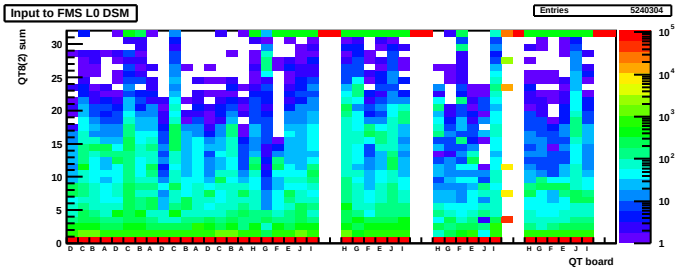

Input to QT4 crate (north-bottom)
Entries 1614860
ADC
channel

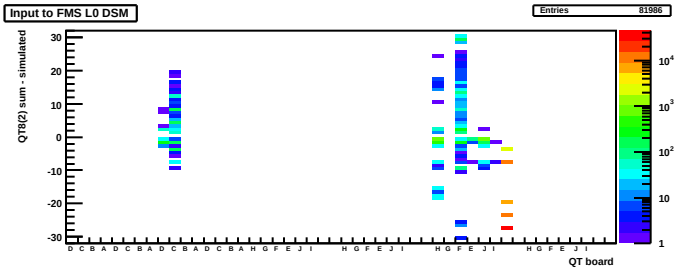

Input to FMS L0 DSM
Entries 5240304
QT8(0) sum
QT board

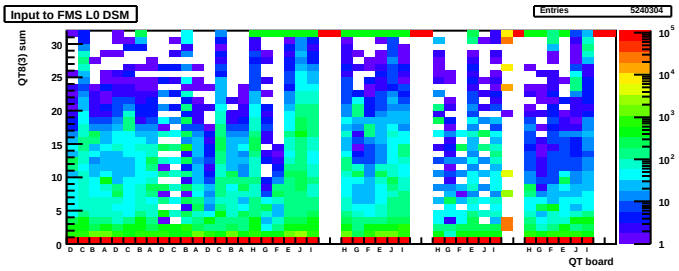

Input to FMS L0 DSM
Entries 8206
QT8(0) sum - simulated
QT board

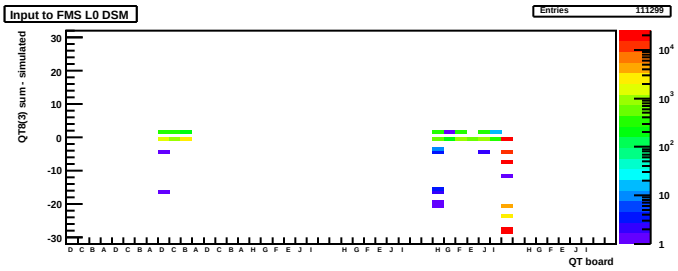

Input to FMS L0 DSM
Entries 5240304
QT8(1) sum
QT board

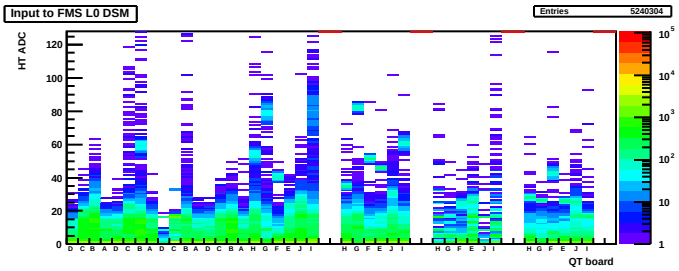

Input to FMS L0 DSM
Entries 6489
QT8(1) sum - simulated
QT board

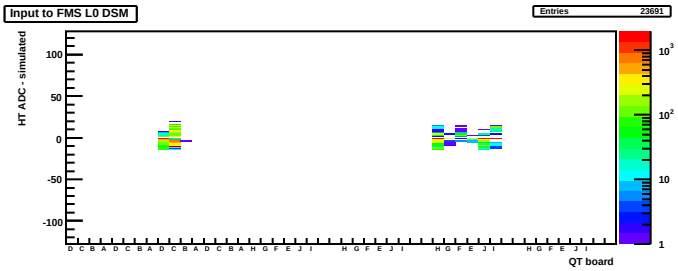

Input to FMS L0 DSM
Entries 5240304
QT8(2) sum
QT board

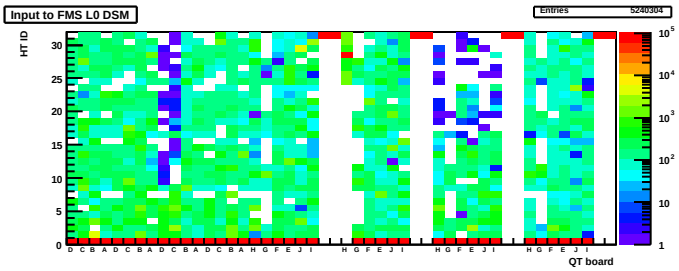

Input to FMS L0 DSM
Entries 81986
QT8(2) sum - simulated
QT board

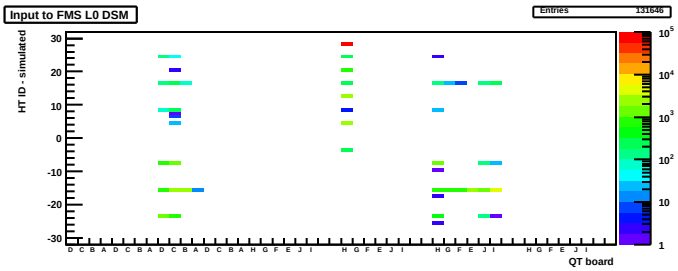

Input to FMS L0 DSM
Entries 5240304
QT8(3) sum
QT board

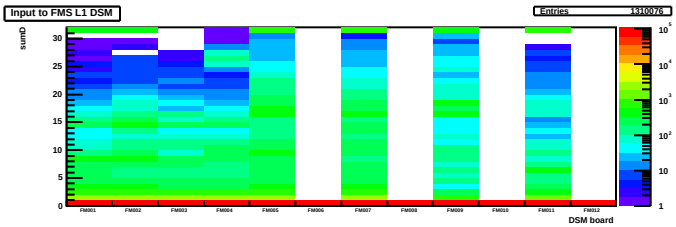

Input to FMS L0 DSM
Entries 111299
QT8(3) sum - simulated
QT board

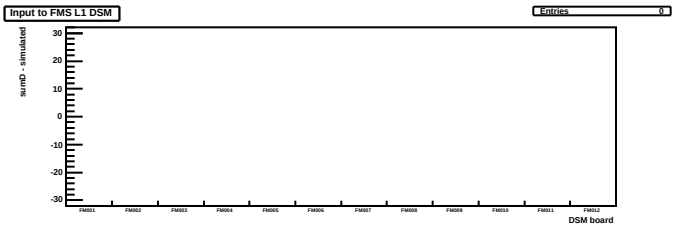

Input to FMS L0 DSM
Entries 5240304
HT ADC
QT board

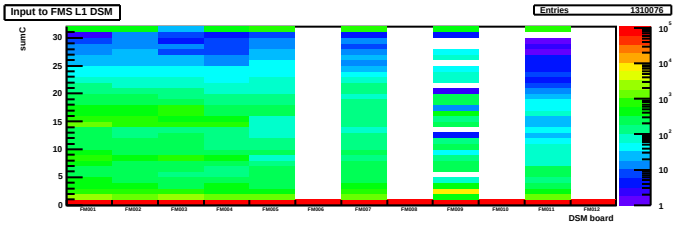

Input to FMS L0 DSM
Entries 23691
HT ADC - simulated
QT board

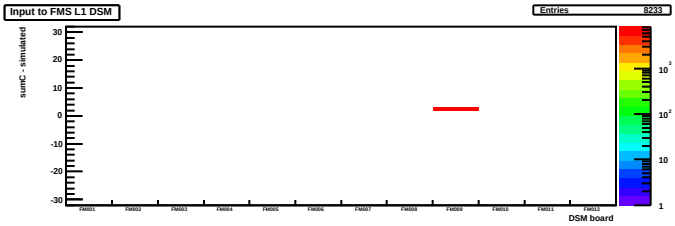

Input to FMS L0 DSM
Entries 5240304
HT ID
QT board

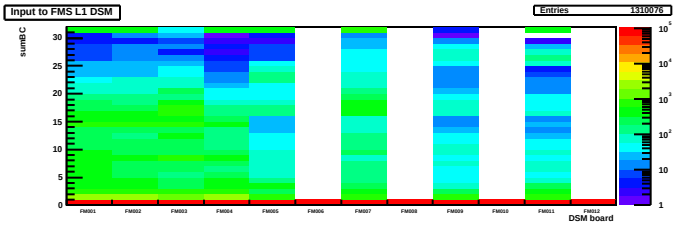

Input to FMS L0 DSM
Entries 131646
HT ID - simulated
QT board

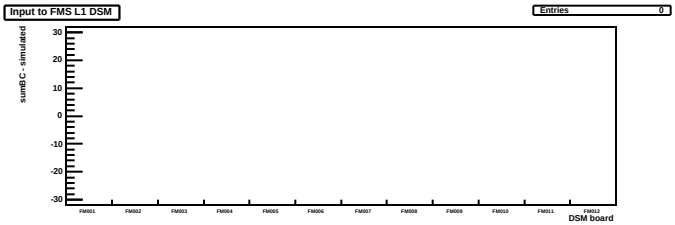

Input to FMS L1 DSM
Entries 1310076
sumD
DSM board

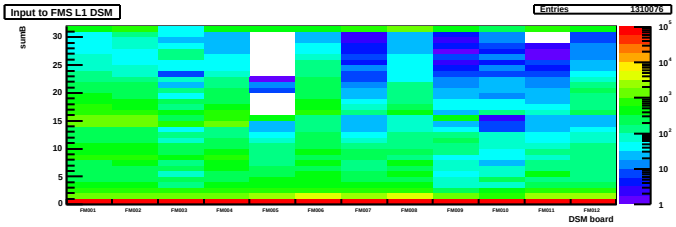

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

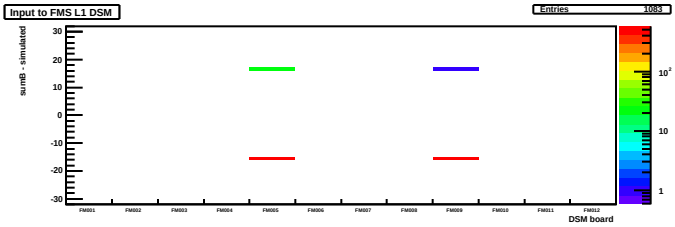

Input to FMS L1 DSM
Entries 1310076
sumC
DSM board

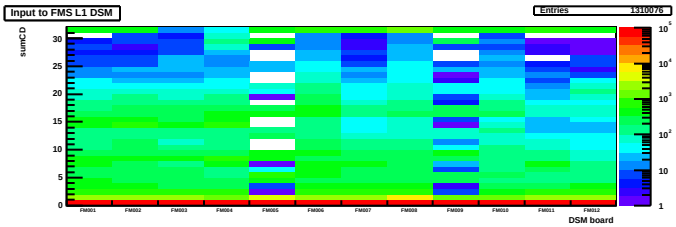

Input to FMS L1 DSM
Entries 8233
sumC - simulated
DSM board

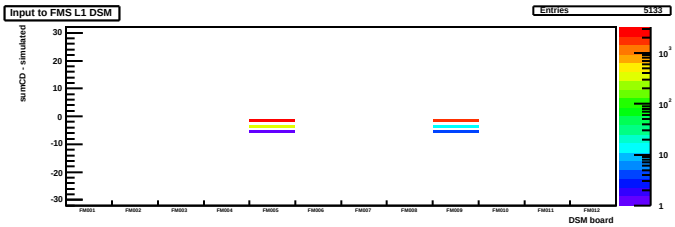

Input to FMS L1 DSM
Entries 1310076
sumBC
DSM board

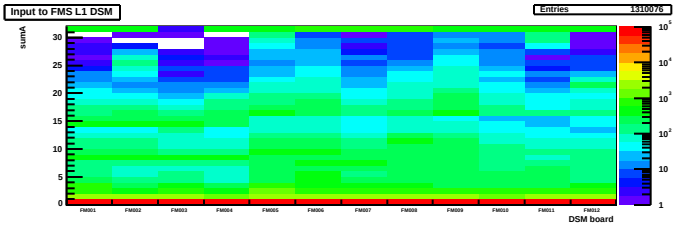

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

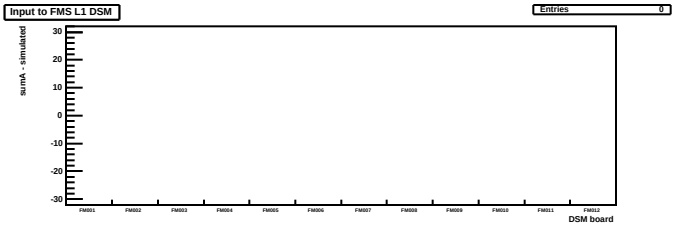

Input to FMS L1 DSM
Entries 1310076
sumB
DSM board

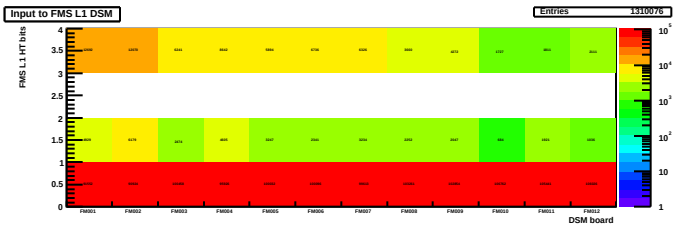

Input to FMS L1 DSM
Entries 1083
sumB - simulated
DSM board

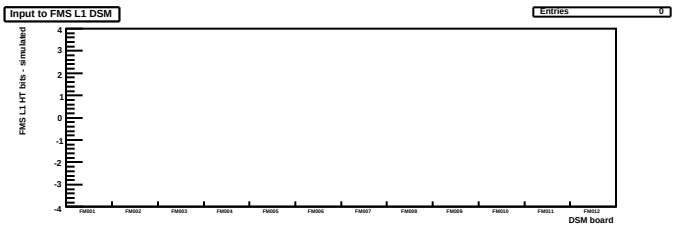

Input to FMS L1 DSM
Entries 1310076
sumCD
DSM board

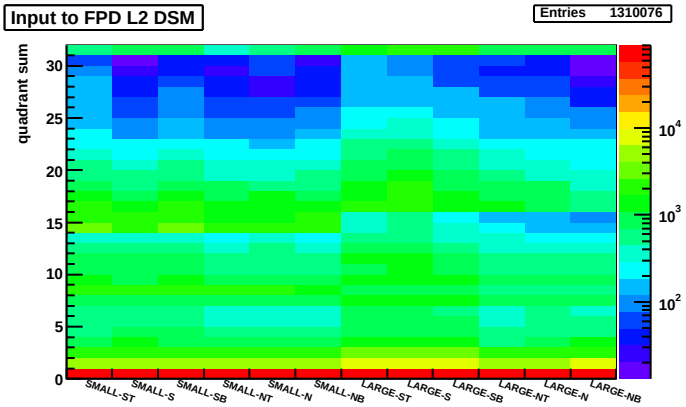

Input to FMS L1 DSM
Entries 5133
sumCD - simulated
DSM board

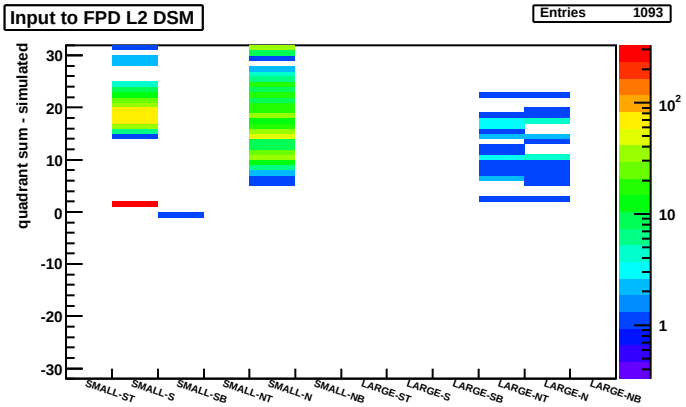

Input to FMS L1 DSM
Entries 1310076
sumA
DSM board

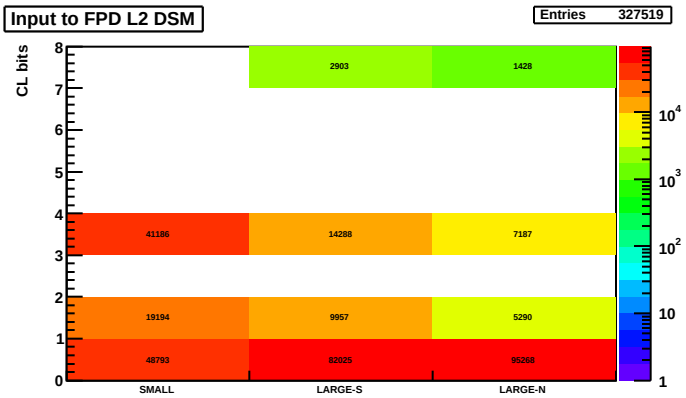

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

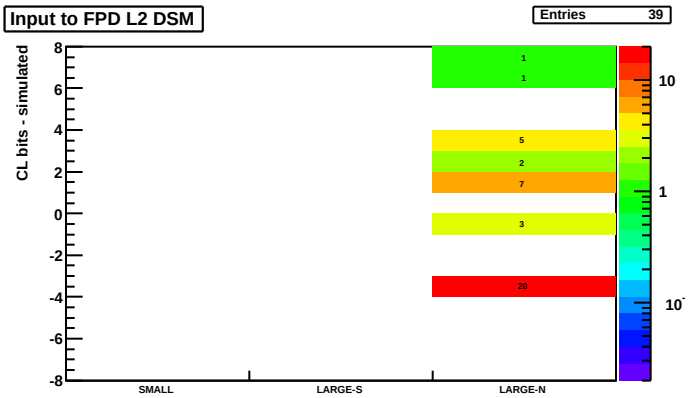

Input to FMS L1 DSM
Entries 1310076
FMS L1 HT bits
DSM board

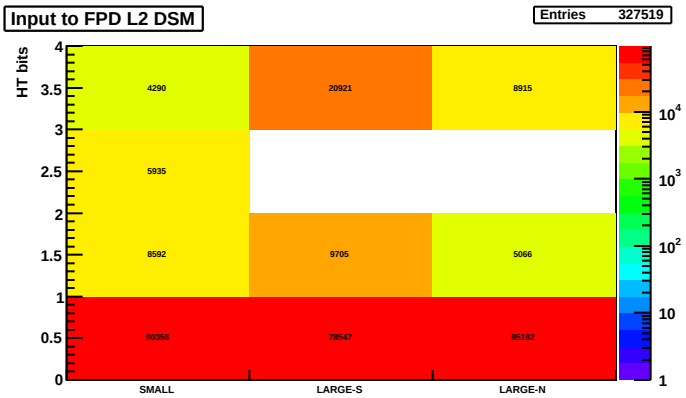

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

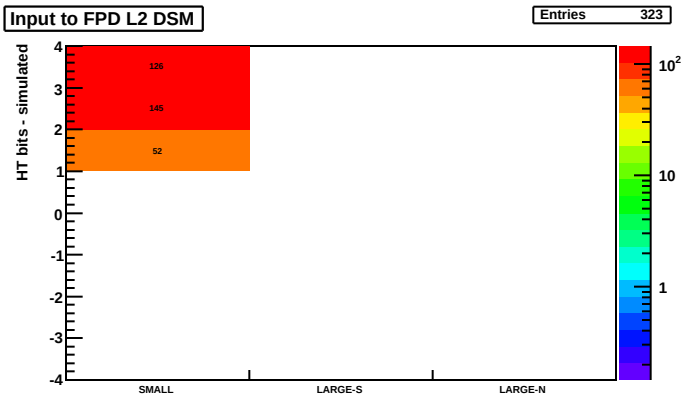

Input to FPD L2 DSM
Entries 1310076
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

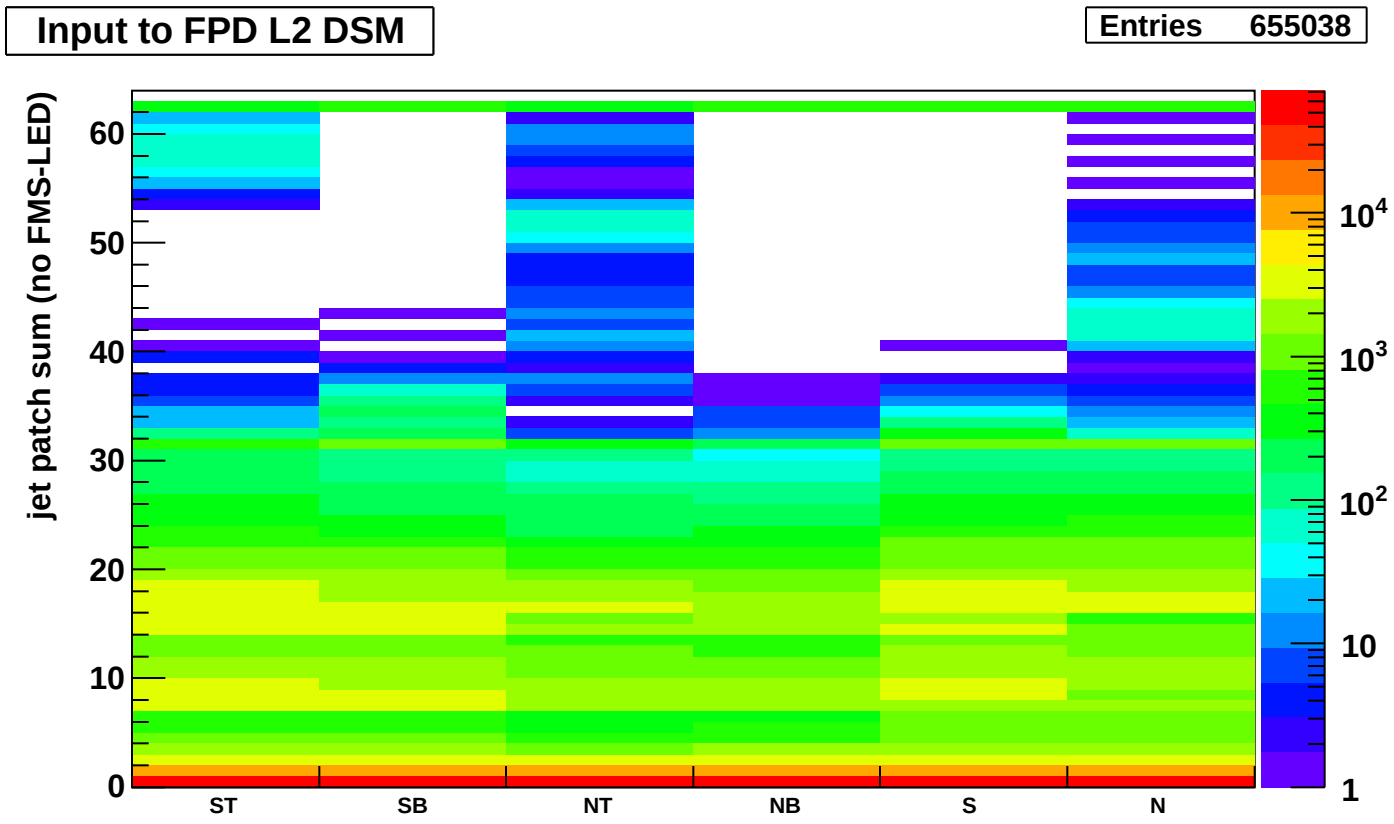

Input to FPD L2 DSM
Entries 1093
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

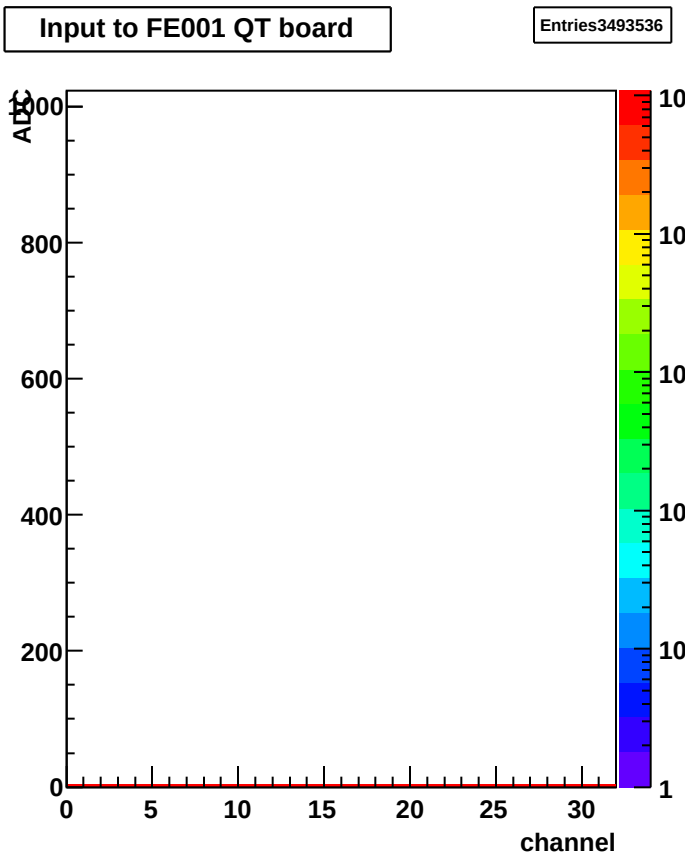

Input to FPD L2 DSM
Entries 327519
CL bits
2903
1428
41186
14288
7187
19194
9957
5290
48793
82025
95268
SMALL
LARGE-S
LARGE-N

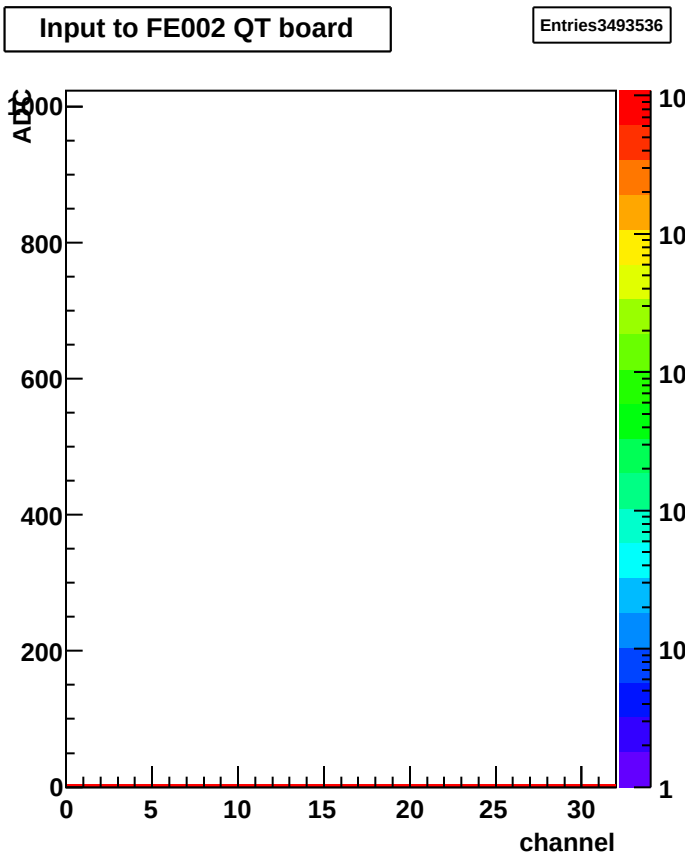

Input to FPD L2 DSM
Entries 39
CL bits - simulated
1
1
5
2
7
3
20
SMALL
LARGE-S
LARGE-N

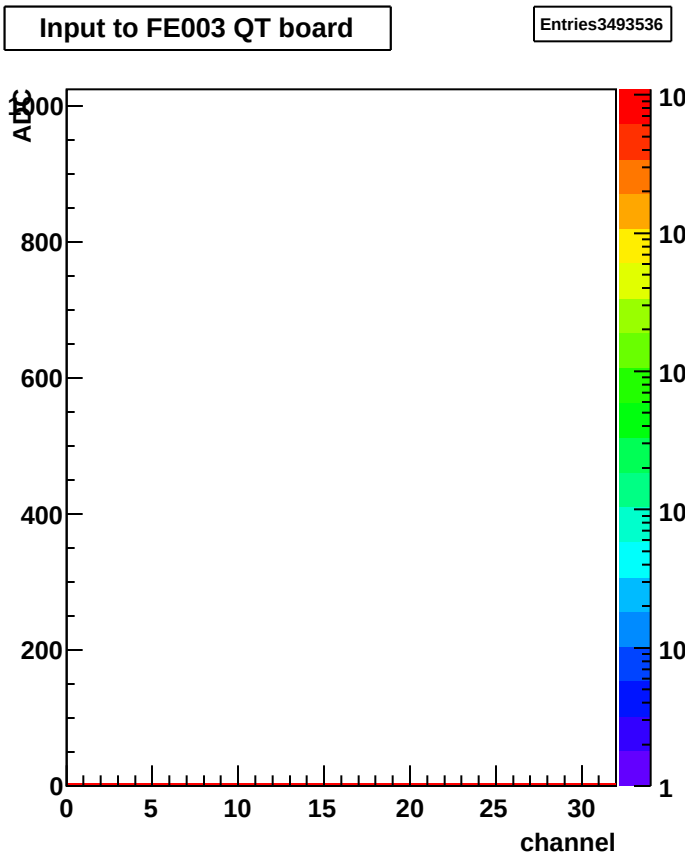

Input to FPD L2 DSM
Entries 327519
HT bits
4290
20921
8915
5935
8592
9705
5066
90356
78547
95192
SMALL
LARGE-S
LARGE-N

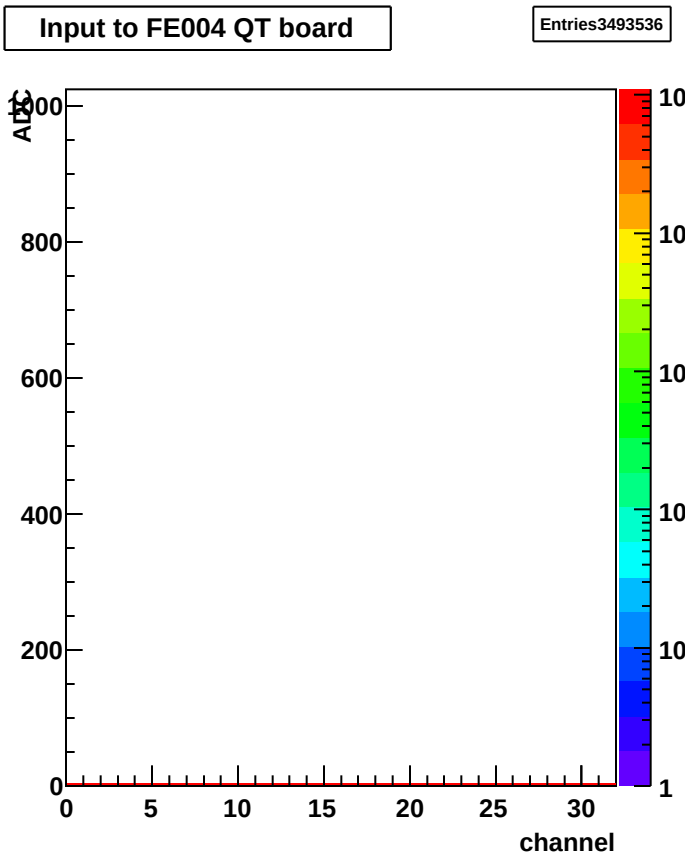

Input to FPD L2 DSM
Entries 323
HT bits - simulated
126
145
52
SMALL
LARGE-S
LARGE-N

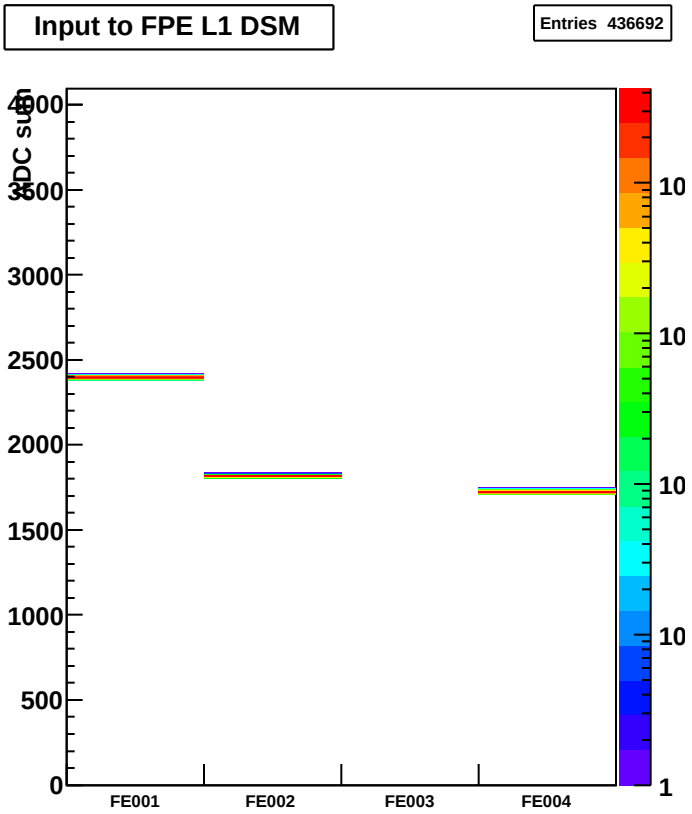

Input to FPD L2 DSM
Entries 655038
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

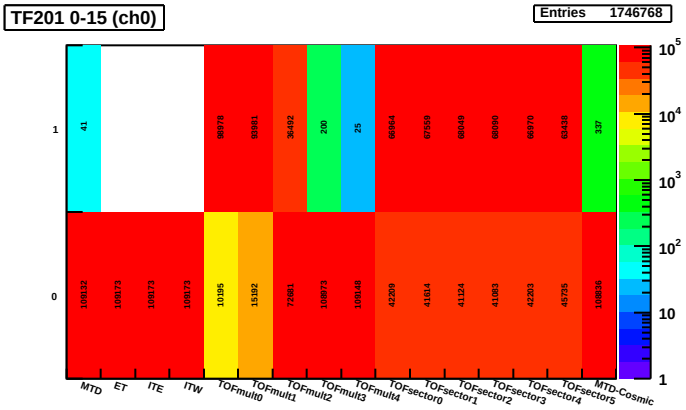

Input to FE001 QT board
Entries3493536
ADC
channel

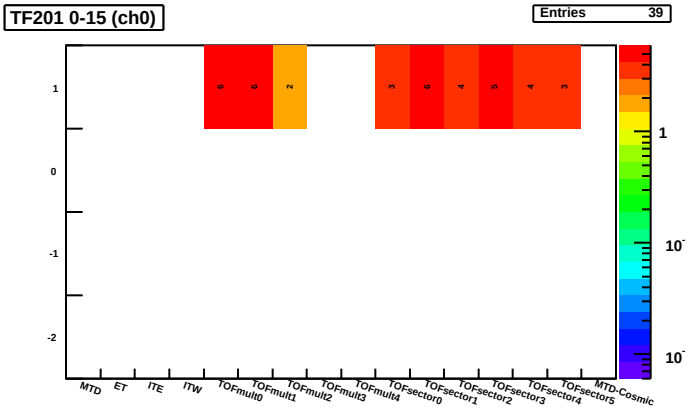

Input to FE002 QT board
Entries3493536
ADC
channel

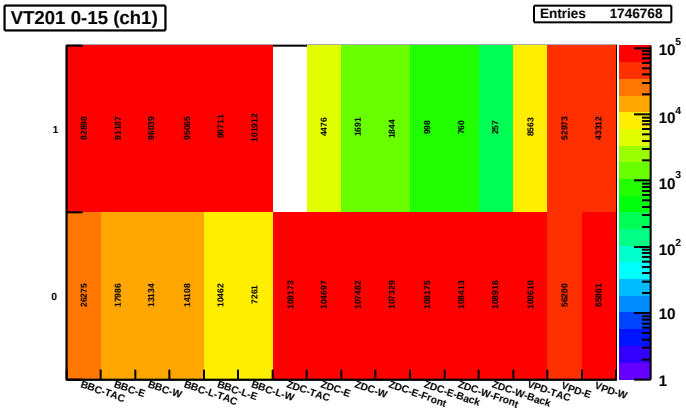

Input to FE003 QT board
Entries3493536
ADC
channel

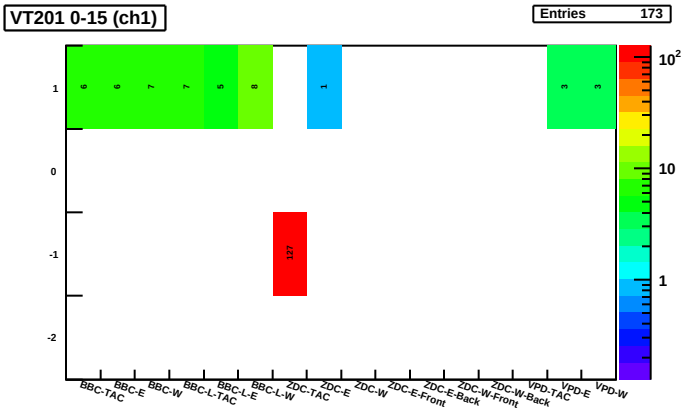

Input to FE004 QT board
Entries3493536
ADC
channel

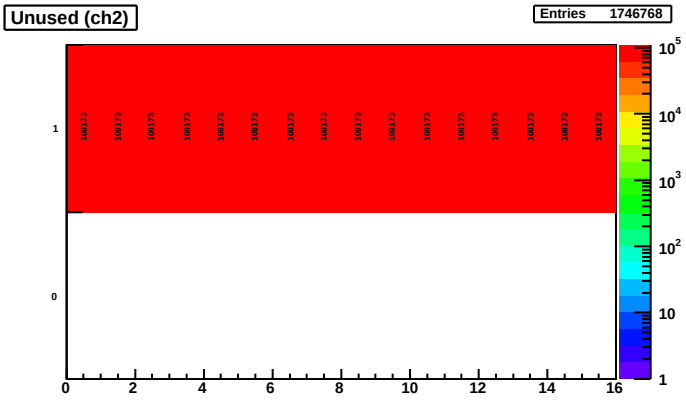

Input to FPE L1 DSM
Entries 436692
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

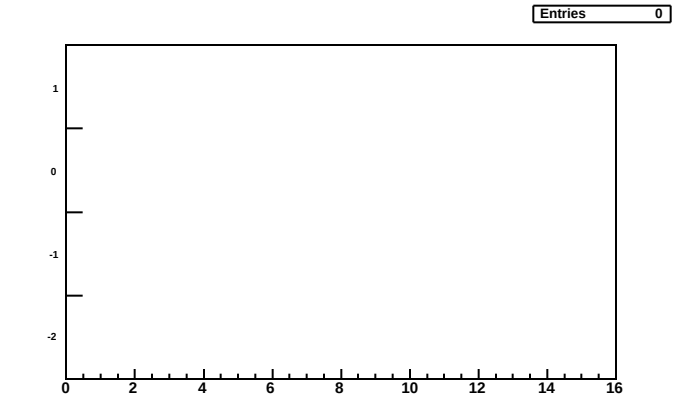

TF201 0-15 (ch0)
Entries 1746768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 39
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1746768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 173
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1746768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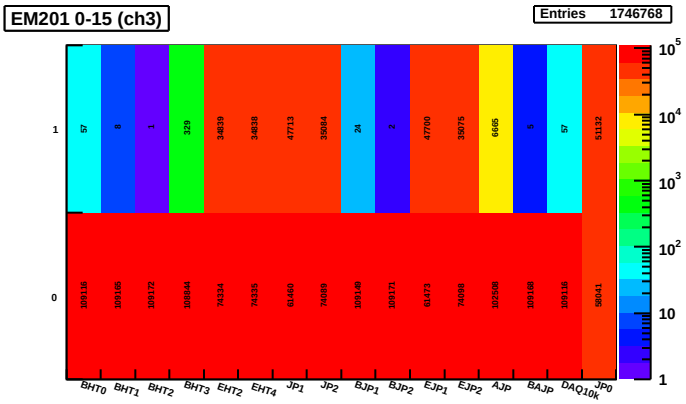
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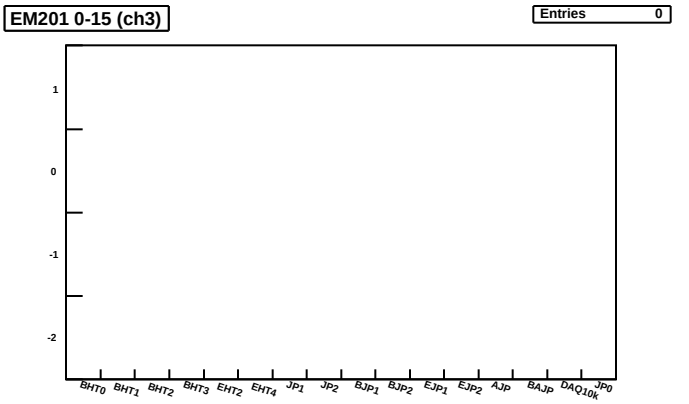
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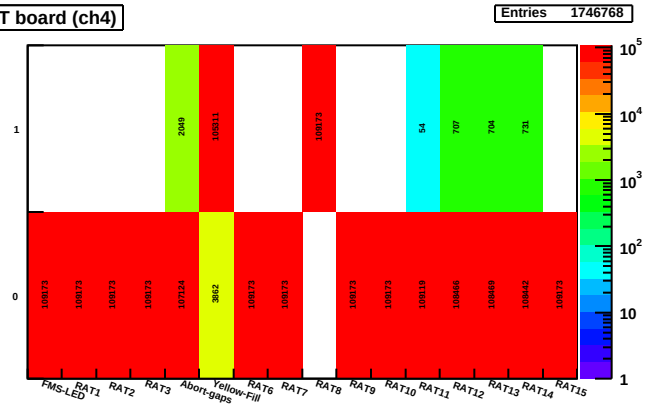
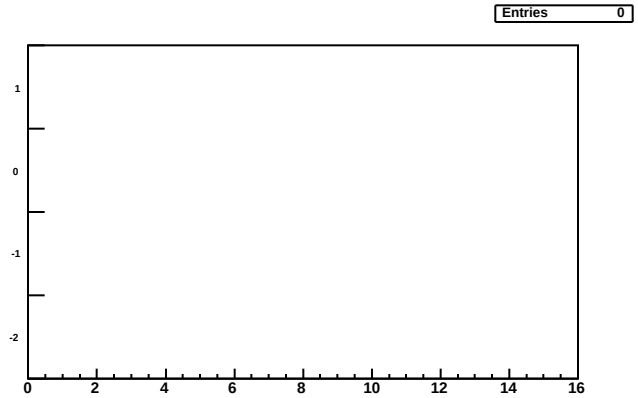
RAT board (ch4)





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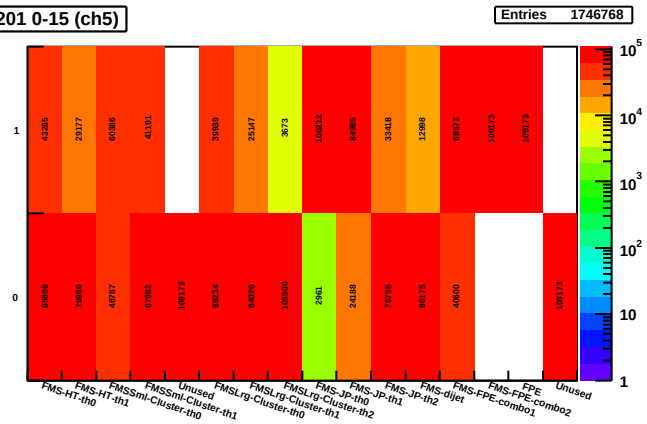

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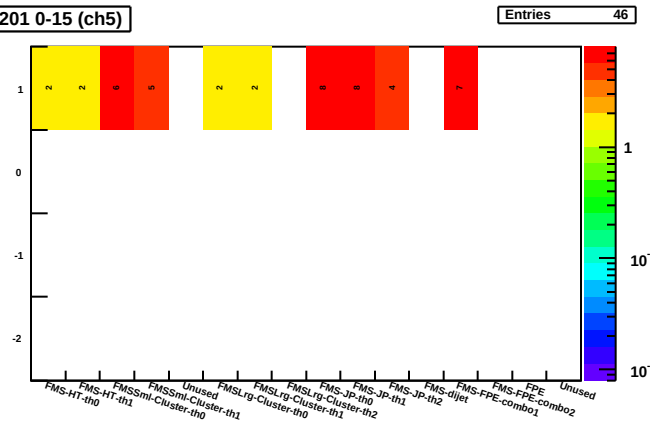

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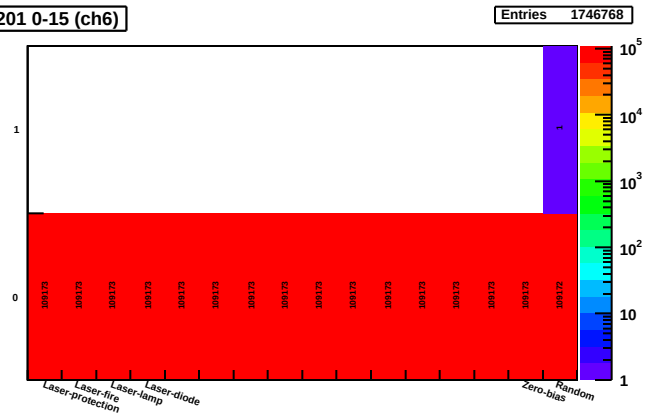

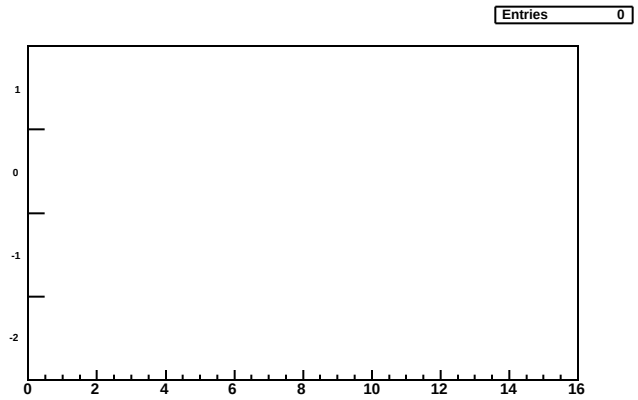
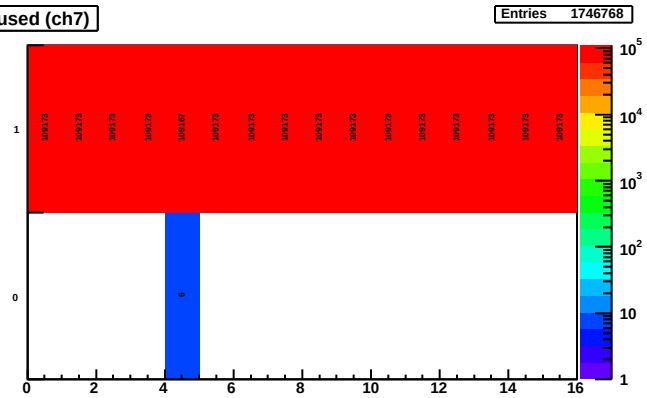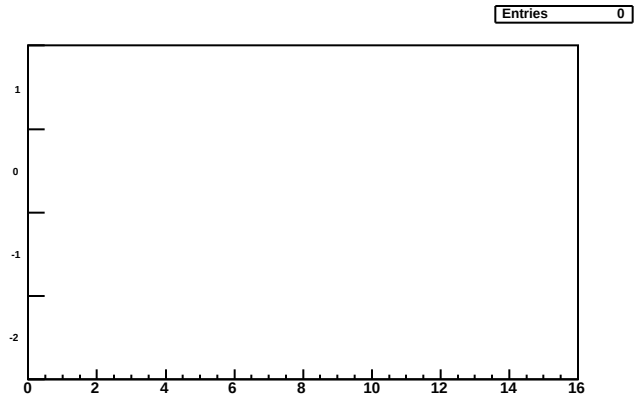
Unused (ch7)