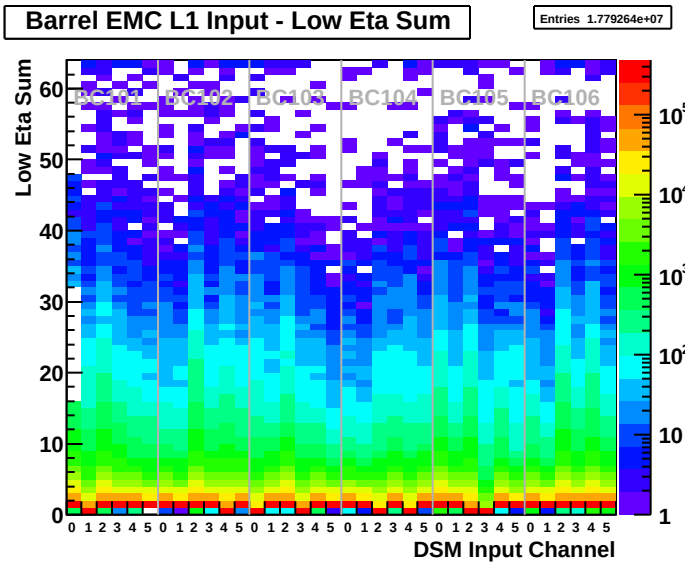
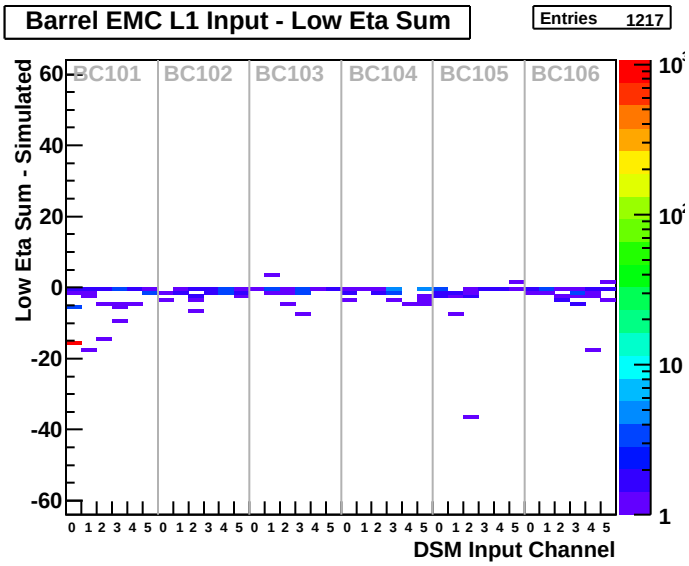
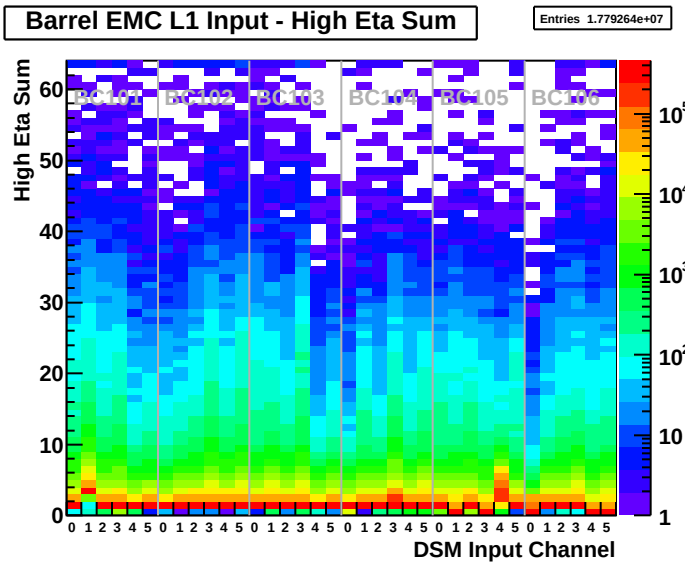
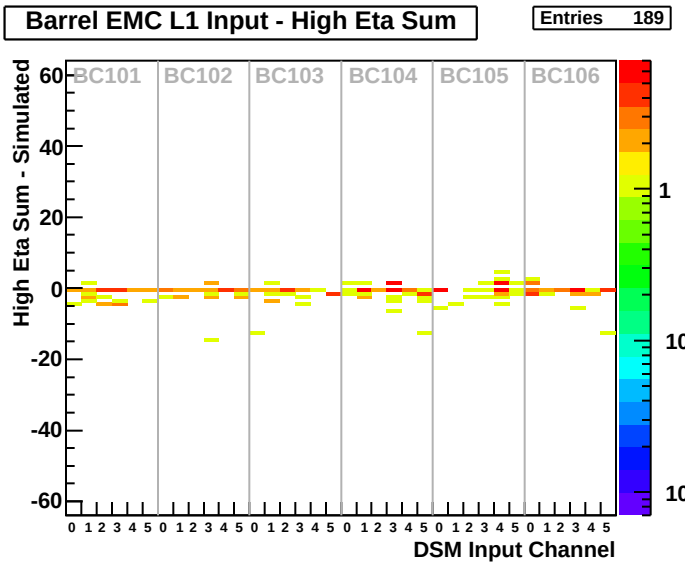
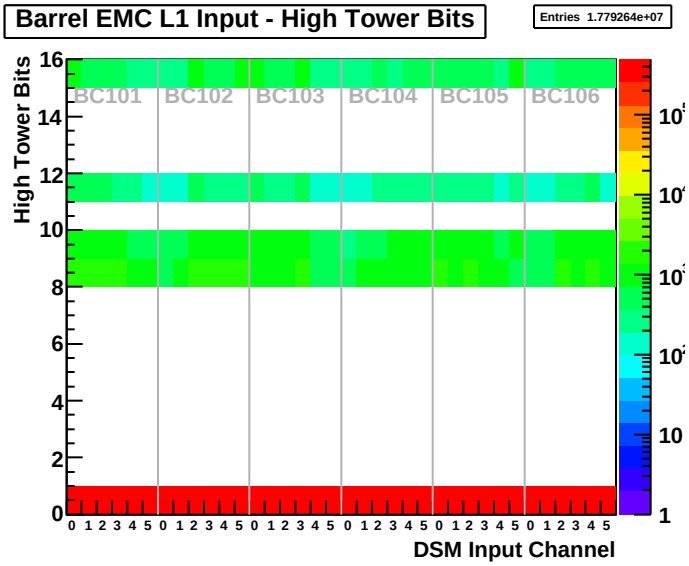
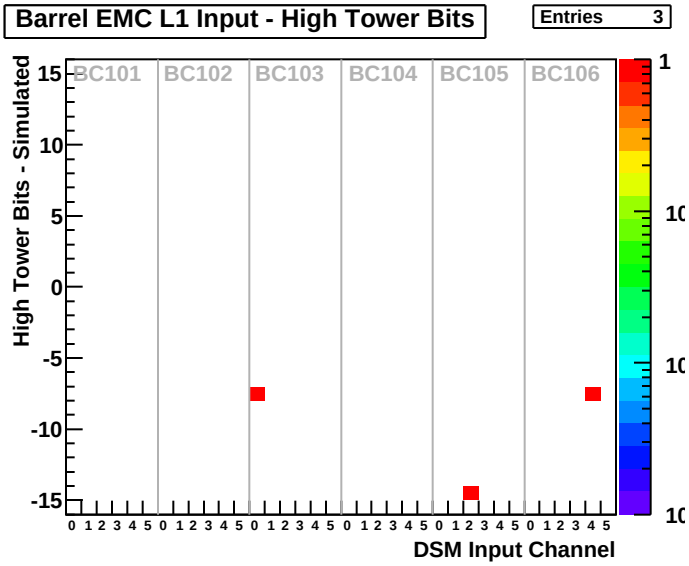

Barrel EMC L1 Input - Low Eta Sum
Entries 1.779264e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

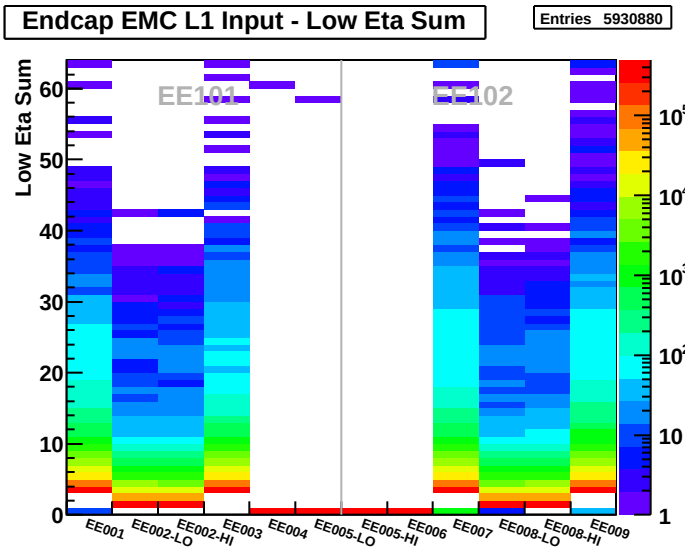

Barrel EMC L1 Input - Low Eta Sum
Entries 1217
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

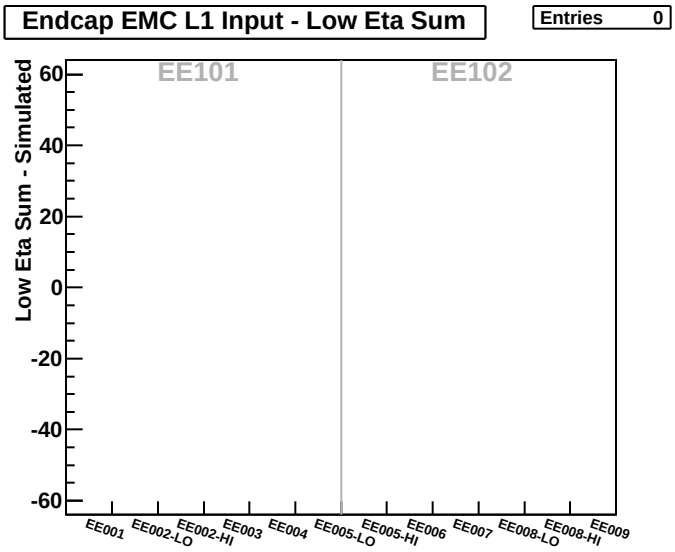

Barrel EMC L1 Input - High Eta Sum
Entries 1.779264e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

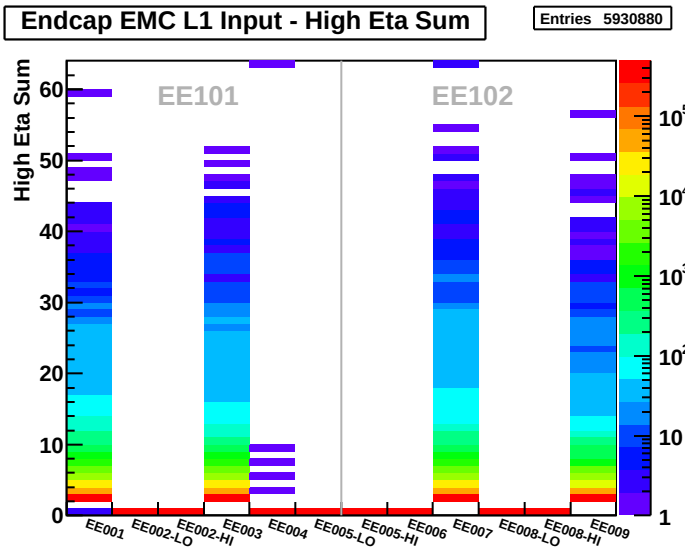

Barrel EMC L1 Input - High Eta Sum
Entries 189
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

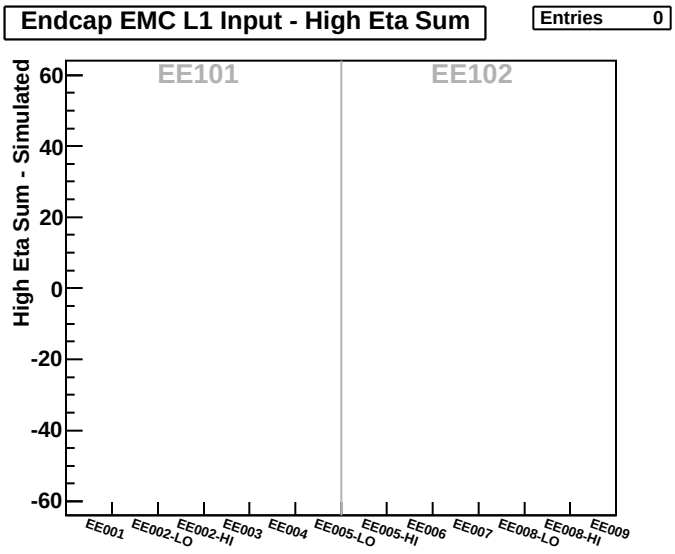

Barrel EMC L1 Input - High Tower Bits
Entries 1.779264e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

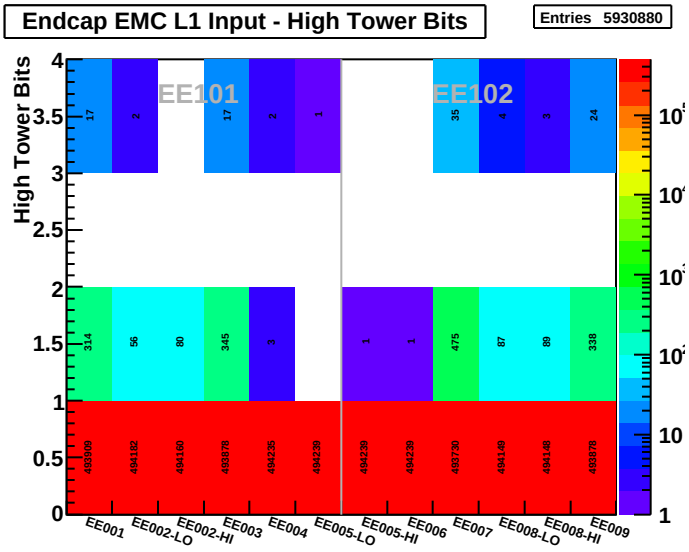

Barrel EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

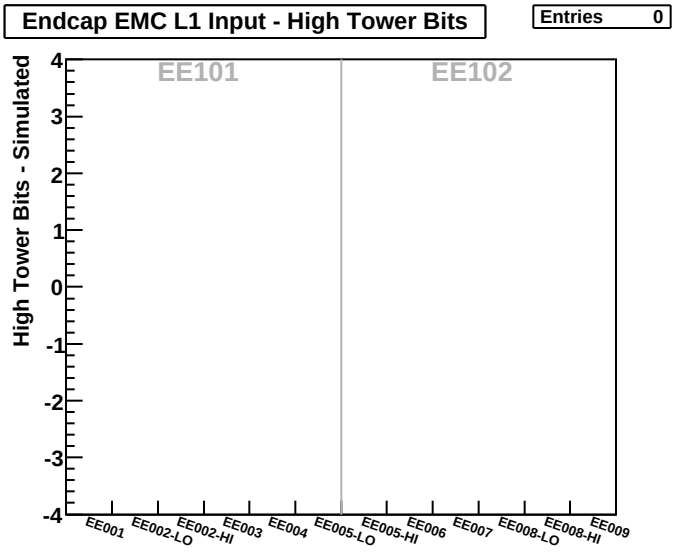

Endcap EMC L1 Input - Low Eta Sum
Entries 5930880
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

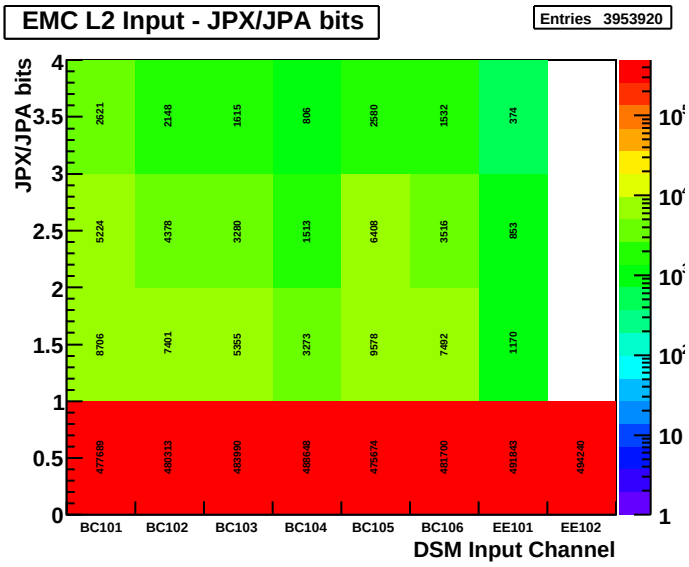

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

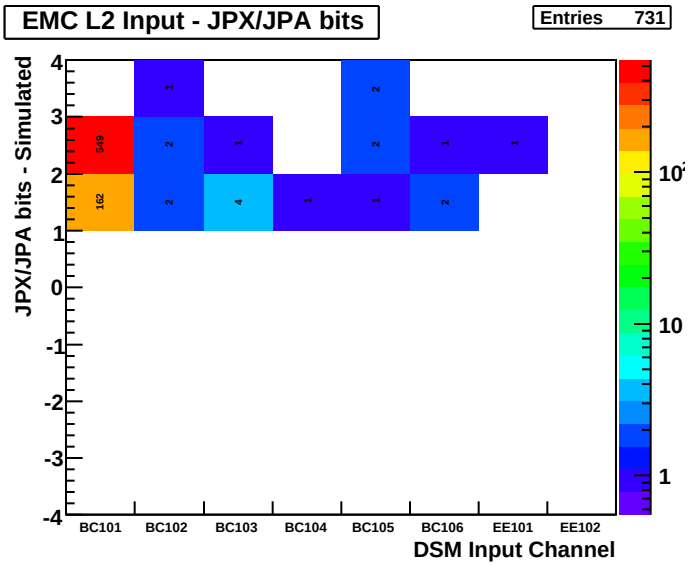

Endcap EMC L1 Input - High Eta Sum
Entries 5930880
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

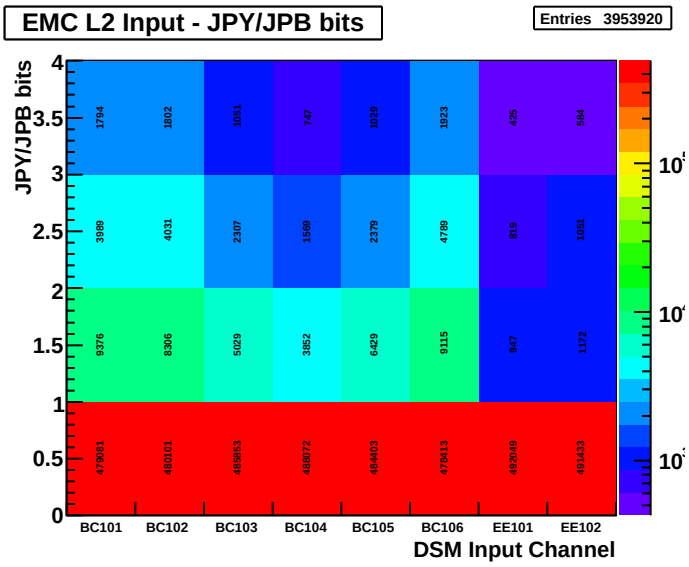

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

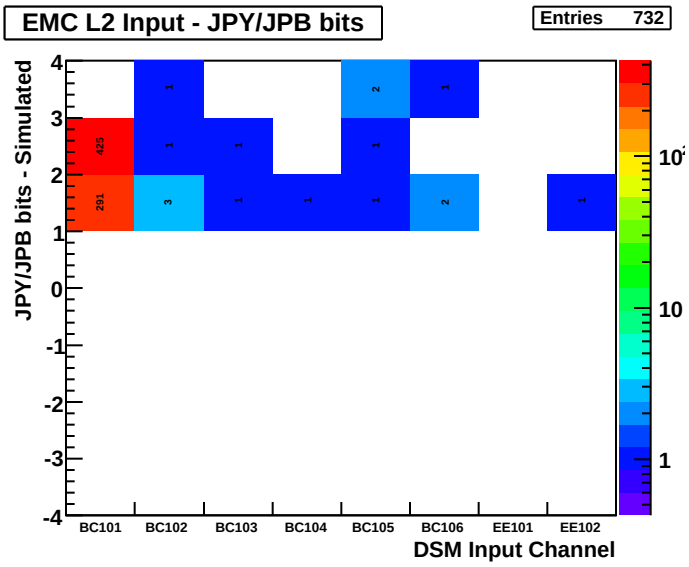

Endcap EMC L1 Input - High Tower Bits
Entries 5930880
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

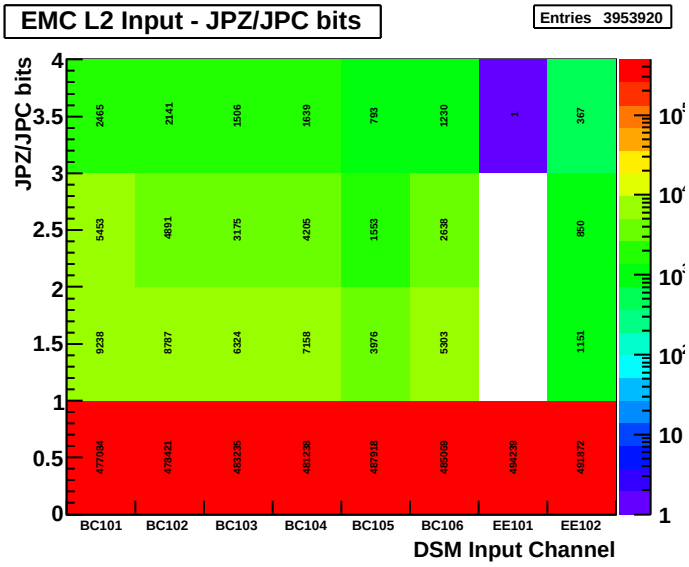

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

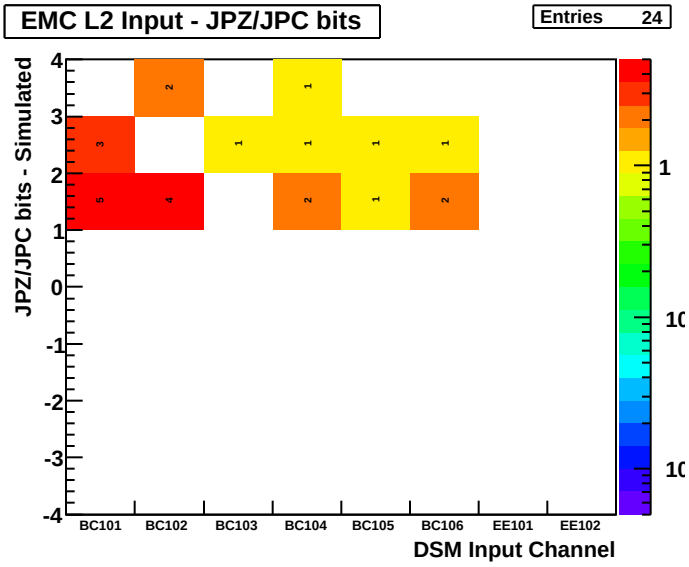

EMC L2 Input - JPX/JPA bits
Entries 3953920
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

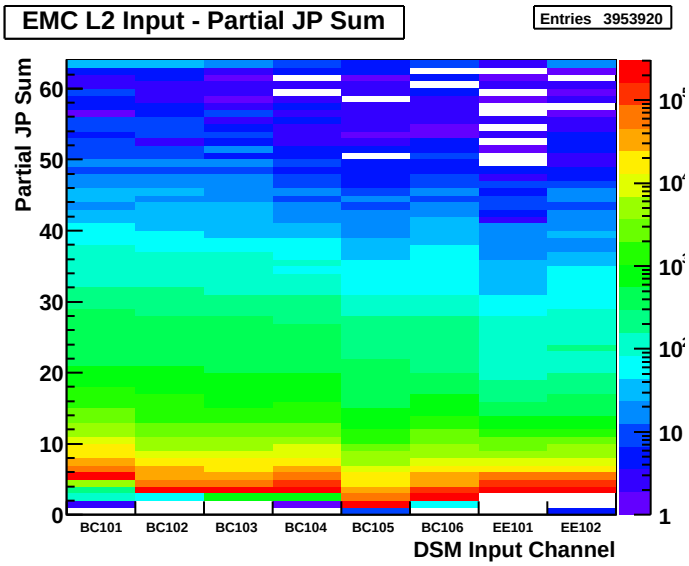

EMC L2 Input - JPX/JPA bits
Entries 731
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

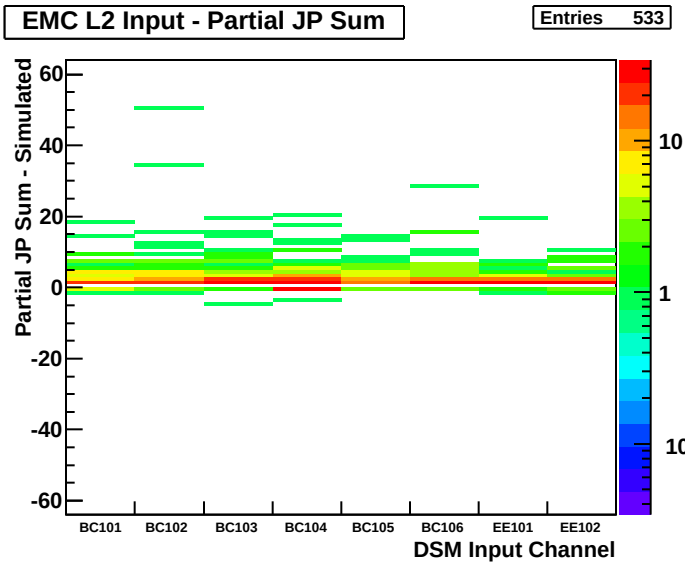

EMC L2 Input - JPY/JPB bits
Entries 3953920
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

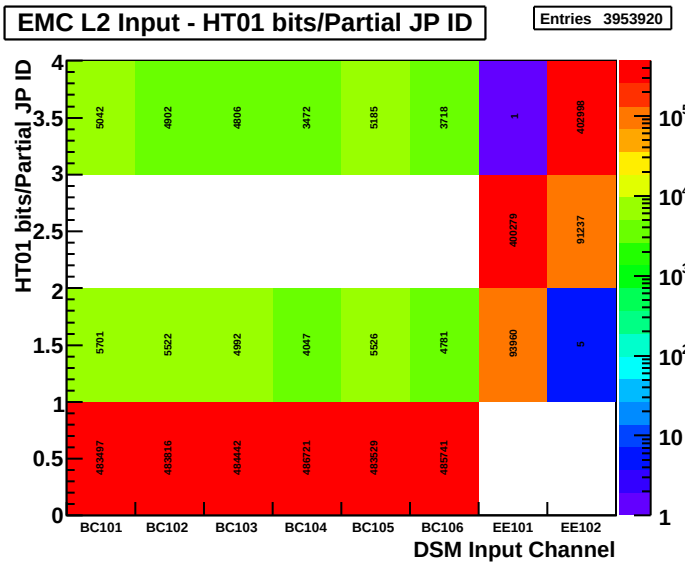

EMC L2 Input - JPY/JPB bits
Entries 732
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

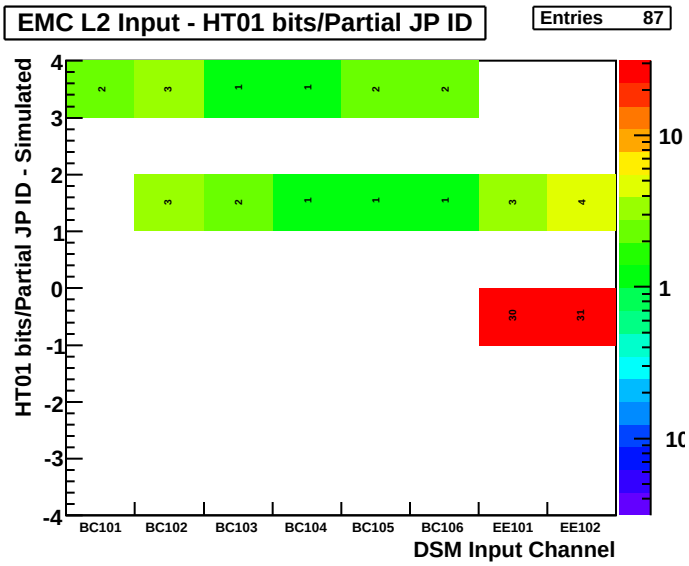

EMC L2 Input - JPZ/JPC bits
Entries 3953920
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

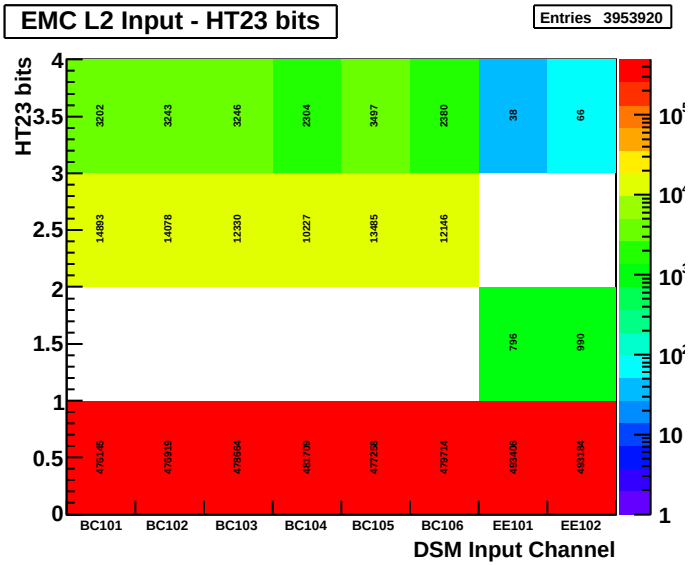

EMC L2 Input - JPZ/JPC bits
Entries 24
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

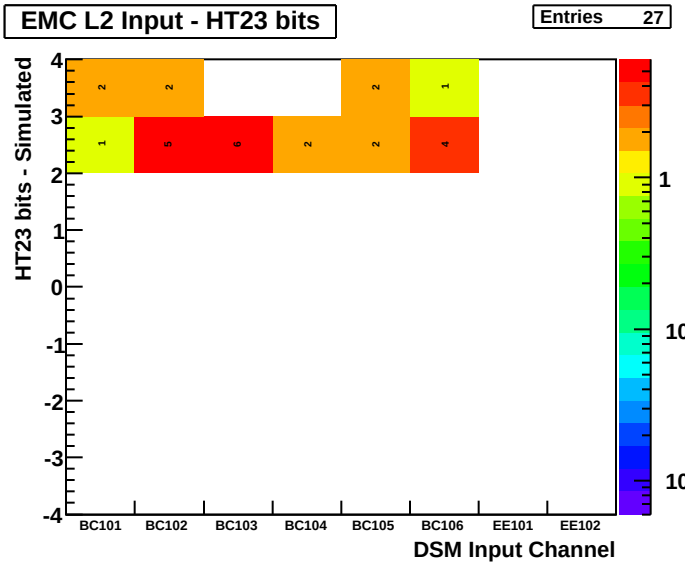

EMC L2 Input - Partial JP Sum
Entries 3953920
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

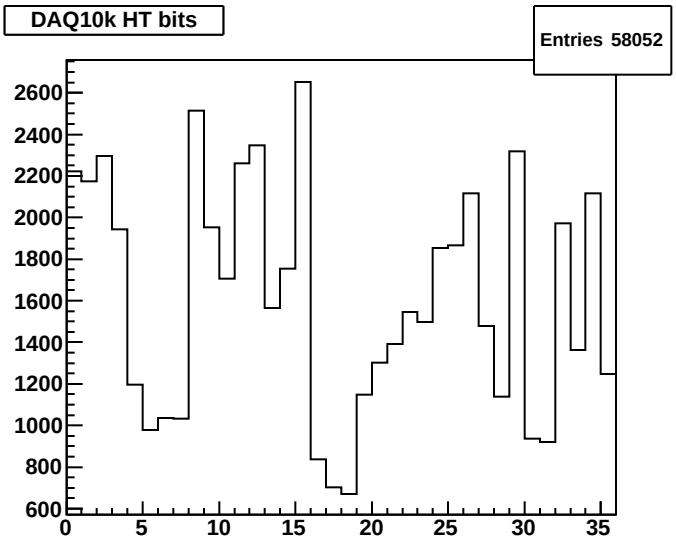

EMC L2 Input - Partial JP Sum
Entries 533
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

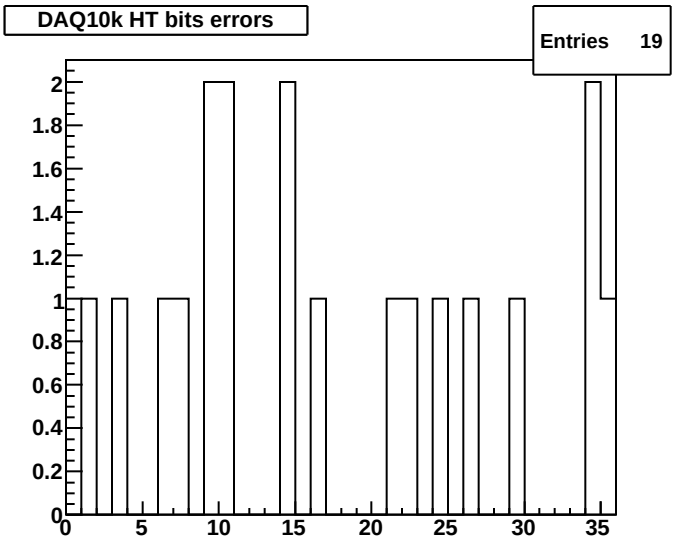

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3953920
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

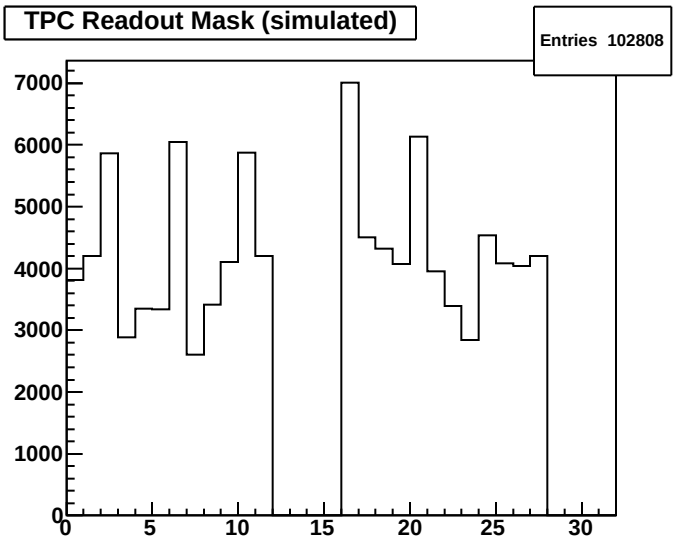

EMC L2 Input - HT01 bits/Partial JP ID
Entries 87
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

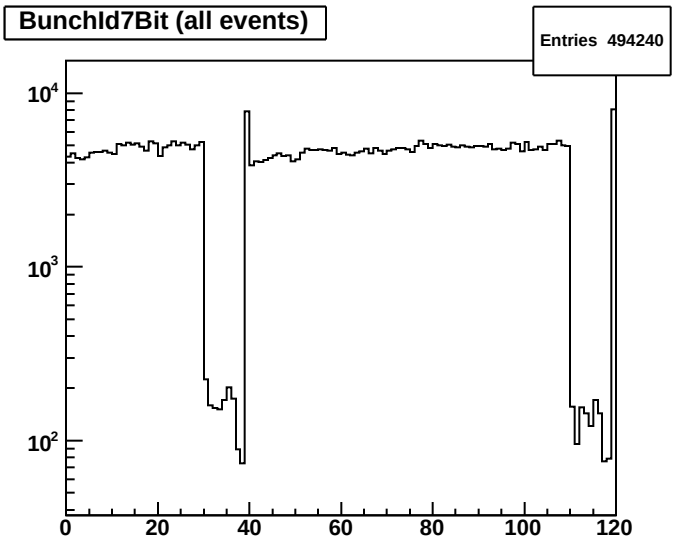

EMC L2 Input - HT23 bits
Entries 3953920
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

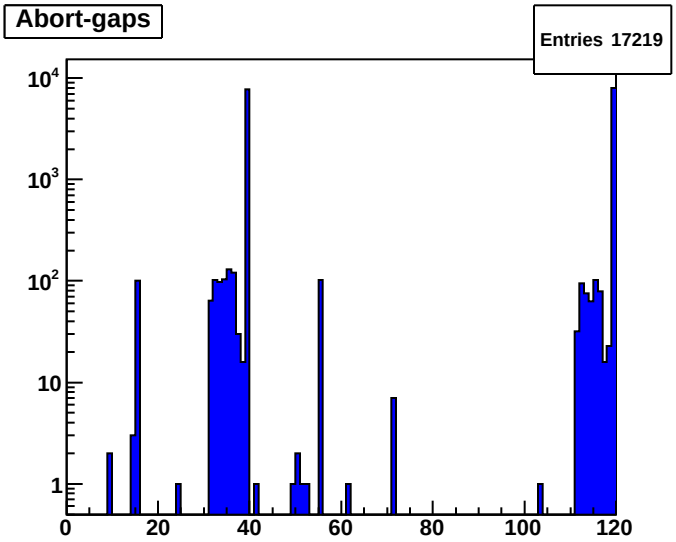

EMC L2 Input - HT23 bits
Entries 27
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

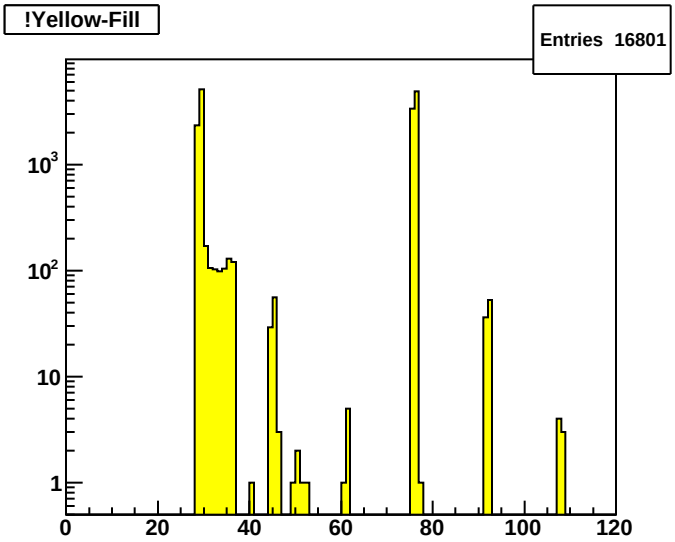

DAQ10k HT bits
Entries 58052

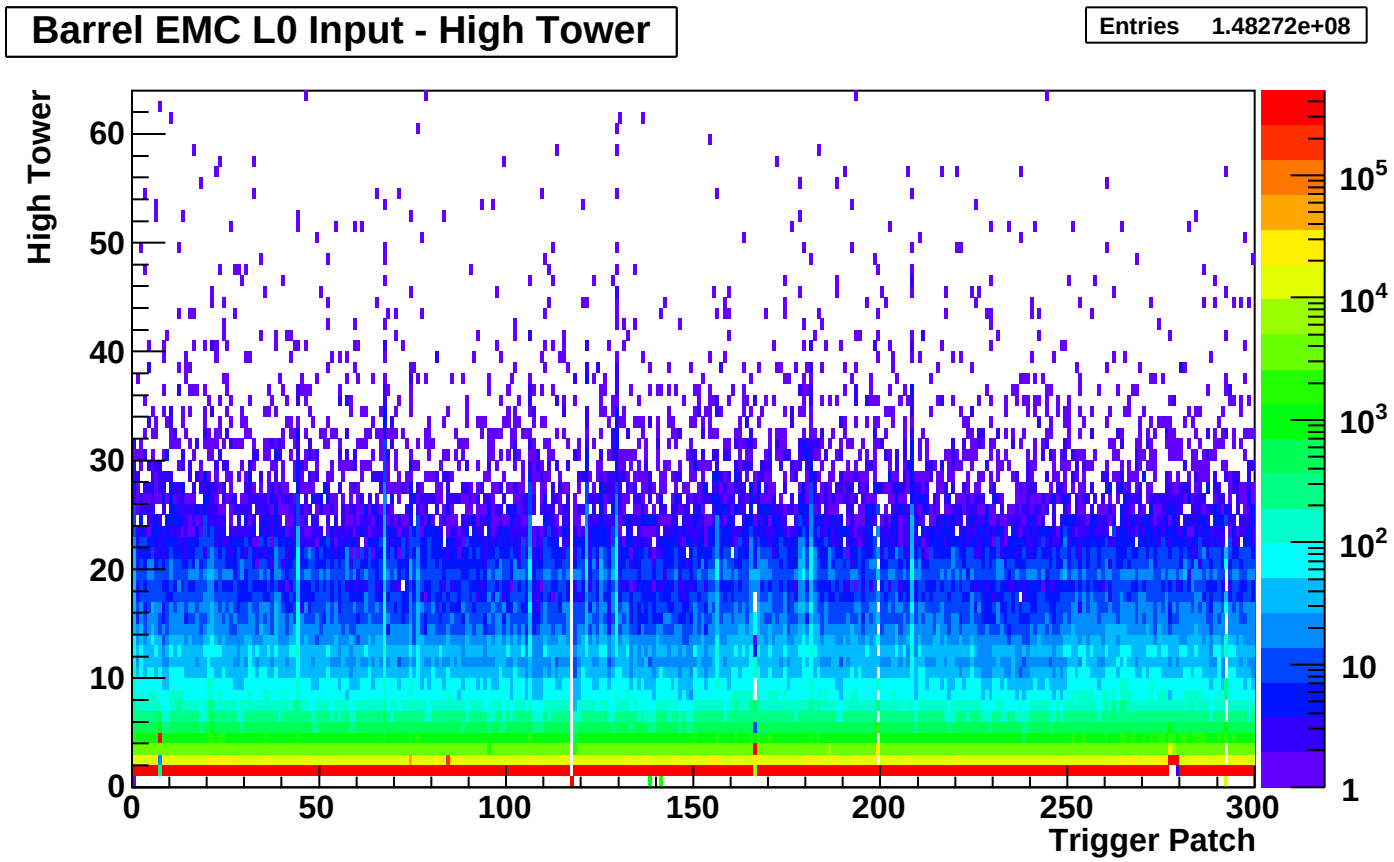

DAQ10k HT bits errors
Entries 19

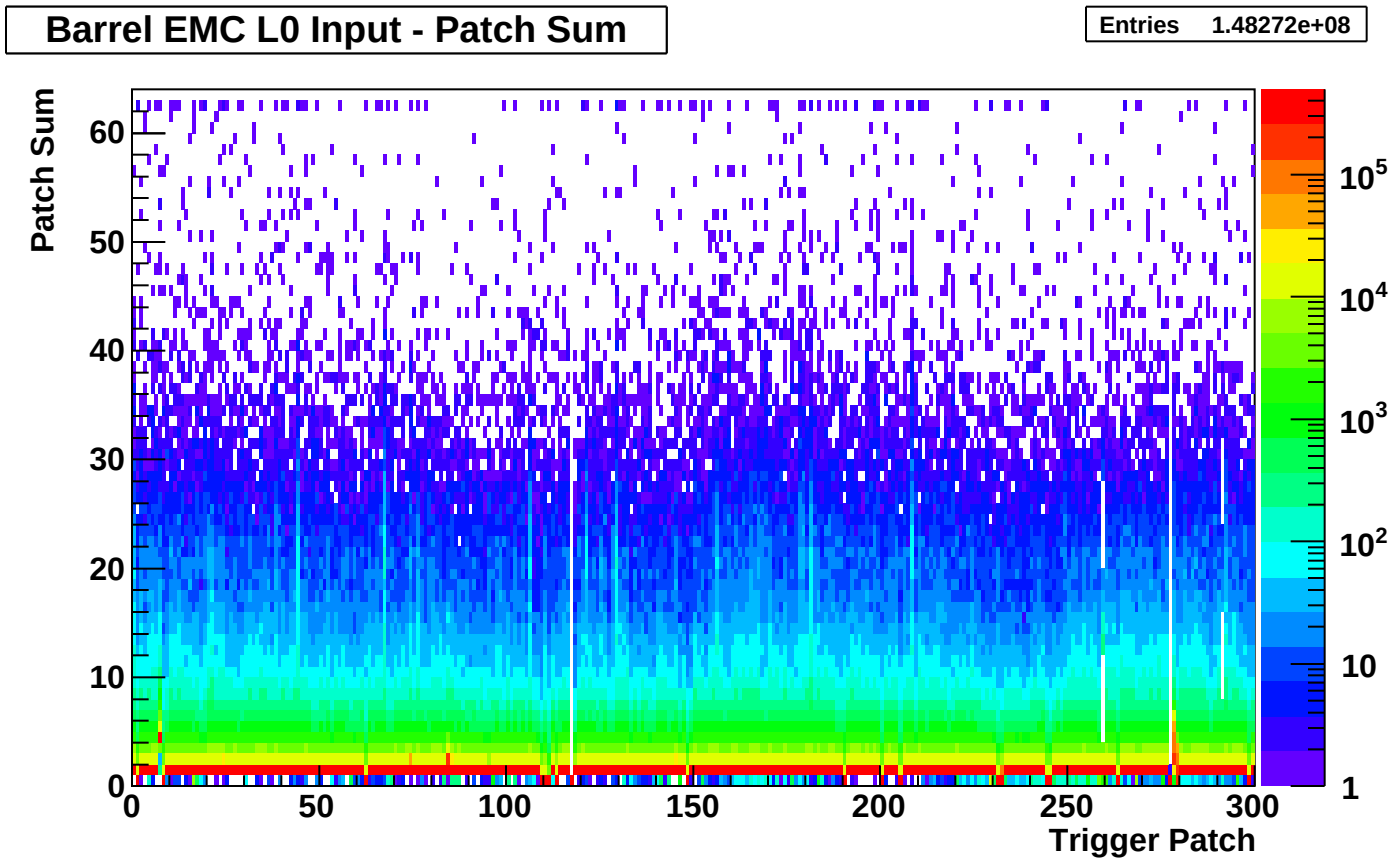

TPC Readout Mask (simulated)
Entries 102808

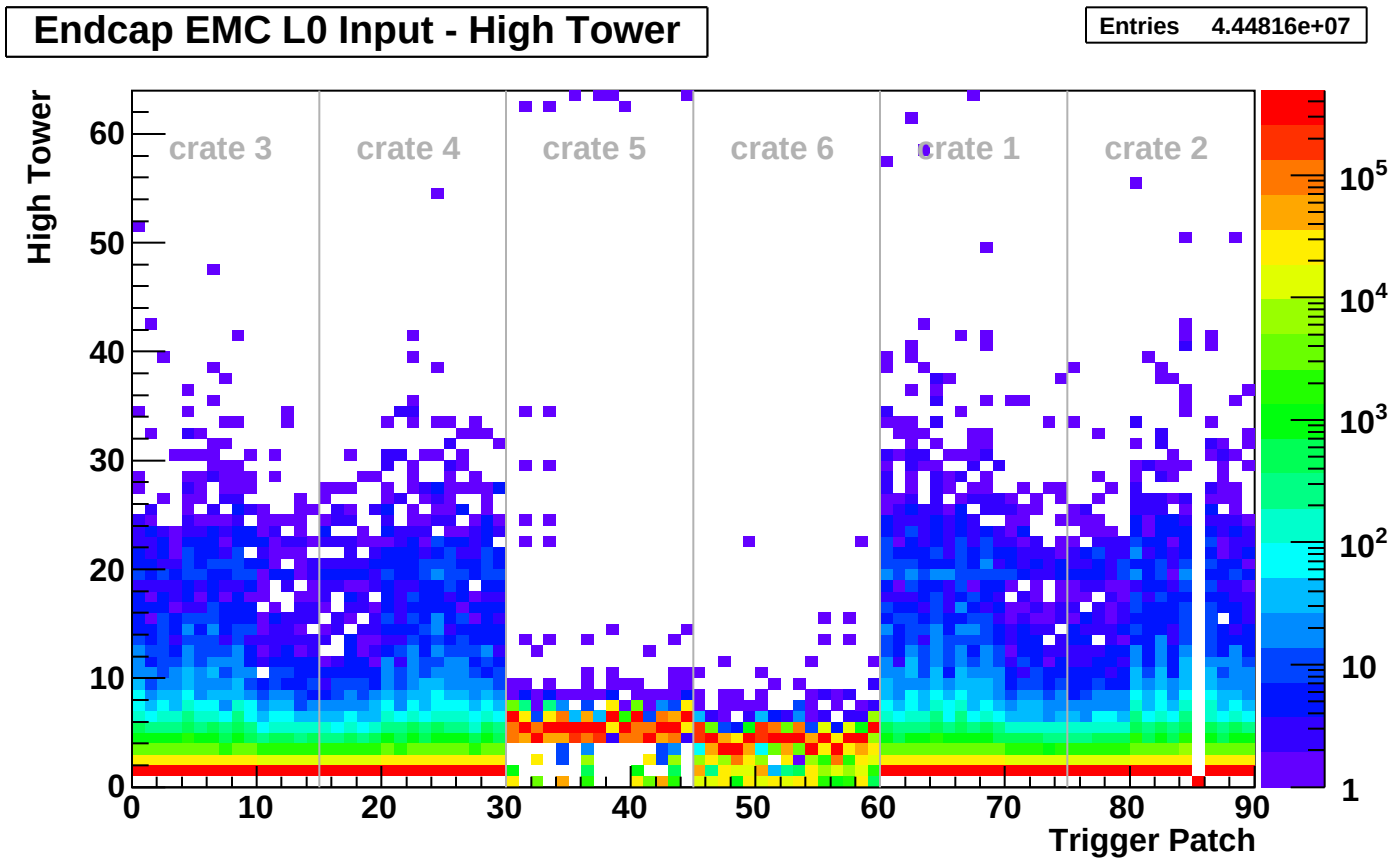

BunchId7Bit (all events)
Entries 494240

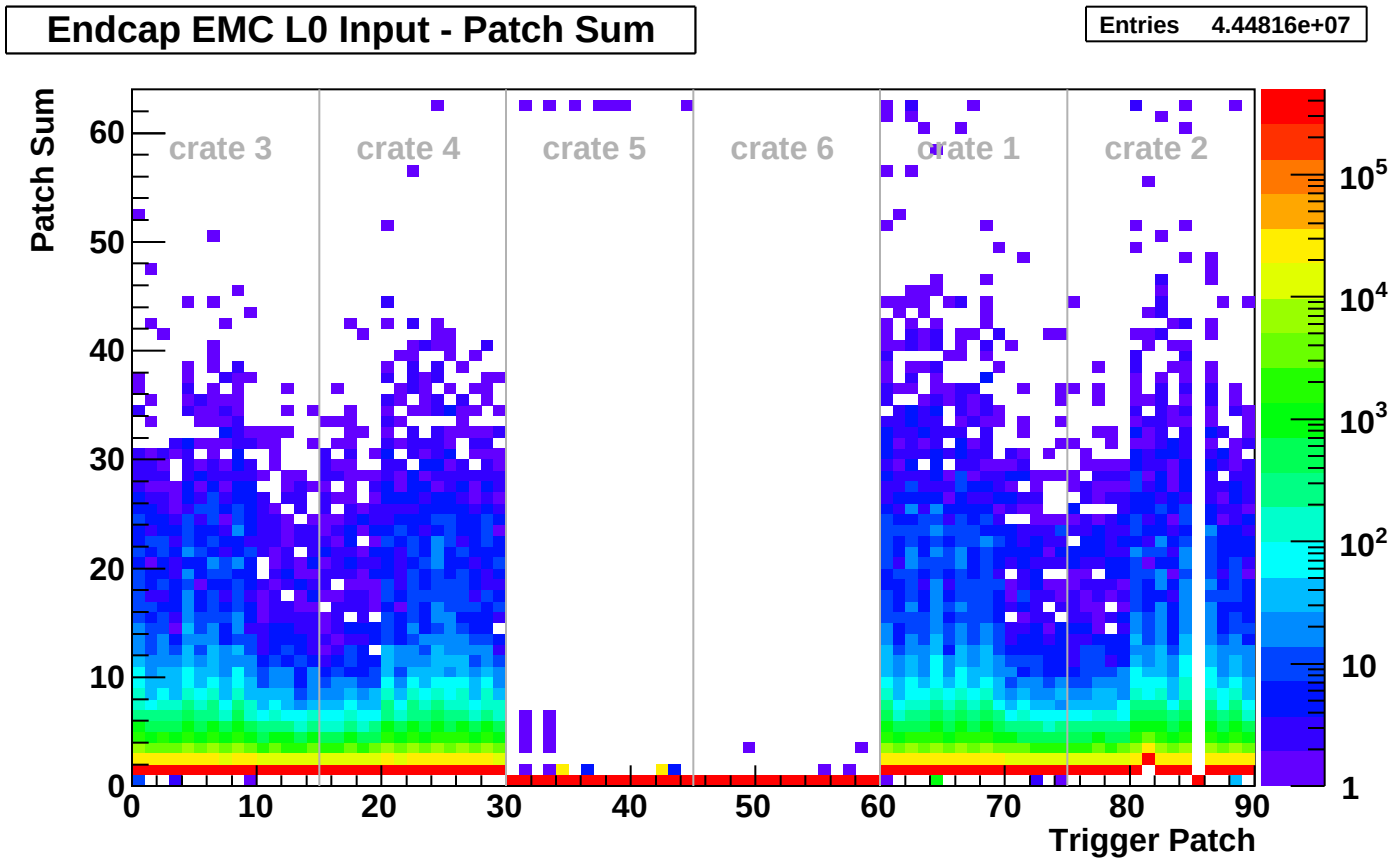

Abort-gaps
Entries 17219

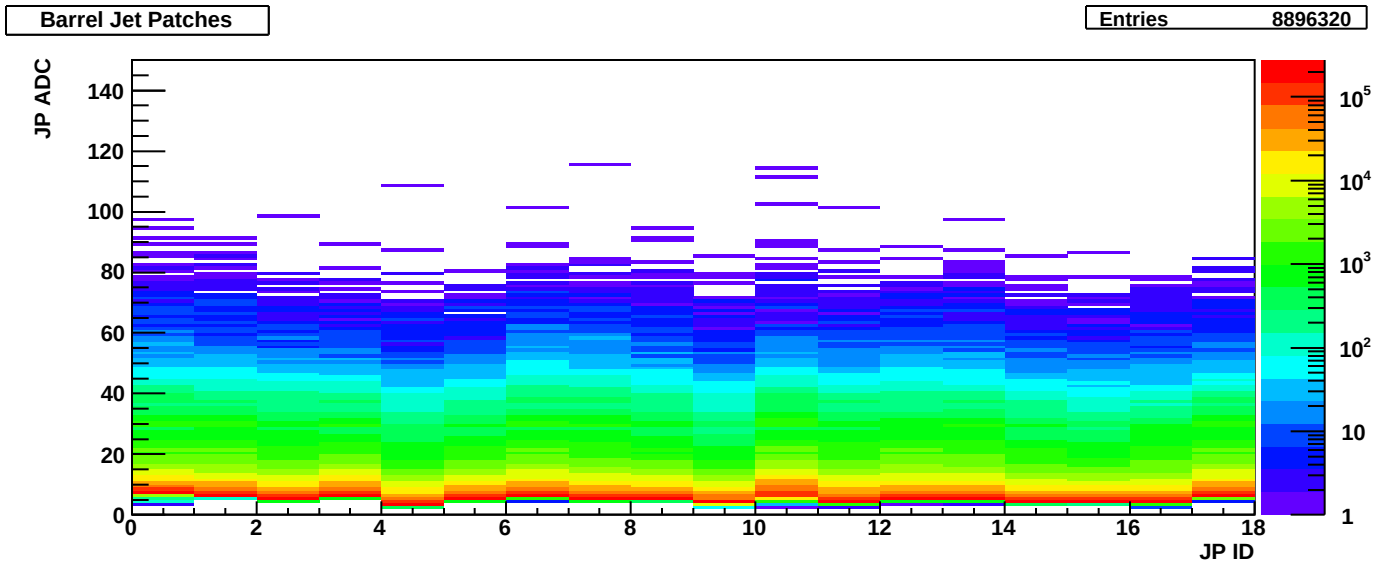

!Yellow-Fill
Entries 16801

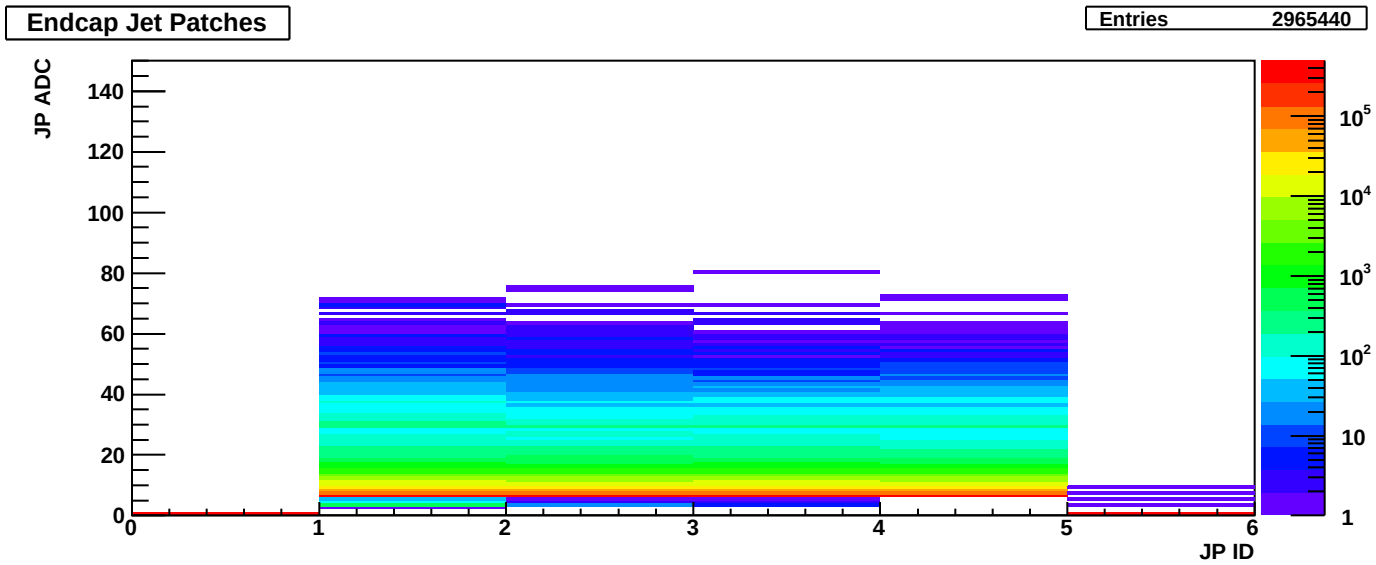

Barrel EMC L0 Input - High Tower
Entries 1.48272e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

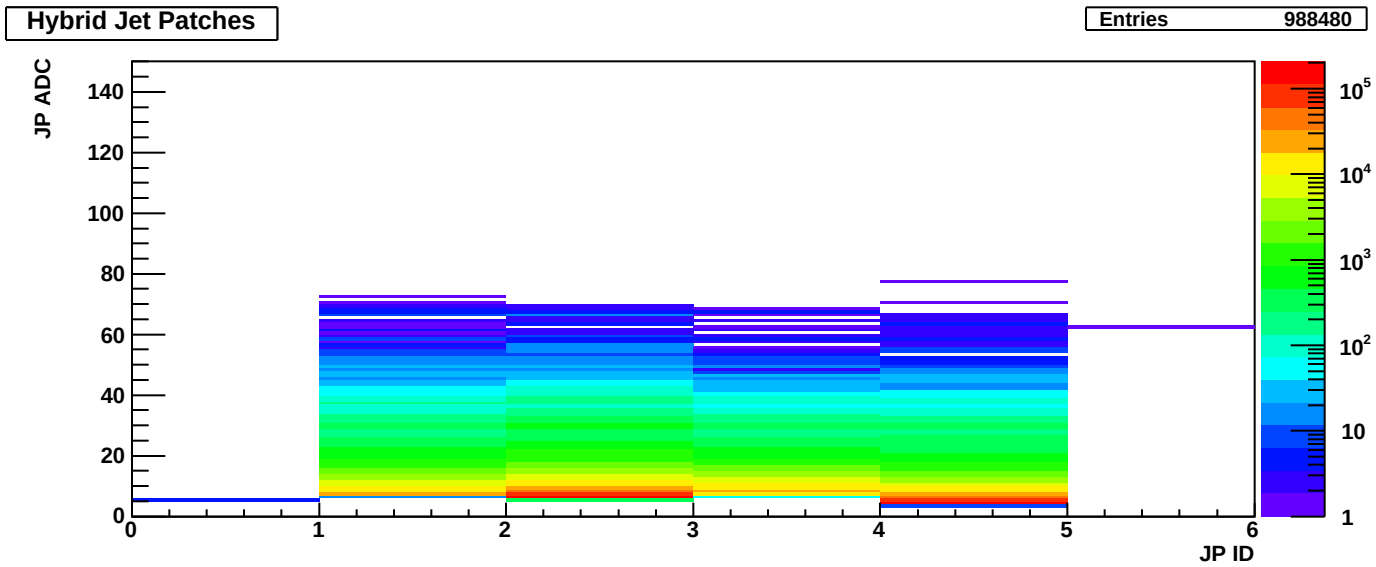

Barrel EMC L0 Input - Patch Sum
Entries 1.48272e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

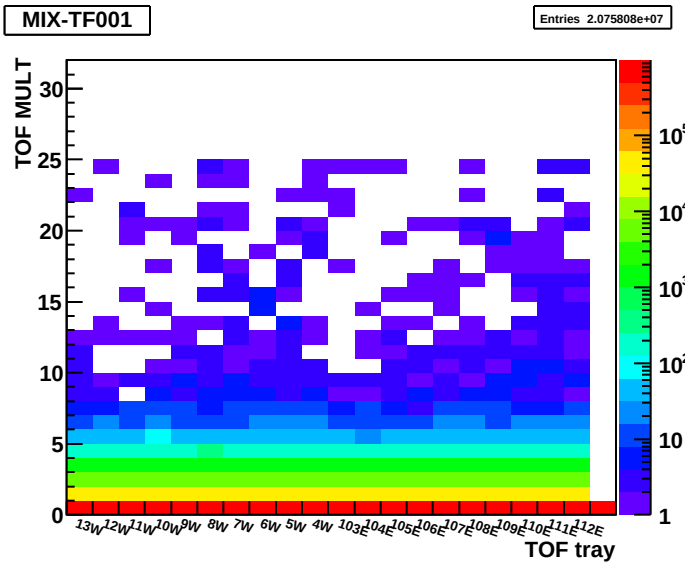

Endcap EMC L0 Input - High Tower
Entries 4.44816e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

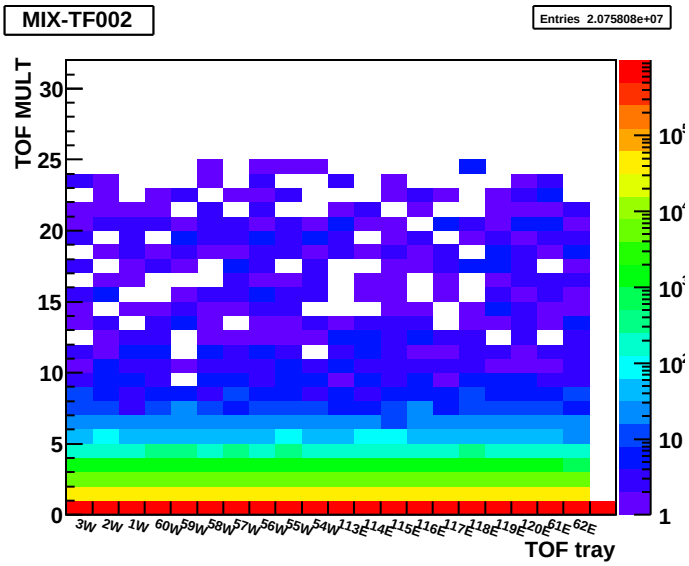

Endcap EMC L0 Input - Patch Sum
Entries 4.44816e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

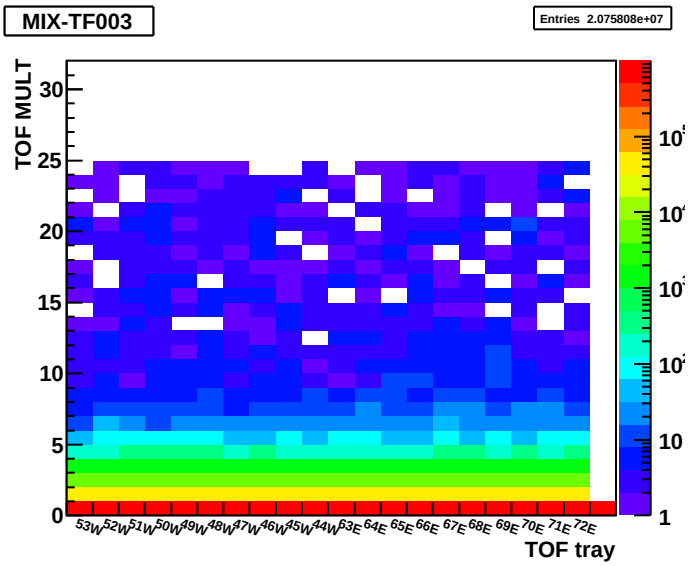

Barrel Jet Patches
Entries 8896320
JP ADC
JP ID

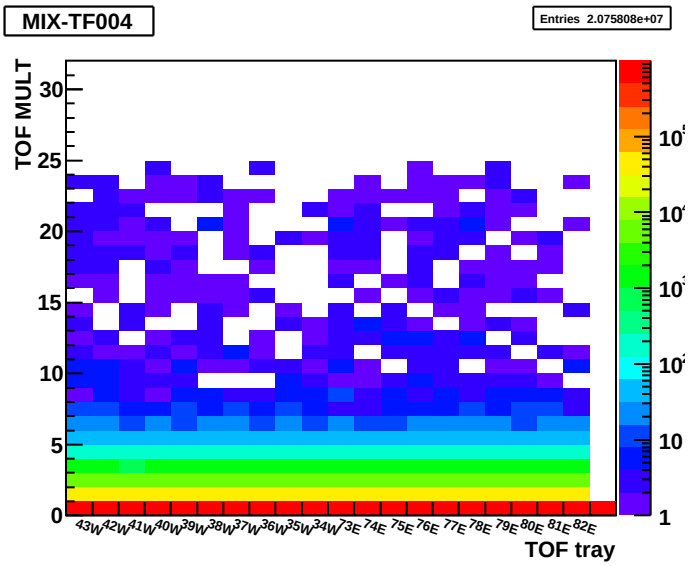

Endcap Jet Patches
Entries 2965440
JP ADC
JP ID

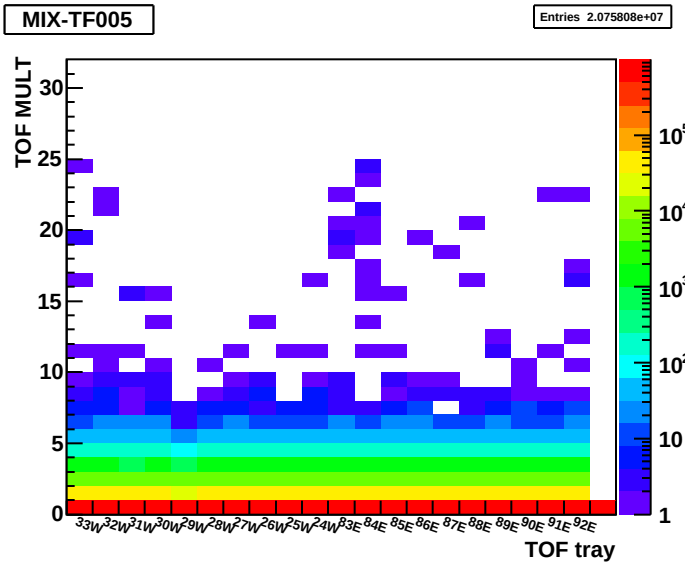

Hybrid Jet Patches
Entries 988480
JP ADC
JP ID

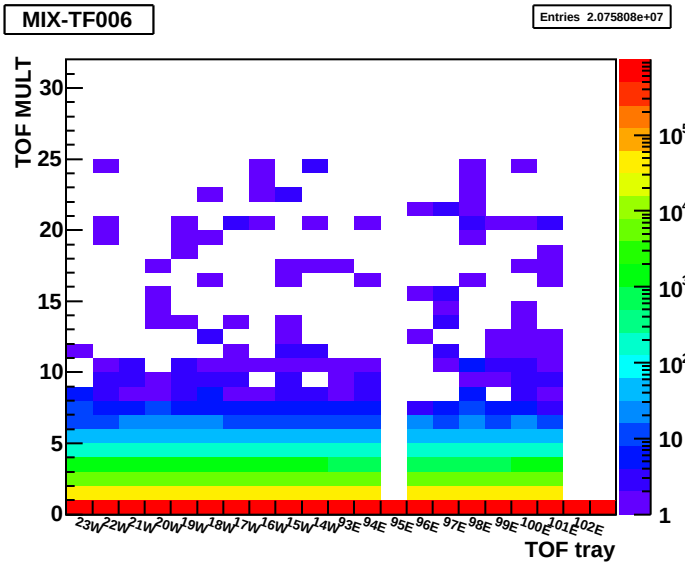

MIX-TF001
Entries 2.075808e+07
TOF MULT
TOF tray

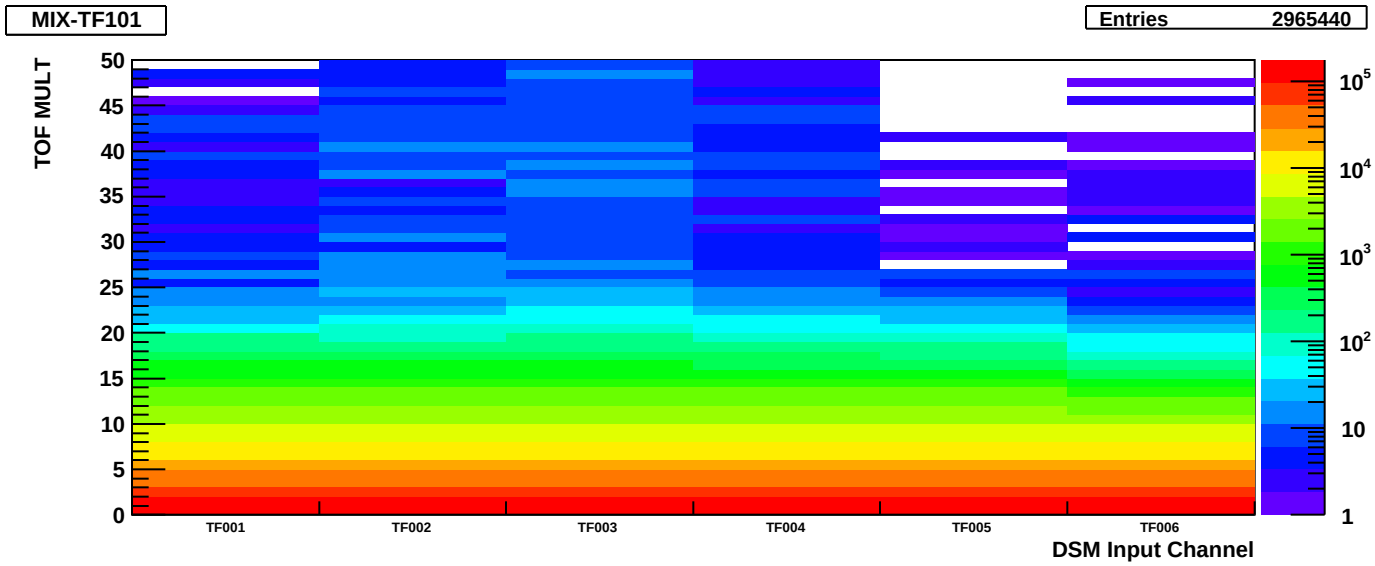

MIX-TF002
Entries 2.075808e+07
TOF MULT
TOF tray

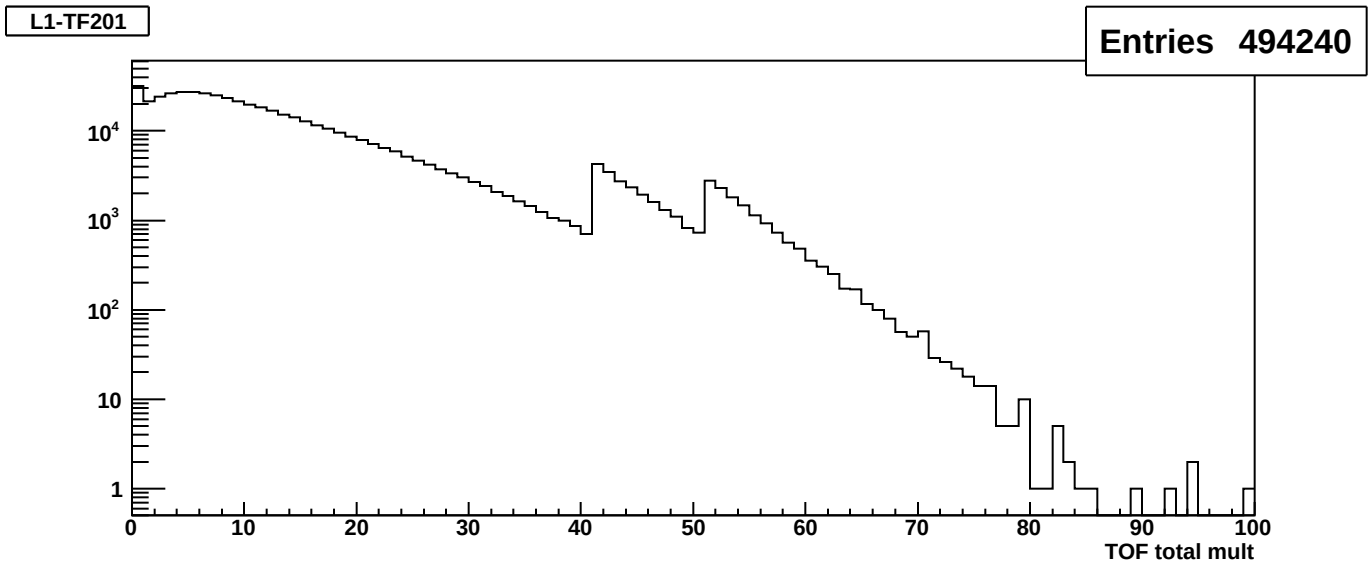

MIX-TF003
Entries 2.075808e+07
TOF MULT
TOF tray

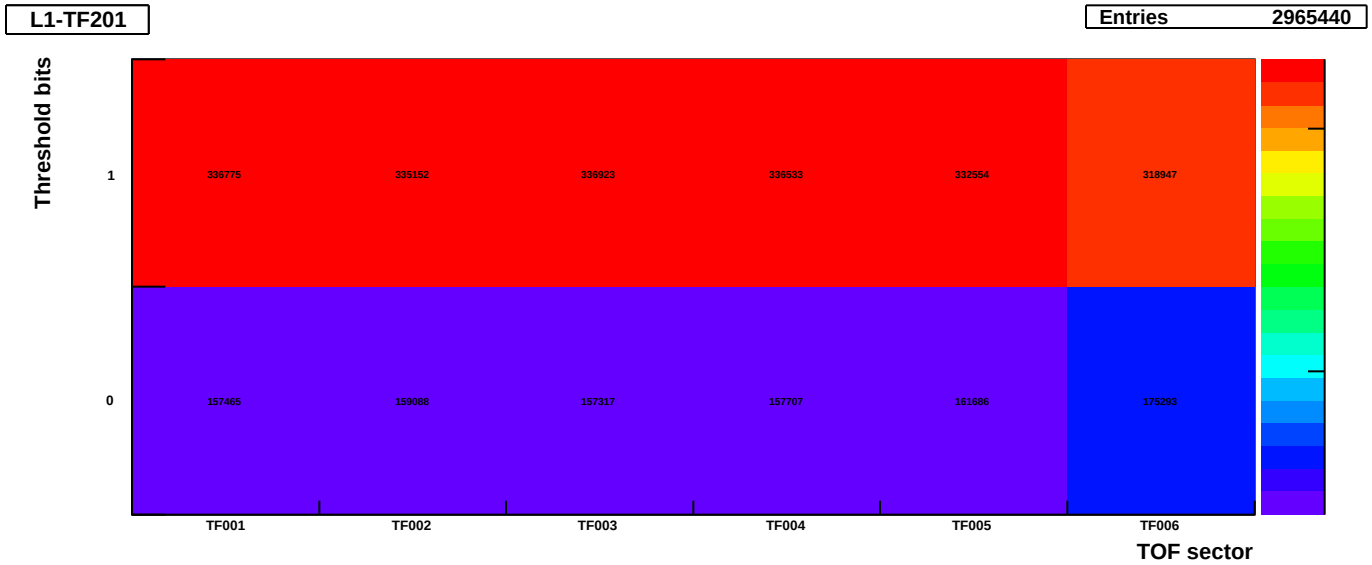

MIX-TF004
Entries 2.075808e+07
TOF MULT
TOF tray

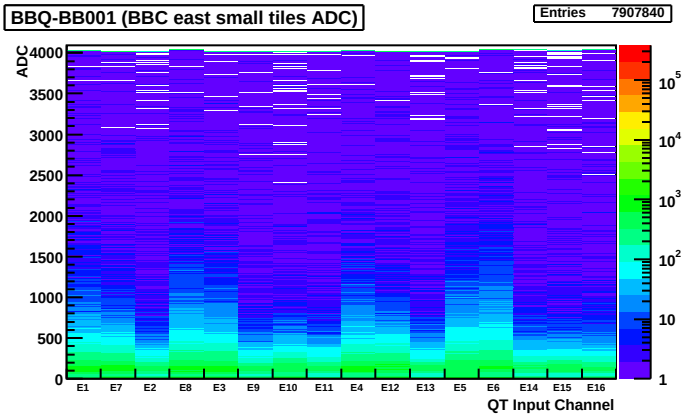

MIX-TF005
Entries 2.075808e+07
TOF MULT
TOF tray

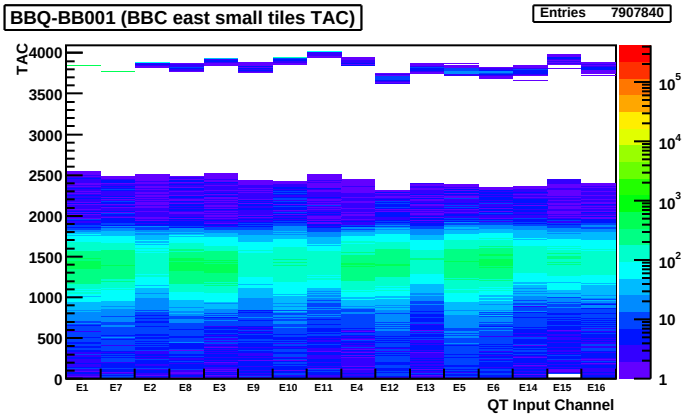

MIX-TF006
Entries 2.075808e+07
TOF MULT
TOF tray

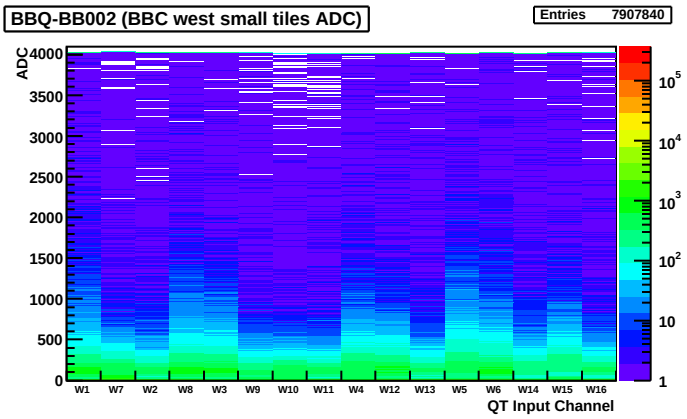

MIX-TF101
Entries 2965440
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

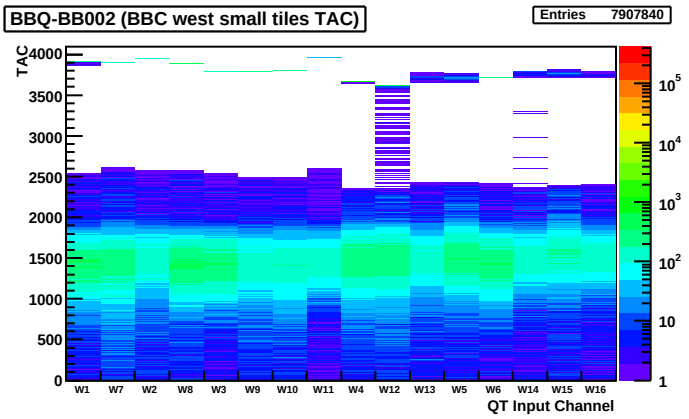

L1-TF201
Entries 494240
TOF total mult

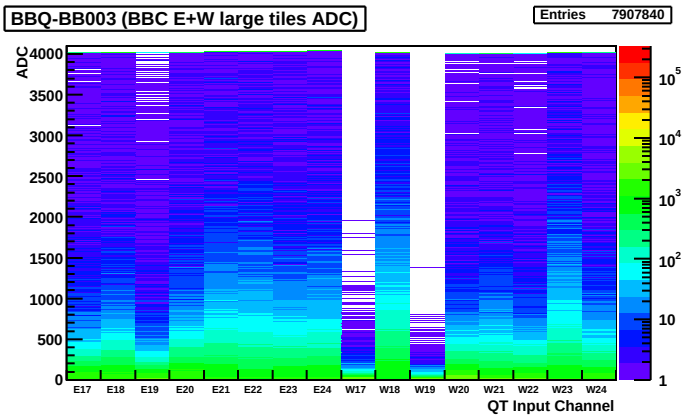

L1-TF201
Entries 2965440
Threshold bits
336775
335152
336923
336533
332554
318947
157465
159088
157317
157707
161686
175293
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

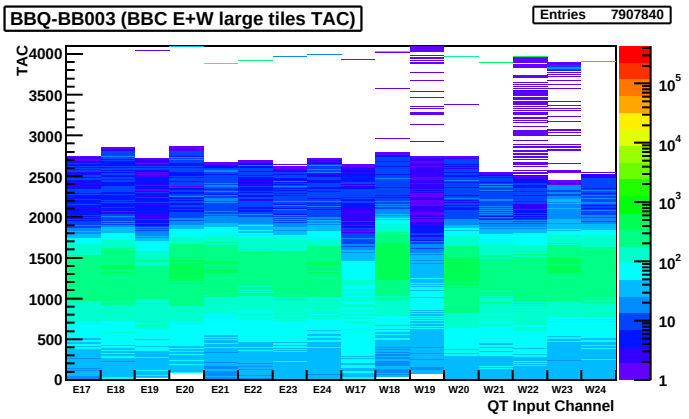

BBQ-BB001 (BBC east small tiles ADC)
Entries 7907840
ADC
QT Input Channel

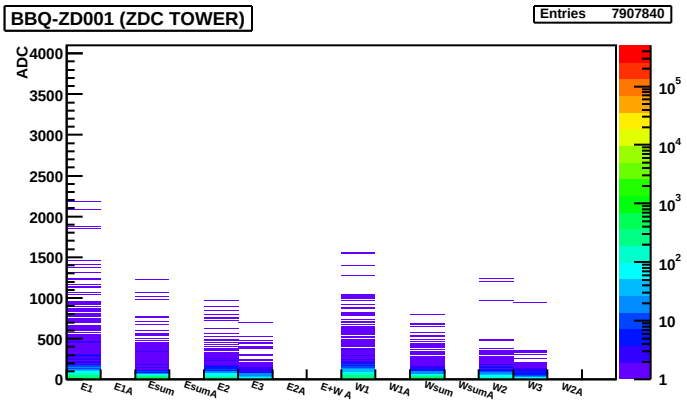

BBQ-BB001 (BBC east small tiles TAC)
Entries 7907840
TAC
QT Input Channel

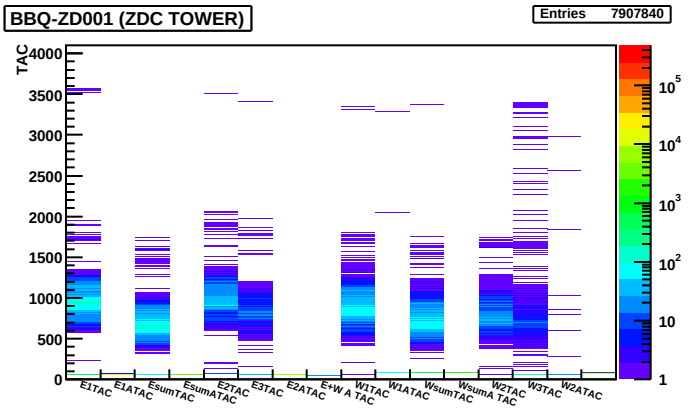

BBQ-BB002 (BBC west small tiles ADC)
Entries 7907840
ADC
QT Input Channel

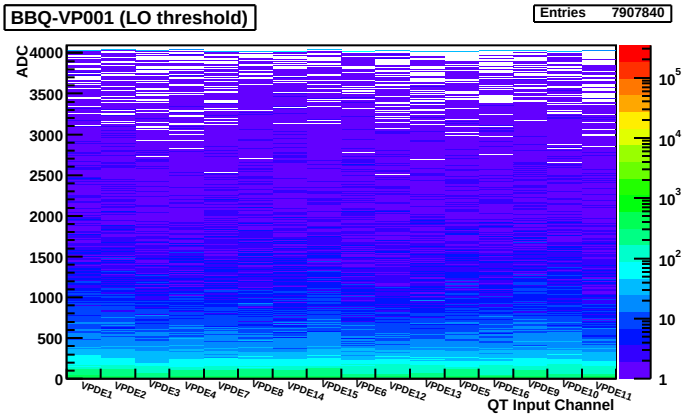

BBQ-BB002 (BBC west small tiles TAC)
Entries 7907840
TAC
QT Input Channel

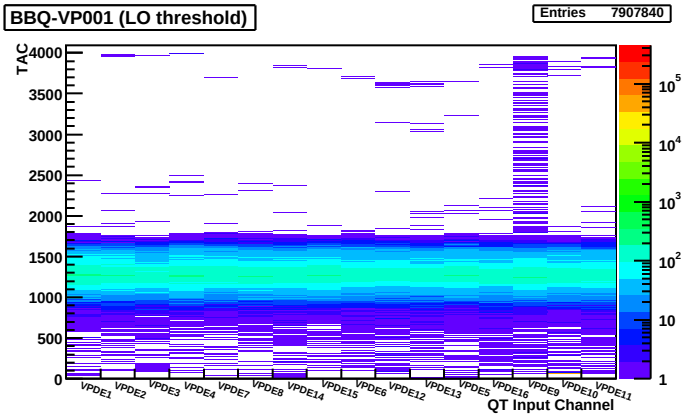

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7907840
ADC
QT Input Channel

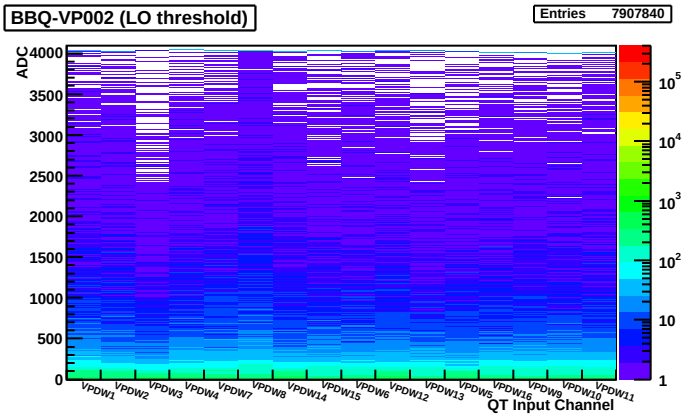

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7907840
TAC
QT Input Channel

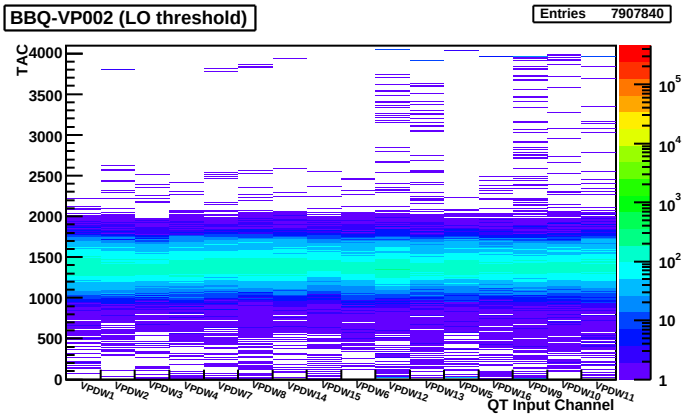

BBQ-ZD001 (ZDC TOWER)
Entries 7907840
ADC

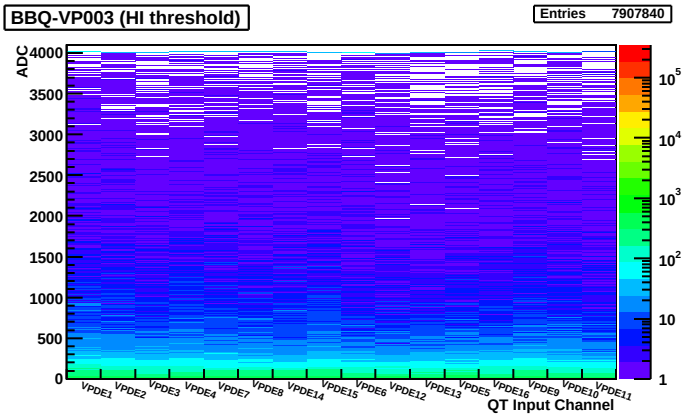

BBQ-ZD001 (ZDC TOWER)
Entries 7907840
TAC

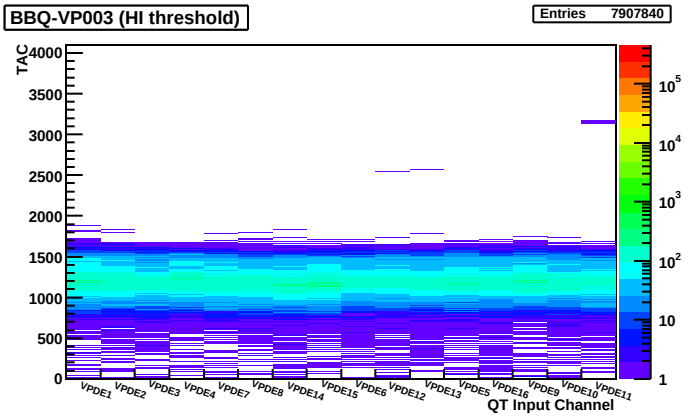

BBQ-VP001 (LO threshold)
Entries 7907840
ADC
QT Input Channel

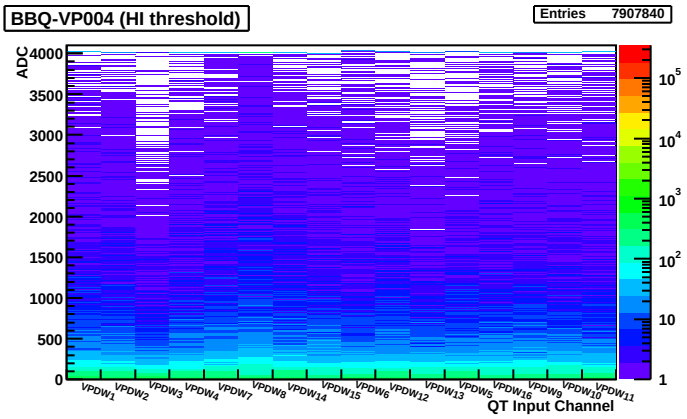

BBQ-VP001 (LO threshold)
Entries 7907840
TAC
QT Input Channel

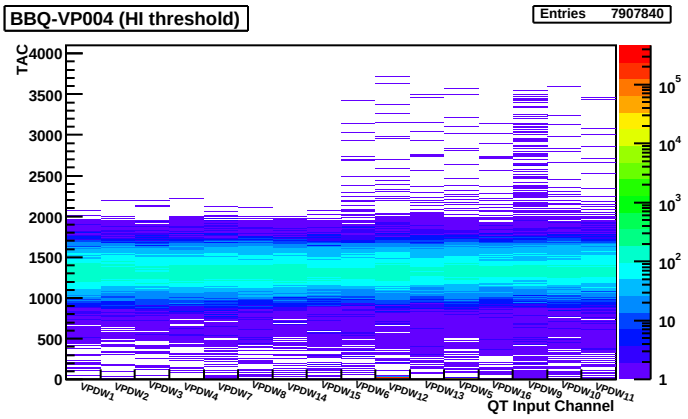

BBQ-VP002 (LO threshold)
Entries 7907840
ADC
QT Input Channel

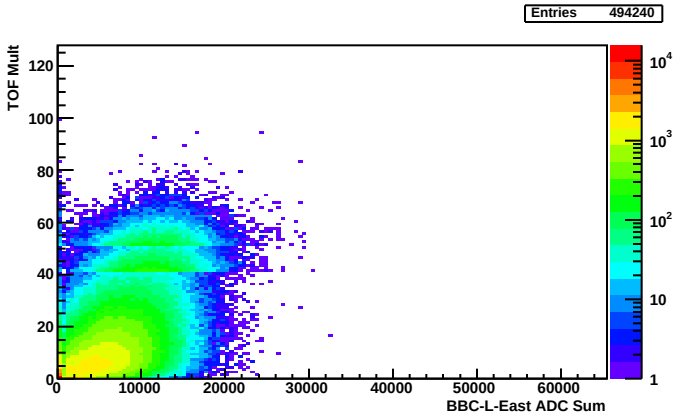

BBQ-VP002 (LO threshold)
Entries 7907840
TAC
QT Input Channel

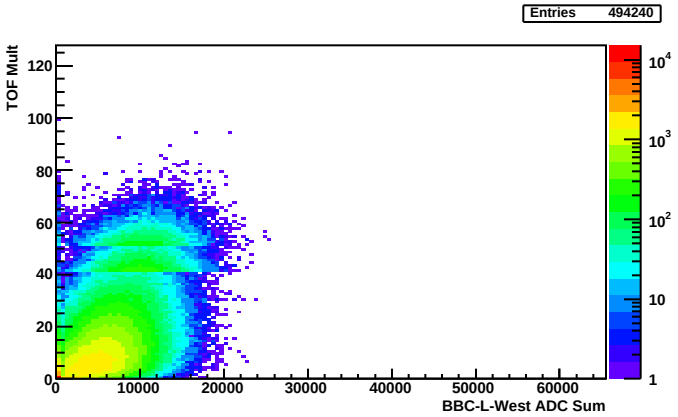

BBQ-VP003 (HI threshold)
Entries 7907840
ADC
QT Input Channel

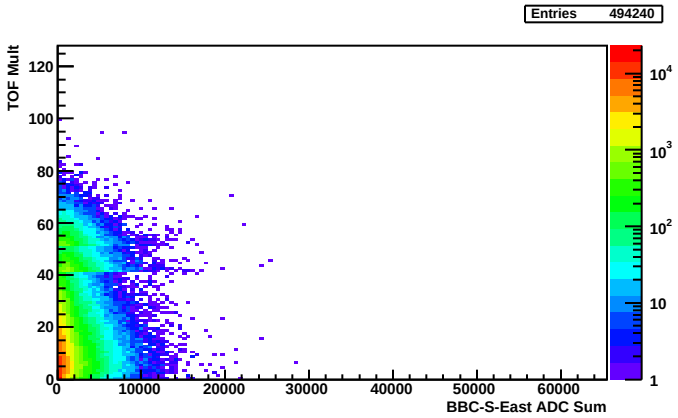

BBQ-VP003 (HI threshold)
Entries 7907840
TAC
QT Input Channel

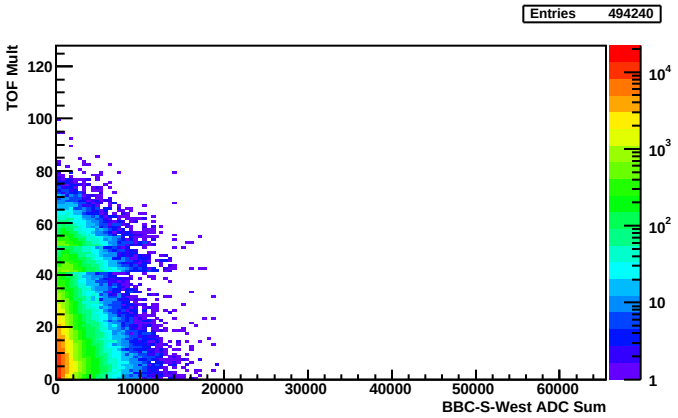

BBQ-VP004 (HI threshold)
Entries 7907840
ADC
QT Input Channel

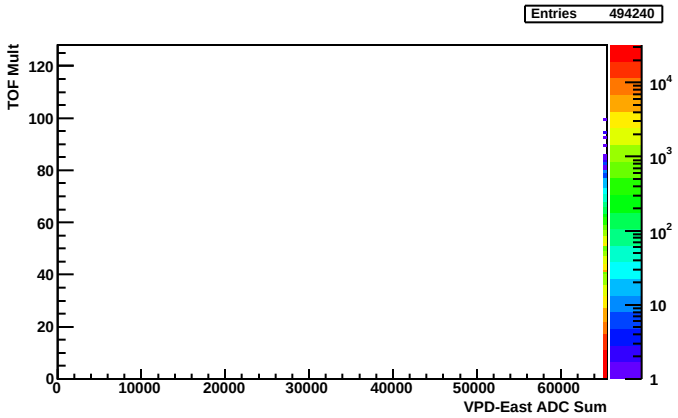

BBQ-VP004 (HI threshold)
Entries 7907840
TAC
QT Input Channel

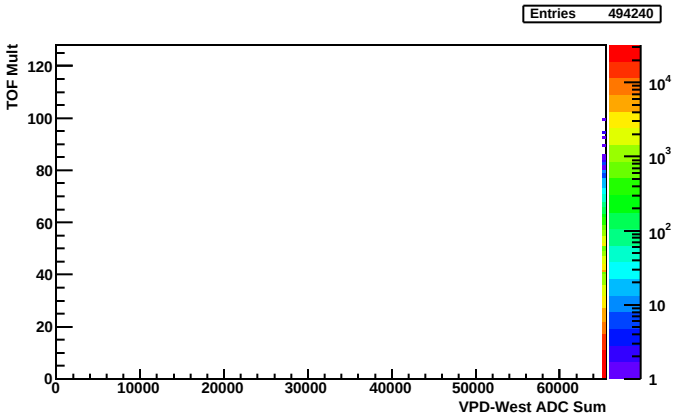

Entries 494240
TOF Mult
BBC-L-East ADC Sum

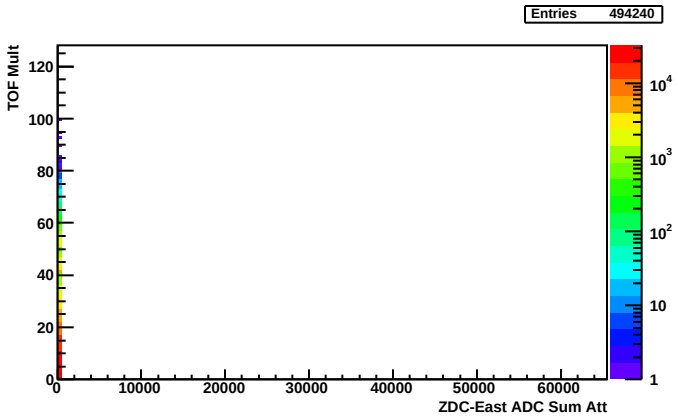

Entries 494240
TOF Mult
BBC-L-West ADC Sum

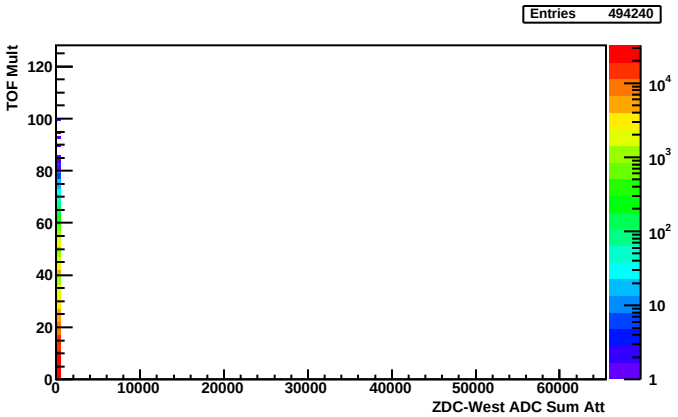

Entries 494240
TOF Mult
BBC-S-East ADC Sum

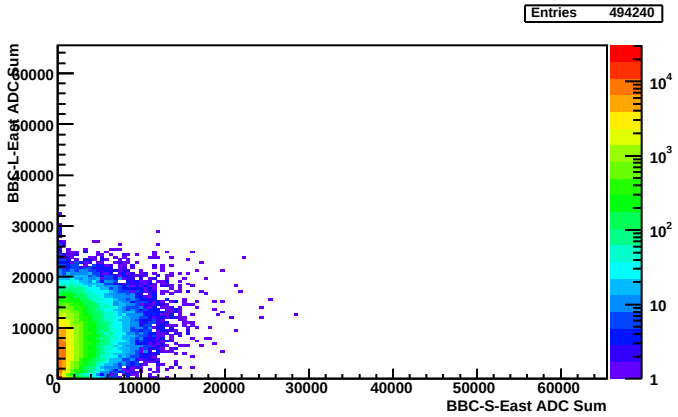

Entries 494240
TOF Mult
BBC-S-West ADC Sum

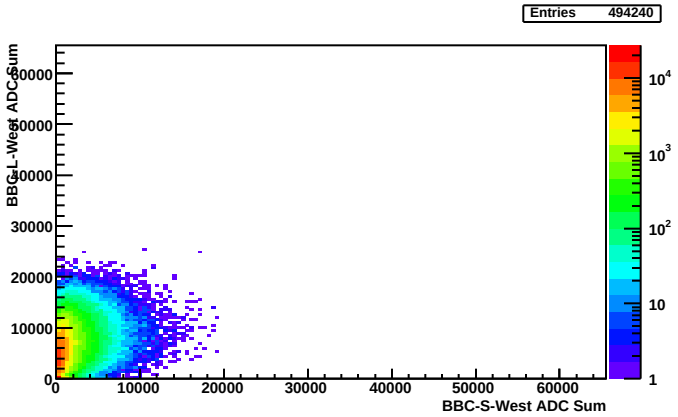

Entries 494240
TOF Mult
VPD-East ADC Sum

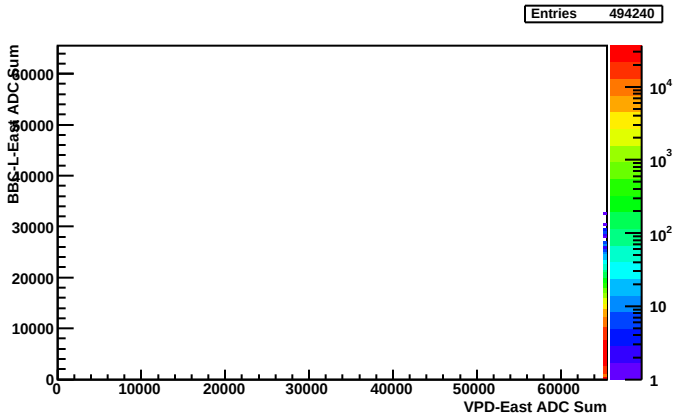

Entries 494240
TOF Mult
VPD-West ADC Sum

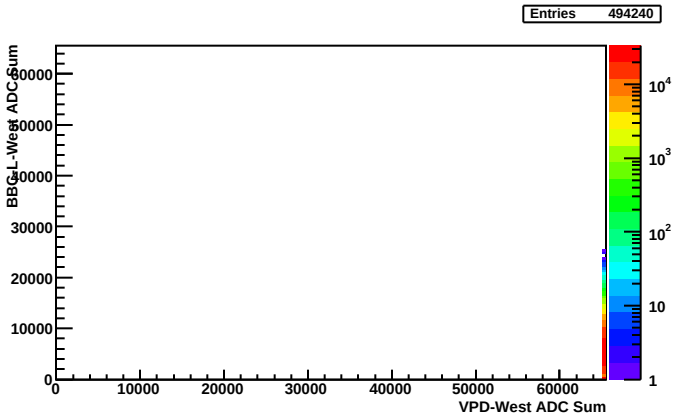

Entries 494240
TOF Mult
ZDC-East ADC Sum Att

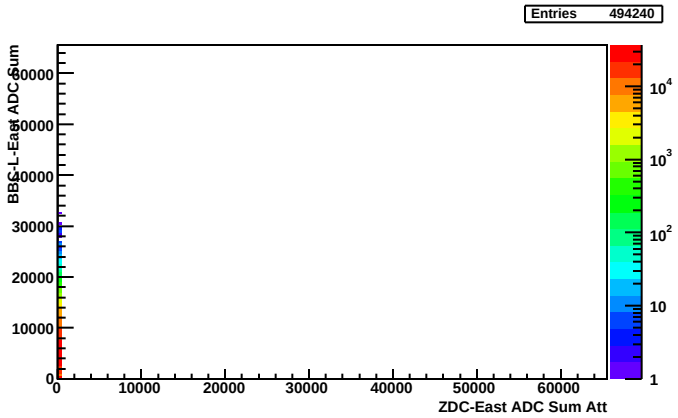

Entries 494240
TOF Mult
ZDC-West ADC Sum Att

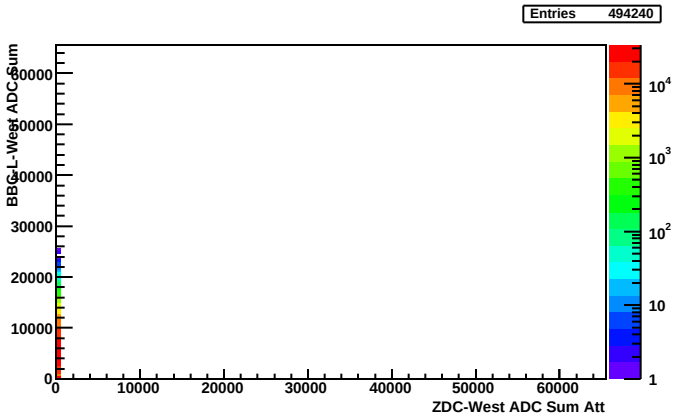

Entries 494240
BBC-L-East ADC Sum
BBC-S-East ADC Sum

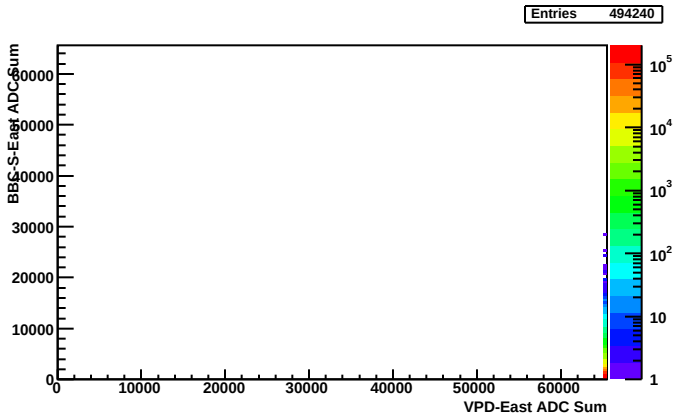

Entries 494240
BBC-L-West ADC Sum
BBC-S-West ADC Sum

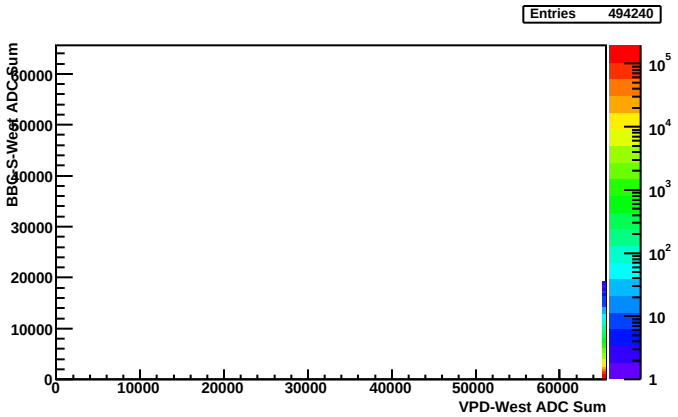

Entries 494240
BBC-L-East ADC Sum
VPD-East ADC Sum

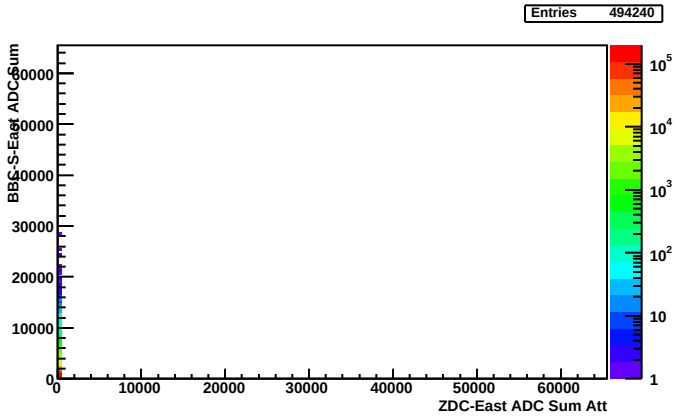

Entries 494240
BBC-L-West ADC Sum
VPD-West ADC Sum

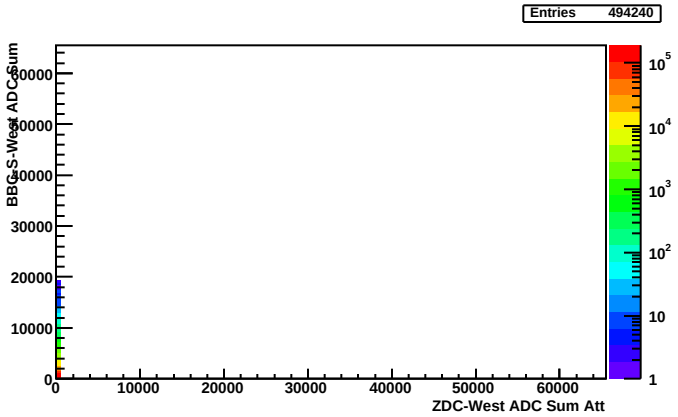

Entries 494240
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

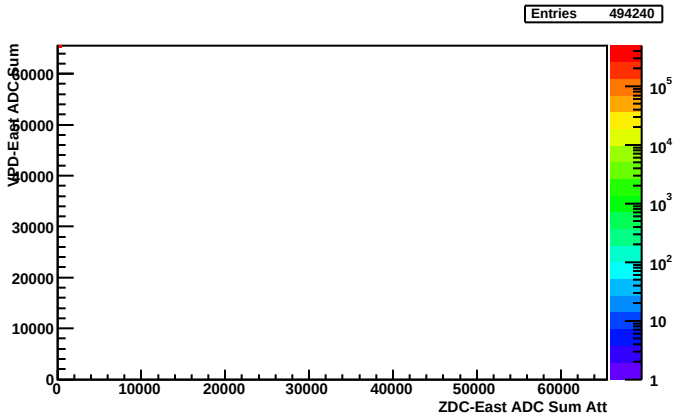

Entries 494240
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

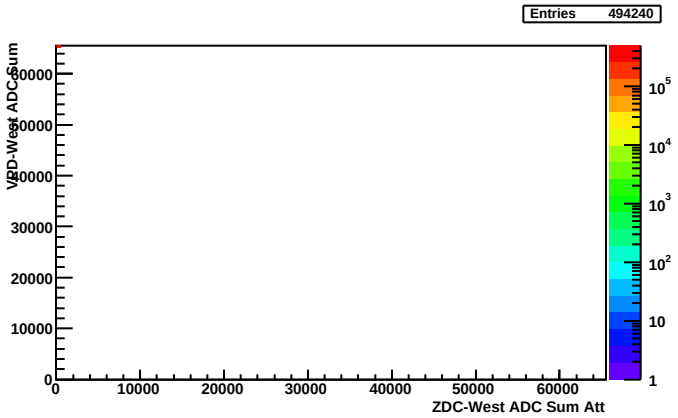

Entries 494240
BBC-S-East ADC Sum
VPD-East ADC Sum

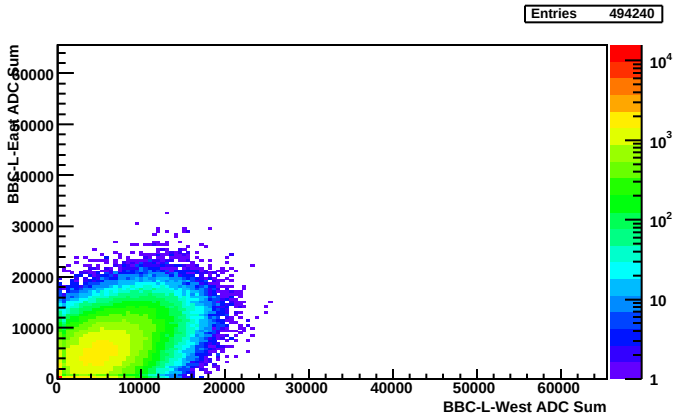

Entries 494240
BBC-S-West ADC Sum
VPD-West ADC Sum

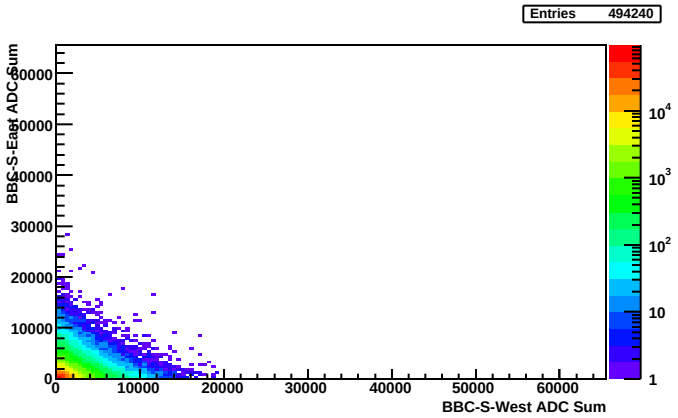

Entries 494240
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

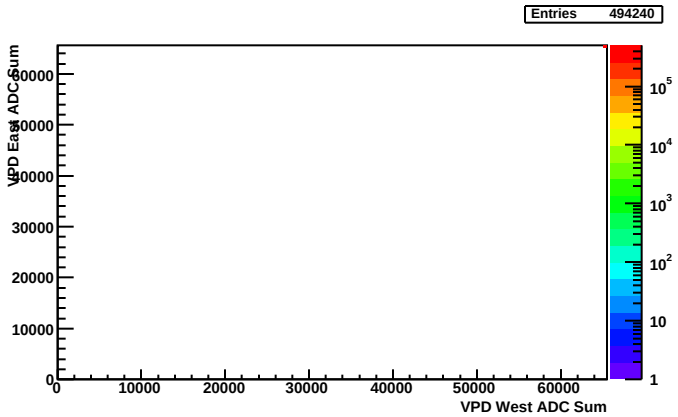

Entries 494240
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

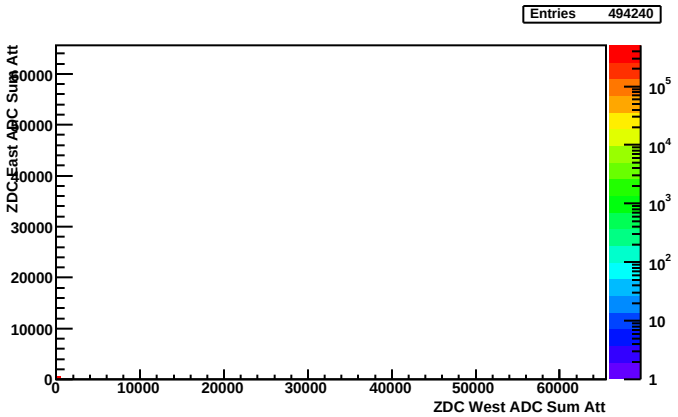

Entries 494240
VPD-East ADC Sum
ZDC-East ADC Sum Att

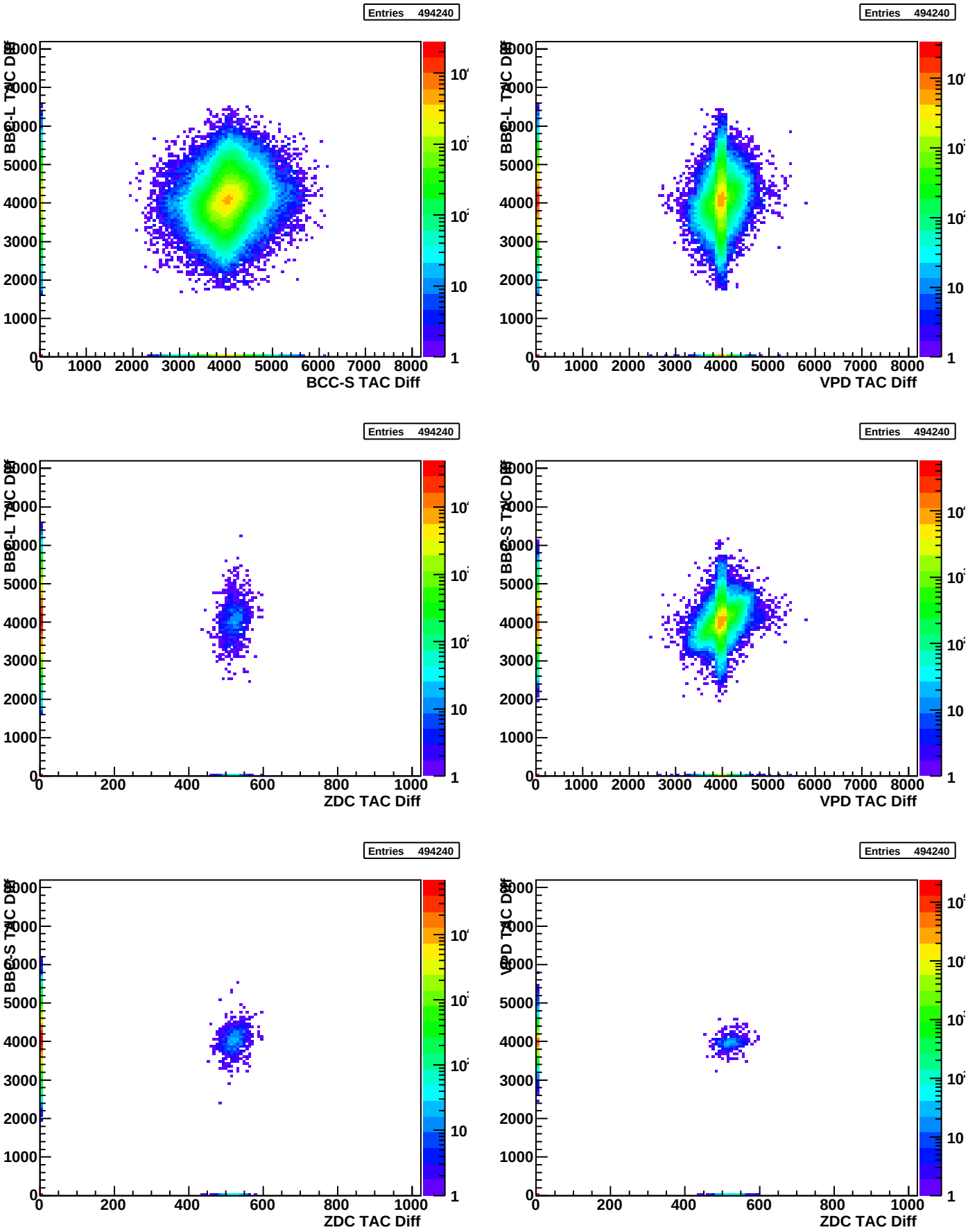

Entries 494240
VPD-West ADC Sum
ZDC-West ADC Sum Att

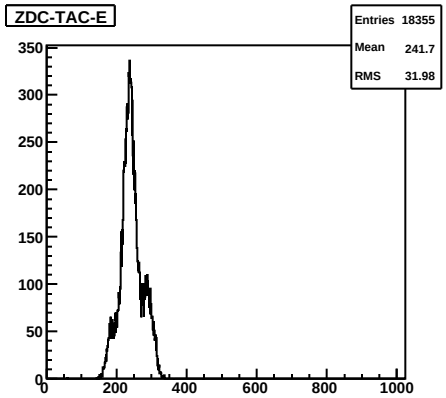

Entries 494240
BBC-L-East ADC Sum
BBC-L-West ADC Sum

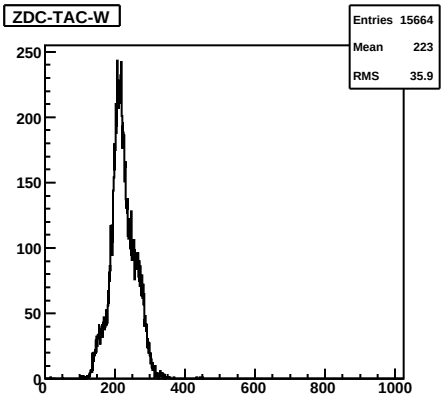

Entries 494240
BBC-S-East ADC Sum
BBC-S-West ADC Sum

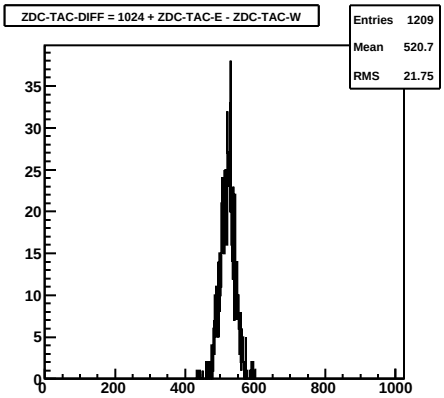

Entries 494240
VPD East ADC Sum
VPD West ADC Sum

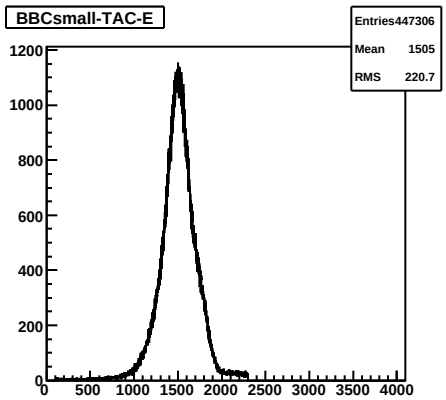

Entries 494240
ZDC East ADC Sum Att
ZDC West ADC Sum Att

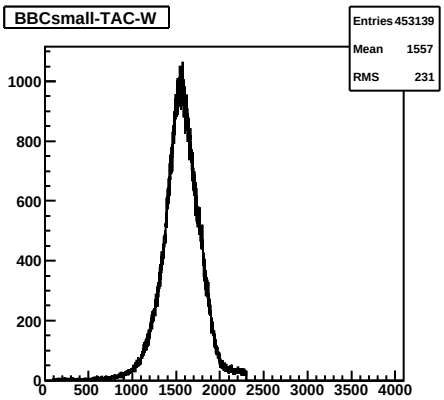

Entries 494240
BBC-L TAC Diff
BCC-S TAC Diff
Entries 494240
BBC-L TAC Diff
VPD TAC Diff
Entries 494240
BBC-L TAC Diff
ZDC TAC Diff
Entries 494240
BBC-S TAC Diff
VPD TAC Diff
Entries 494240
BBC-S TAC Diff
ZDC TAC Diff
Entries 494240
VPD TAC Diff
ZDC TAC Diff

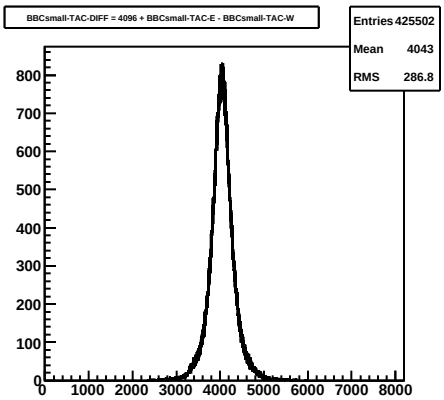

ZDC-TAC-E
Entries 18355
Mean 241.7
RMS 31.98

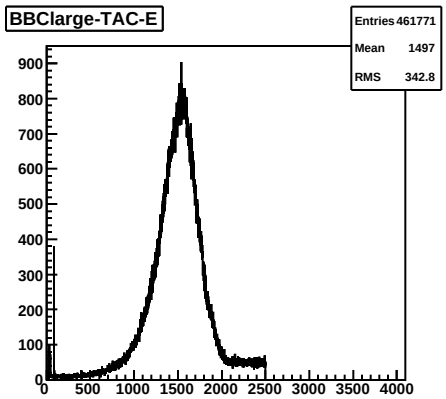

ZDC-TAC-W
Entries 15664
Mean 223
RMS 35.9

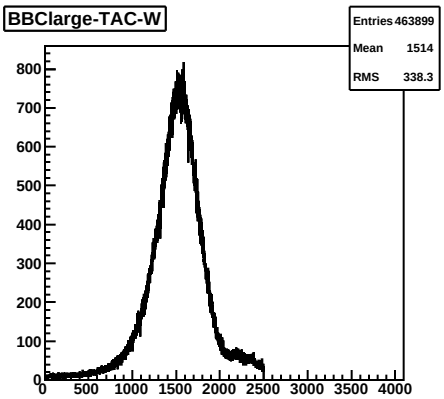

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1209
Mean 520.7
RMS 21.75

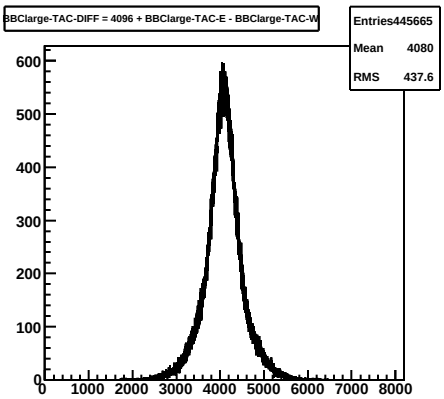

BBCsmall-TAC-E
Entries 447306
Mean 1505
RMS 220.7

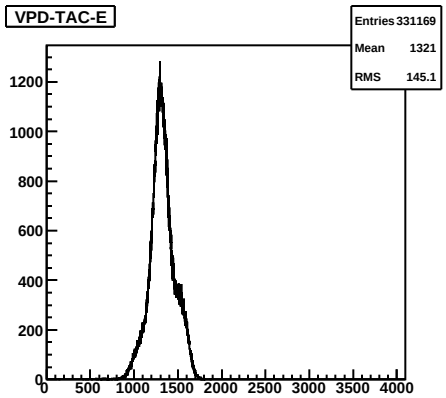

BBCsmall-TAC-W
Entries 453139
Mean 1557
RMS 231

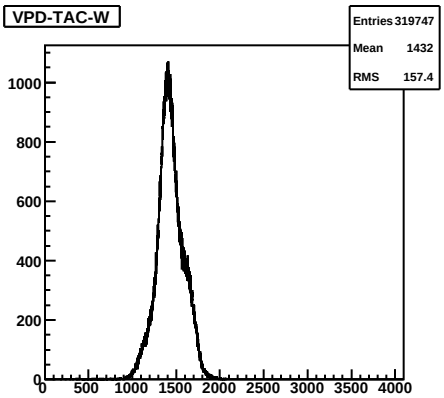

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 425502
Mean 4043
RMS 286.8

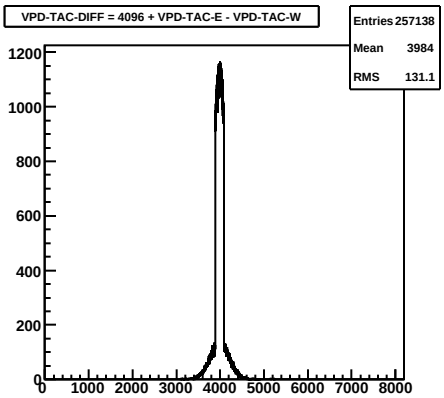

BBClarge-TAC-E
Entries 461771
Mean 1497
RMS 342.8

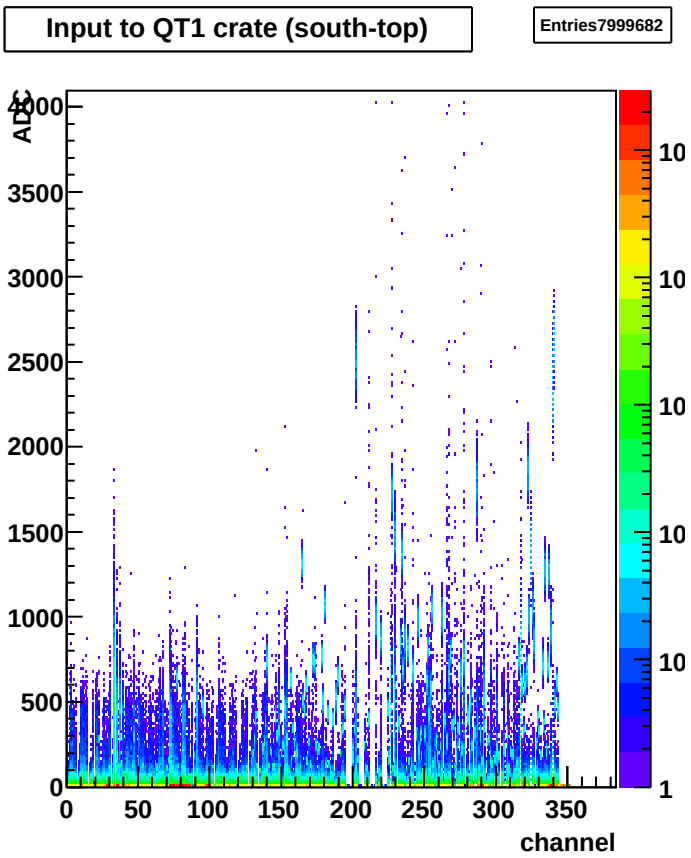

BBClarge-TAC-W
Entries 463899
Mean 1514
RMS 338.3

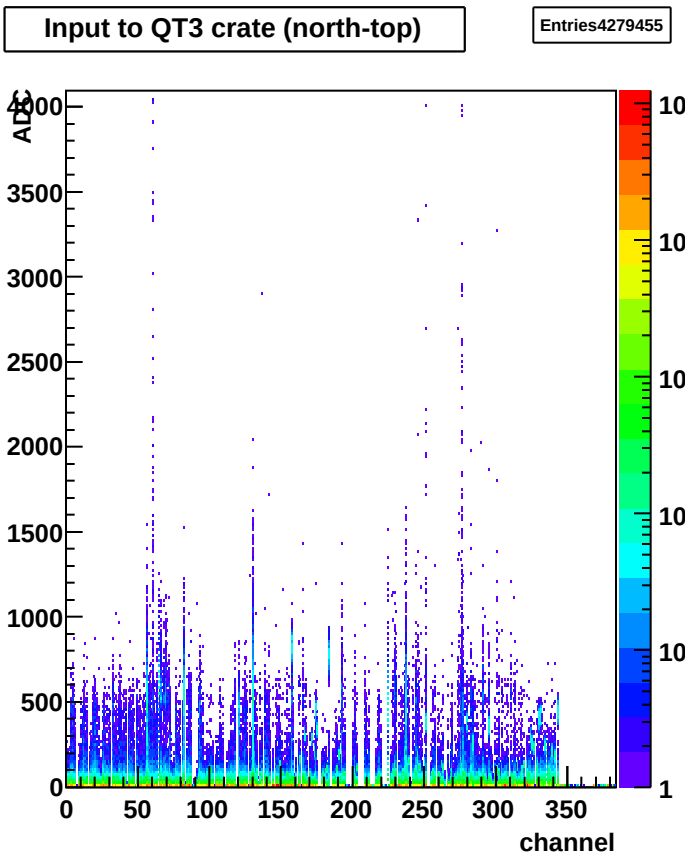

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 445665
Mean 4080
RMS 437.6

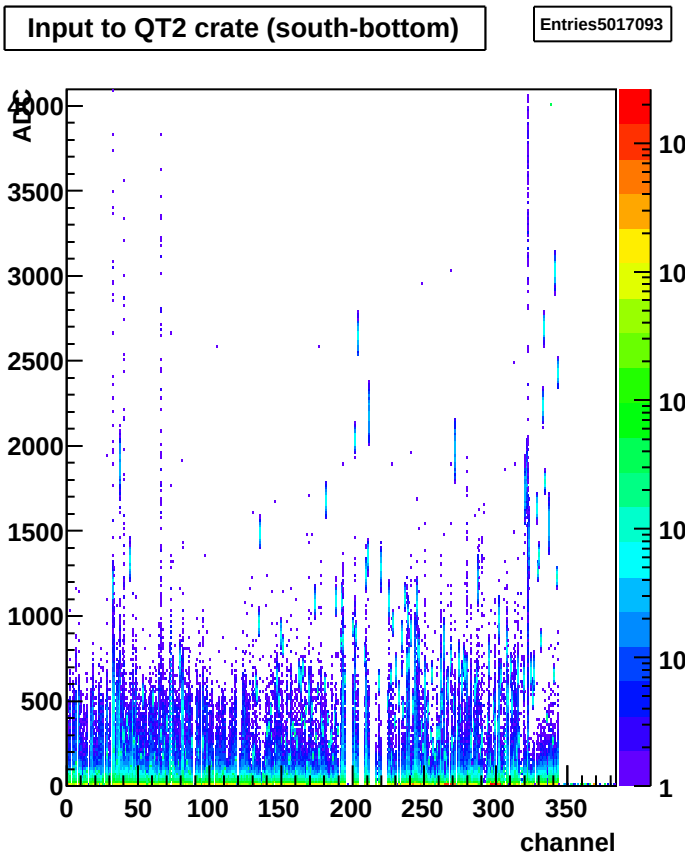

VPD-TAC-E
Entries 331169
Mean 1321
RMS 145.1

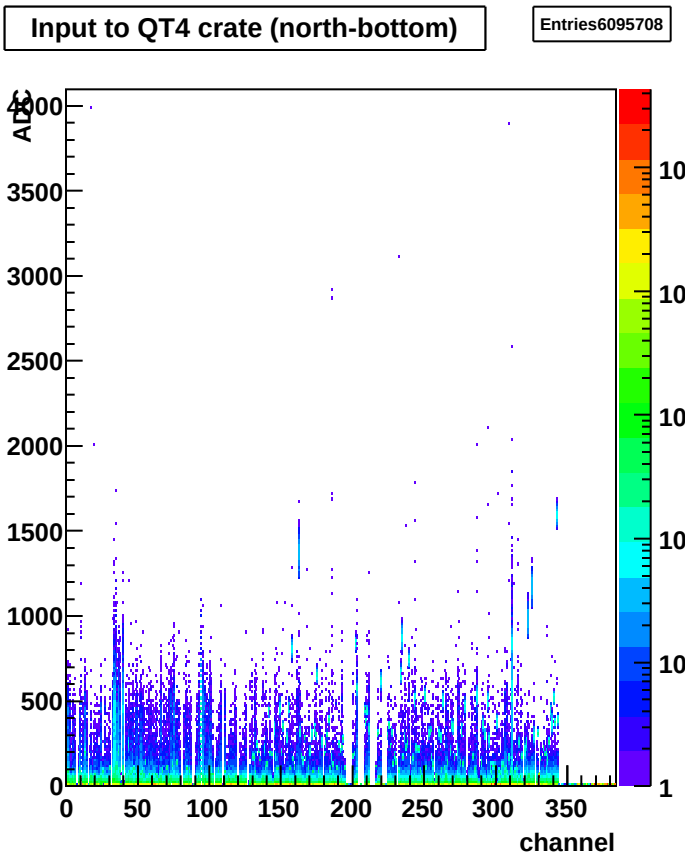

VPD-TAC-W
Entries 319747
Mean 1432
RMS 157.4

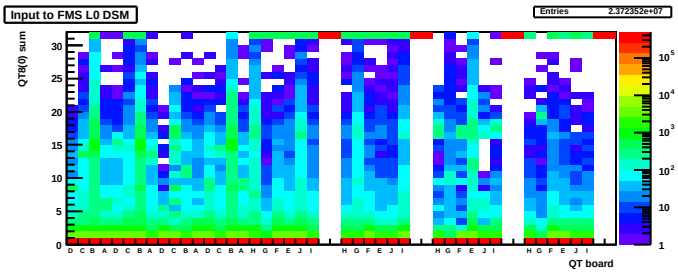

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 257138
Mean 3984
RMS 131.1

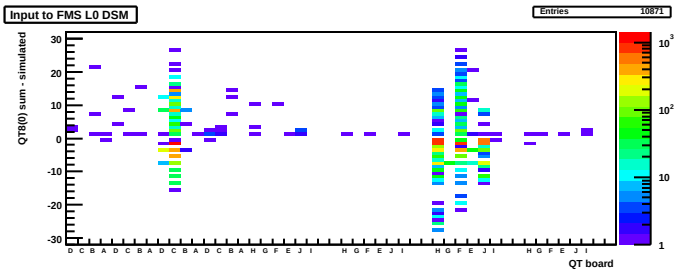

Input to QT1 crate (south-top)
Entries7999682
ADC
channel

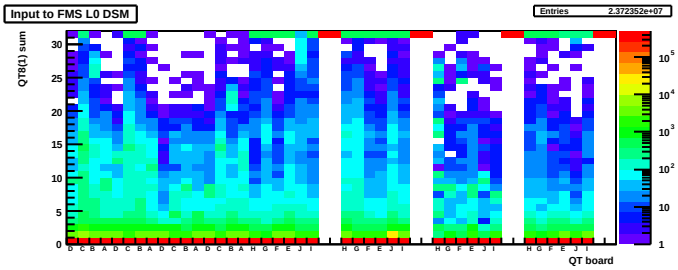

Input to QT3 crate (north-top)
Entries4279455
ADC
channel

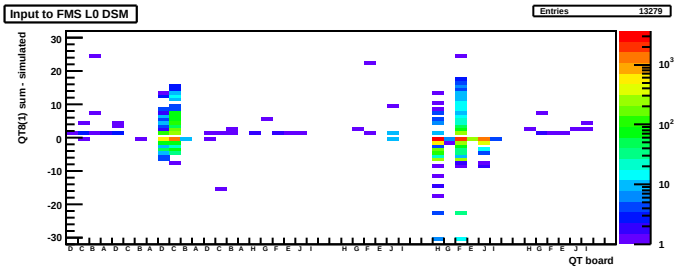

Input to QT2 crate (south-bottom)
Entries5017093
ADC
channel

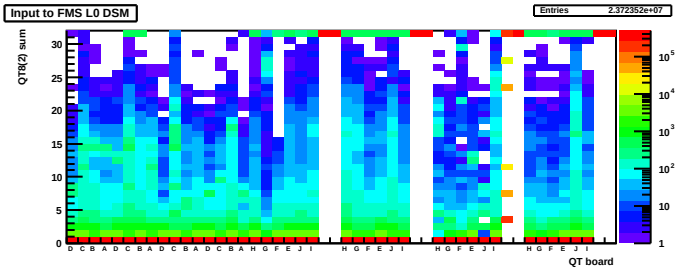

Input to QT4 crate (north-bottom)
Entries6095708
ADC
channel

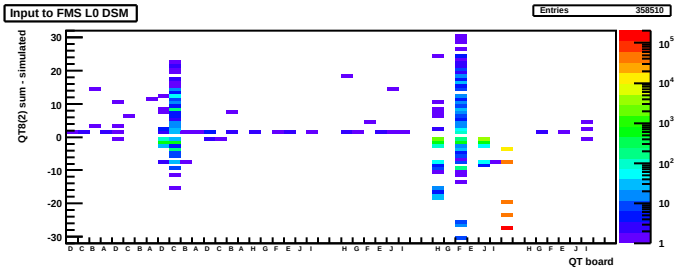

Input to FMS L0 DSM
Entries 2.372352e+07
QT8(0) sum
QT board

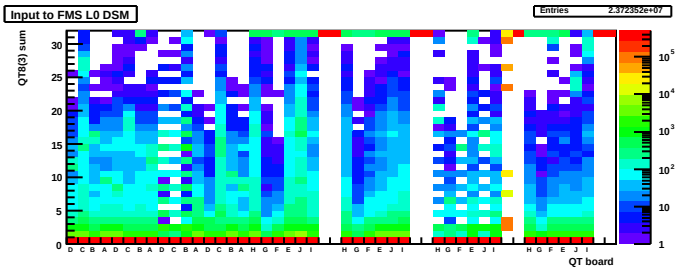

Input to FMS L0 DSM
Entries 10871
QT8(0) sum - simulated
QT board

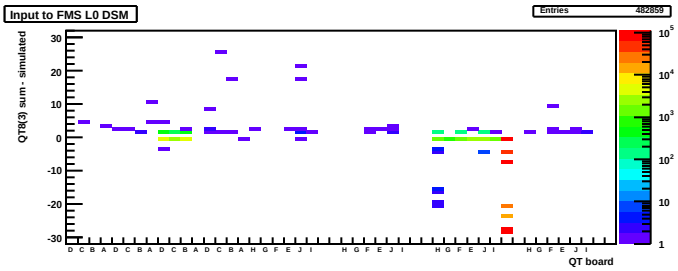

Input to FMS L0 DSM
Entries 2.372352e+07
QT8(1) sum
QT board

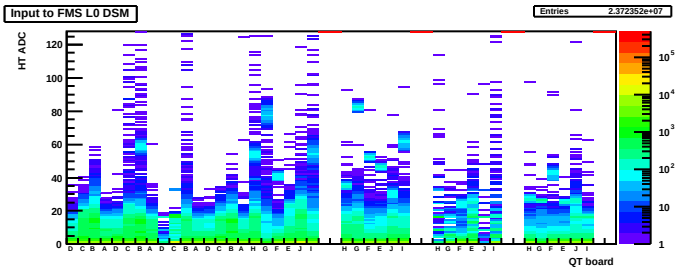

Input to FMS L0 DSM
Entries 13279
QT8(1) sum - simulated
QT board

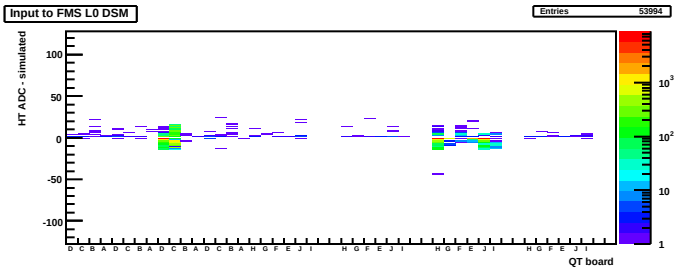

Input to FMS L0 DSM
Entries 2.372352e+07
QT8(2) sum
QT board

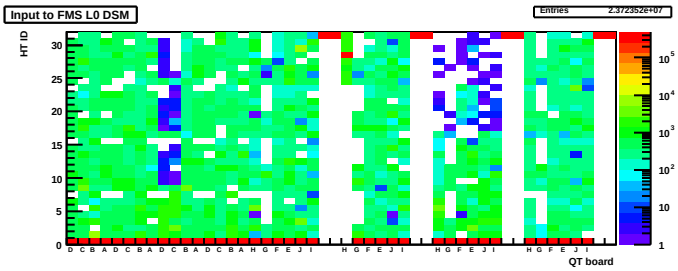

Input to FMS L0 DSM
Entries 358510
QT8(2) sum - simulated
QT board

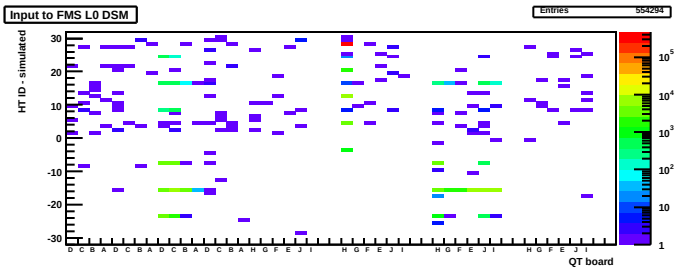

Input to FMS L0 DSM
Entries 2.372352e+07
QT8(3) sum
QT board

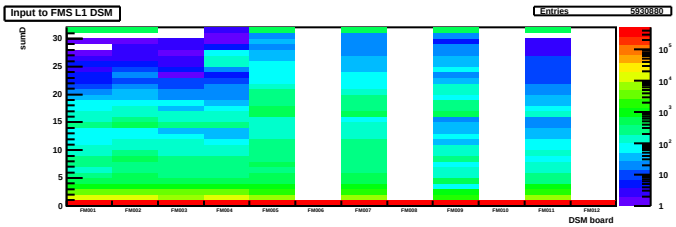

Input to FMS L0 DSM
Entries 482859
QT8(3) sum - simulated
QT board

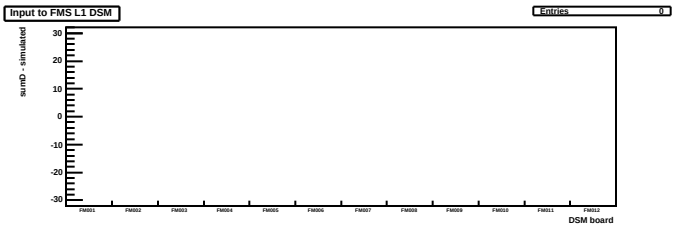

Input to FMS L0 DSM
Entries 2.372352e+07
HT ADC
QT board

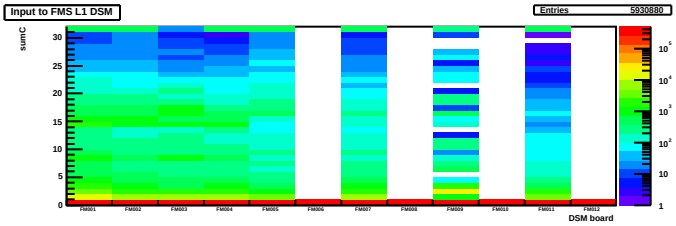

Input to FMS L0 DSM
Entries 53994
HT ADC - simulated
QT board

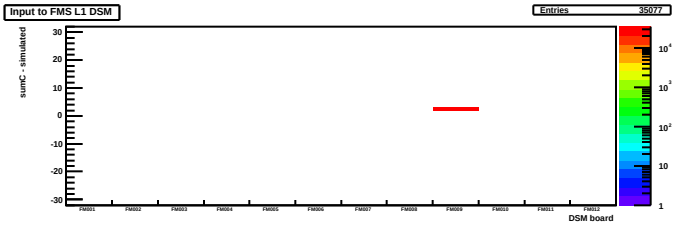

Input to FMS L0 DSM
Entries 2.372352e+07
HT ID
QT board

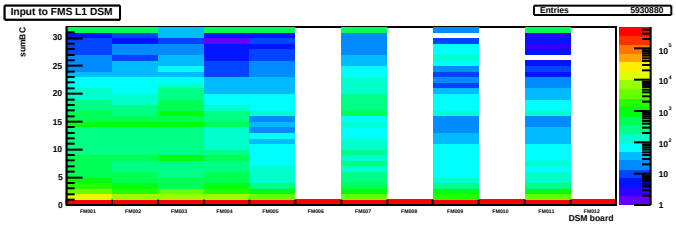

Input to FMS L0 DSM
Entries 554294
HT ID - simulated
QT board

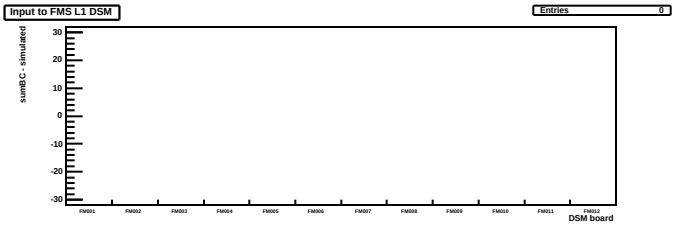

Input to FMS L1 DSM
Entries 5930880
sumD
DSM board

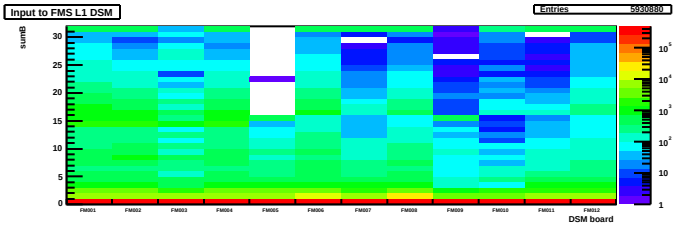

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

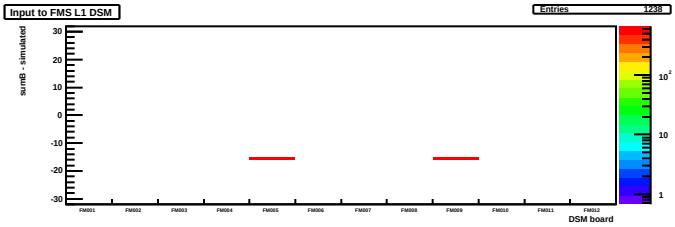

Input to FMS L1 DSM
Entries 5930880
sumC
DSM board

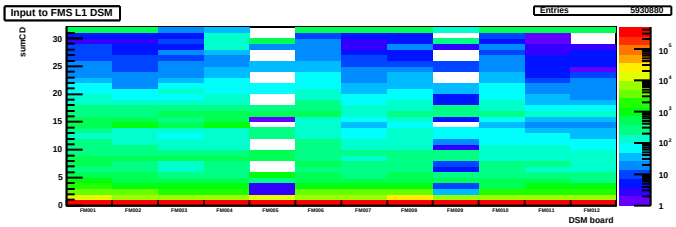

Input to FMS L1 DSM
Entries 35077
sumC - simulated
DSM board

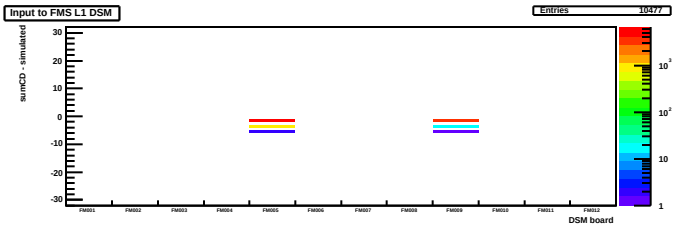

Input to FMS L1 DSM
Entries 5930880
sumBC
DSM board

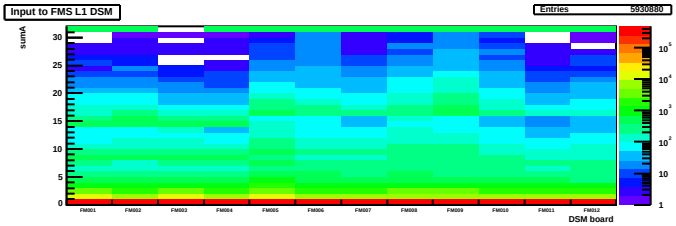

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

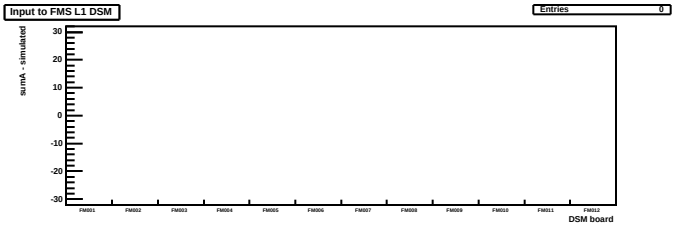

Input to FMS L1 DSM
Entries 5930880
sumB
DSM board

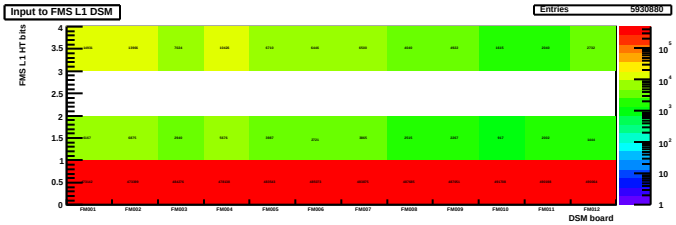

Input to FMS L1 DSM
Entries 1238
sumB - simulated
DSM board

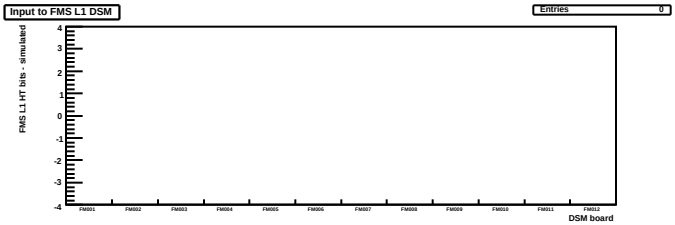

Input to FMS L1 DSM
Entries 5930880
sumCD
DSM board

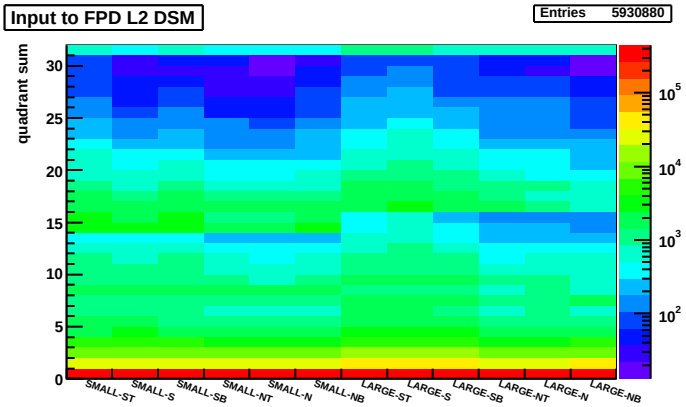

Input to FMS L1 DSM
Entries 10477
sumCD - simulated
DSM board

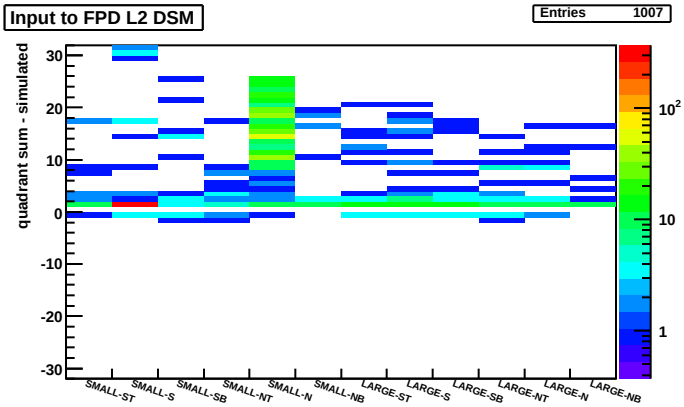

Input to FMS L1 DSM
Entries 5930880
sumA
DSM board

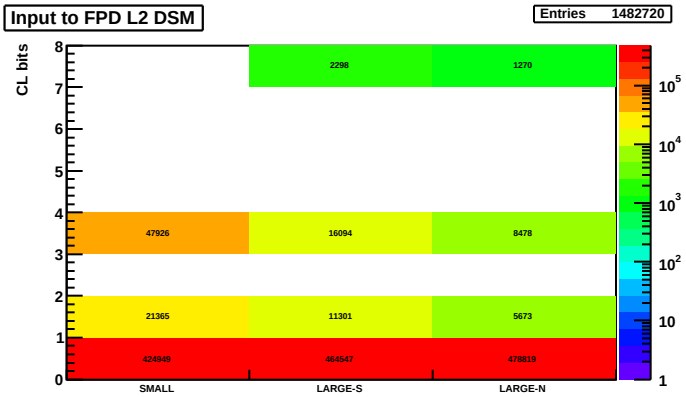

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

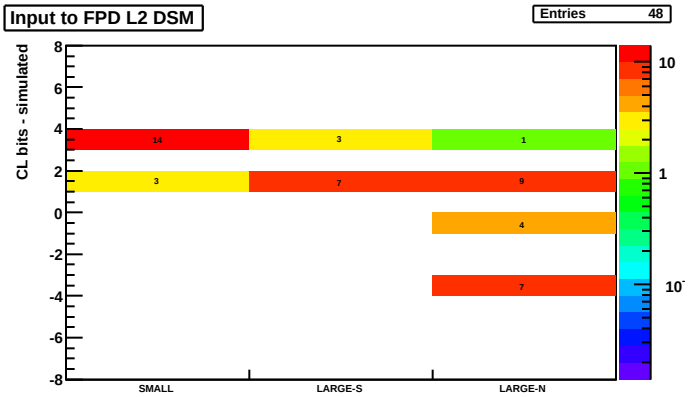

Input to FMS L1 DSM
Entries 5930880
FMS L1 HT bits
DSM board

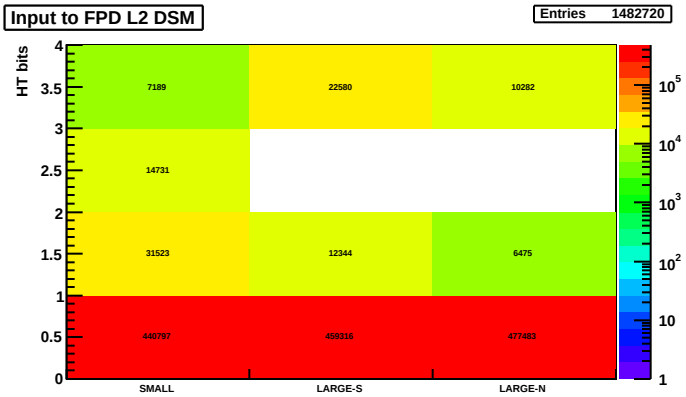

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

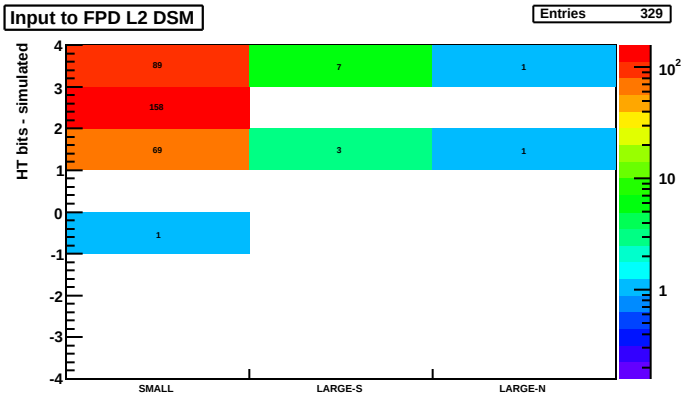

Input to FPD L2 DSM
Entries 5930880
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

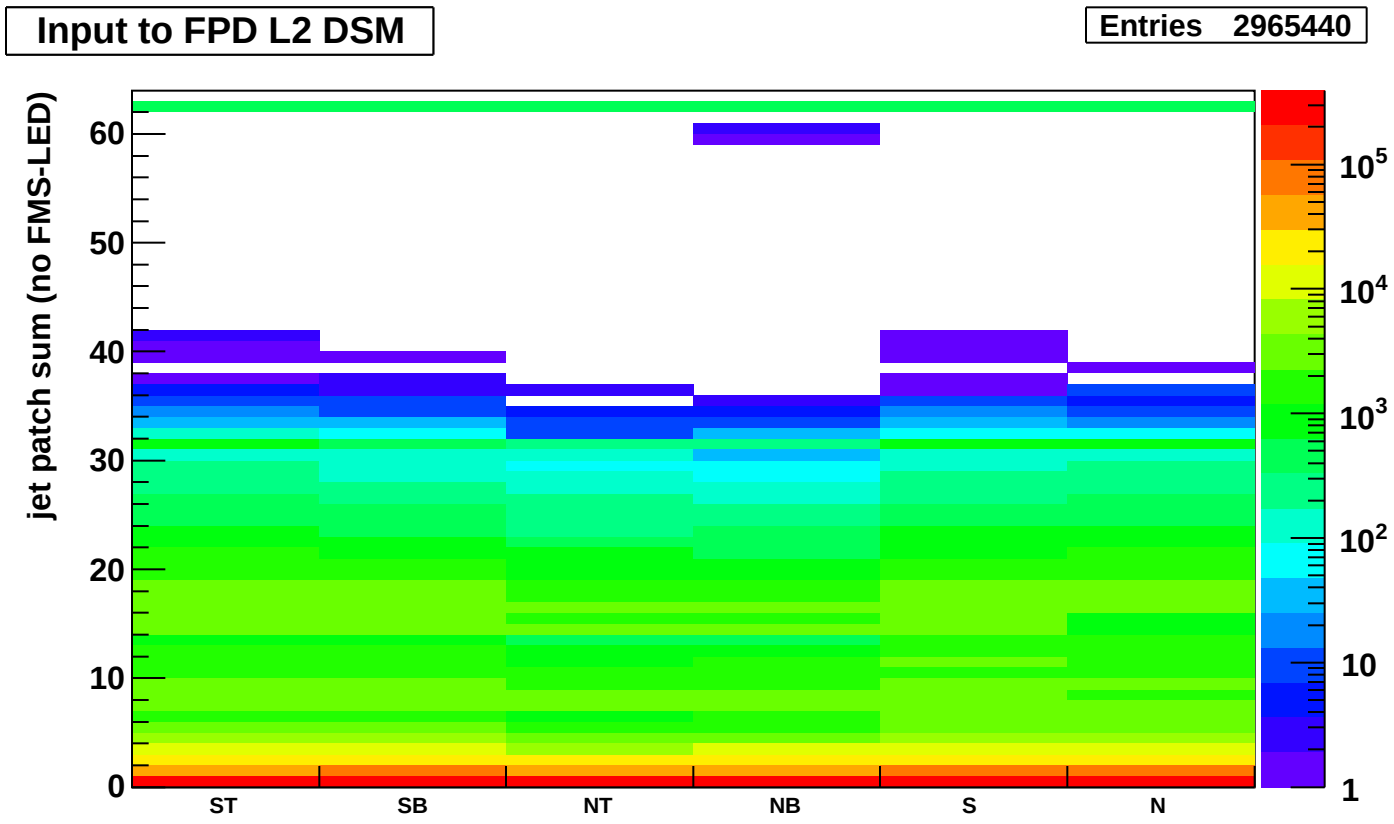

Input to FPD L2 DSM
Entries 1007
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

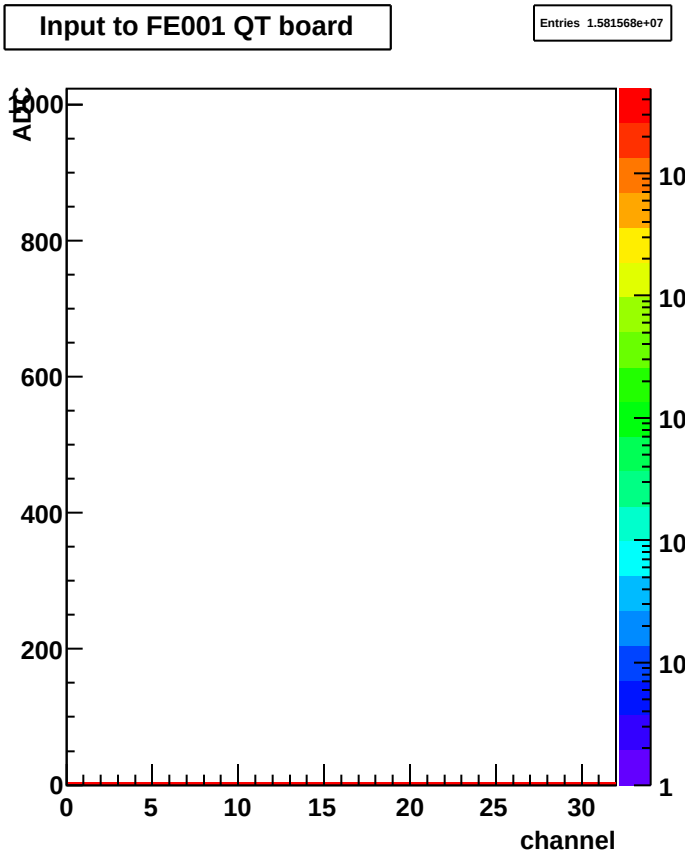

Input to FPD L2 DSM
Entries 1482720
CL bits
2298
1270
47926
16094
8478
21365
11301
5673
424949
464547
478819
SMALL
LARGE-S
LARGE-N

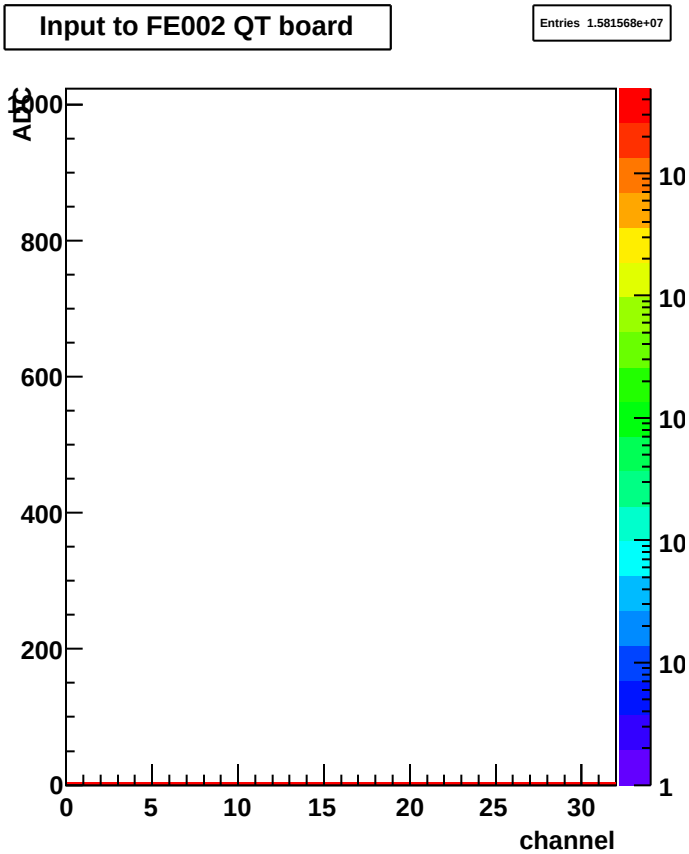

Input to FPD L2 DSM
Entries 48
CL bits - simulated
14
3
1
3
7
9
4
7
SMALL
LARGE-S
LARGE-N

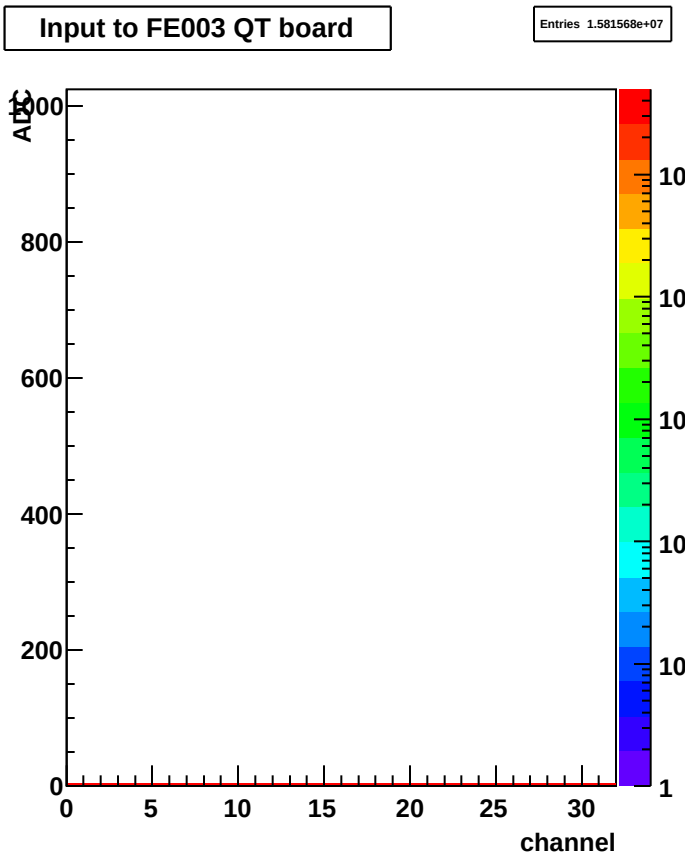

Input to FPD L2 DSM
Entries 1482720
HT bits
7189
22580
10282
14731
31523
12344
6475
440797
459316
477483
SMALL
LARGE-S
LARGE-N

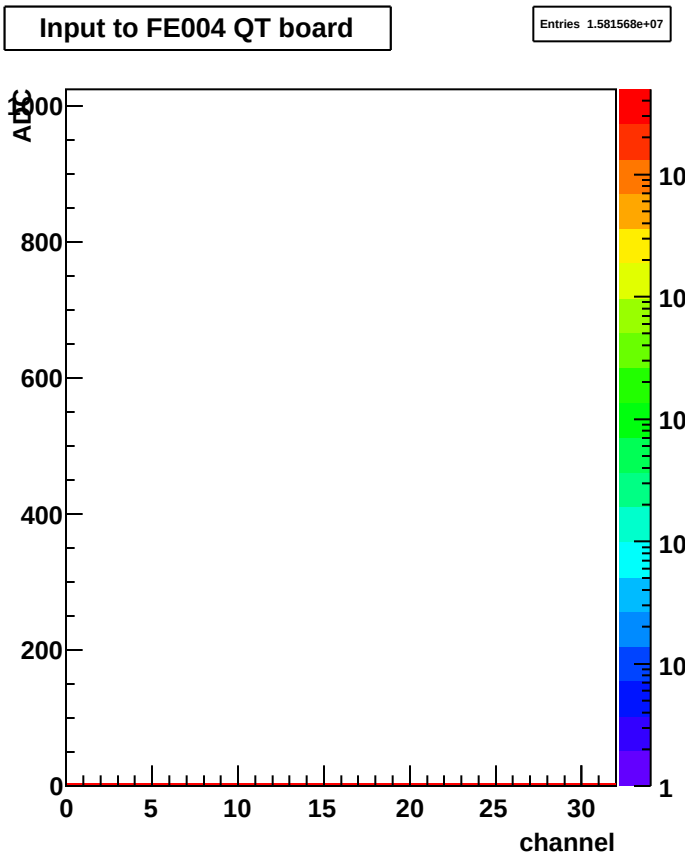

Input to FPD L2 DSM
Entries 329
HT bits - simulated
89
7
1
158
69
3
1
1
SMALL
LARGE-S
LARGE-N

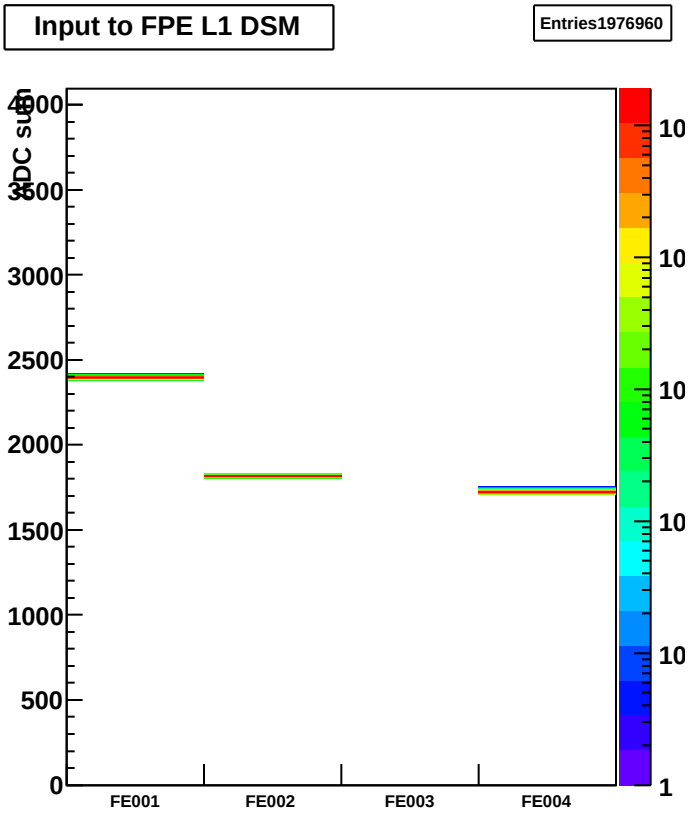

Input to FPD L2 DSM
Entries 2965440
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

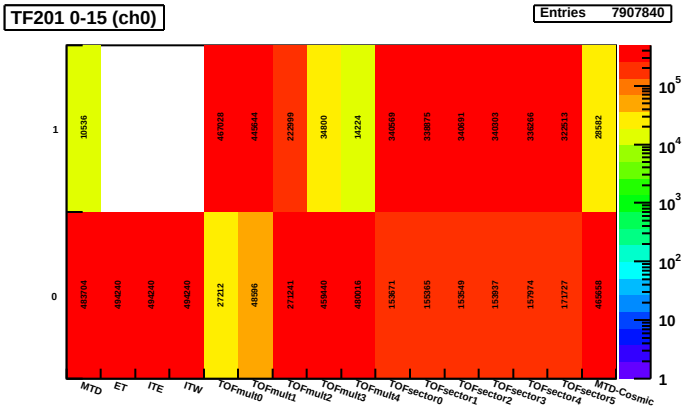

Input to FE001 QT board
Entries 1.581568e+07
ADC
channel

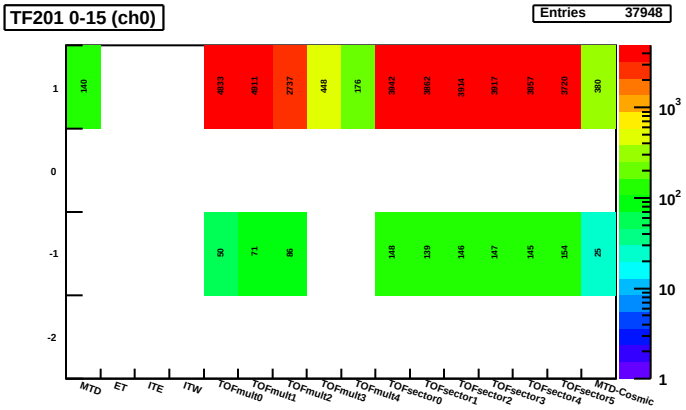

Input to FE002 QT board
Entries 1.581568e+07
ADC
channel

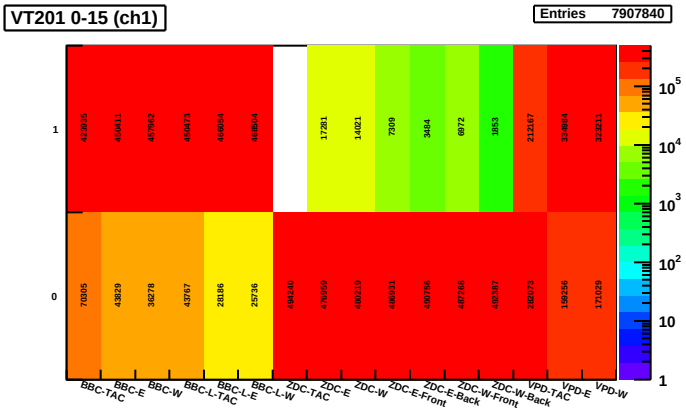

Input to FE003 QT board
Entries 1.581568e+07
ADC
channel

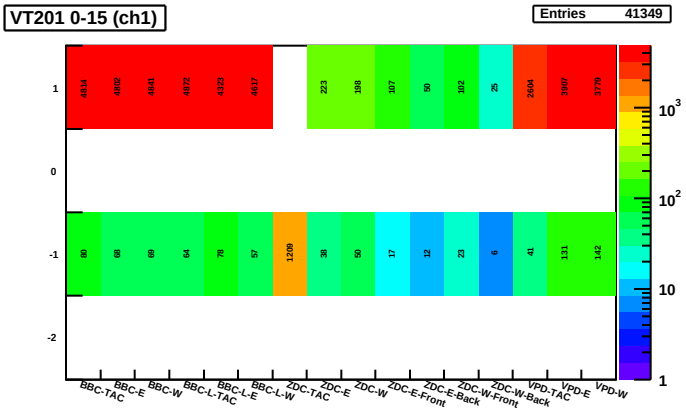

Input to FE004 QT board
Entries 1.581568e+07
ADC
channel

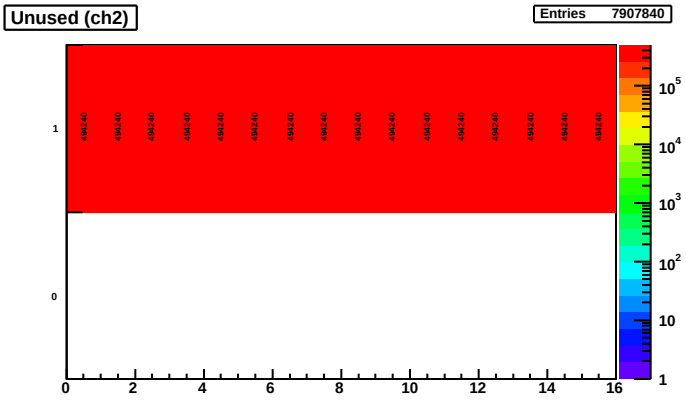

Input to FPE L1 DSM
Entries1976960
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

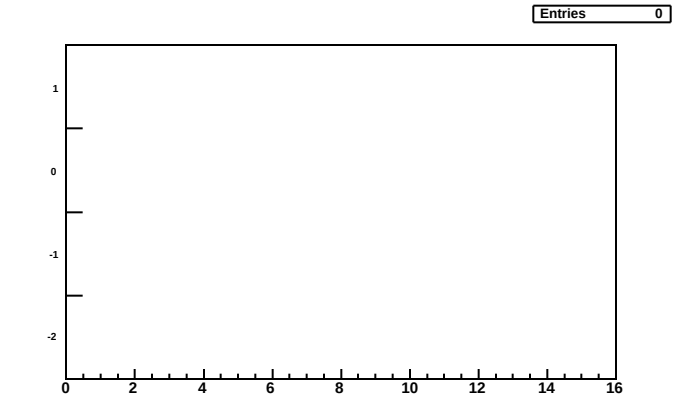

TF201 0-15 (ch0)
Entries 7907840
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

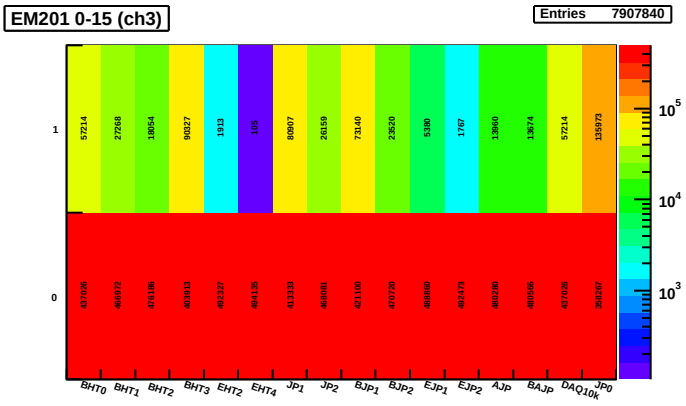

TF201 0-15 (ch0)
Entries 37948
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 7907840
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 41349
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 7907840


Entries 0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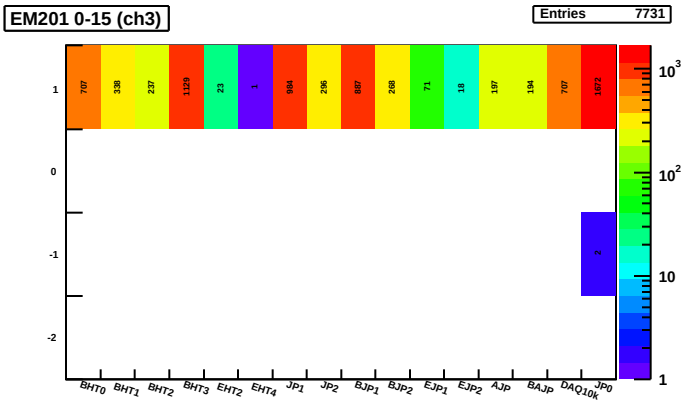
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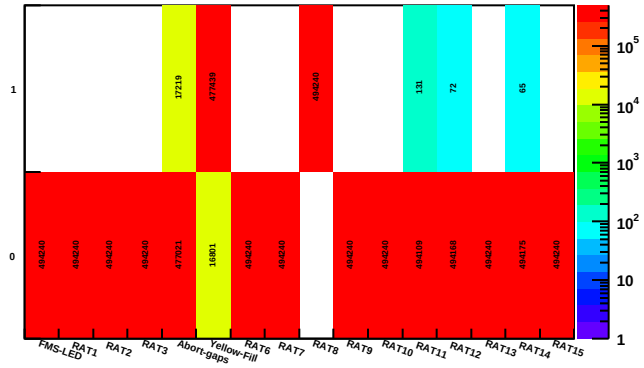
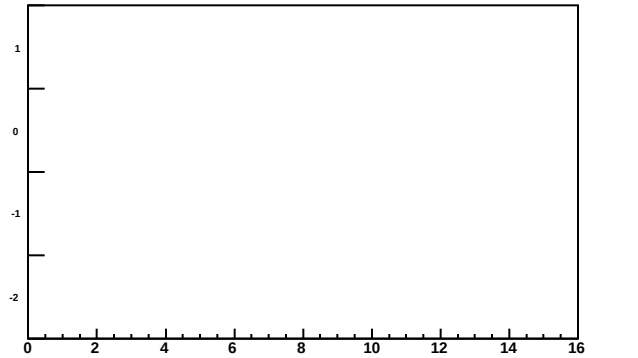
RAT board (ch4)





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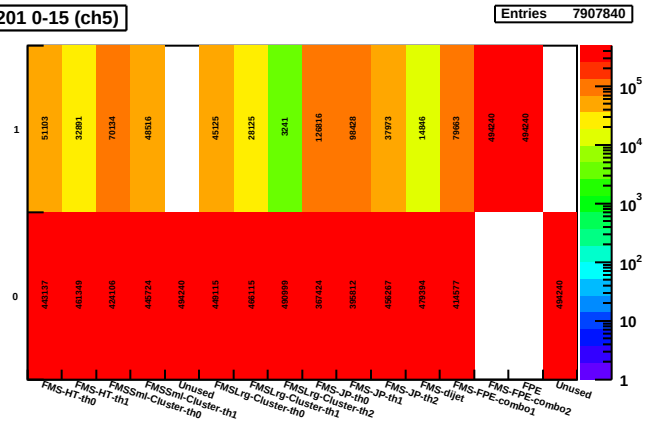

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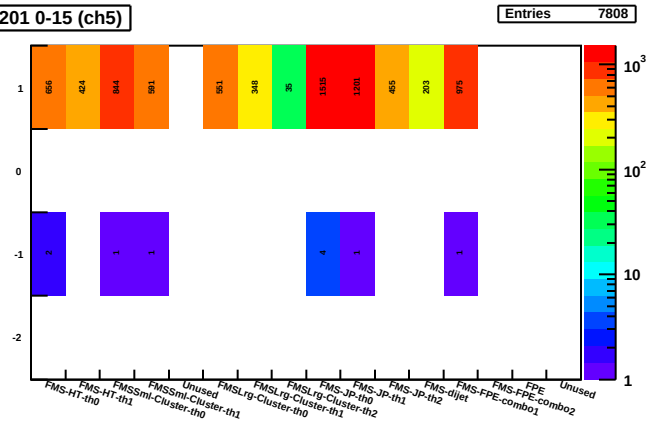

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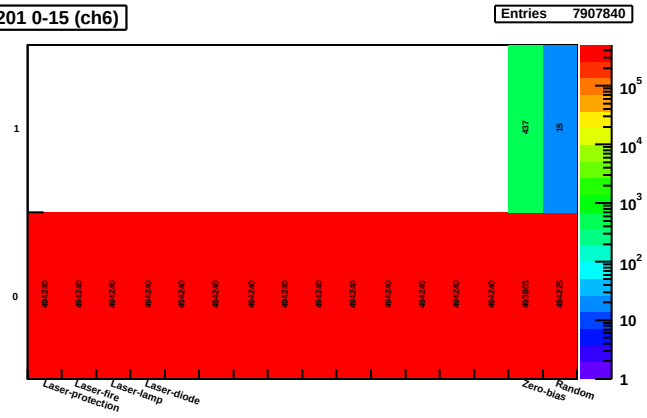

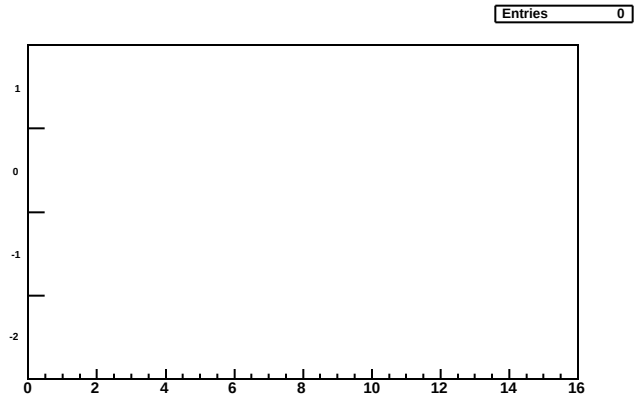
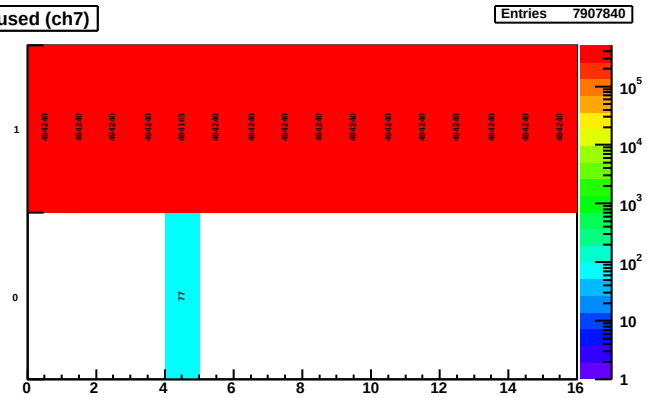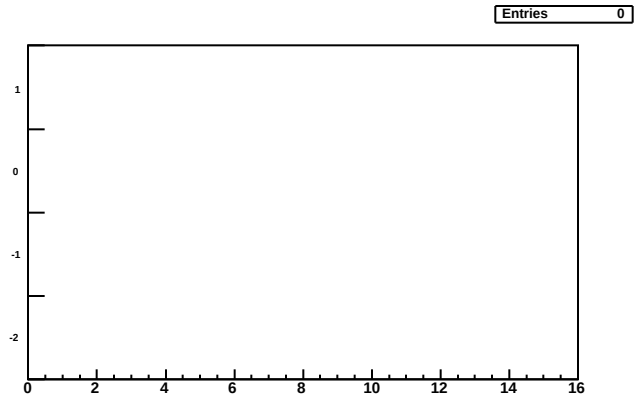
Unused (ch7)