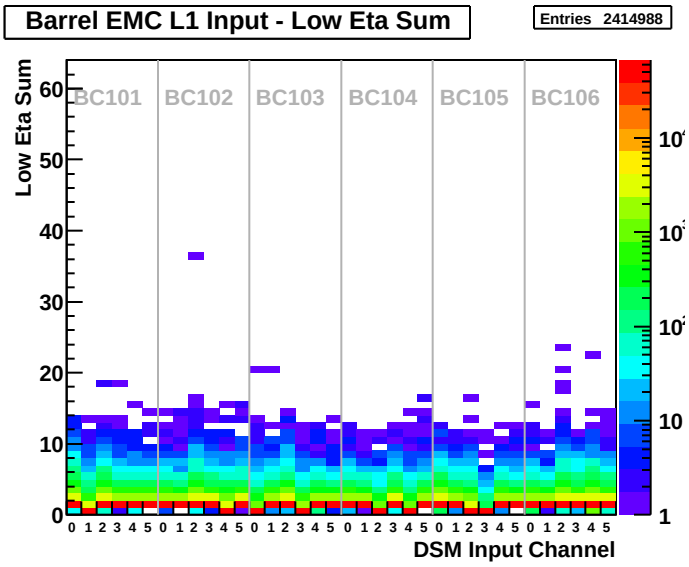
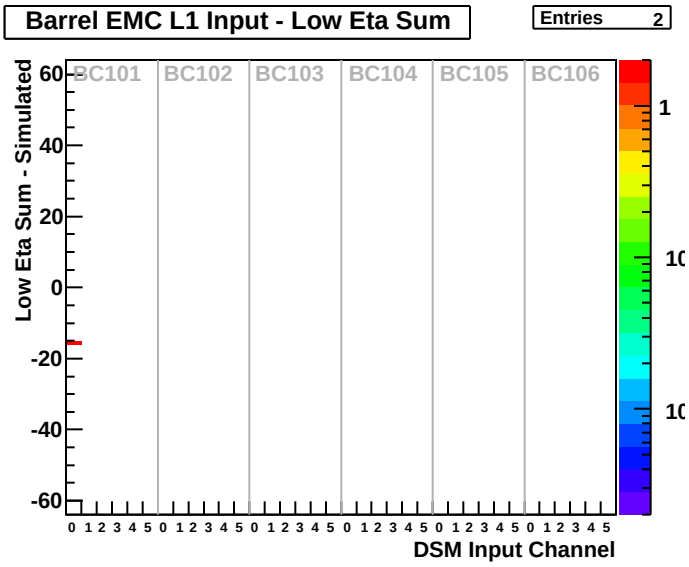
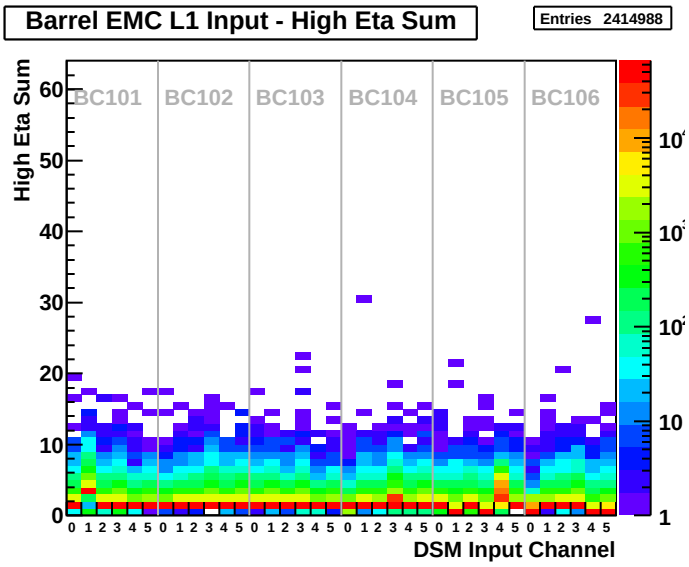
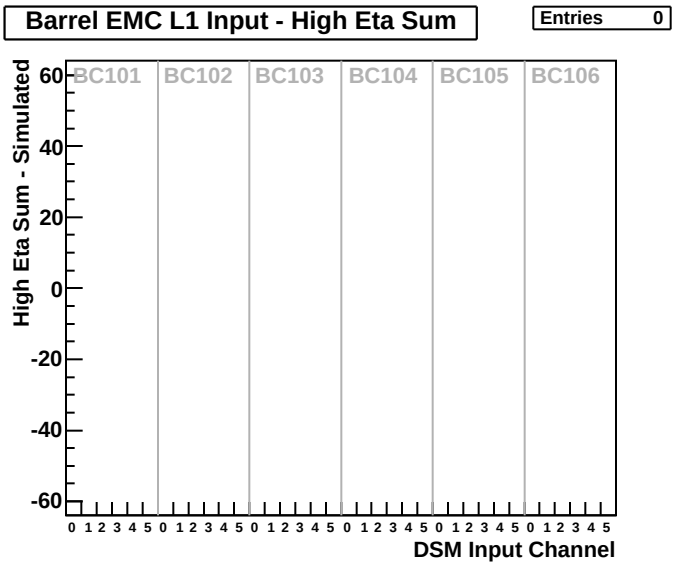
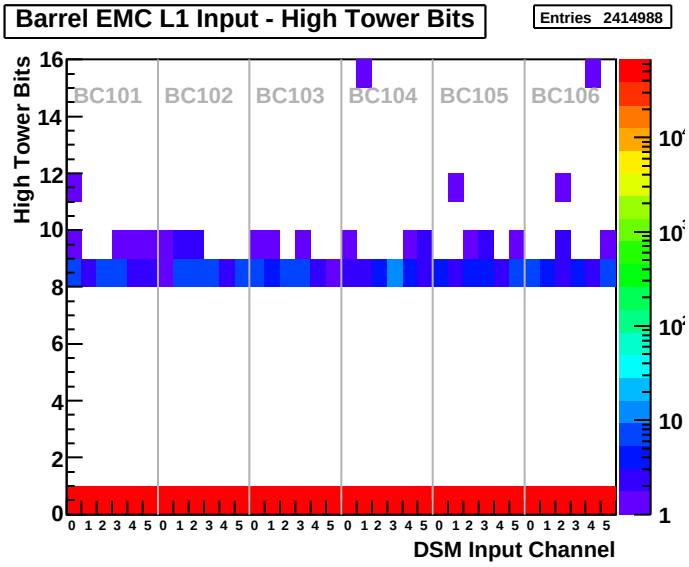
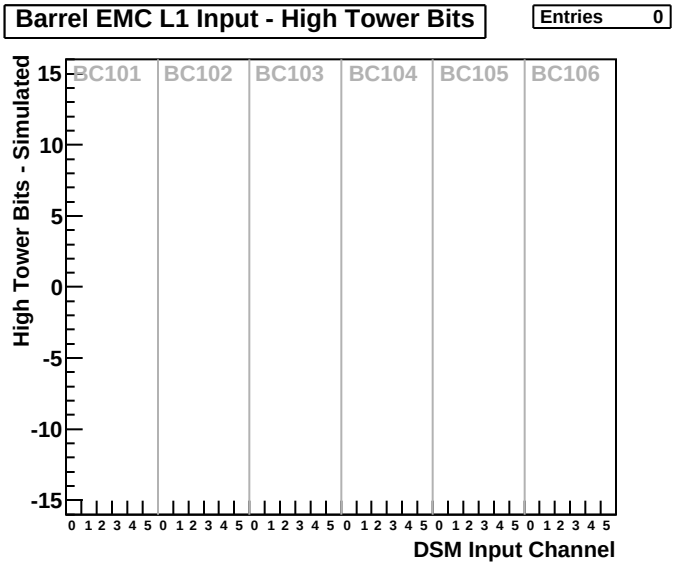

Barrel EMC L1 Input - Low Eta Sum
Entries 2414988
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

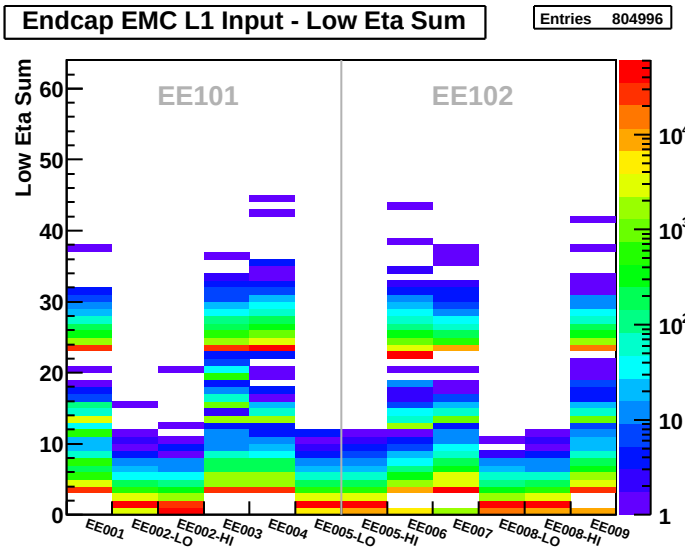

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

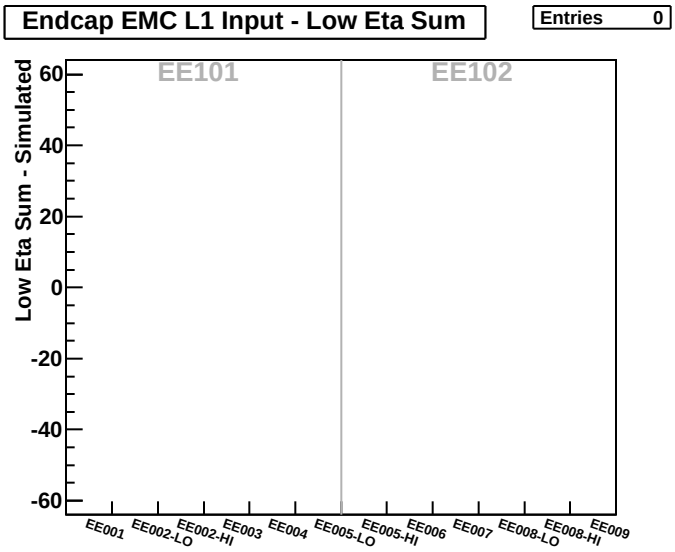

Barrel EMC L1 Input - High Eta Sum
Entries 2414988
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

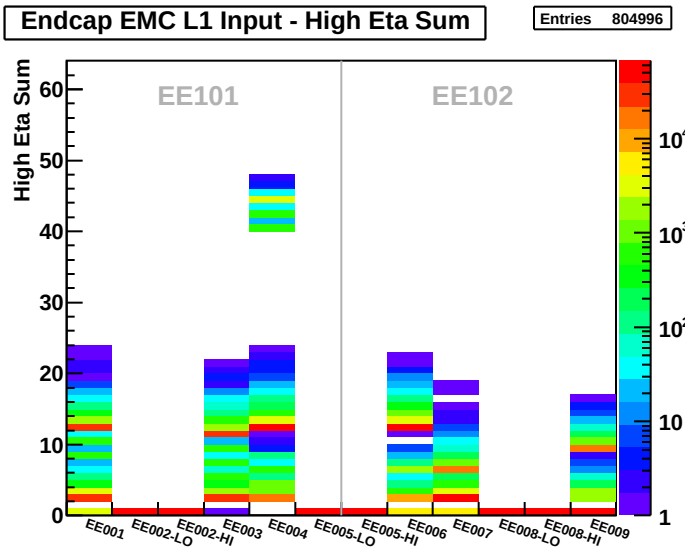

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

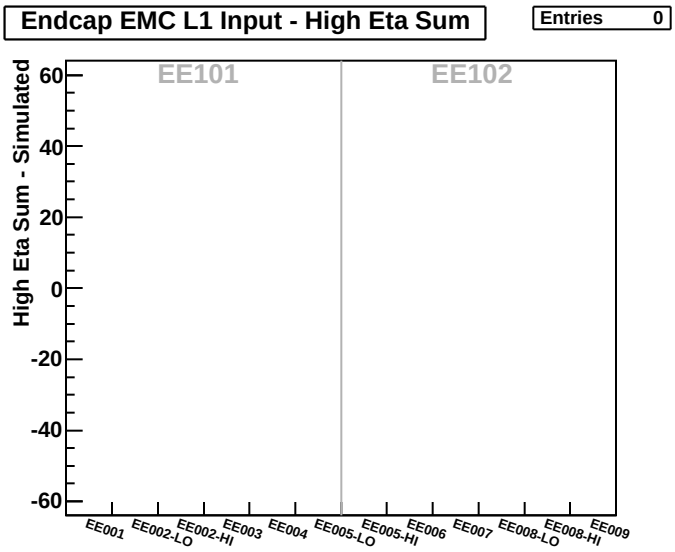

Barrel EMC L1 Input - High Tower Bits
Entries 2414988
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

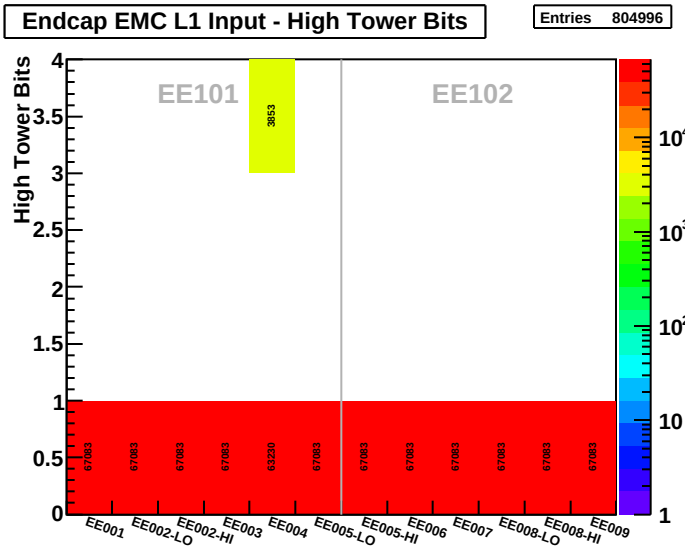

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

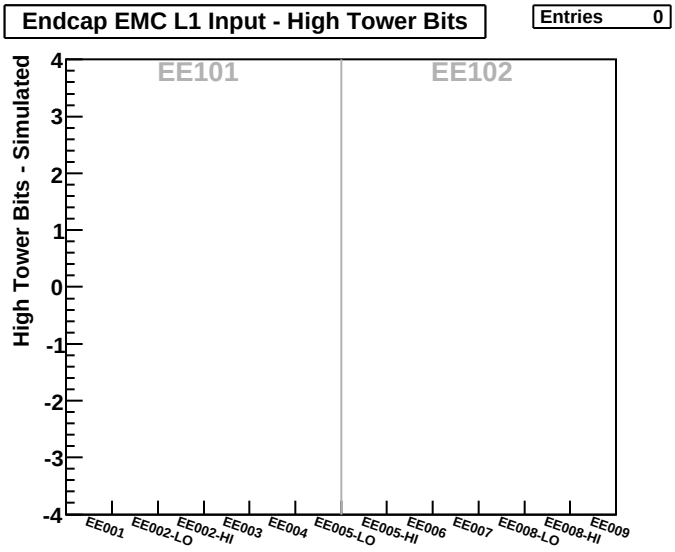

Endcap EMC L1 Input - Low Eta Sum
Entries 804996
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

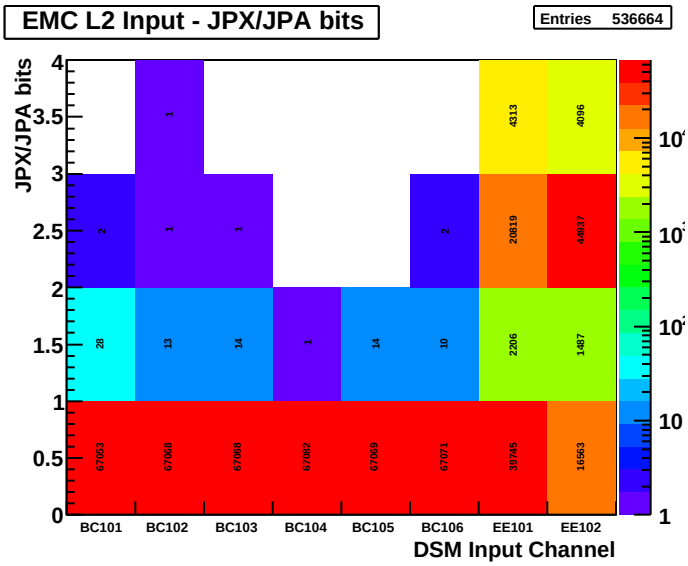

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

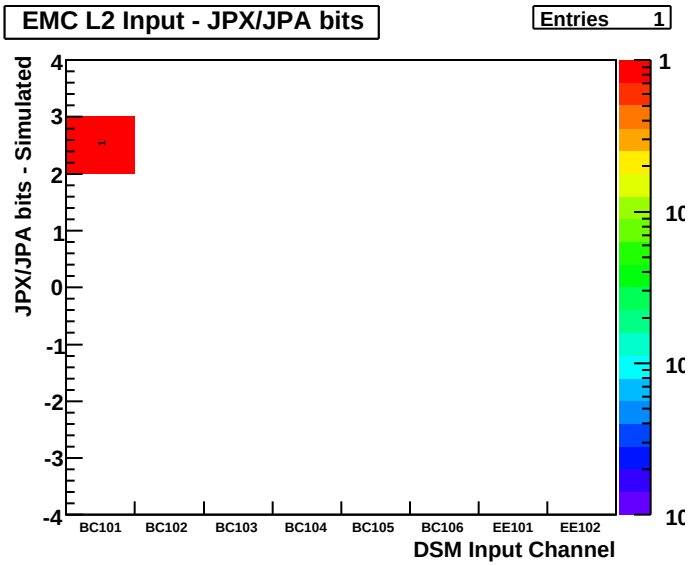

Endcap EMC L1 Input - High Eta Sum
Entries 804996
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

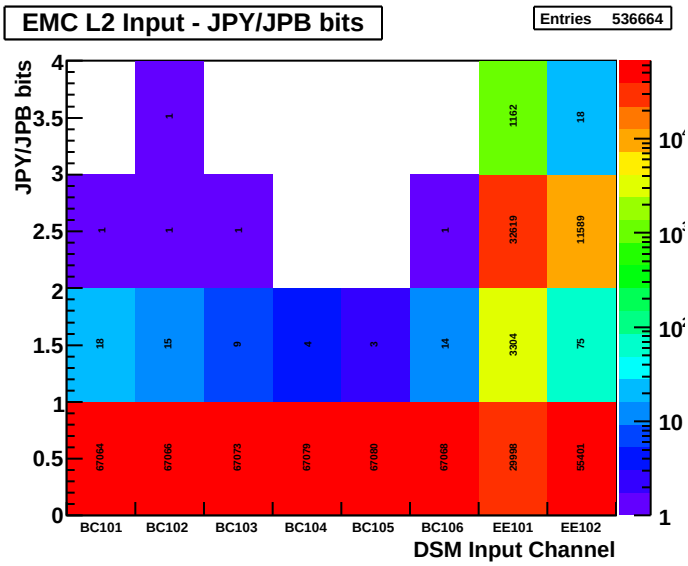

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

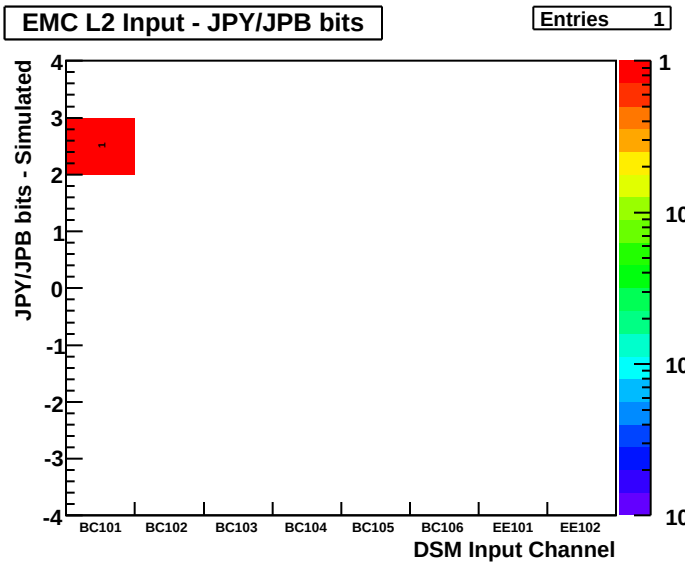

Endcap EMC L1 Input - High Tower Bits
Entries 804996
High Tower Bits
EE101
EE102
3853
67683 67683 67683 67683 63230 67683 67683 67683 67683 67683 67683 67683
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

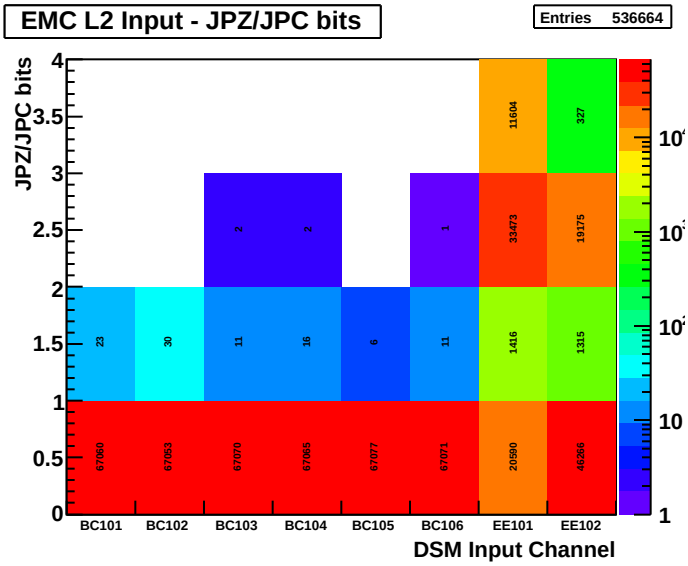

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

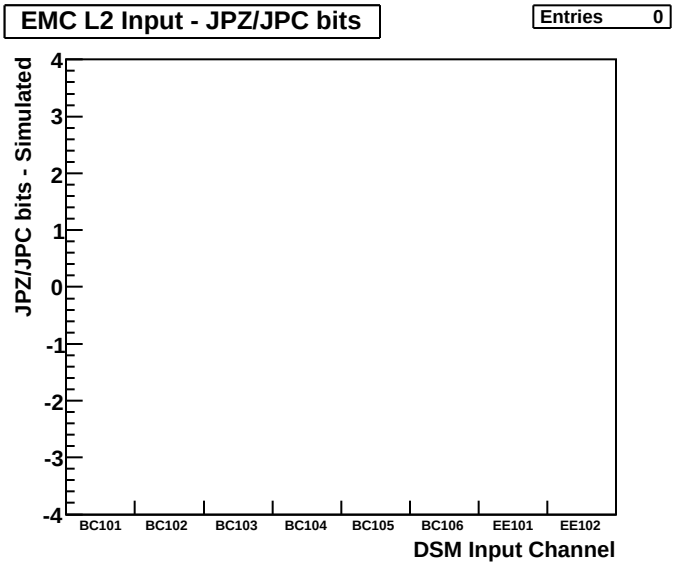

EMC L2 Input - JPX/JPA bits
Entries 536664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

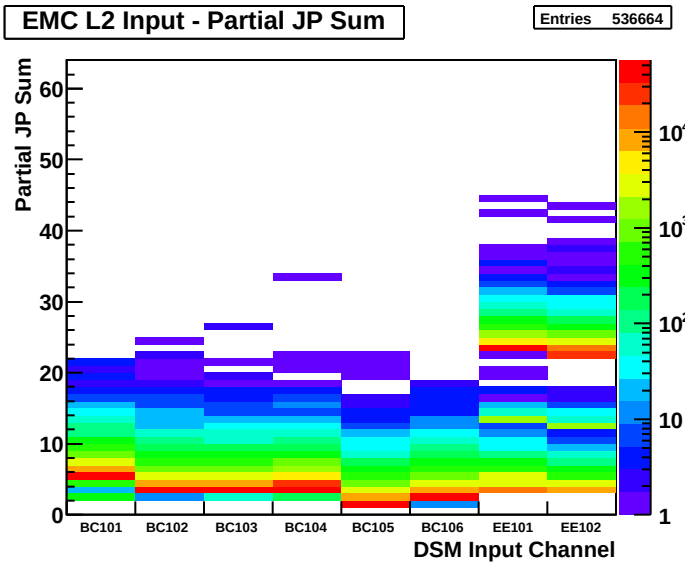

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

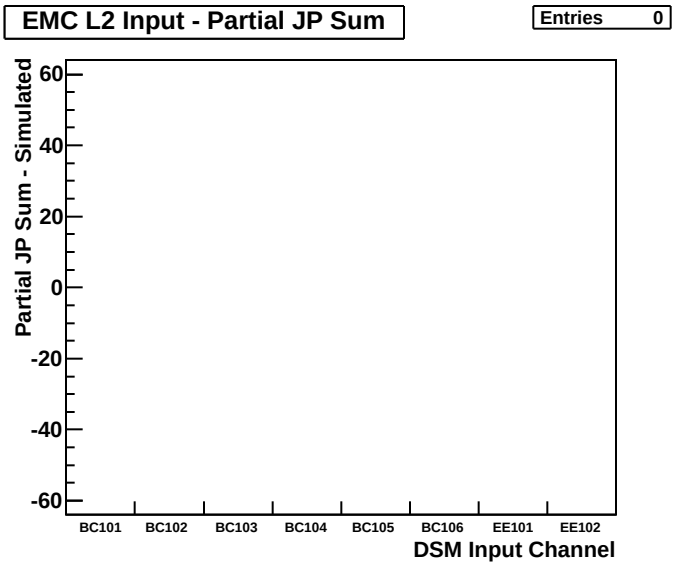

EMC L2 Input - JPY/JPB bits
Entries 536664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

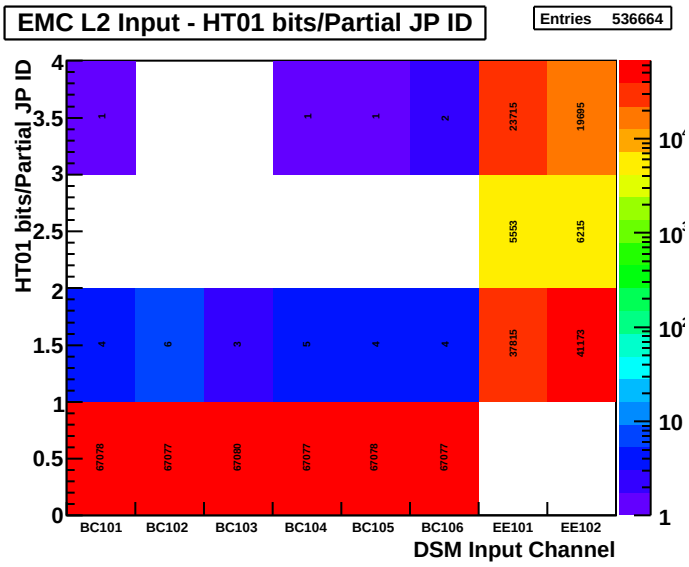

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

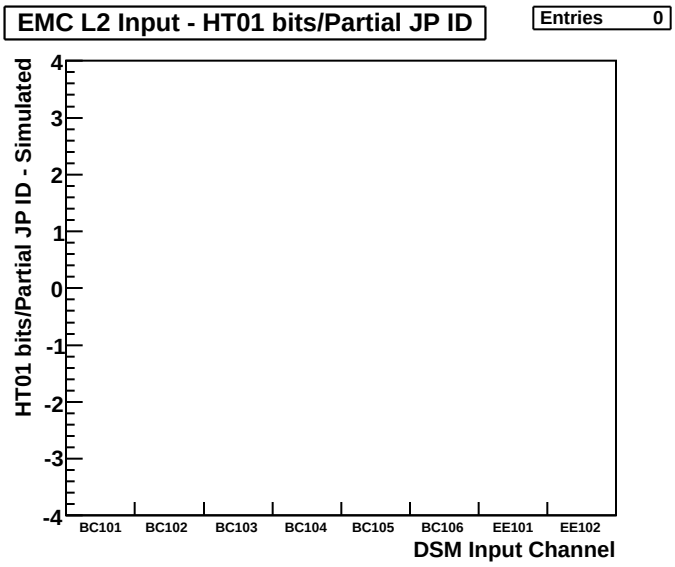

EMC L2 Input - JPZ/JPC bits
Entries 536664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

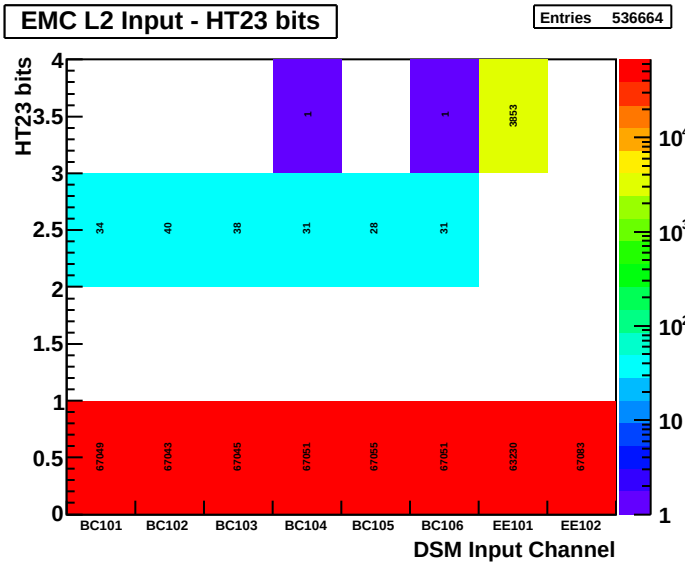

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

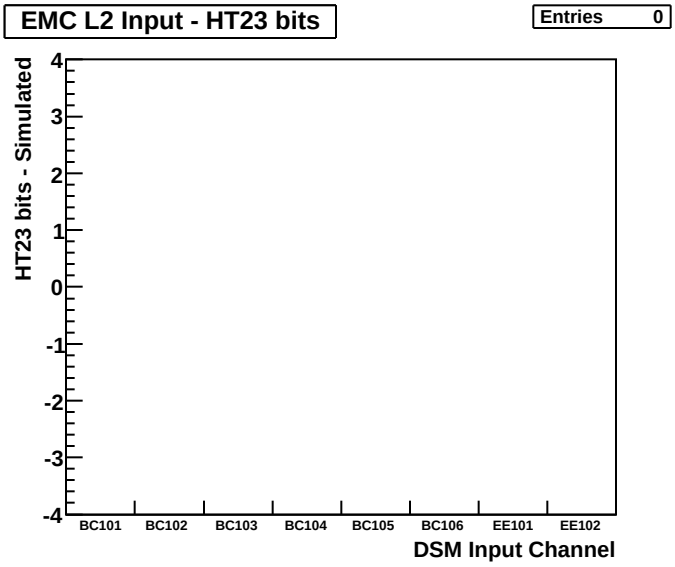

EMC L2 Input - Partial JP Sum
Entries 536664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

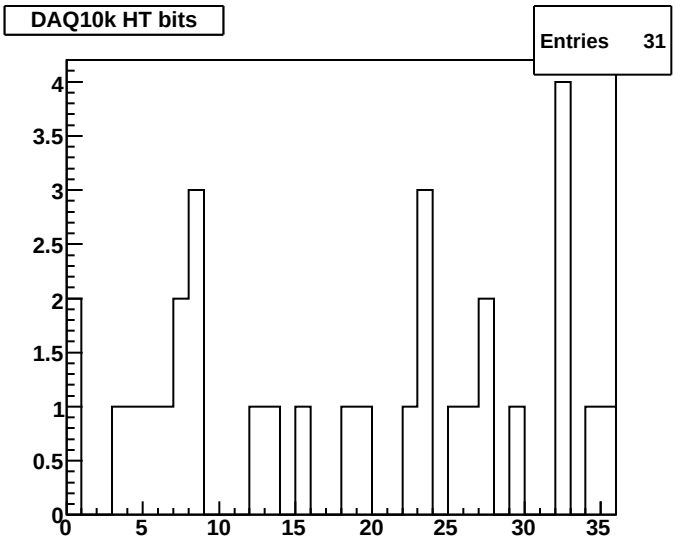

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

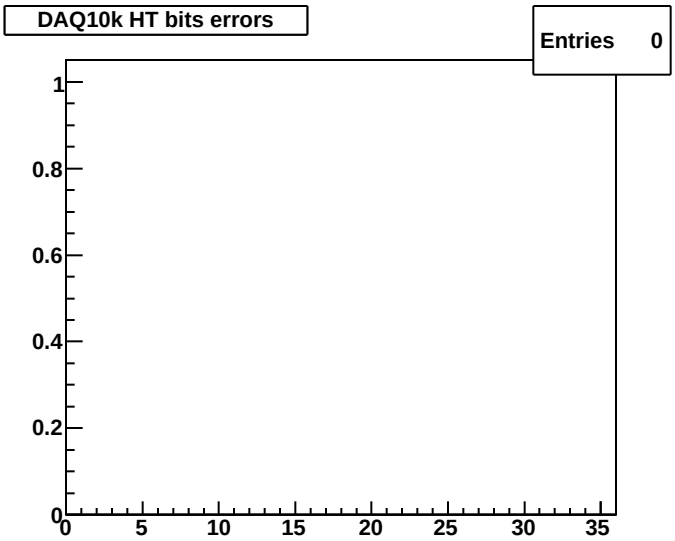

EMC L2 Input - HT01 bits/Partial JP ID
Entries 536664
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

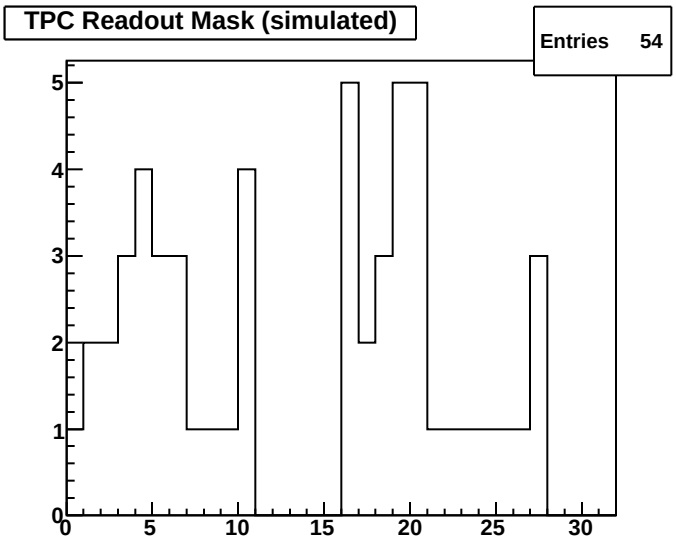

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

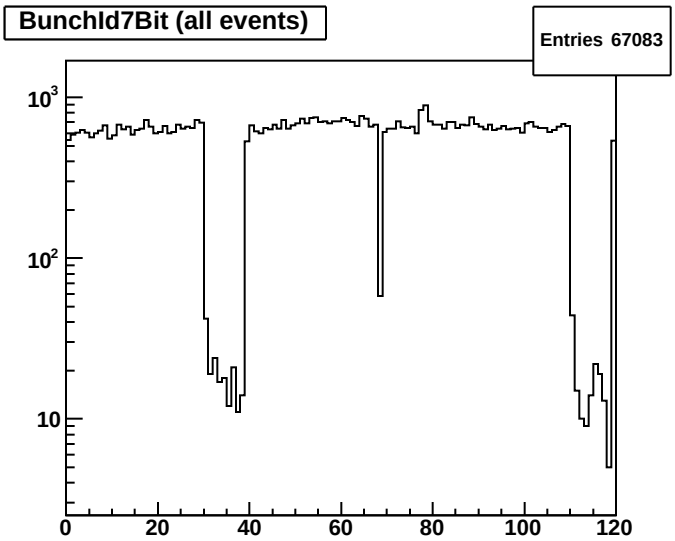

EMC L2 Input - HT23 bits
Entries 536664
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

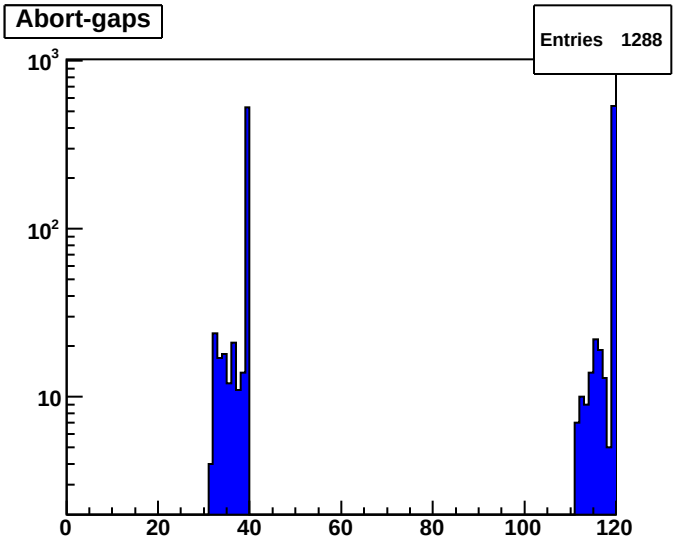

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

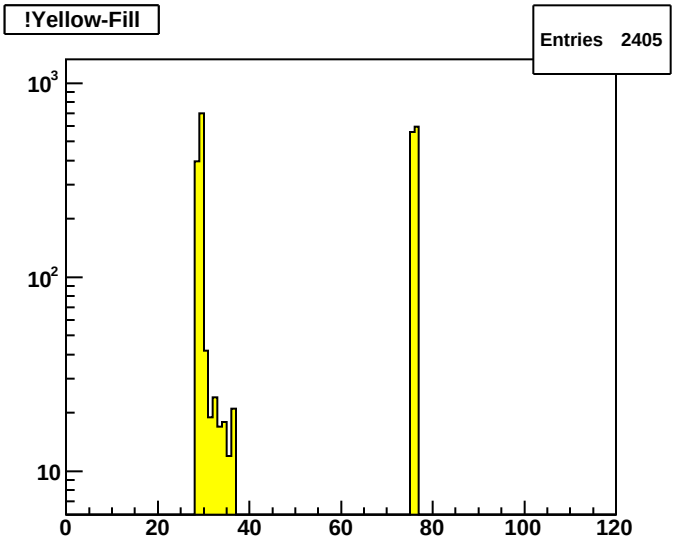

DAQ10k HT bits
Entries 31

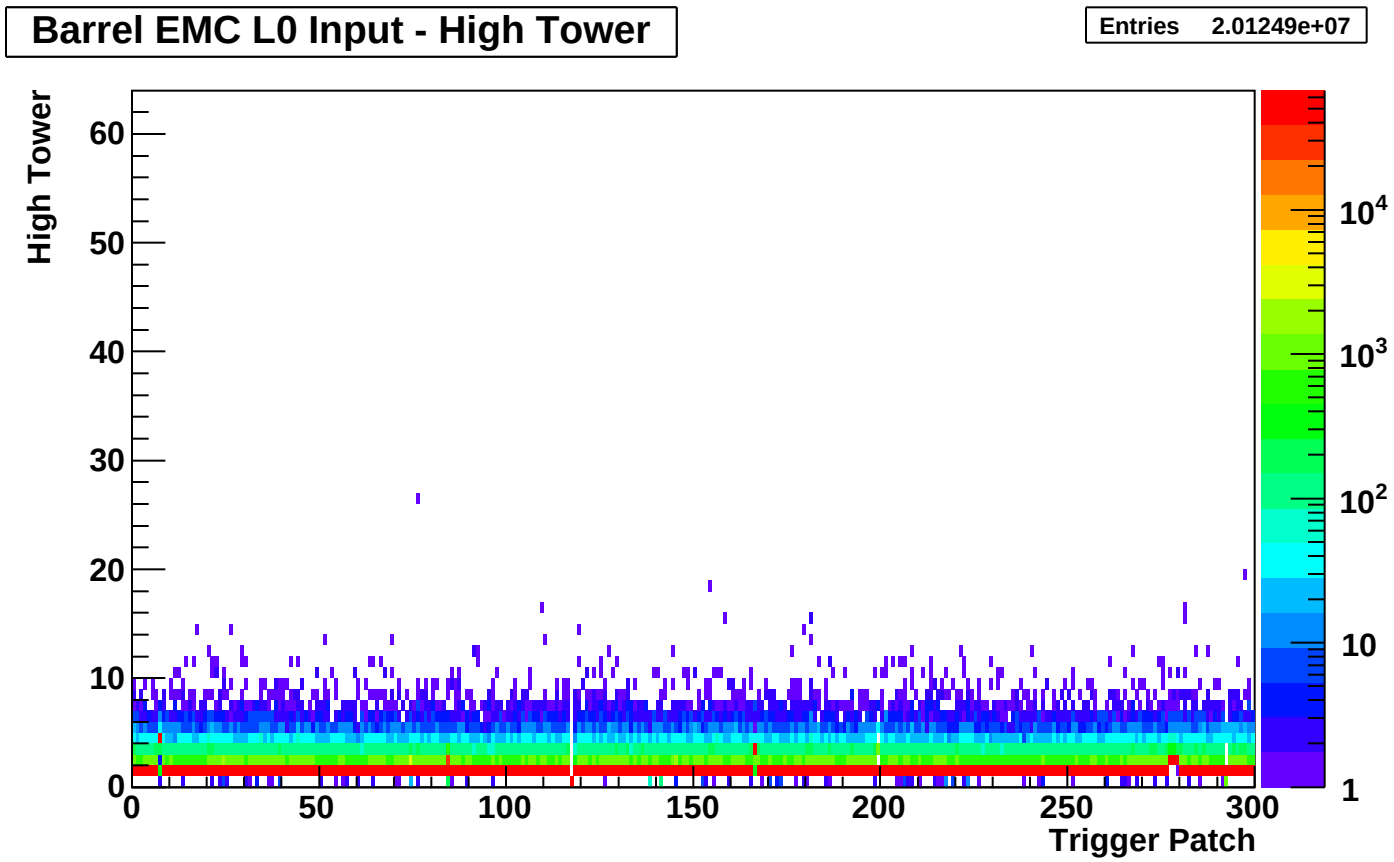

DAQ10k HT bits errors
Entries 0

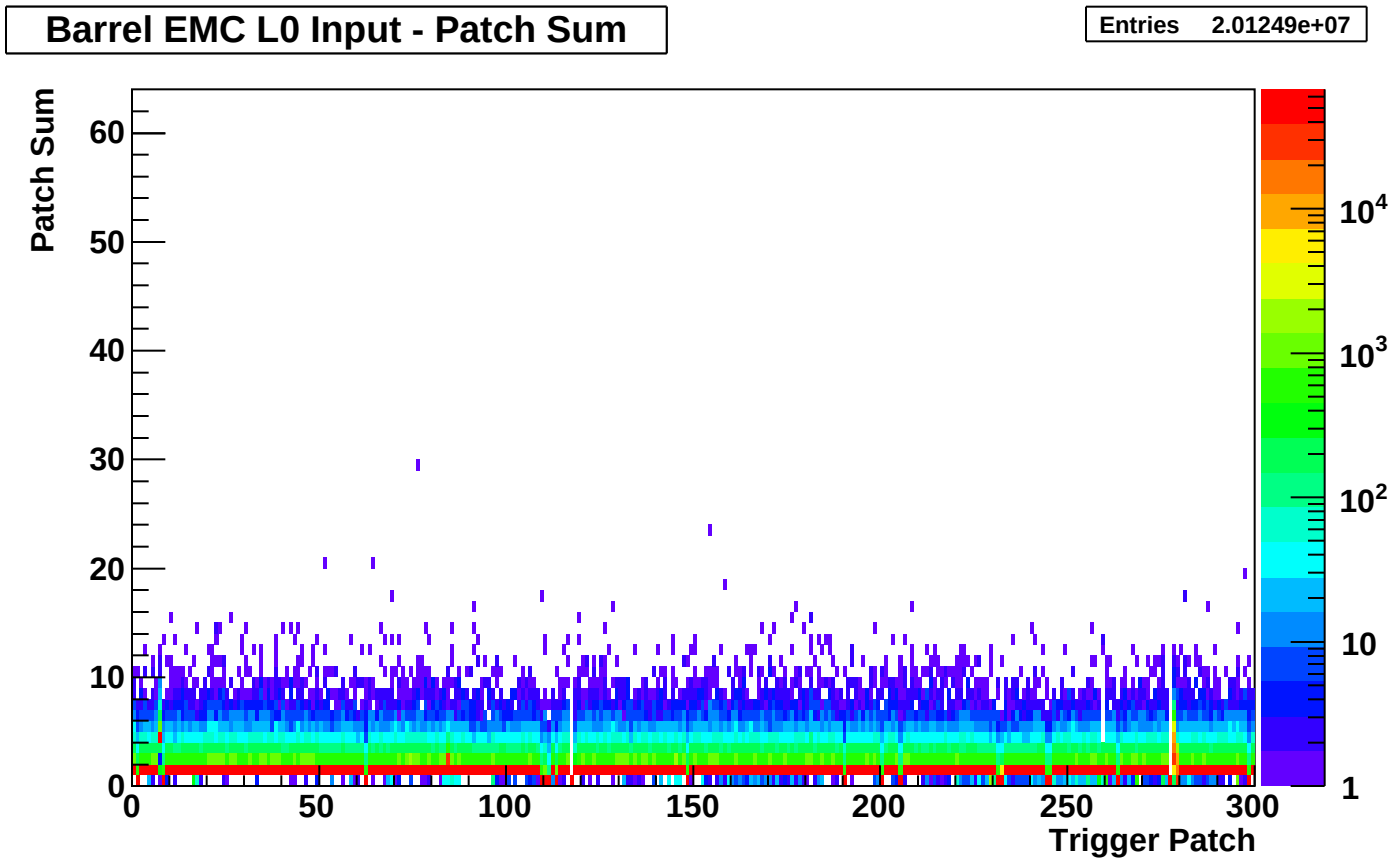

TPC Readout Mask (simulated)
Entries 54

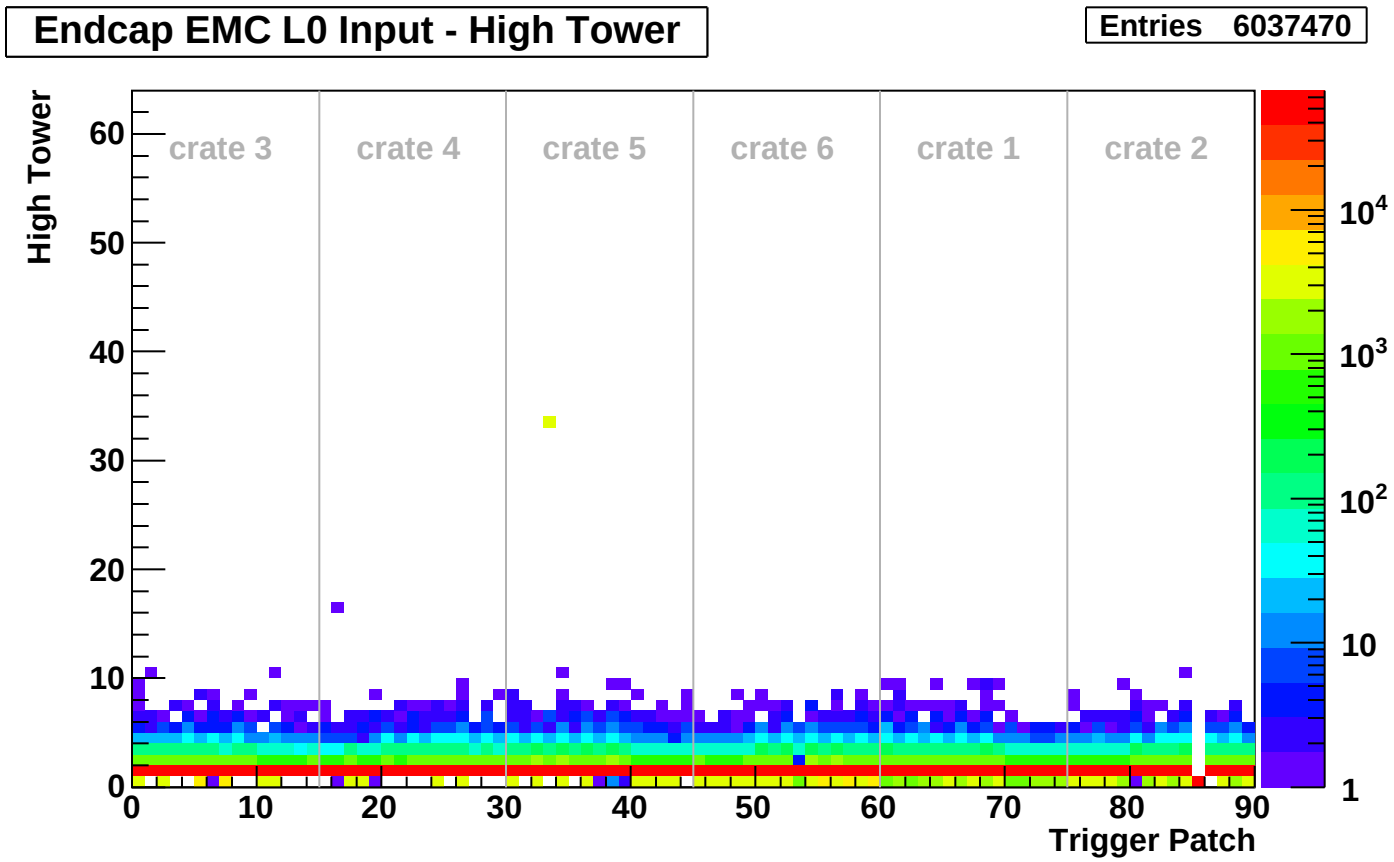

BunchId7Bit (all events)
Entries 67083

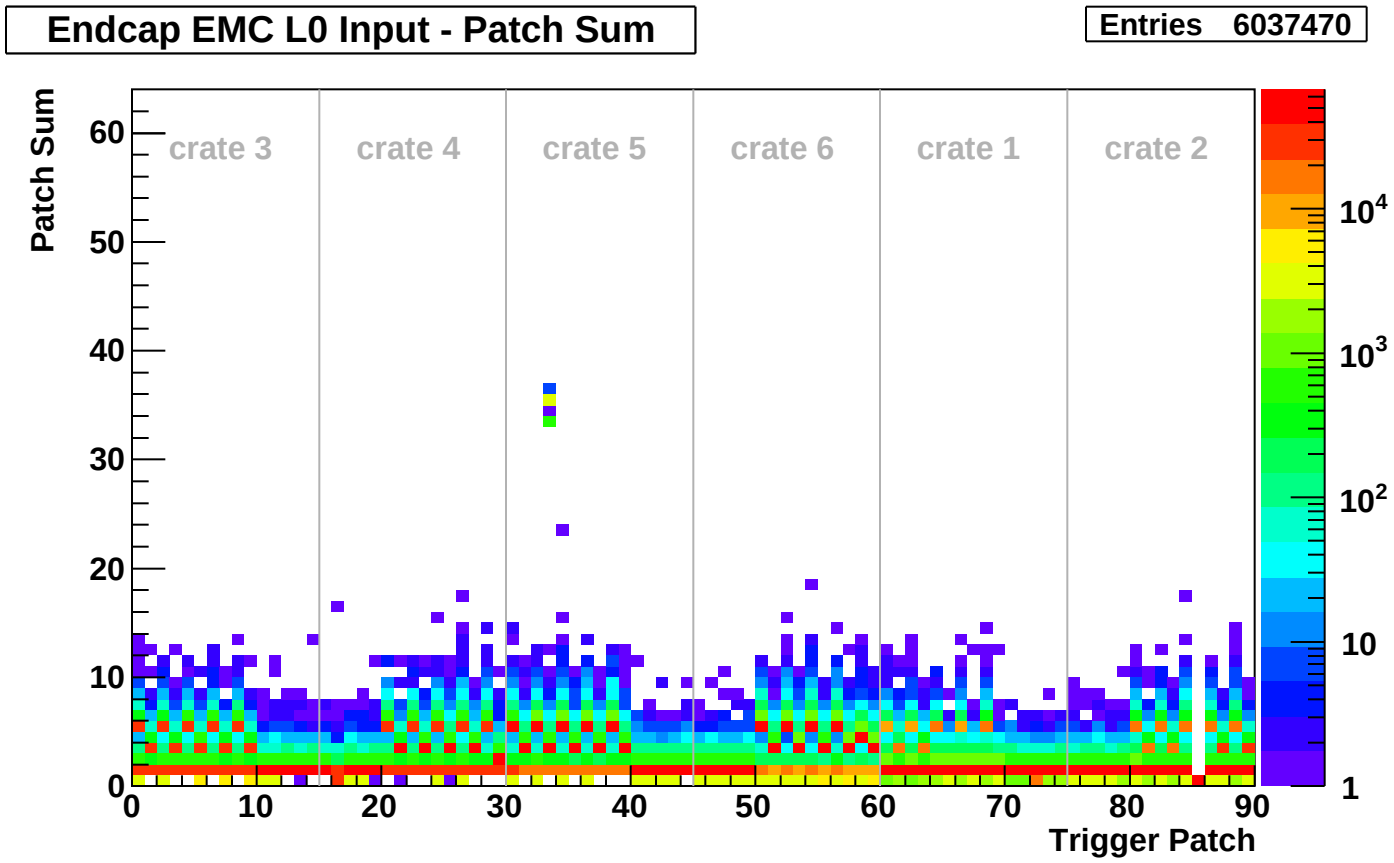

Abort-gaps
Entries 1288

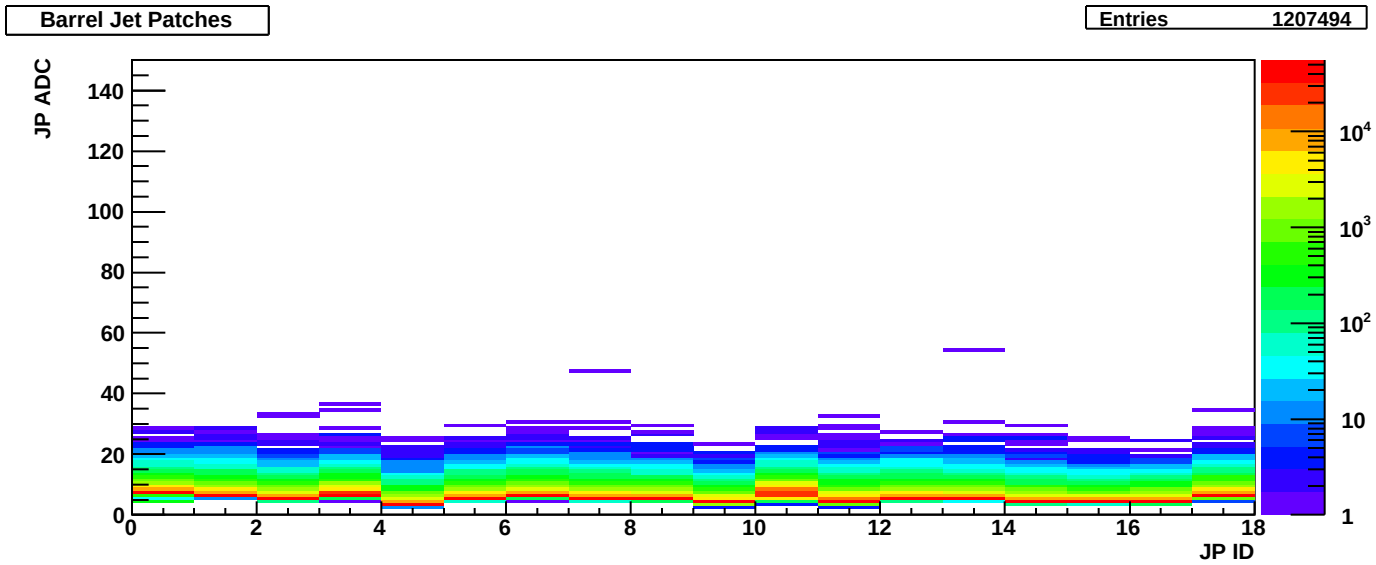

!Yellow-Fill
Entries 2405

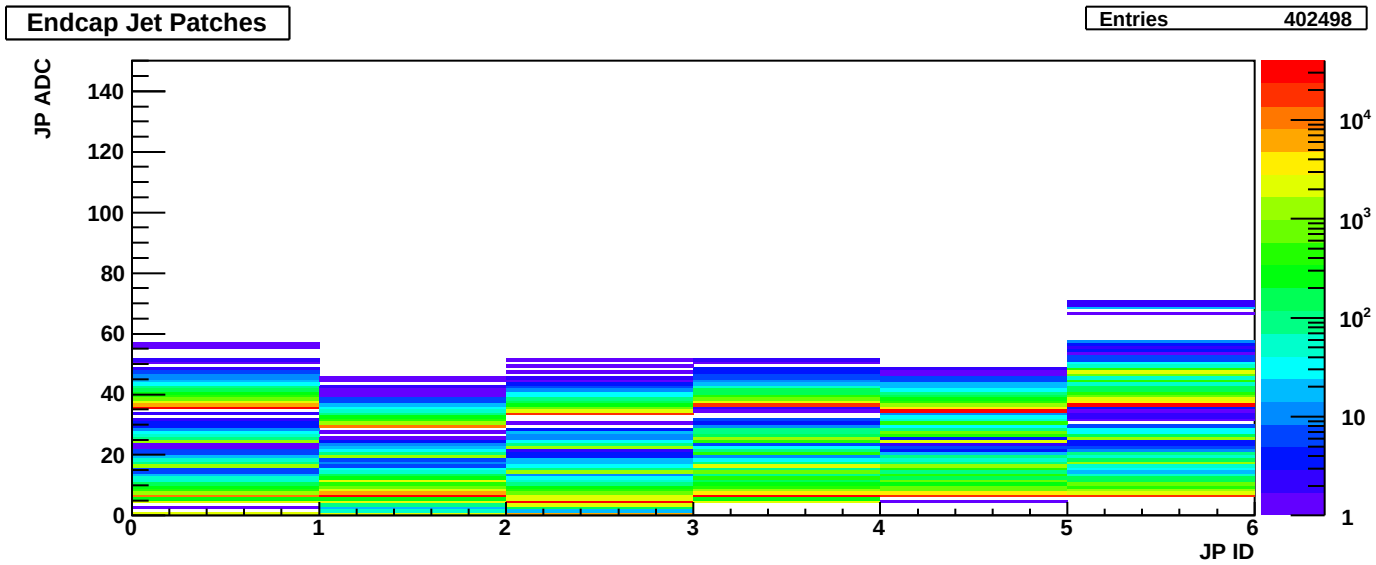

Barrel EMC L0 Input - High Tower
Entries 2.01249e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

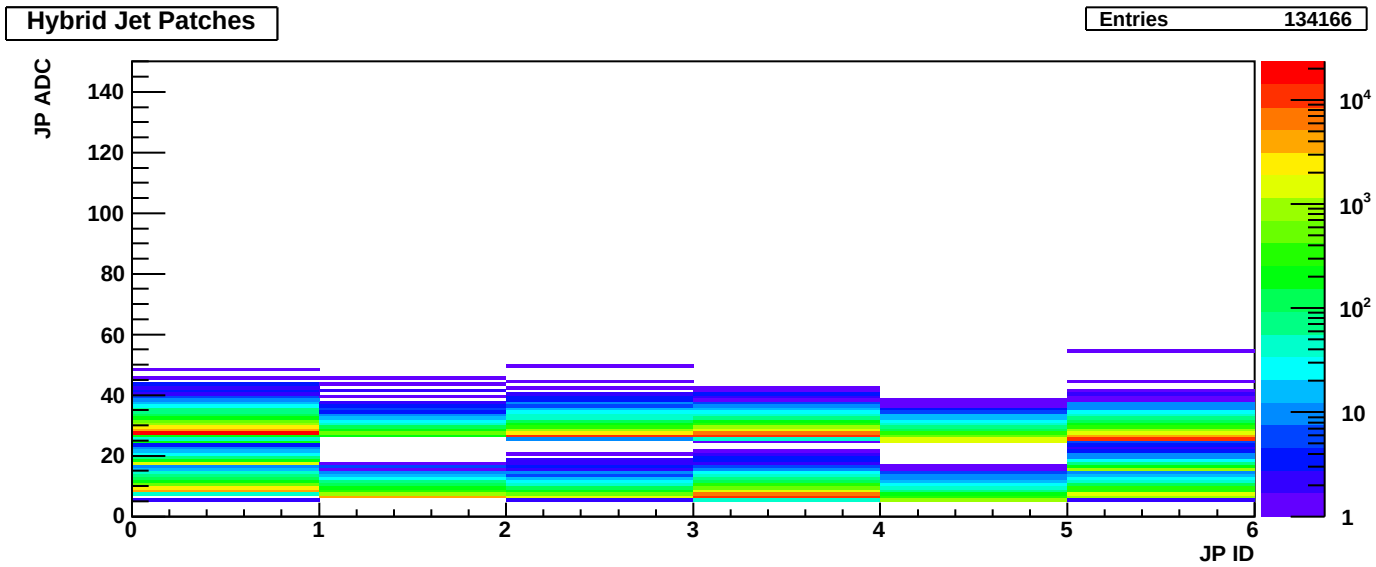

Barrel EMC L0 Input - Patch Sum
Entries 2.01249e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

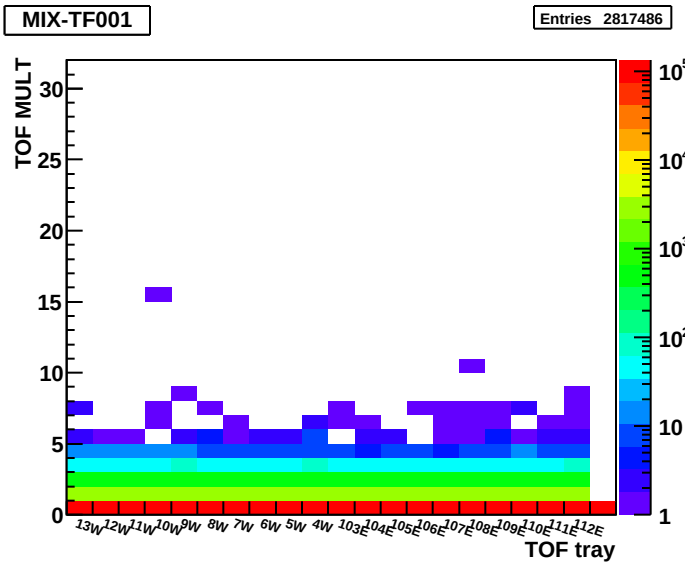

Endcap EMC L0 Input - High Tower
Entries 6037470
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

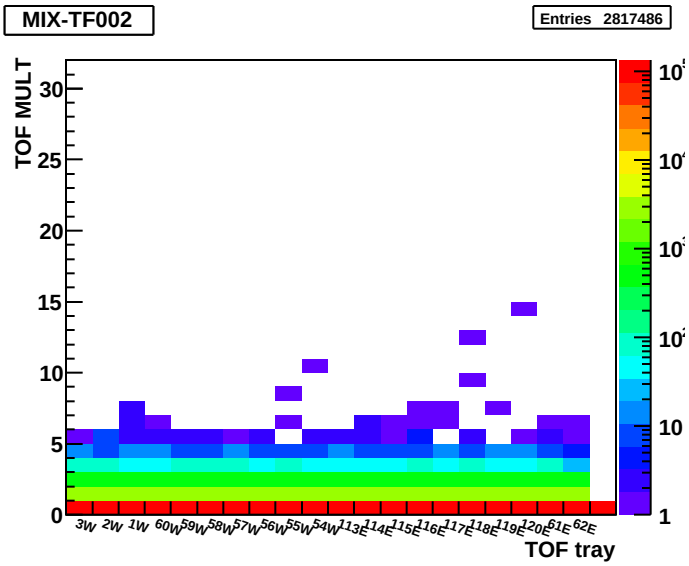

Endcap EMC L0 Input - Patch Sum
Entries 6037470
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

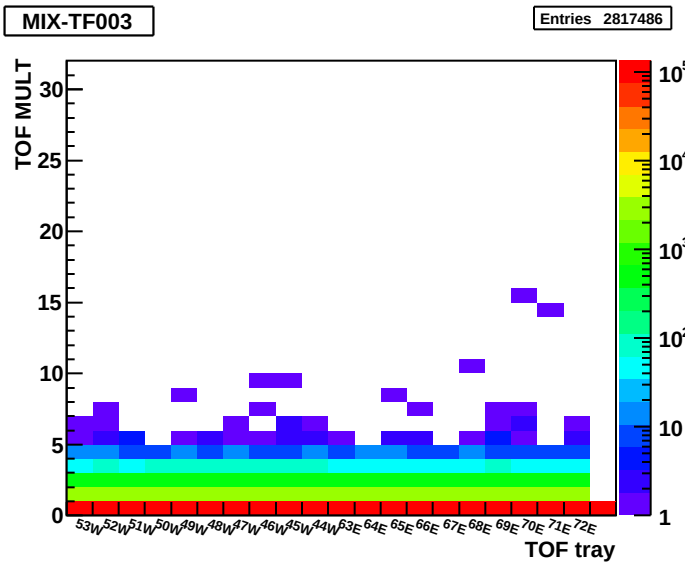

Barrel Jet Patches
Entries 1207494
JP ADC
JP ID

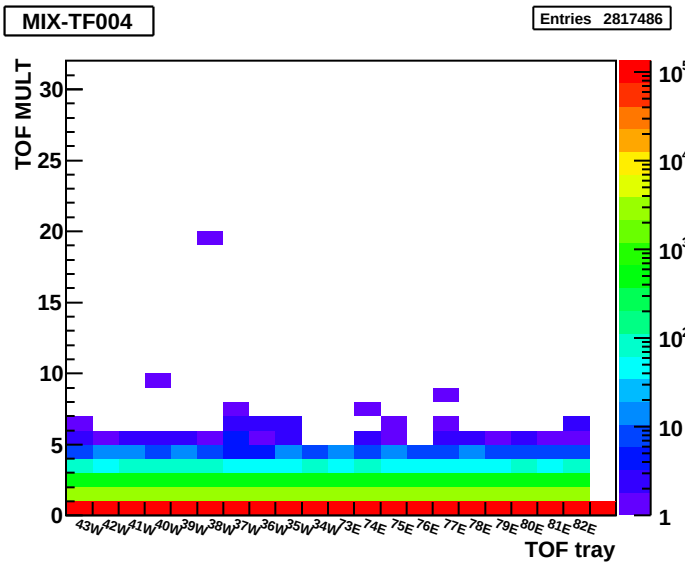

Endcap Jet Patches
Entries 402498
JP ADC
JP ID

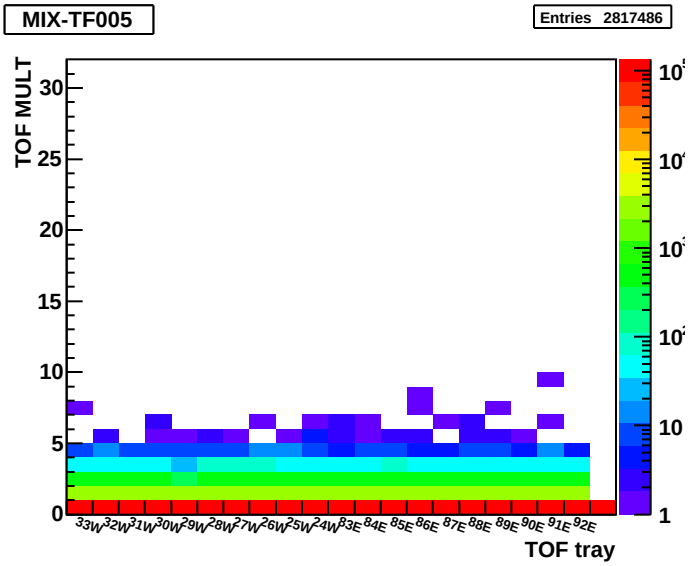

Hybrid Jet Patches
Entries 134166
JP ADC
JP ID

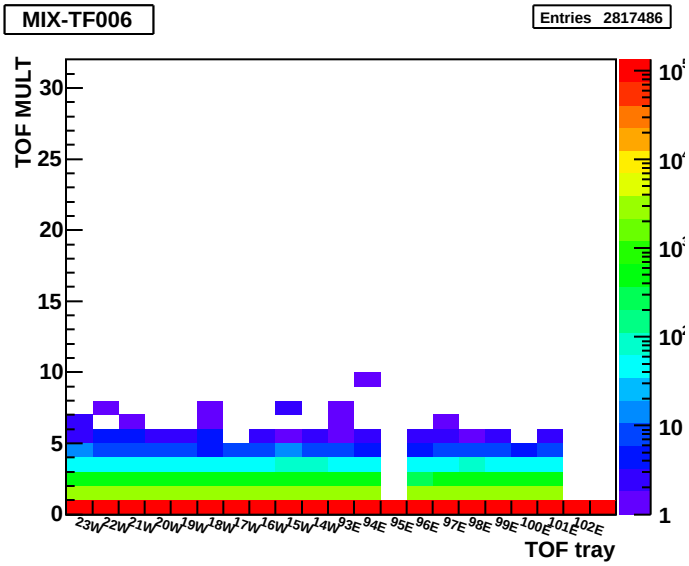

MIX-TF001
Entries 2817486
TOF MULT
TOF tray

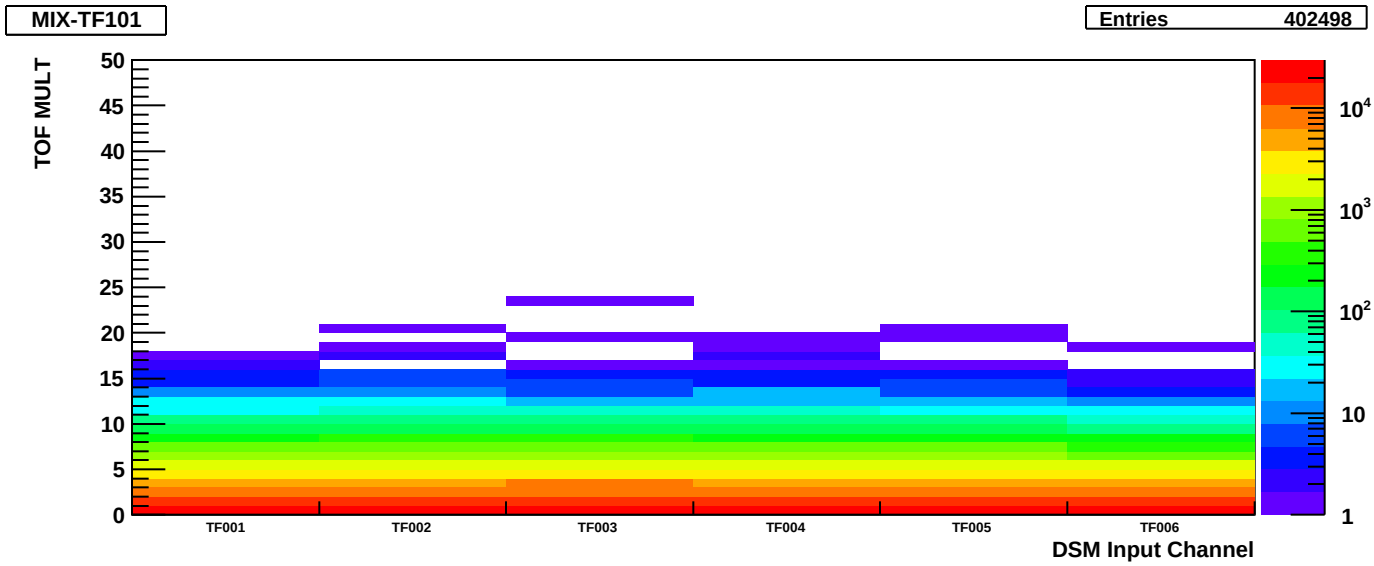

MIX-TF002
Entries 2817486
TOF MULT
TOF tray

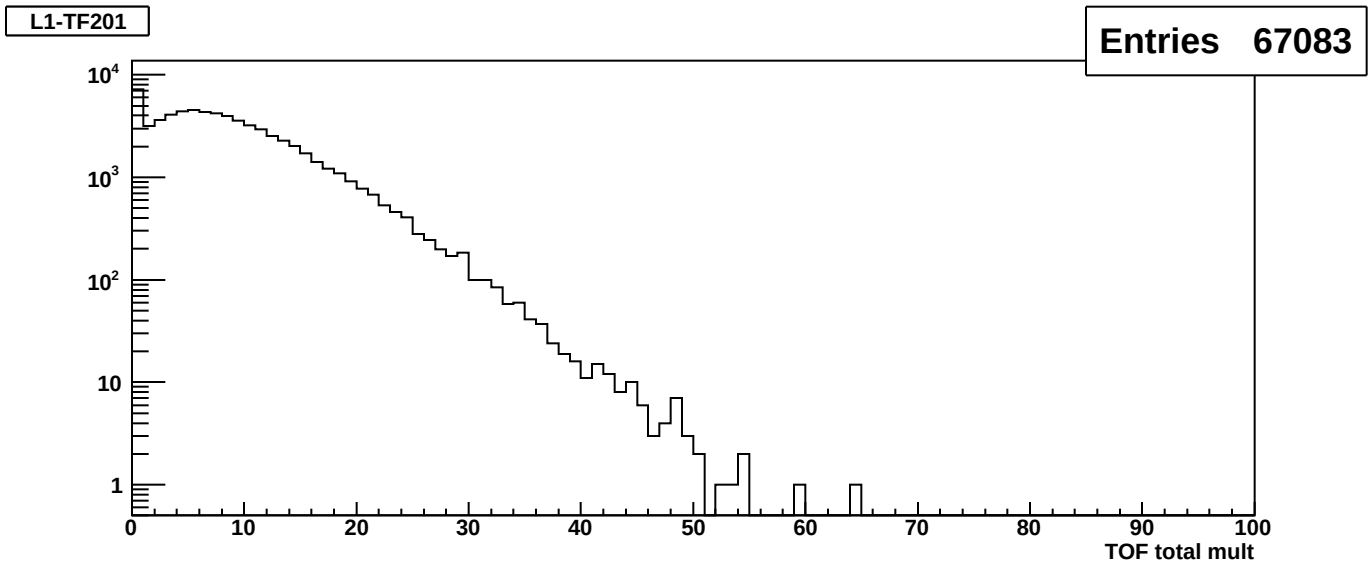

MIX-TF003
Entries 2817486
TOF MULT
TOF tray

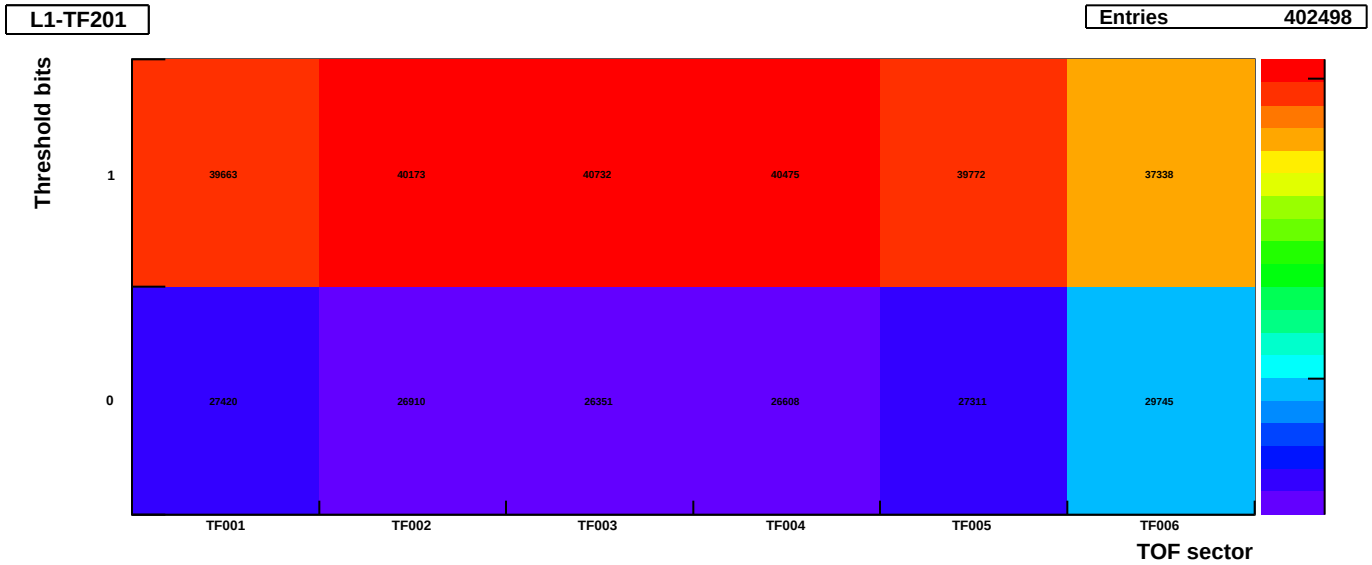

MIX-TF004
Entries 2817486
TOF MULT
TOF tray

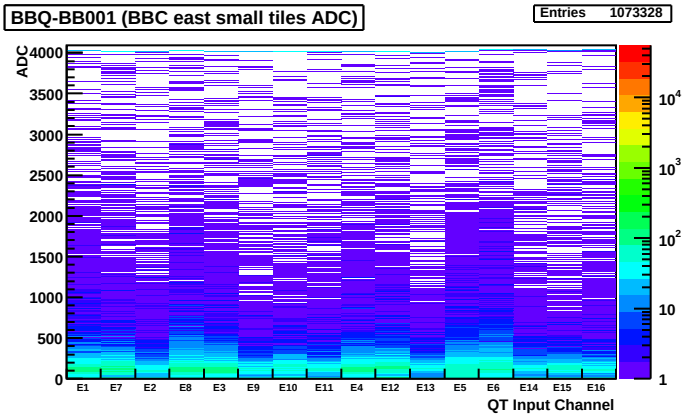

MIX-TF005
Entries 2817486
TOF MULT
TOF tray

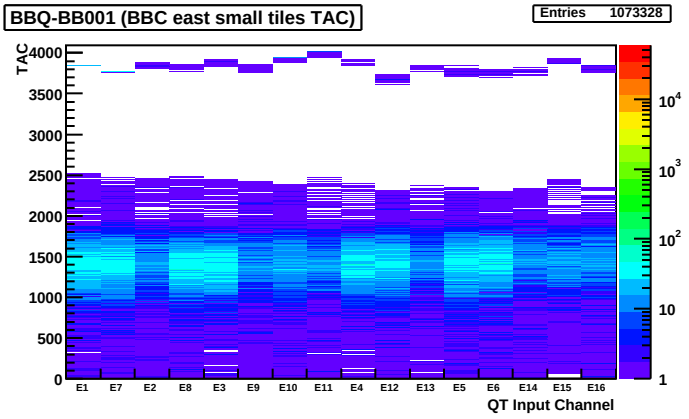

MIX-TF006
Entries 2817486
TOF MULT
TOF tray

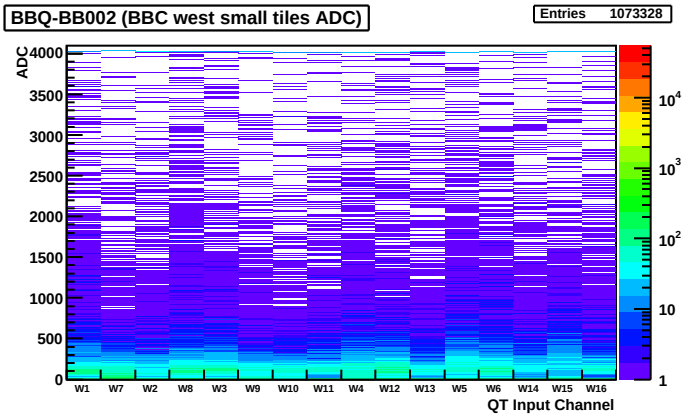

MIX-TF101
Entries 402498
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

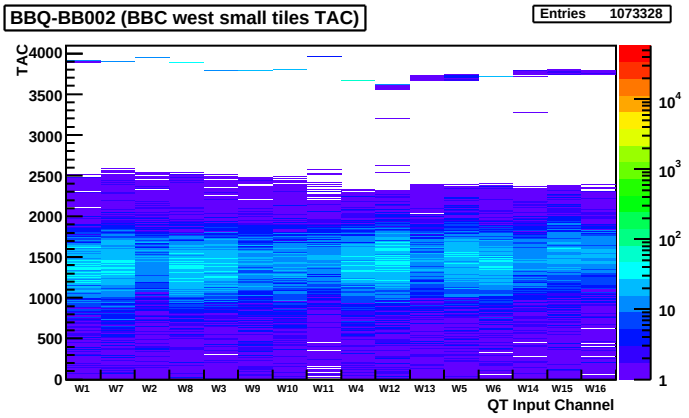

L1-TF201
Entries 67083
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

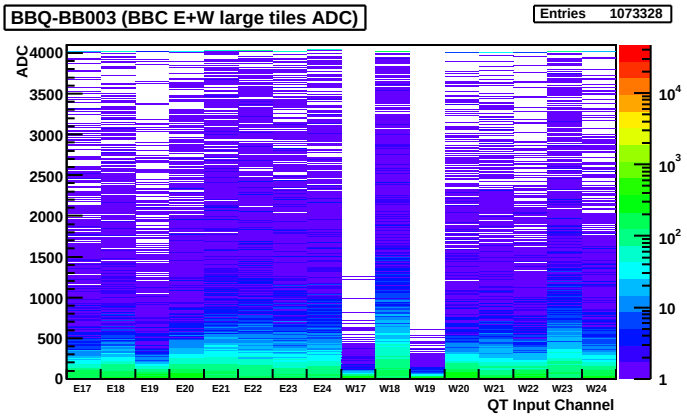

L1-TF201
Entries 402498
Threshold bits
1
0
39663
40173
40732
40475
39772
37338
27420
26910
26351
26608
27311
29745
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

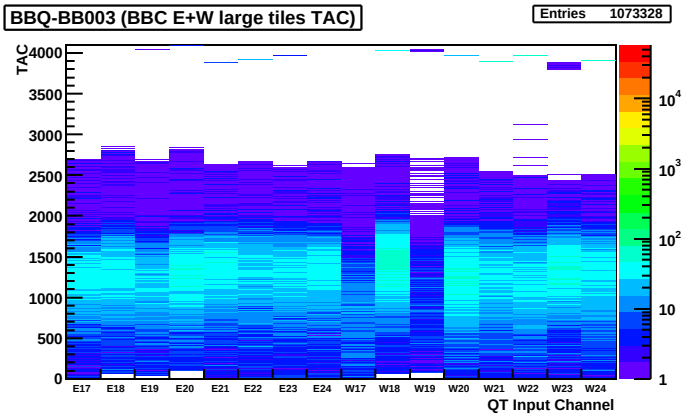

BBQ-BB001 (BBC east small tiles ADC)
Entries 1073328
ADC
QT Input Channel

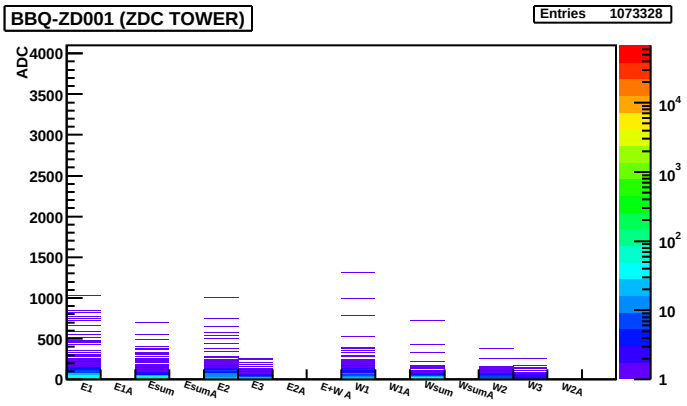

BBQ-BB001 (BBC east small tiles TAC)
Entries 1073328
TAC
QT Input Channel

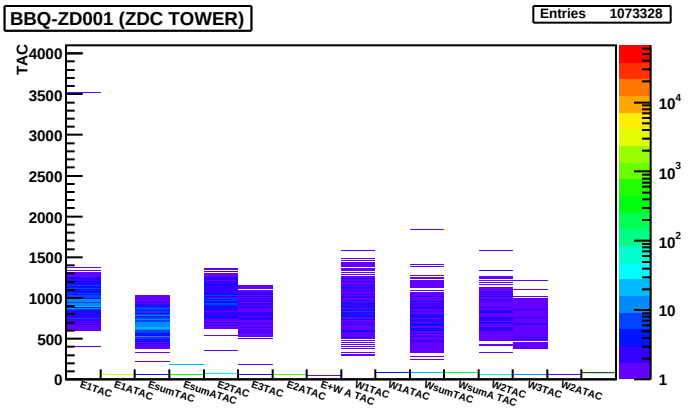

BBQ-BB002 (BBC west small tiles ADC)
Entries 1073328
ADC
QT Input Channel

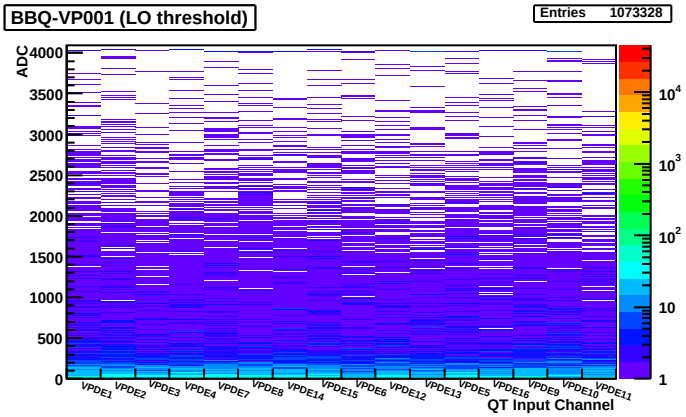

BBQ-BB002 (BBC west small tiles TAC)
Entries 1073328
TAC
QT Input Channel

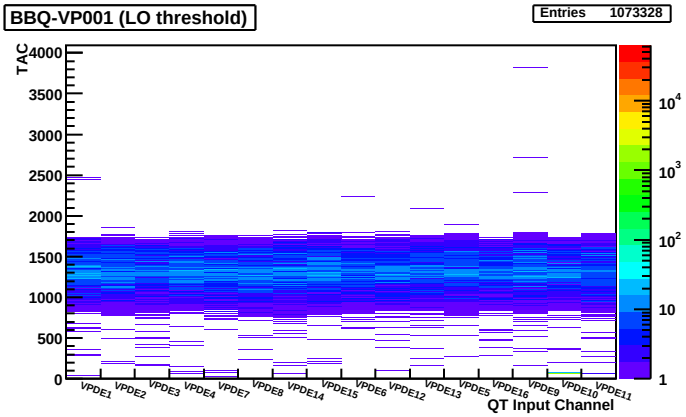

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1073328
ADC
QT Input Channel

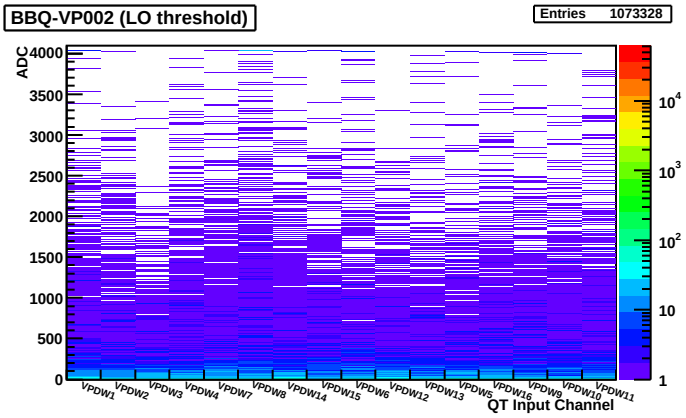

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1073328
TAC
QT Input Channel

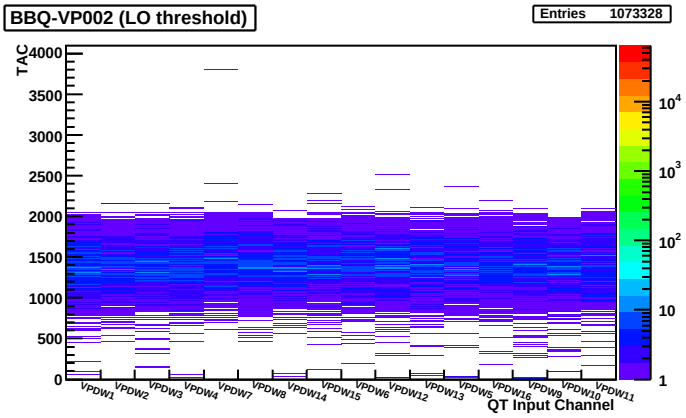

BBQ-ZD001 (ZDC TOWER)
Entries 1073328
ADC

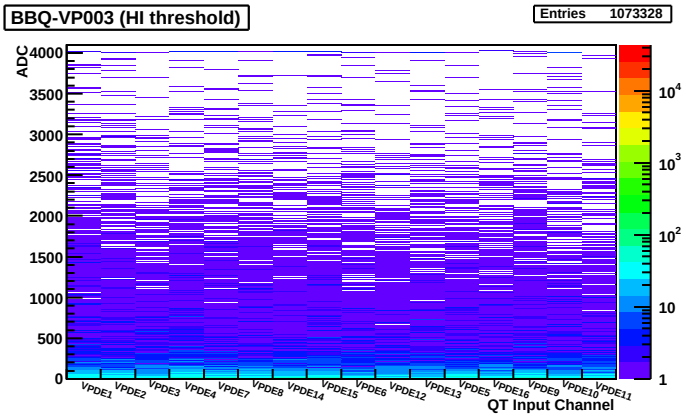

BBQ-ZD001 (ZDC TOWER)
Entries 1073328
TAC

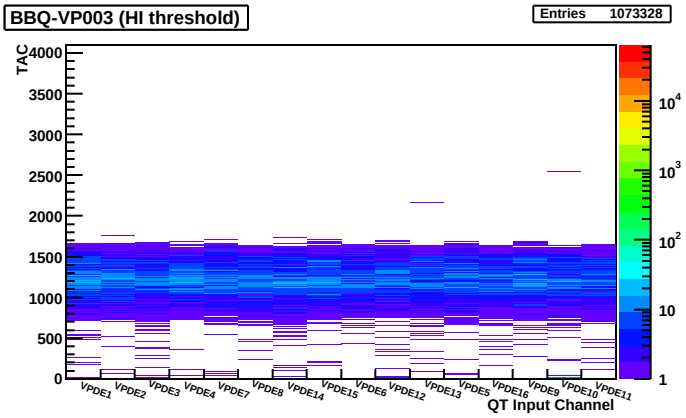

BBQ-VP001 (LO threshold)
Entries 1073328
ADC
QT Input Channel

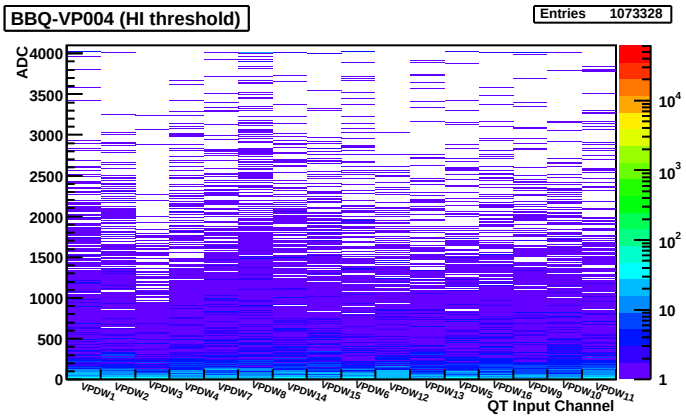

BBQ-VP001 (LO threshold)
Entries 1073328
TAC
QT Input Channel

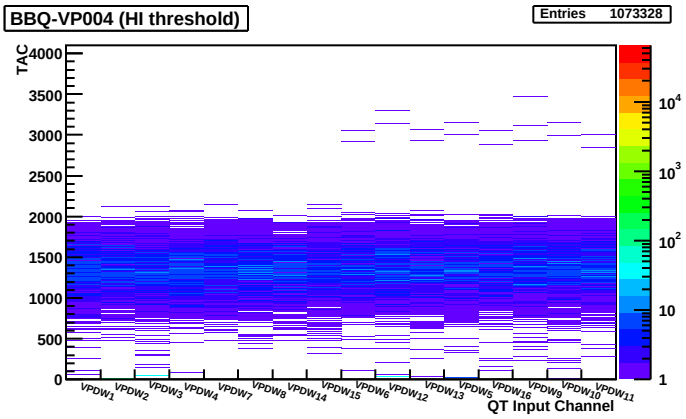

BBQ-VP002 (LO threshold)
Entries 1073328
ADC
QT Input Channel

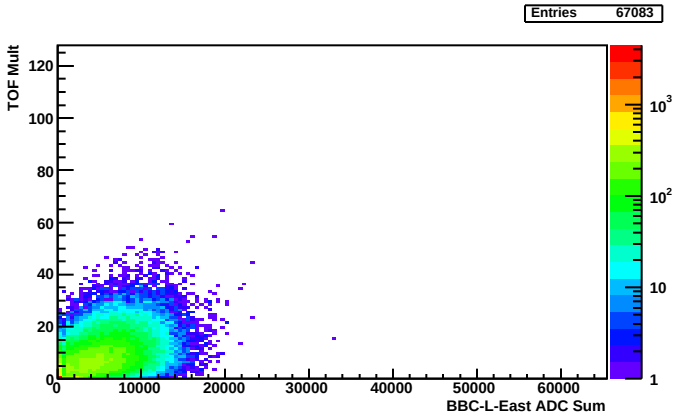

BBQ-VP002 (LO threshold)
Entries 1073328
TAC
QT Input Channel

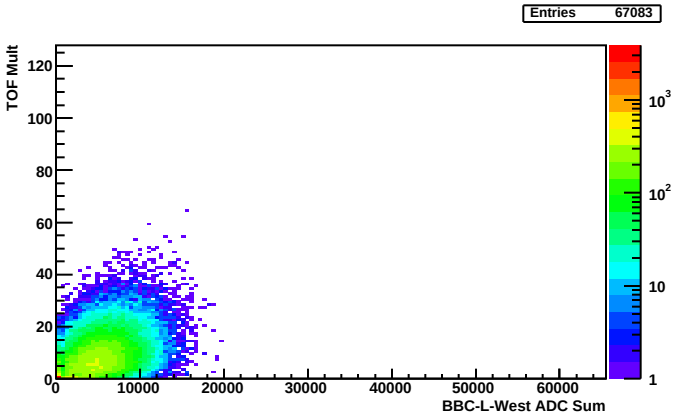

BBQ-VP003 (HI threshold)
Entries 1073328
ADC
QT Input Channel

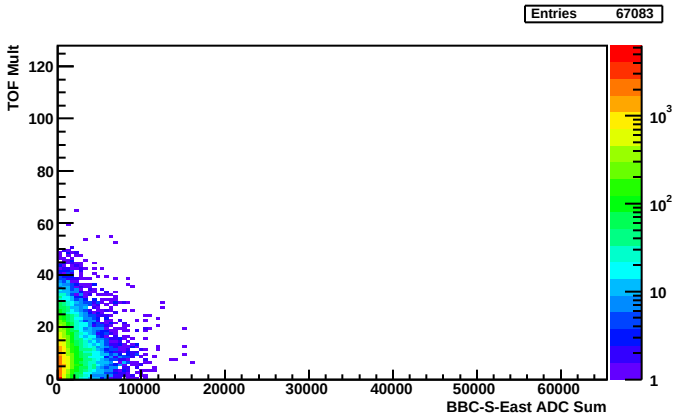

BBQ-VP003 (HI threshold)
Entries 1073328
TAC
QT Input Channel

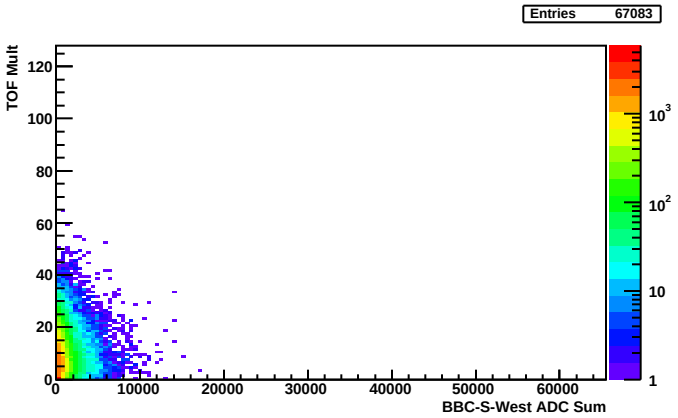

BBQ-VP004 (HI threshold)
Entries 1073328
ADC
QT Input Channel

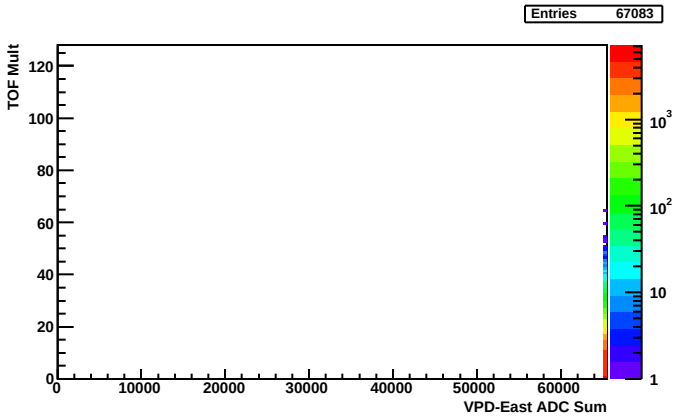

BBQ-VP004 (HI threshold)
Entries 1073328
TAC
QT Input Channel

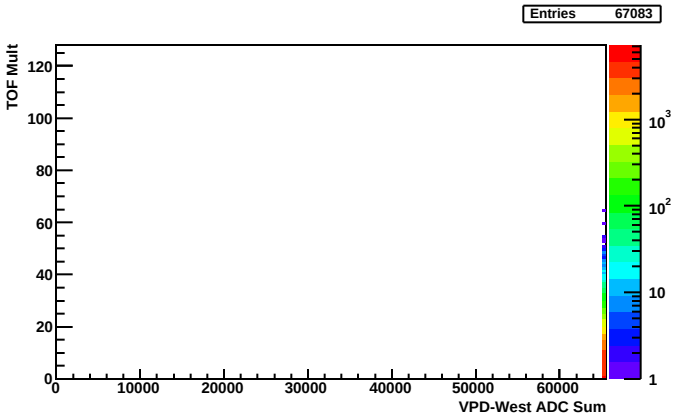

Entries 67083
TOF Mult
BBC-L-East ADC Sum

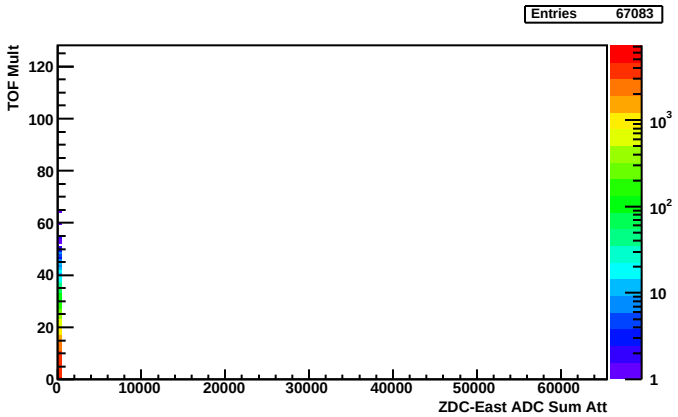

Entries 67083
TOF Mult
BBC-L-West ADC Sum

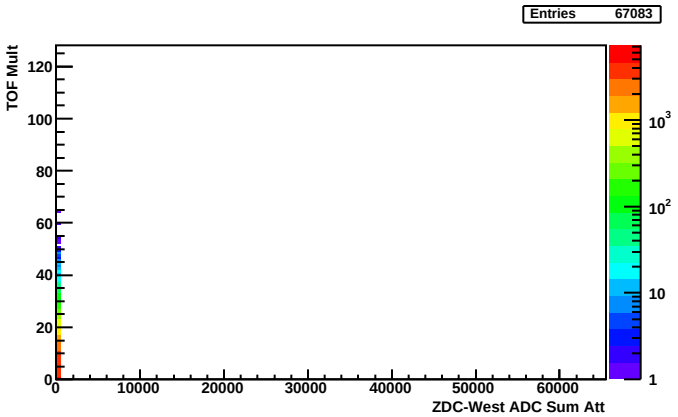

Entries 67083
TOF Mult
BBC-S-East ADC Sum

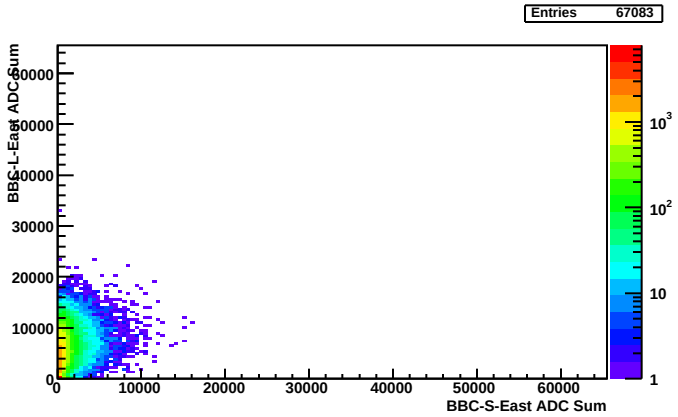

Entries 67083
TOF Mult
BBC-S-West ADC Sum

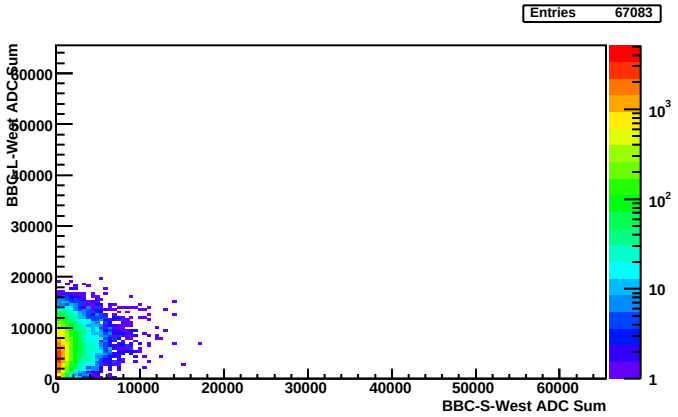

Entries 67083
TOF Mult
VPD-East ADC Sum

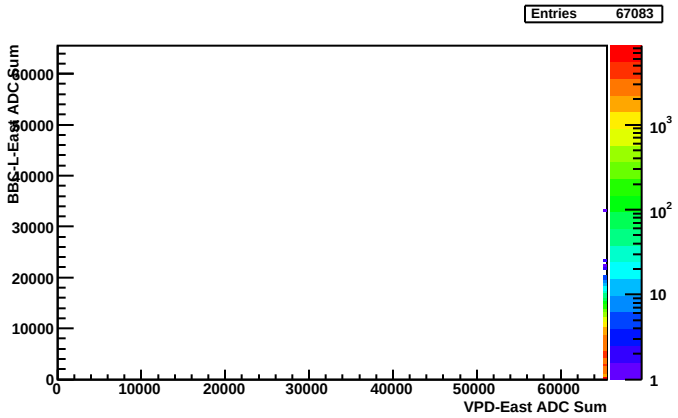

Entries 67083
TOF Mult
VPD-West ADC Sum

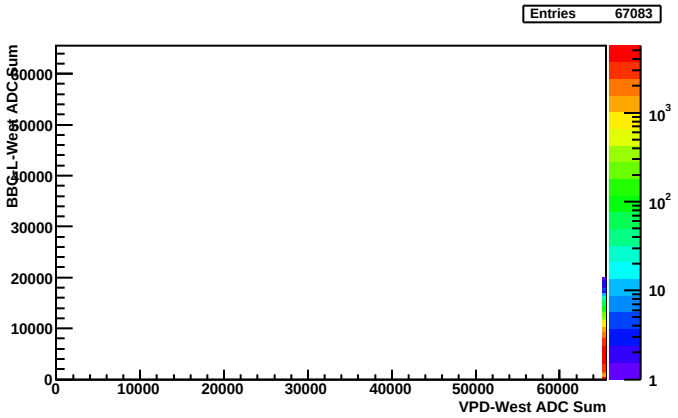

Entries 67083
TOF Mult
ZDC-East ADC Sum Att

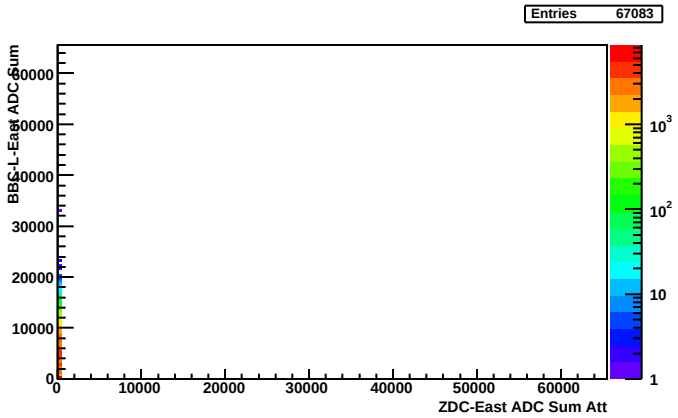

Entries 67083
TOF Mult
ZDC-West ADC Sum Att

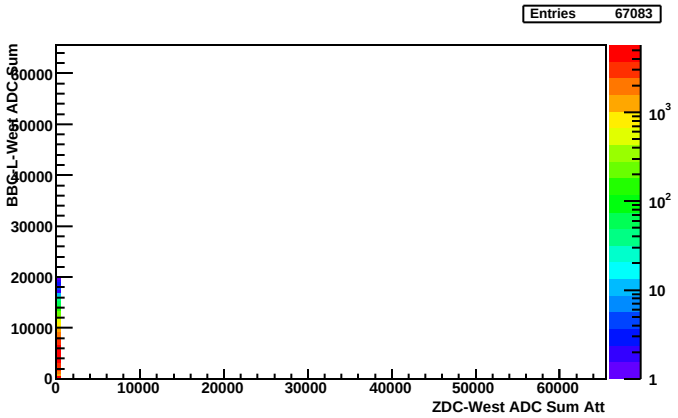

Entries 67083
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 67083
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 67083
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 67083
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 67083
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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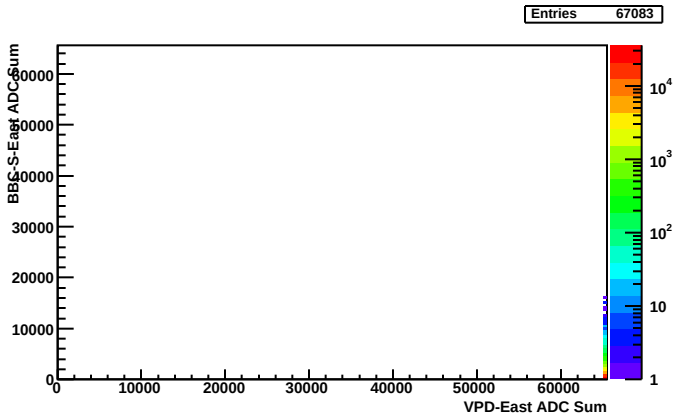
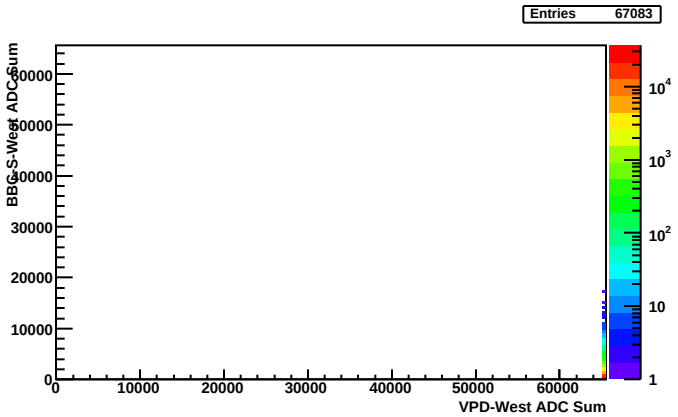
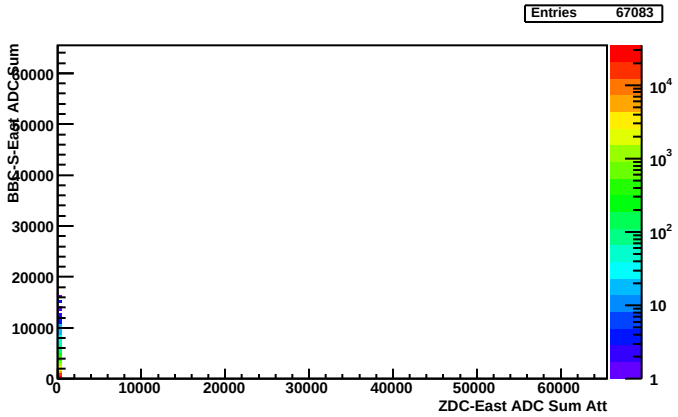
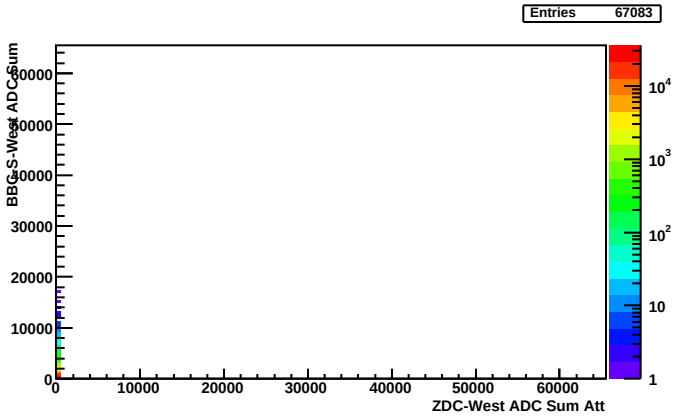
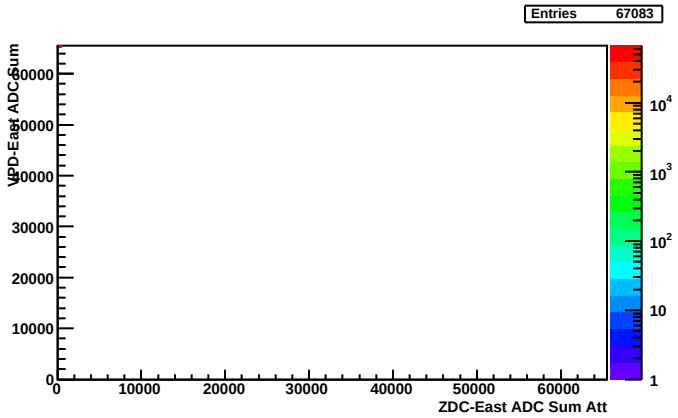
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

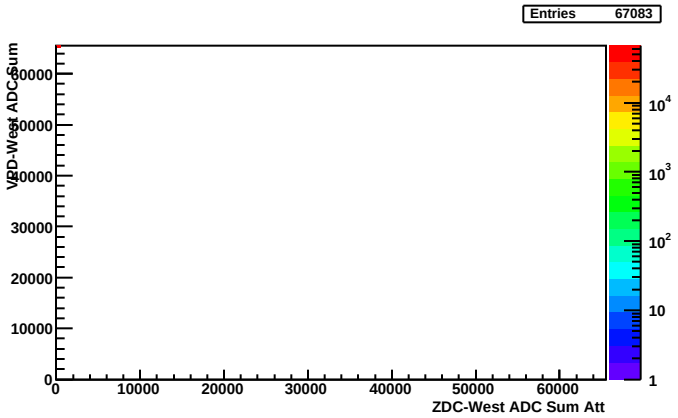

Entries 67083
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 67083
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 67083
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 67083
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 67083
VPD-East ADC Sum
ZDC-East ADC Sum Att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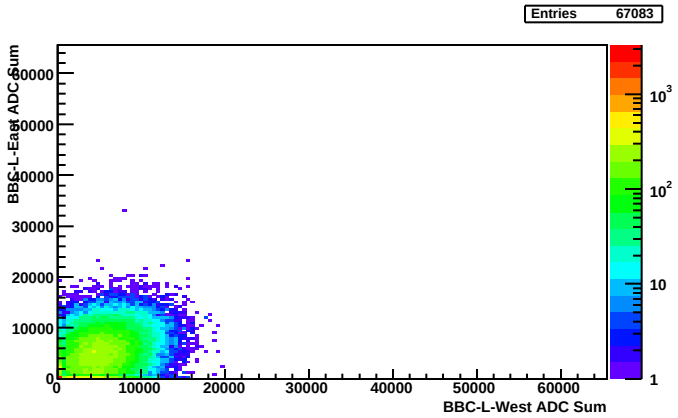
VPD-West ADC Sum
ZDC-West ADC Sum Att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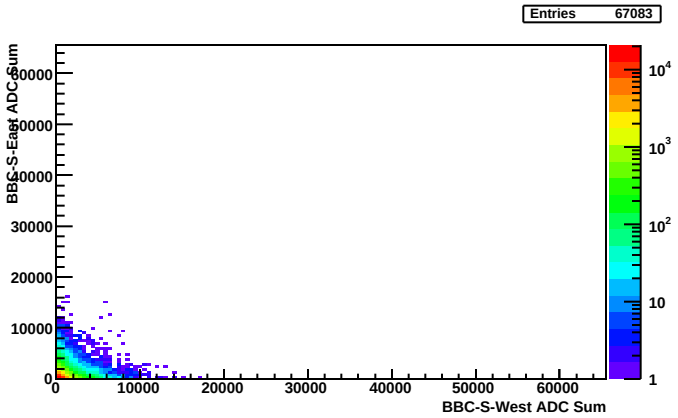
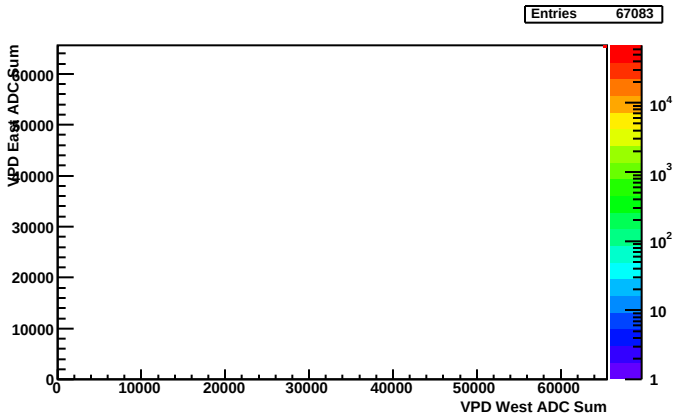
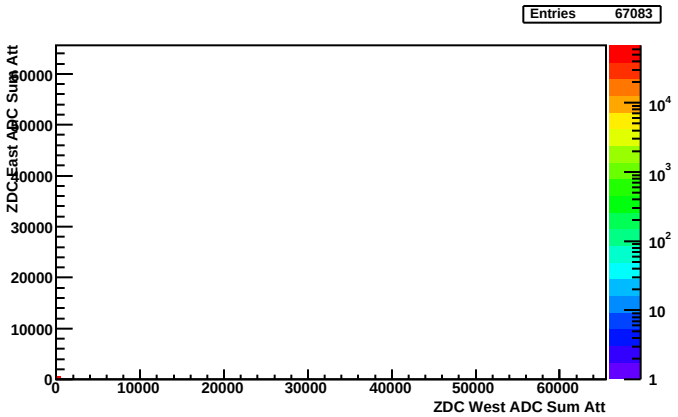
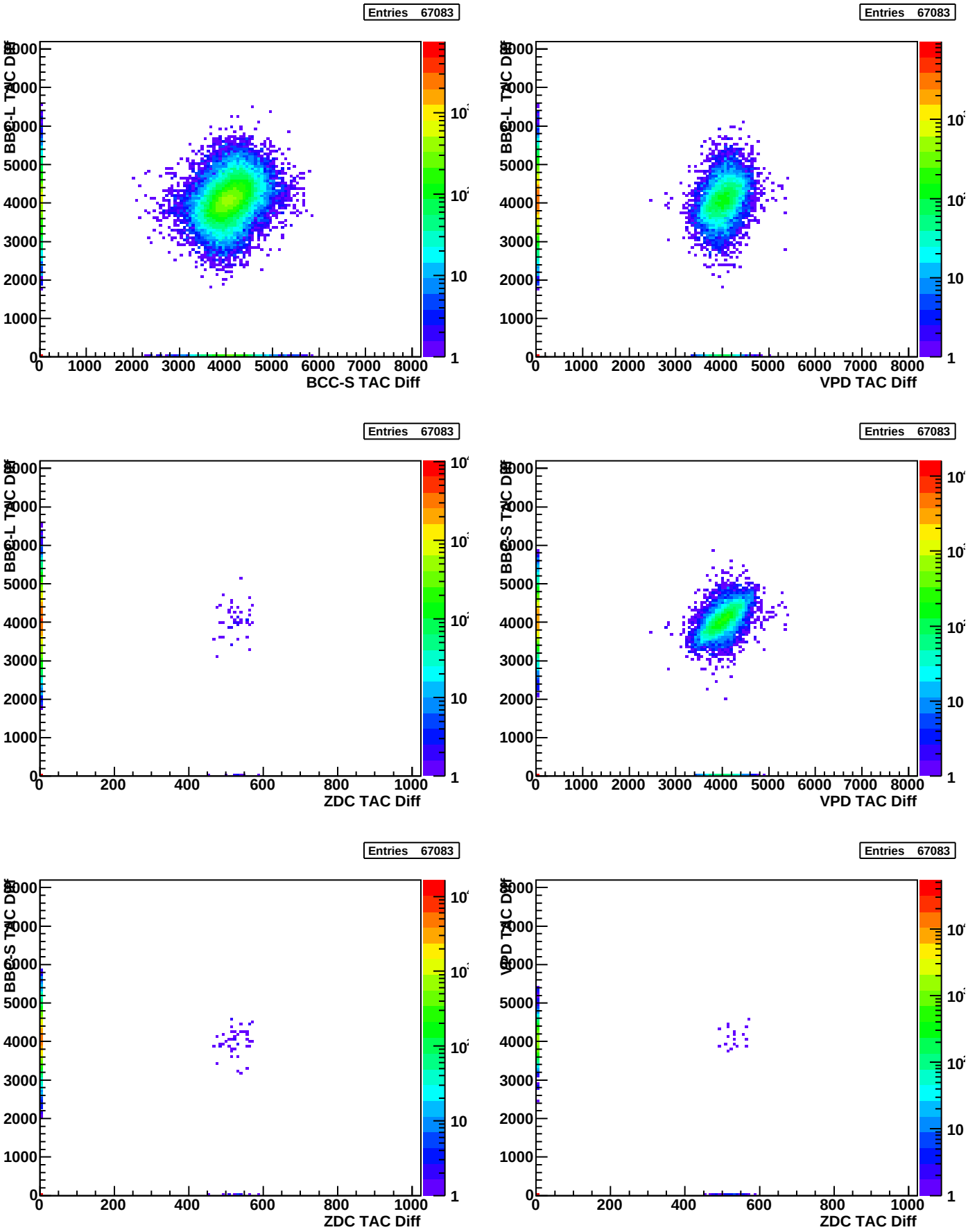
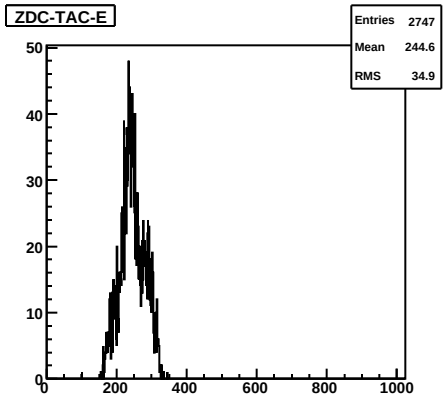
BBC-L-East ADC Sum
BBC-L-West ADC Sum

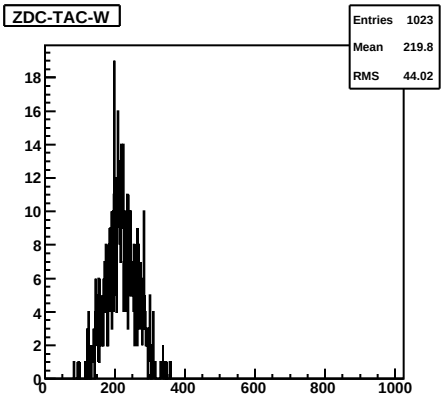

Entries 67083
BBC-S-East ADC Sum
BBC-S-West ADC Sum

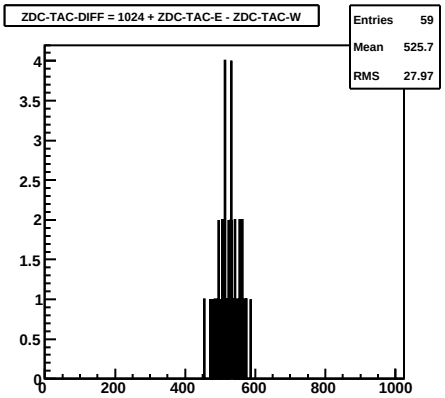

Entries 67083
VPD East ADC Sum
VPD West ADC Sum

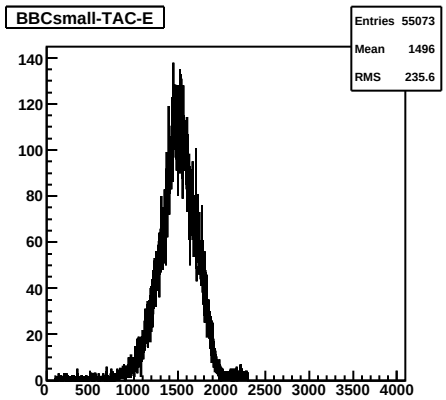

Entries 67083
ZDC East ADC Sum Att
ZDC West ADC Sum Att

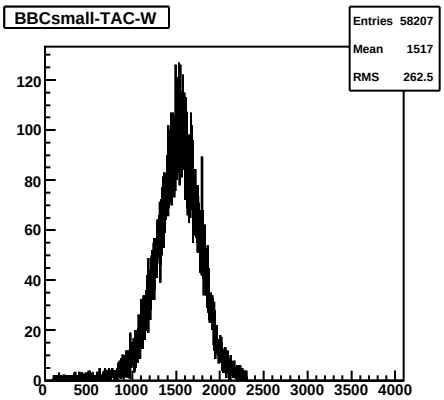

Entries 67083
BBC-L TAC Diff
BCC-S TAC Diff
Entries 67083
BBC-L TAC Diff
VPD TAC Diff
Entries 67083
BBC-L TAC Diff
ZDC TAC Diff
Entries 67083
BBC-S TAC Diff
VPD TAC Diff
Entries 67083
BBC-S TAC Diff
ZDC TAC Diff
Entries 67083
VPD TAC Diff
ZDC TAC Diff

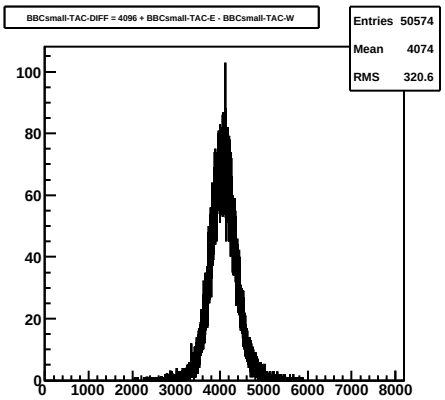

ZDC-TAC-E
Entries 2747
Mean 244.6
RMS 34.9

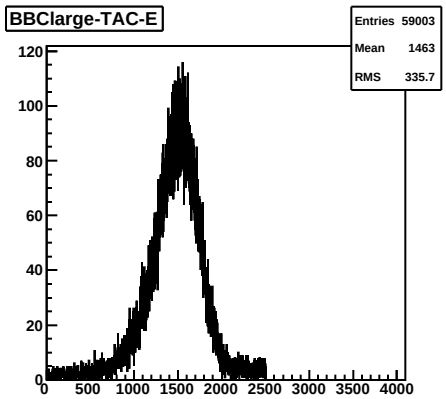

ZDC-TAC-W
Entries 1023
Mean 219.8
RMS 44.02

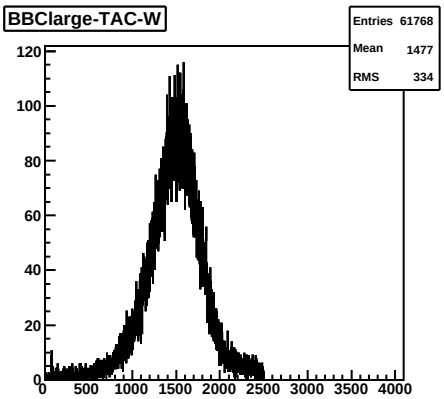

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 59
Mean 525.7
RMS 27.97

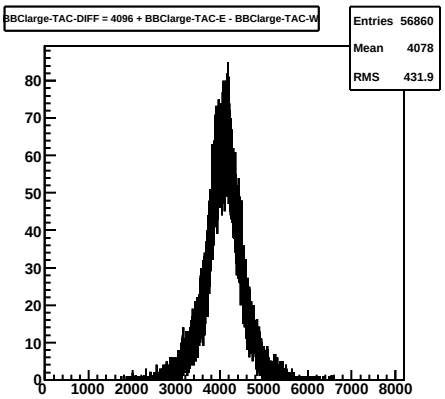

BBCsmall-TAC-E
Entries 55073
Mean 1496
RMS 235.6

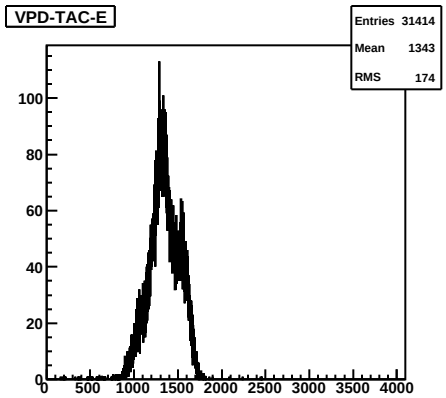

BBCsmall-TAC-W
Entries 58207
Mean 1517
RMS 262.5

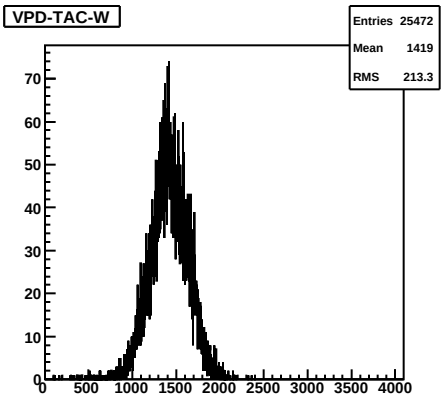

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 50574
Mean 4074
RMS 320.6

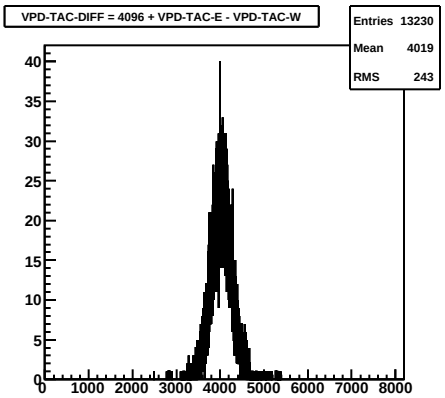

BBClarge-TAC-E
Entries 59003
Mean 1463
RMS 335.7

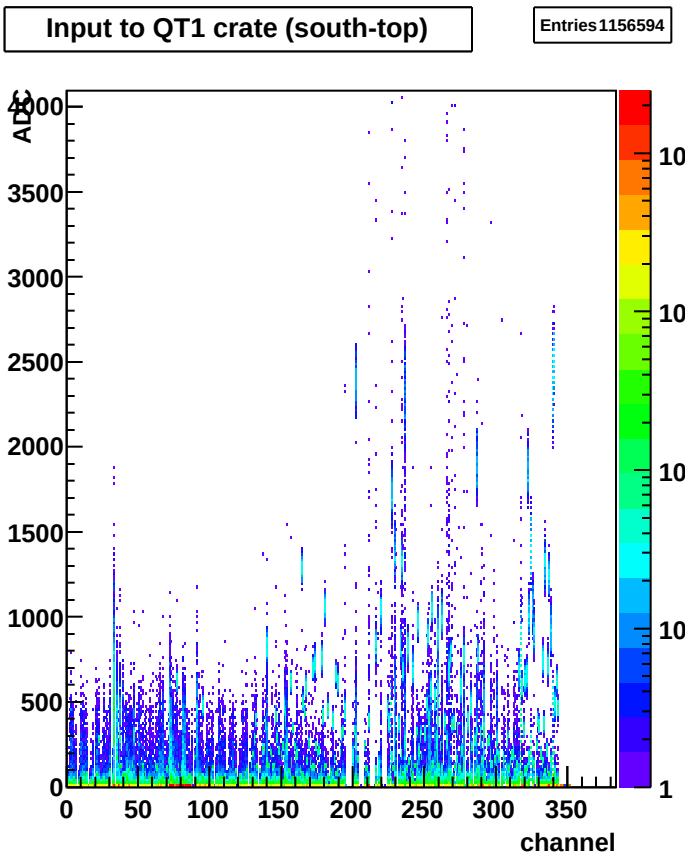

BBClarge-TAC-W
Entries 61768
Mean 1477
RMS 334

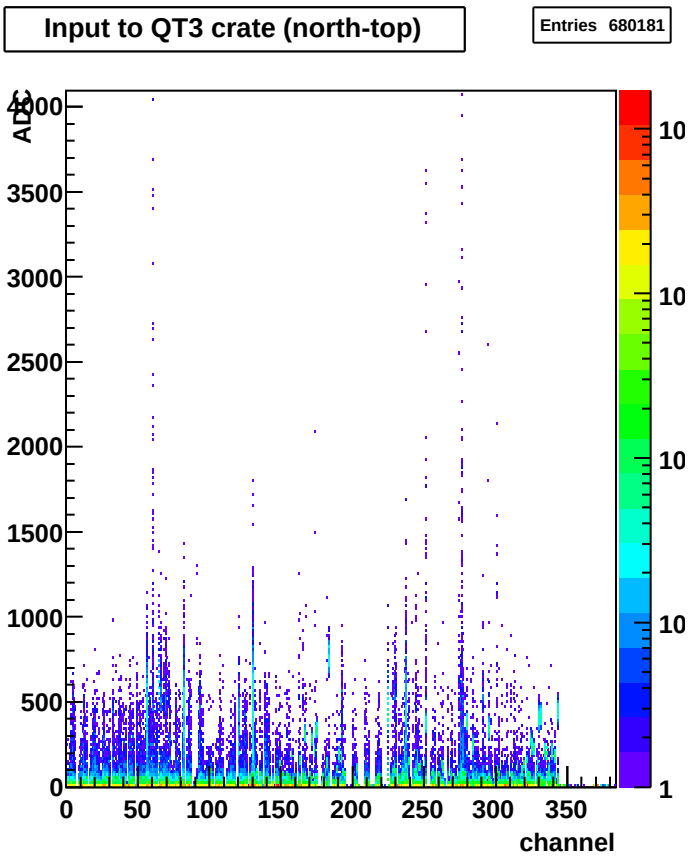

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 56860
Mean 4078
RMS 431.9

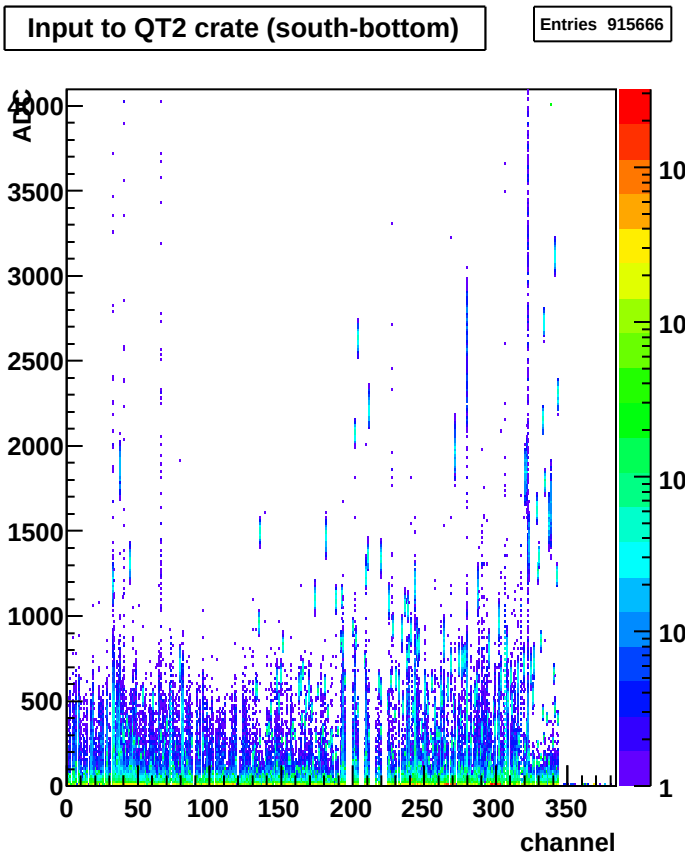

VPD-TAC-E
Entries 31414
Mean 1343
RMS 174

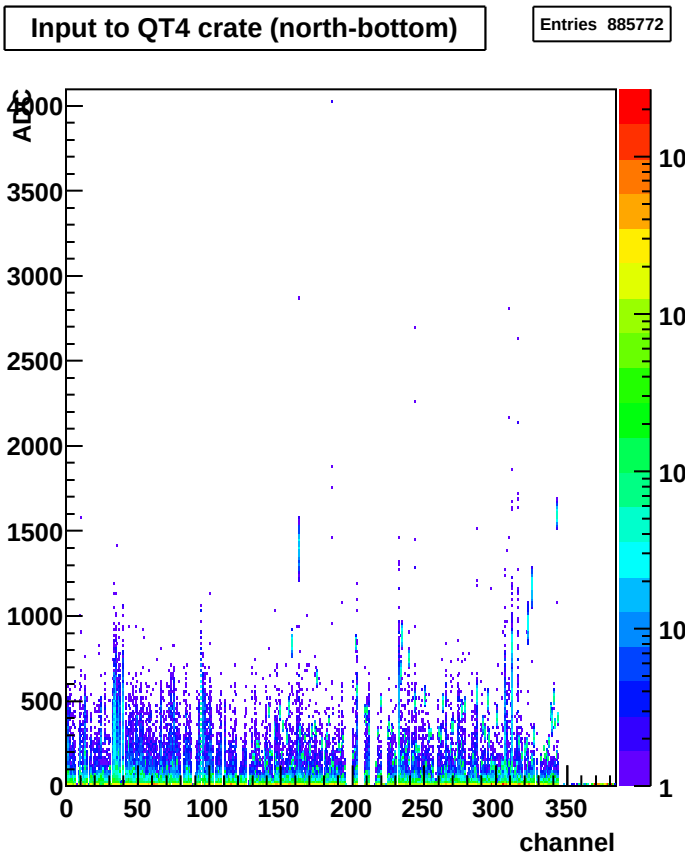

VPD-TAC-W
Entries 25472
Mean 1419
RMS 213.3

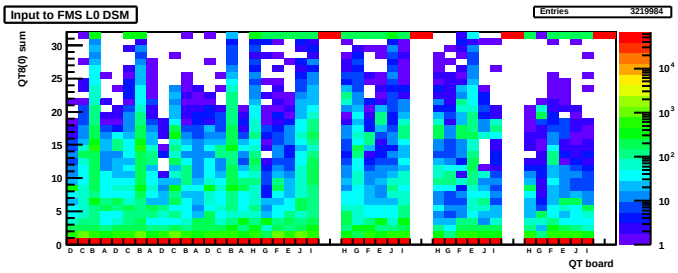

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 13230
Mean 4019
RMS 243

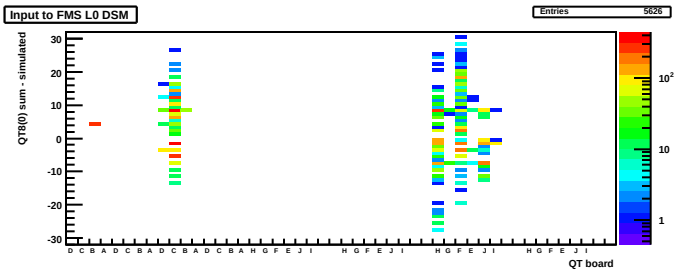

Input to QT1 crate (south-top)
Entries 1156594
ADC
channel

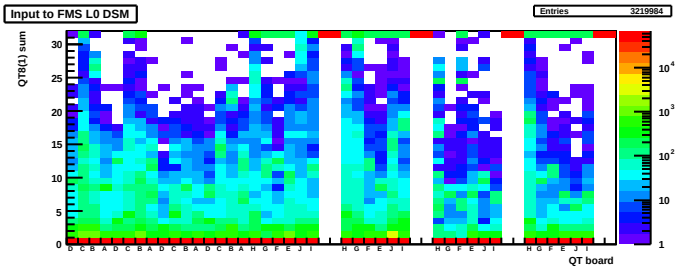

Input to QT3 crate (north-top)
Entries 680181
ADC
channel

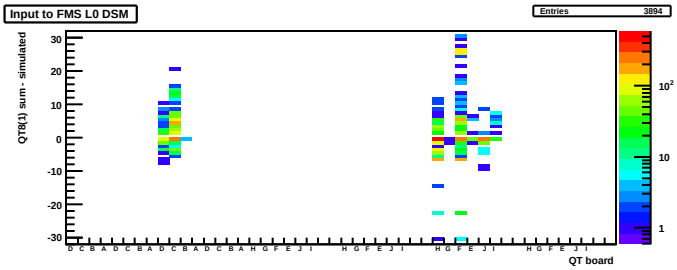

Input to QT2 crate (south-bottom)
Entries 915666
ADC
channel

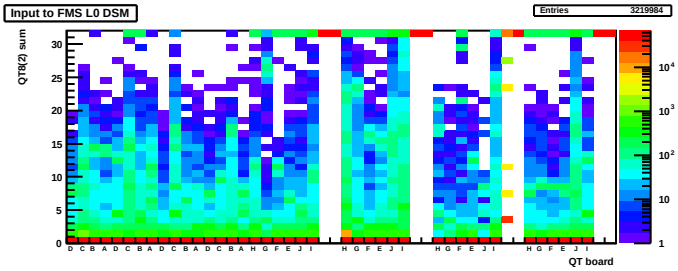

Input to QT4 crate (north-bottom)
Entries 885772
ADC
channel

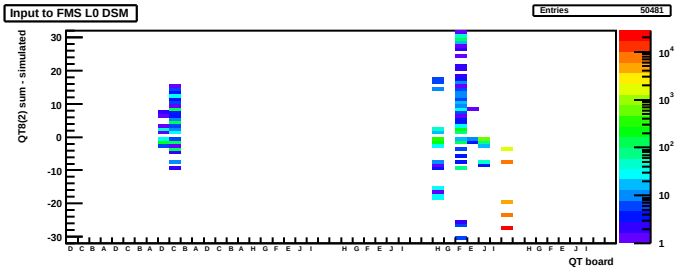

Input to FMS L0 DSM
Entries 3219984
QT8(0) sum
QT board

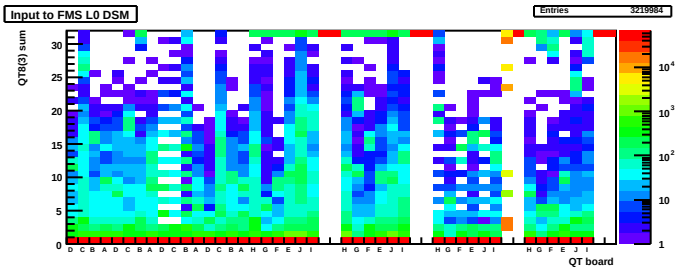

Input to FMS L0 DSM
Entries 5626
QT8(0) sum - simulated
QT board

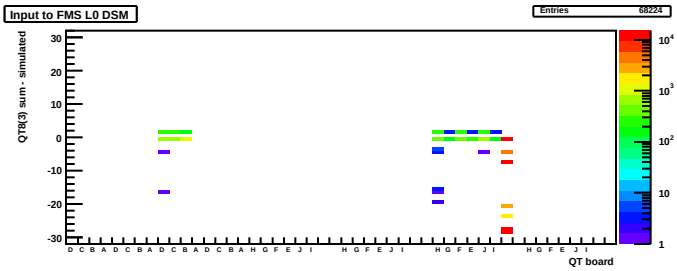

Input to FMS L0 DSM
Entries 3219984
QT8(1) sum
QT board

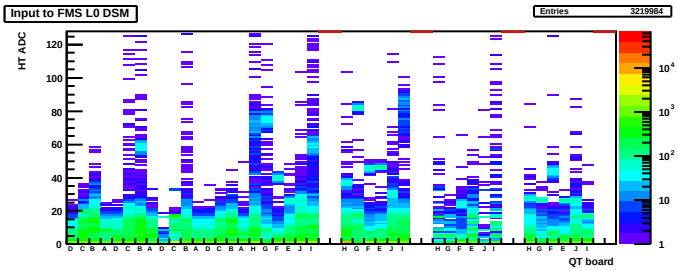

Input to FMS L0 DSM
Entries 3894
QT8(1) sum - simulated
QT board

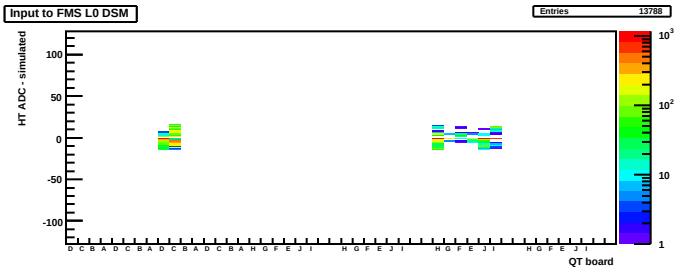

Input to FMS L0 DSM
Entries 3219984
QT8(2) sum
QT board

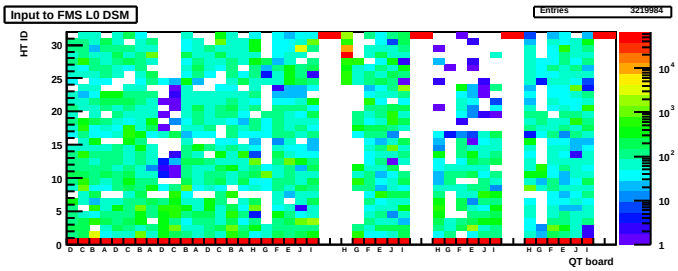

Input to FMS L0 DSM
Entries 50481
QT8(2) sum - simulated
QT board

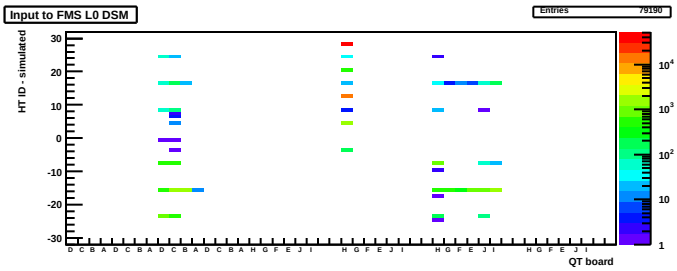

Input to FMS L0 DSM
Entries 3219984
QT8(3) sum
QT board

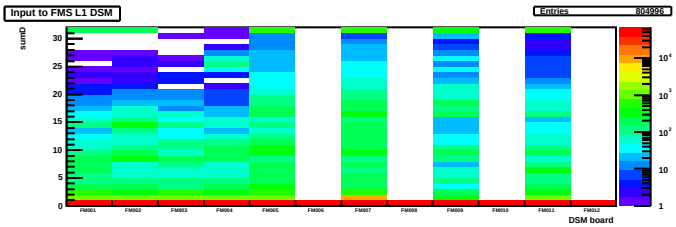

Input to FMS L0 DSM
Entries 68224
QT8(3) sum - simulated
QT board

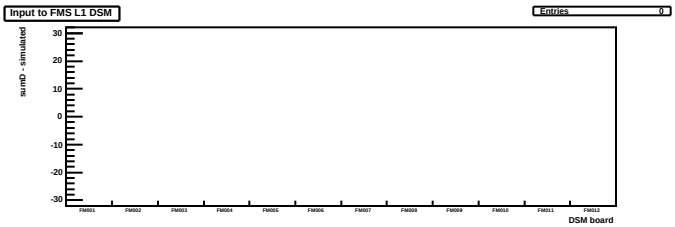

Input to FMS L0 DSM
Entries 3219984
HT ADC
QT board

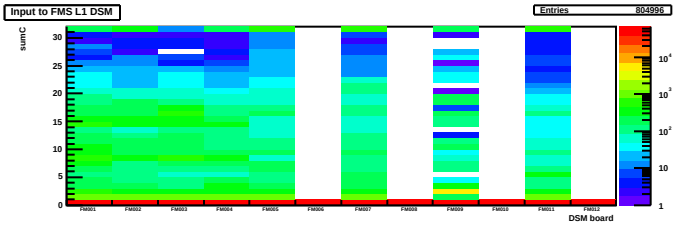

Input to FMS L0 DSM
Entries 13788
HT ADC - simulated
QT board

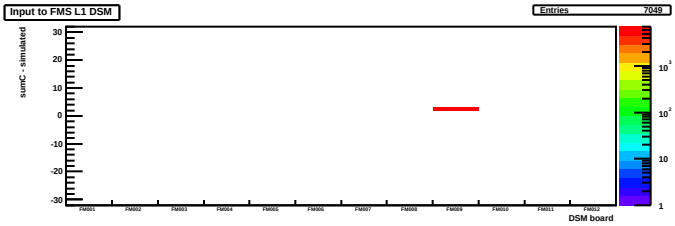

Input to FMS L0 DSM
Entries 3219984
HT ID
QT board

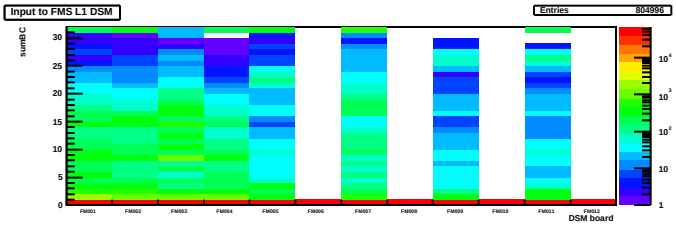

Input to FMS L0 DSM
Entries 79190
HT ID - simulated
QT board

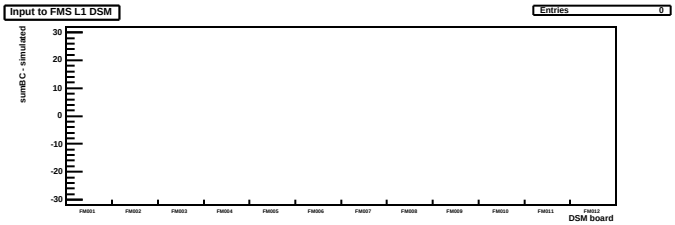

Input to FMS L1 DSM
Entries 804996
sumD
DSM board

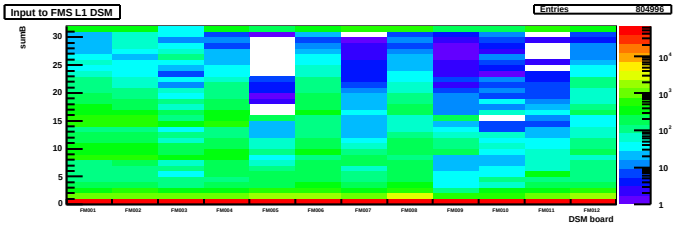

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

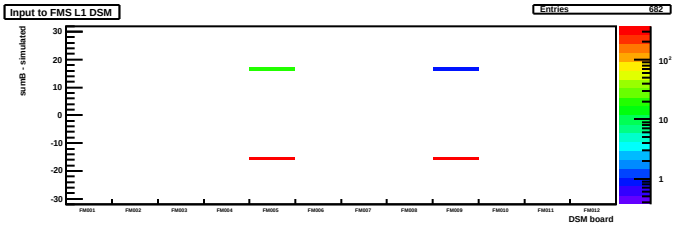

Input to FMS L1 DSM
Entries 804996
sumC
DSM board

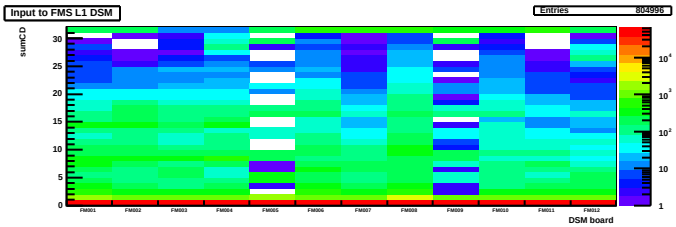

Input to FMS L1 DSM
Entries 7049
sumC - simulated
DSM board

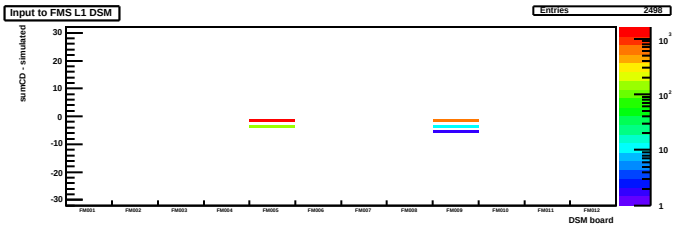

Input to FMS L1 DSM
Entries 804996
sumBC
DSM board

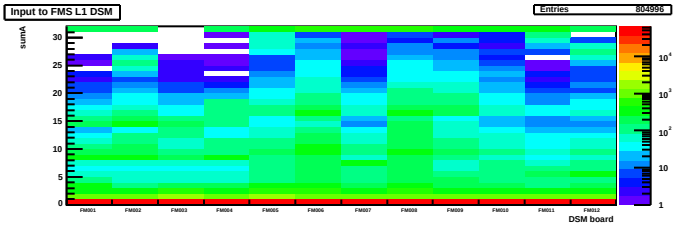

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

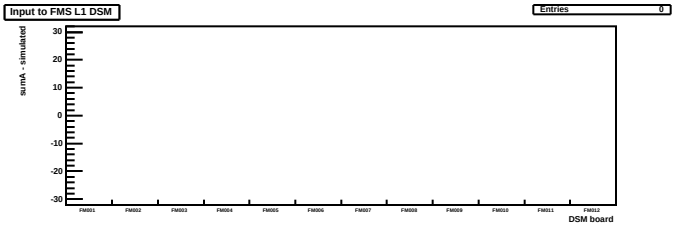

Input to FMS L1 DSM
Entries 804996
sumB
DSM board

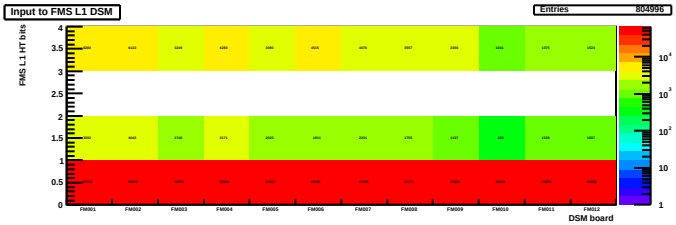

Input to FMS L1 DSM
Entries 682
sumB - simulated
DSM board

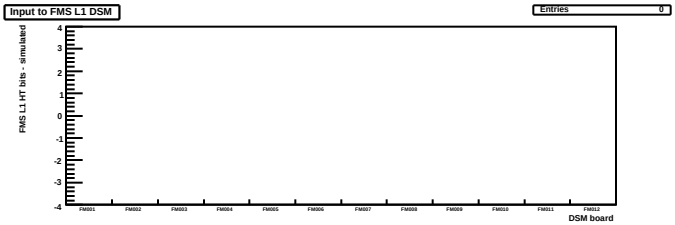

Input to FMS L1 DSM
Entries 804996
sumCD
DSM board

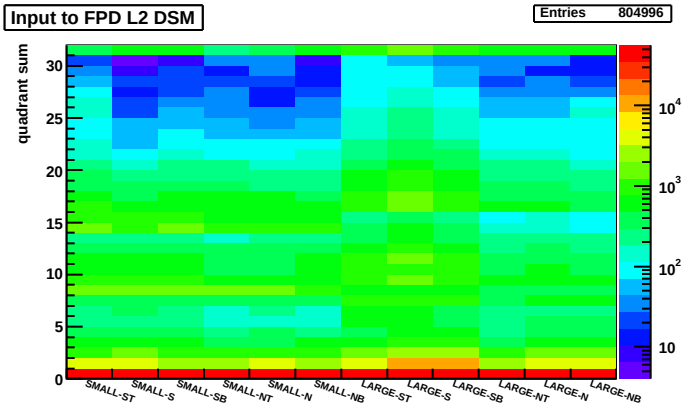

Input to FMS L1 DSM
Entries 2498
sumCD - simulated
DSM board

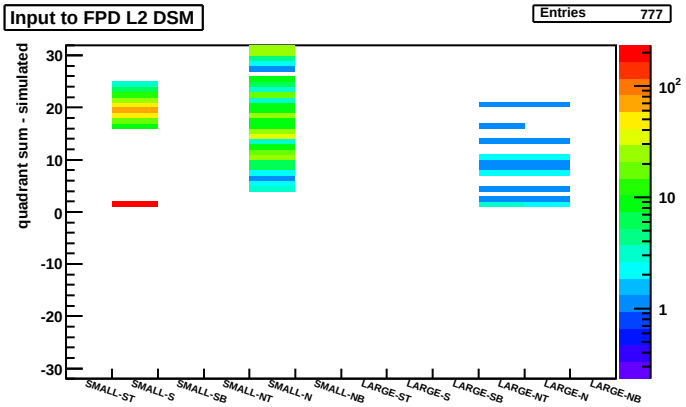

Input to FMS L1 DSM
Entries 804996
sumA
DSM board

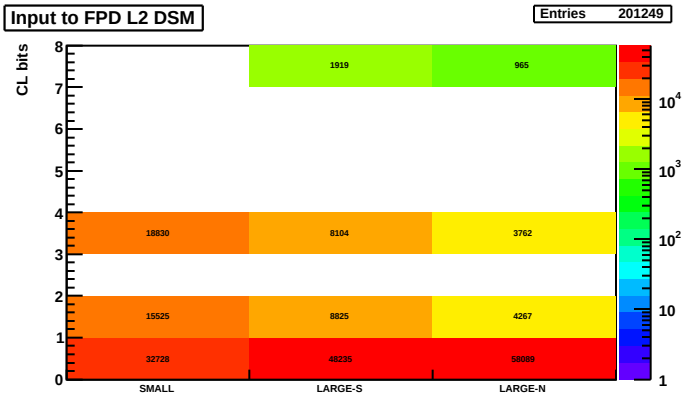

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

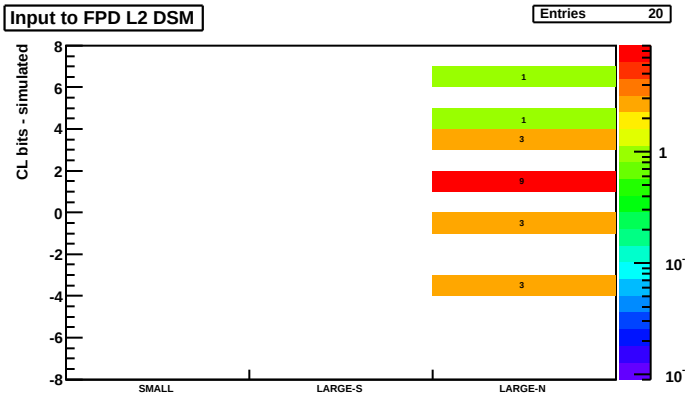

Input to FMS L1 DSM
Entries 804996
FMS L1 HT bits
DSM board

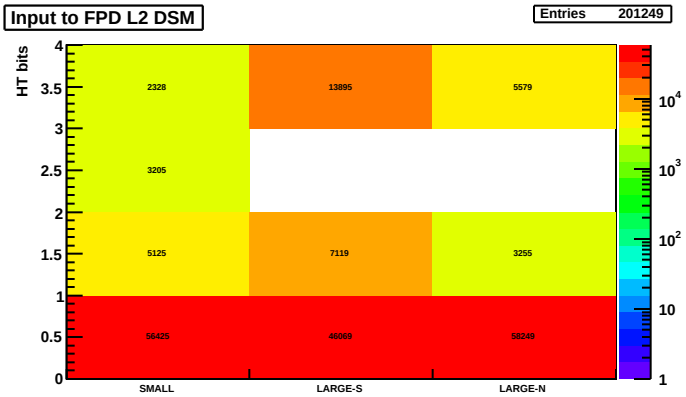

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

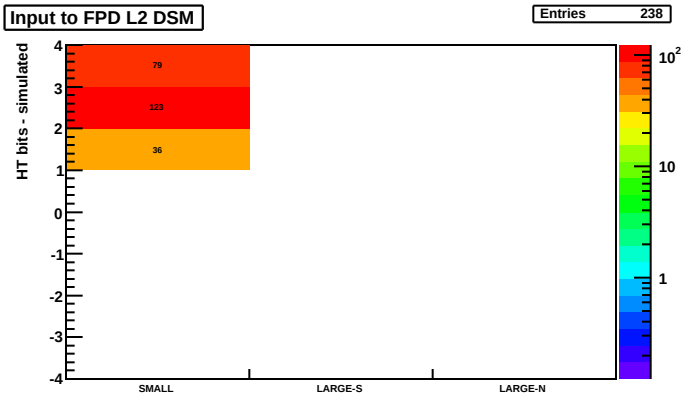

Input to FPD L2 DSM
Entries 804996
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

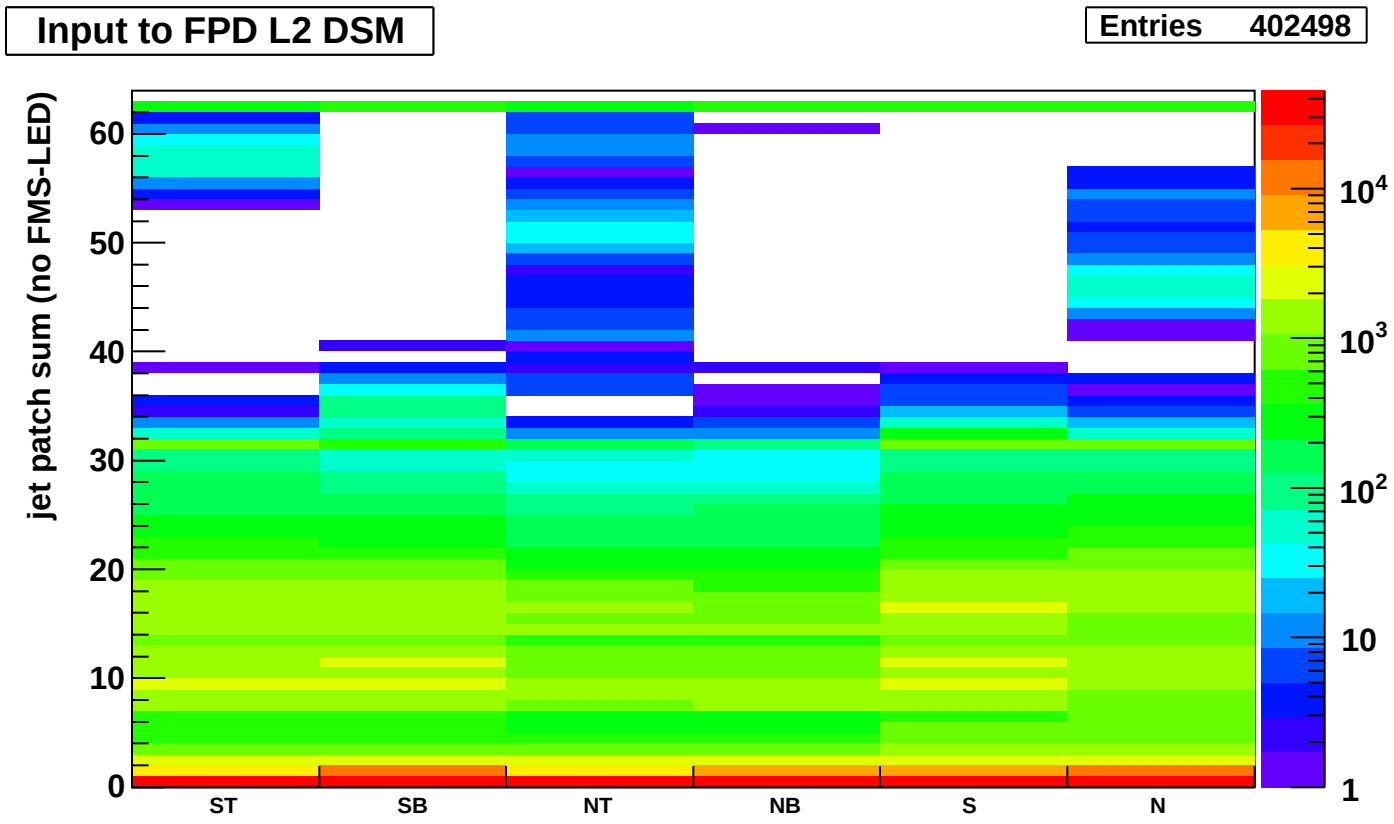

Input to FPD L2 DSM
Entries 777
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

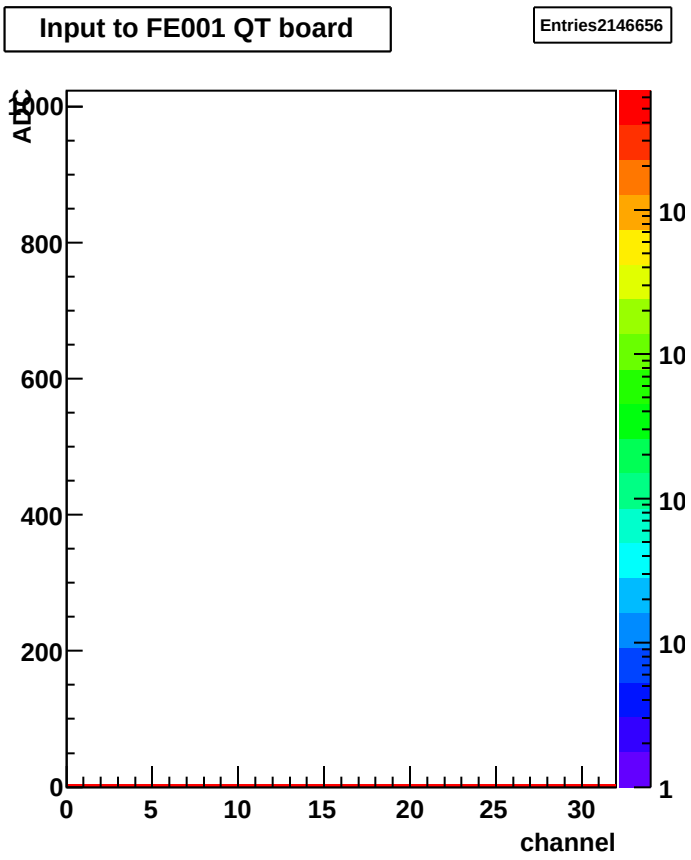

Input to FPD L2 DSM
Entries 201249
CL bits
1919
965
18830
8104
3762
15525
8825
4267
32728
48235
58089
SMALL
LARGE-S
LARGE-N

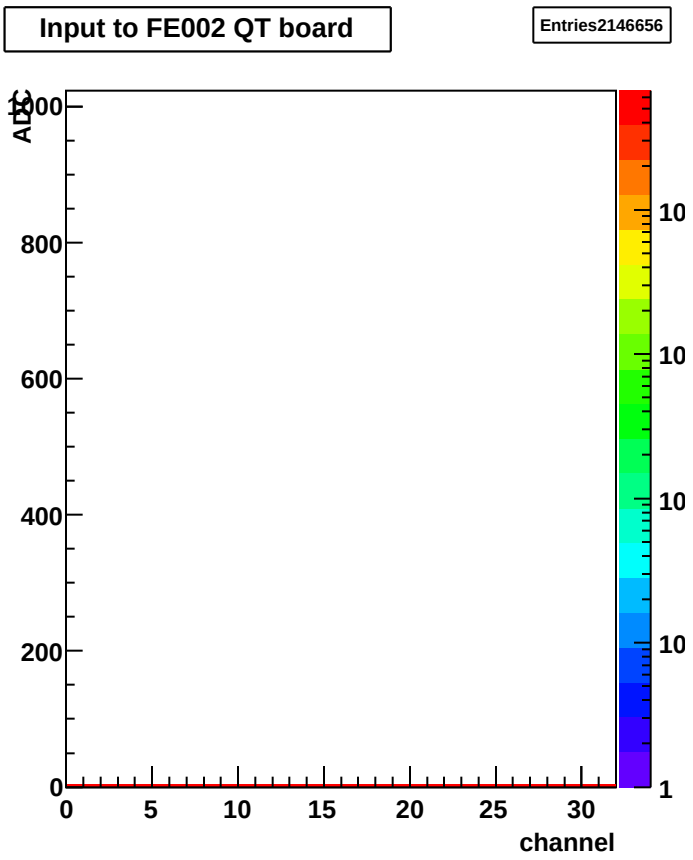

Input to FPD L2 DSM
Entries 20
CL bits - simulated
1
1
3
9
3
3
SMALL
LARGE-S
LARGE-N

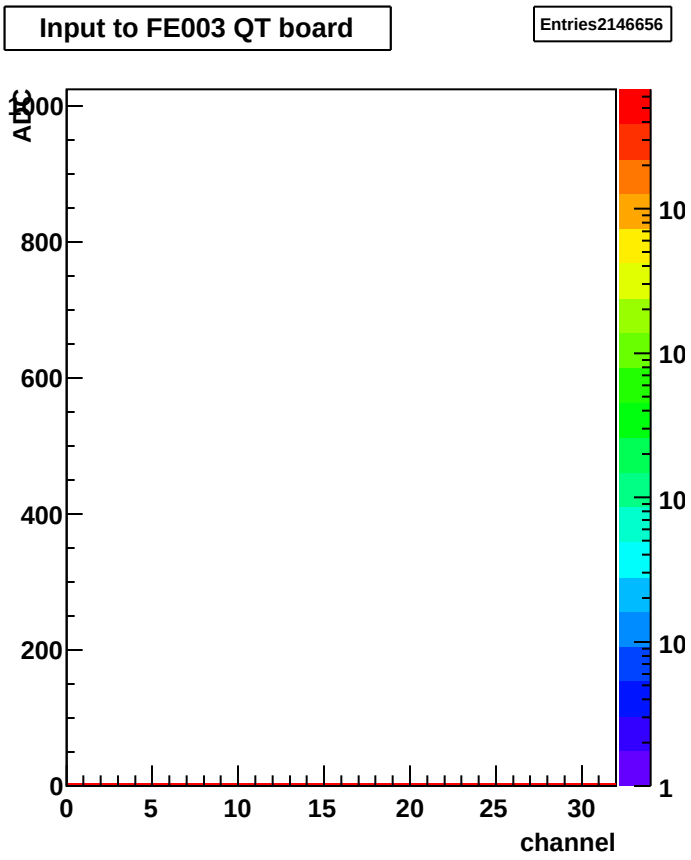

Input to FPD L2 DSM
Entries 201249
HT bits
2328
13895
5579
3205
5125
7119
3255
56425
46069
58249
SMALL
LARGE-S
LARGE-N

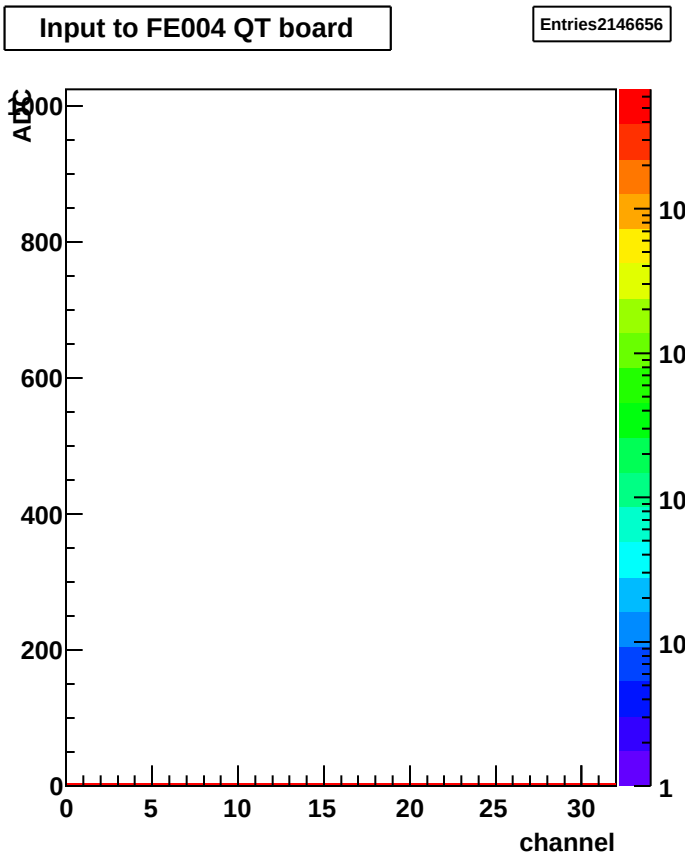

Input to FPD L2 DSM
Entries 238
HT bits - simulated
79
123
36
SMALL
LARGE-S
LARGE-N

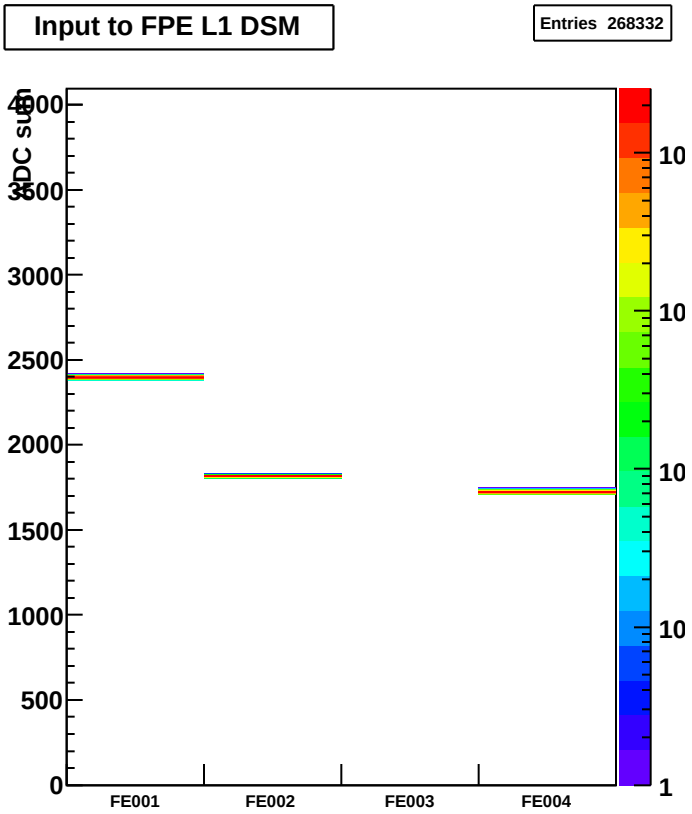

Input to FPD L2 DSM
Entries 402498
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

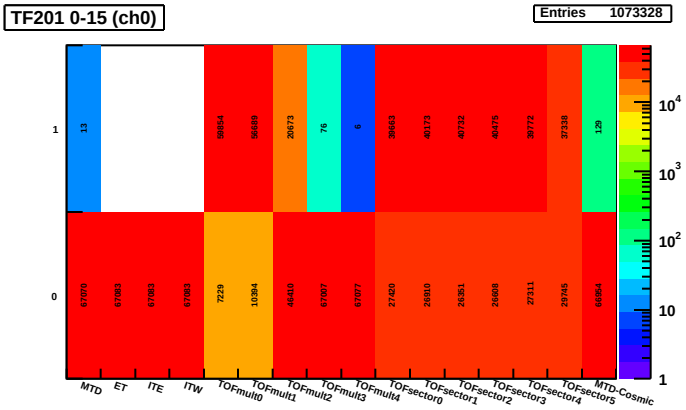

Input to FE001 QT board
Entries2146656
ADC
channel

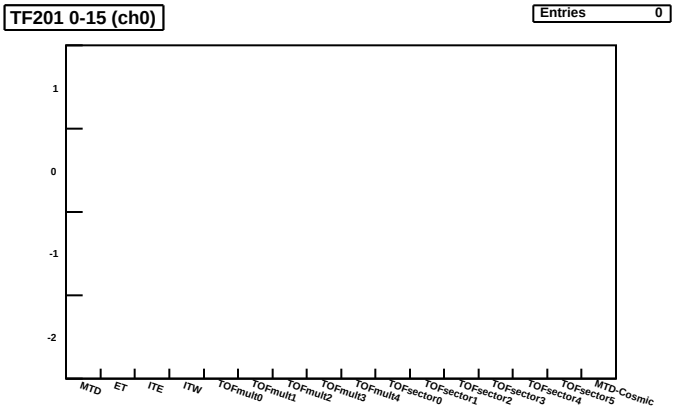

Input to FE002 QT board
Entries2146656
ADC
channel

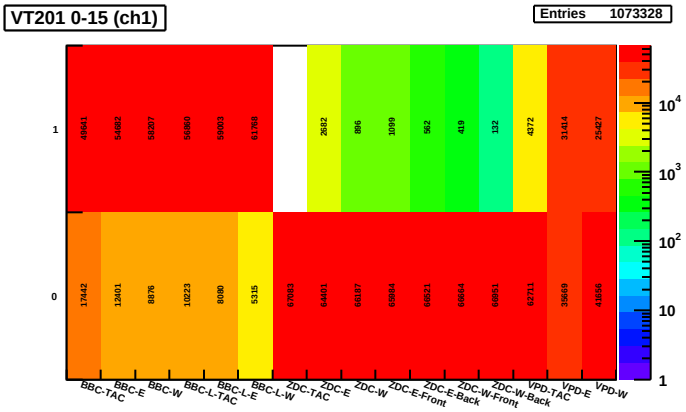

Input to FE003 QT board
Entries2146656
ADC
channel

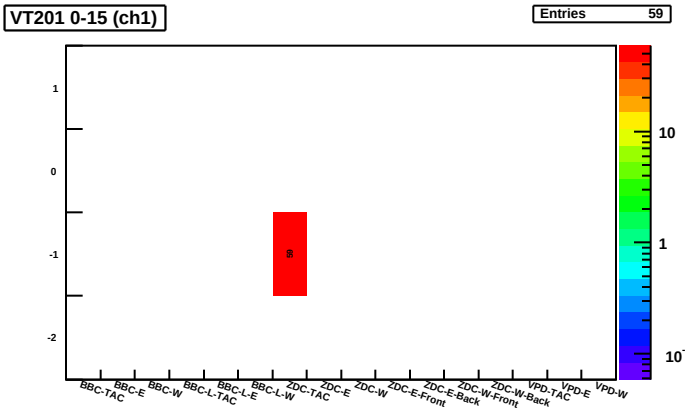

Input to FE004 QT board
Entries2146656
ADC
channel

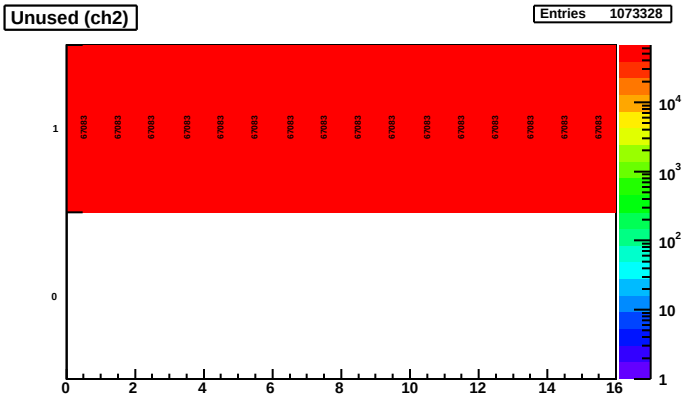

Input to FPE L1 DSM
Entries 268332
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

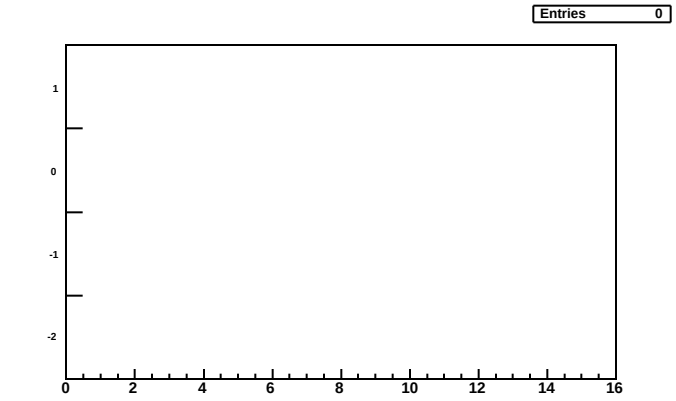

TF201 0-15 (ch0)
Entries 1073328
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

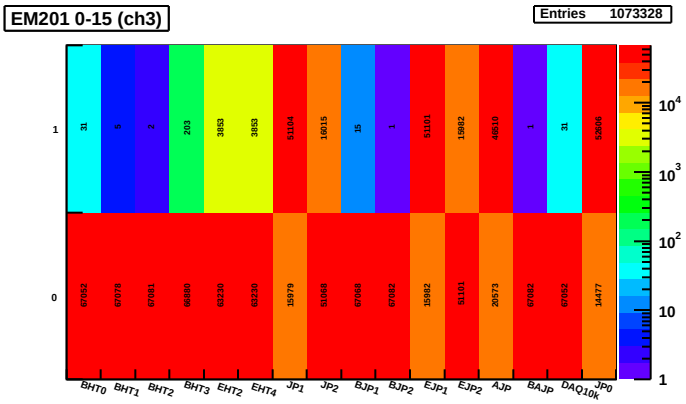

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1073328
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 59
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 1073328


Entries 0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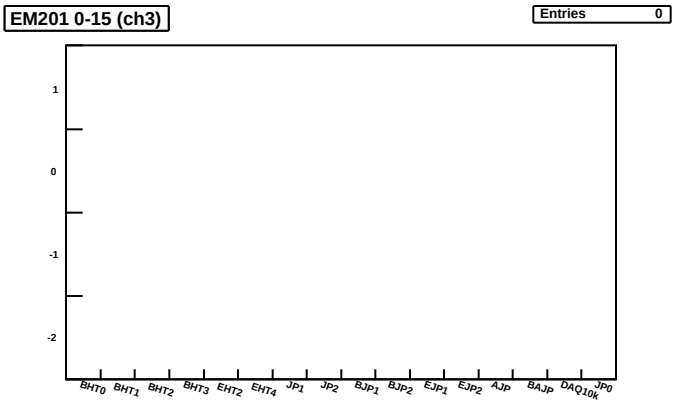
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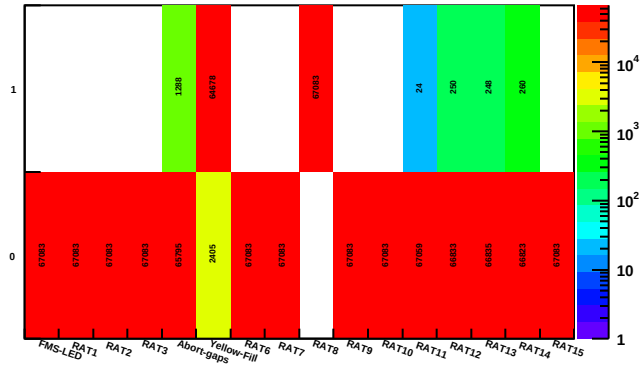
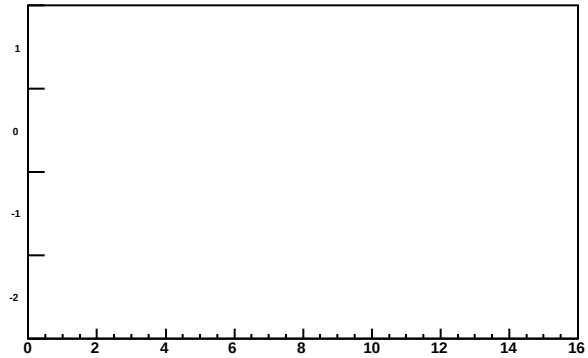
RAT board (ch4)





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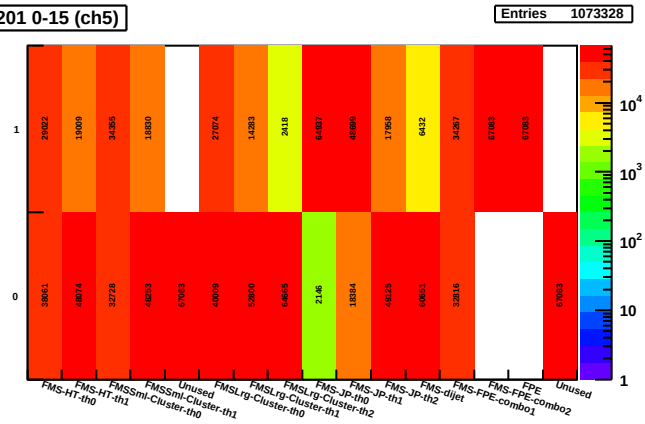

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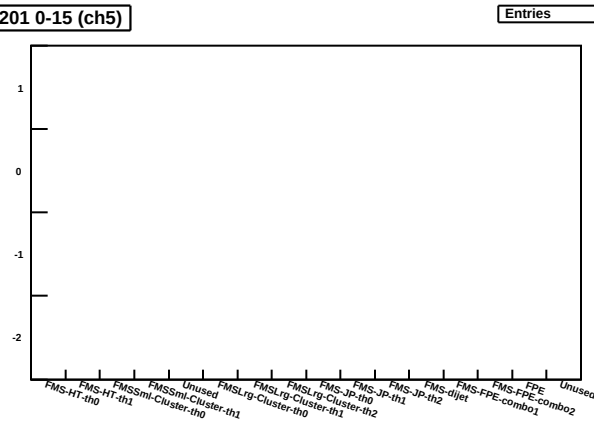

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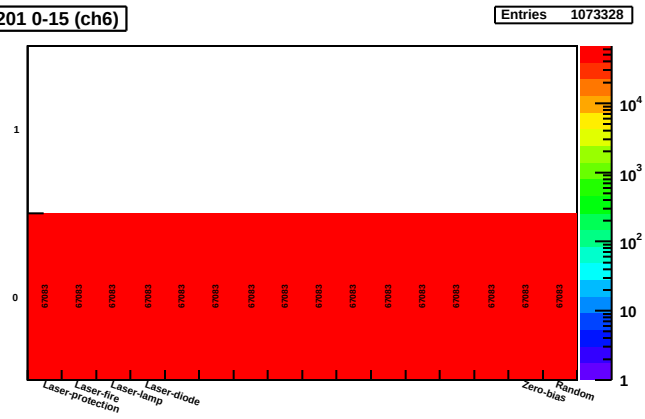

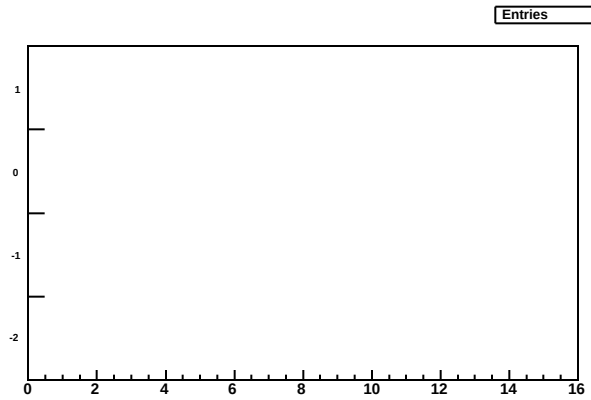
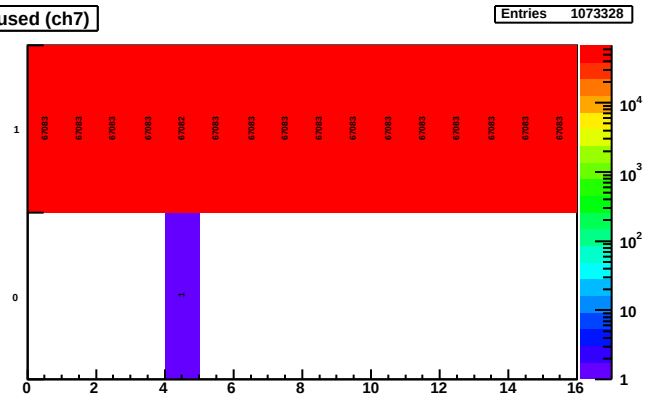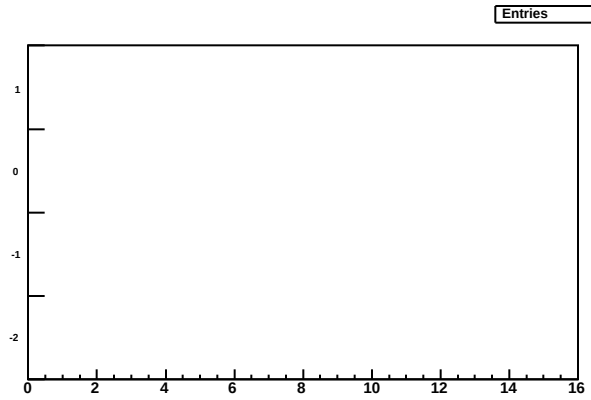
Unused (ch7)