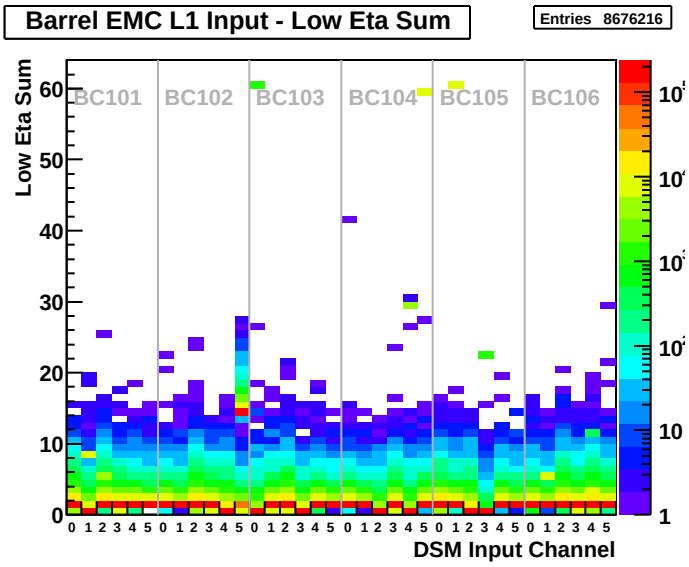
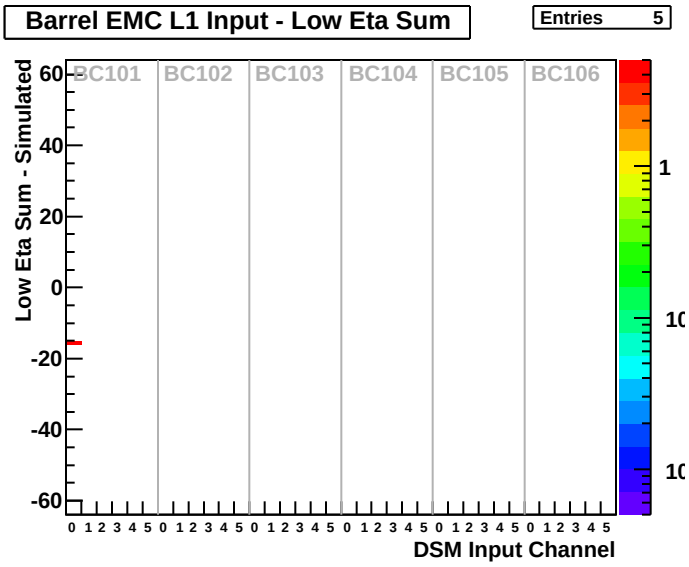
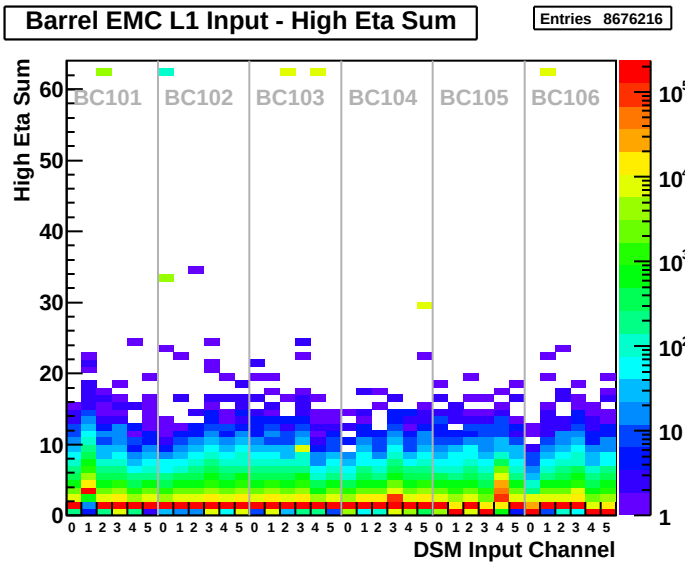
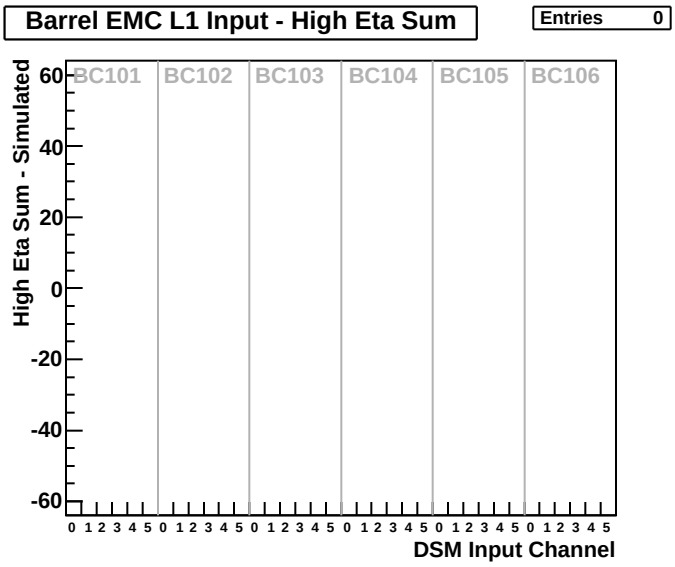
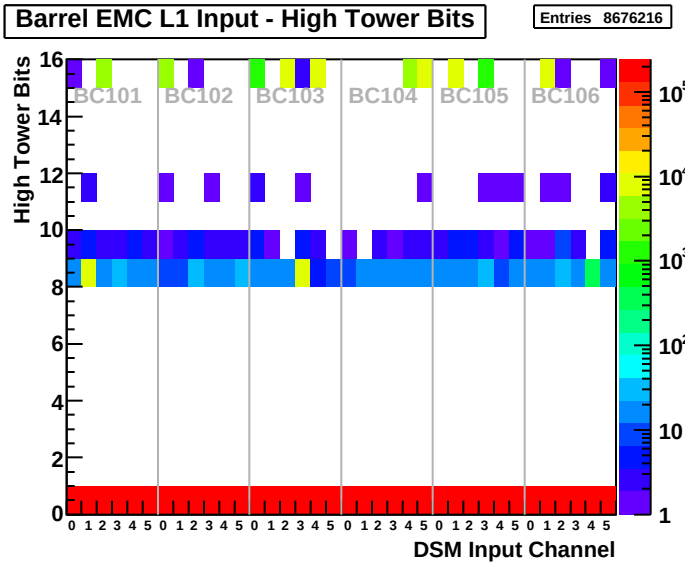
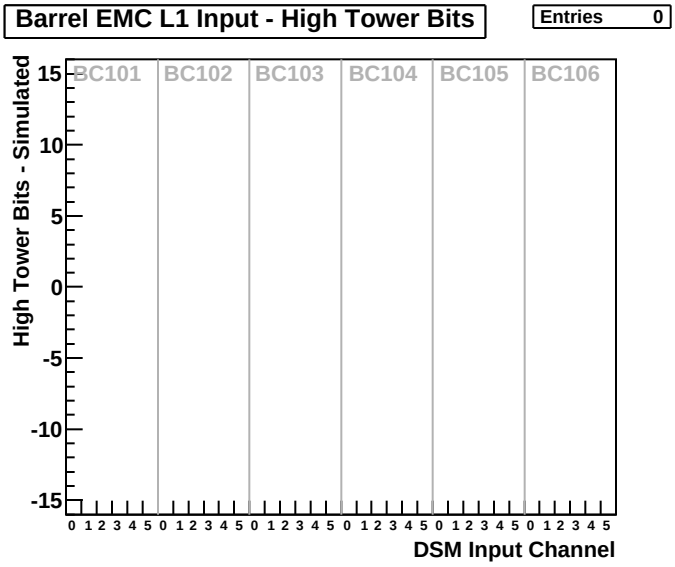

Barrel EMC L1 Input - Low Eta Sum
Entries 8676216
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

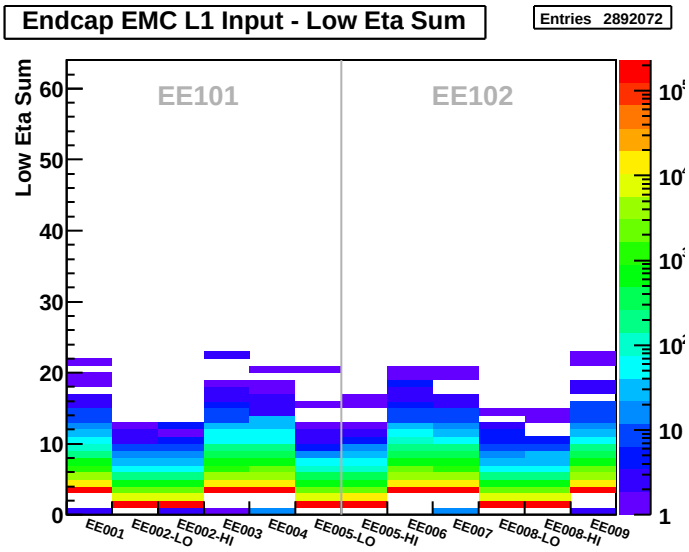

Barrel EMC L1 Input - Low Eta Sum
Entries 5
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

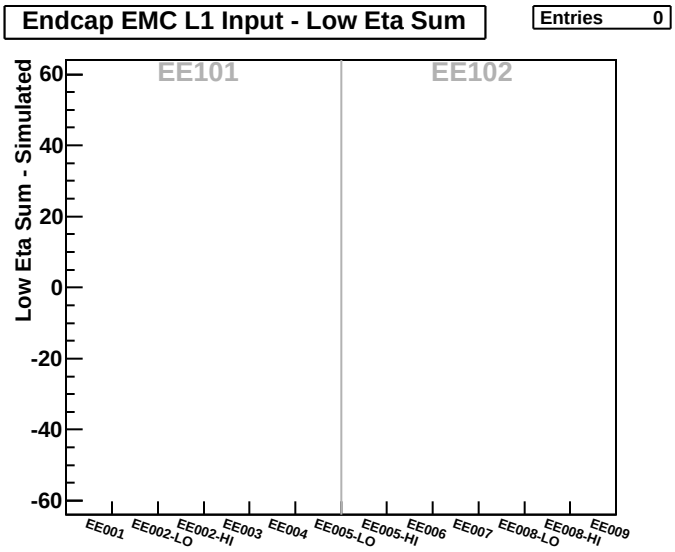

Barrel EMC L1 Input - High Eta Sum
Entries 8676216
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

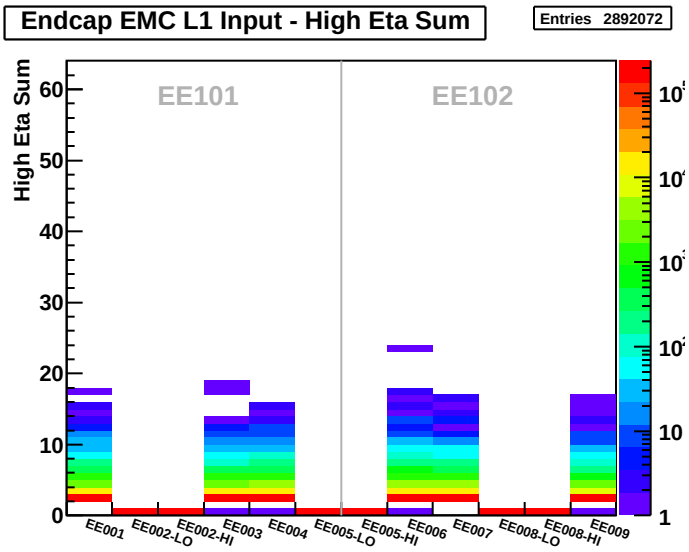

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

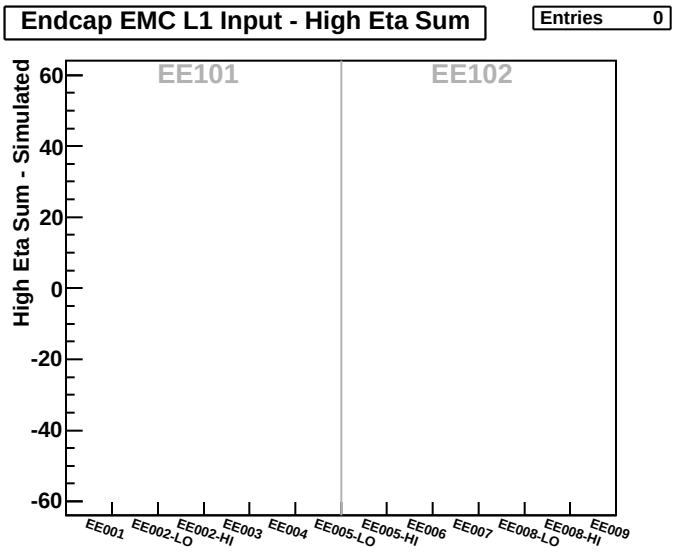

Barrel EMC L1 Input - High Tower Bits
Entries 8676216
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

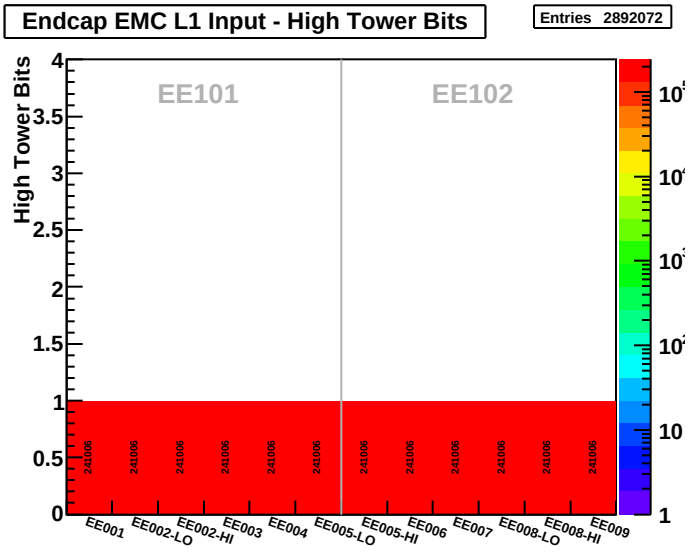

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

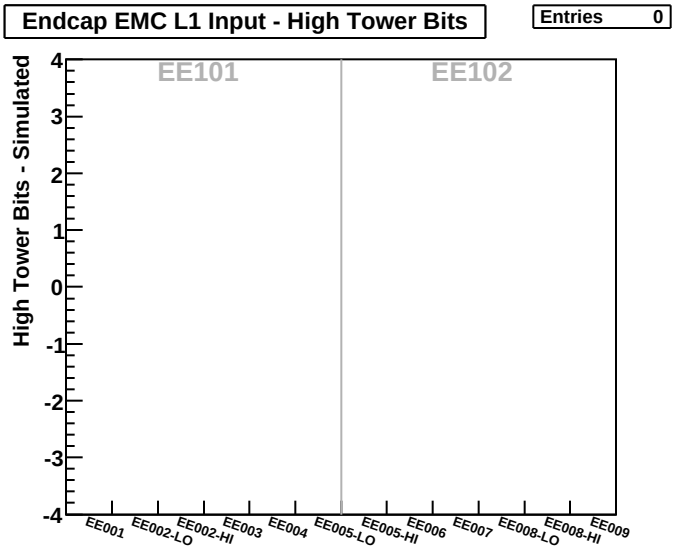

Endcap EMC L1 Input - Low Eta Sum
Entries 2892072
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

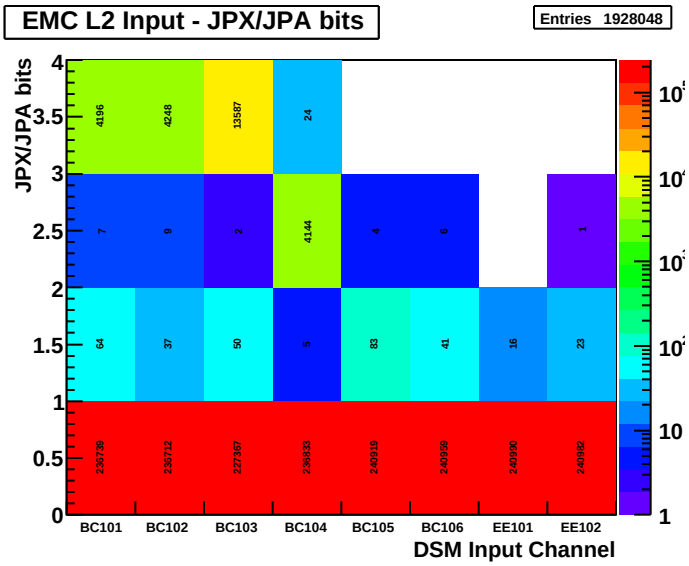

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

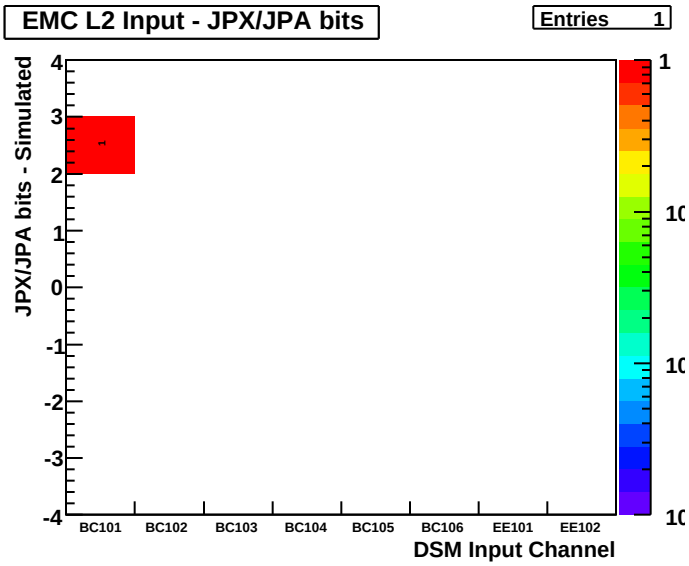

Endcap EMC L1 Input - High Eta Sum
Entries 2892072
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

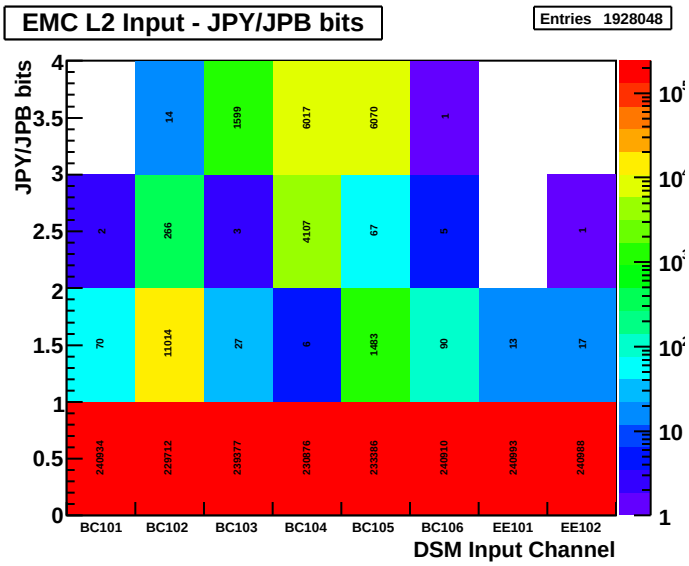

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

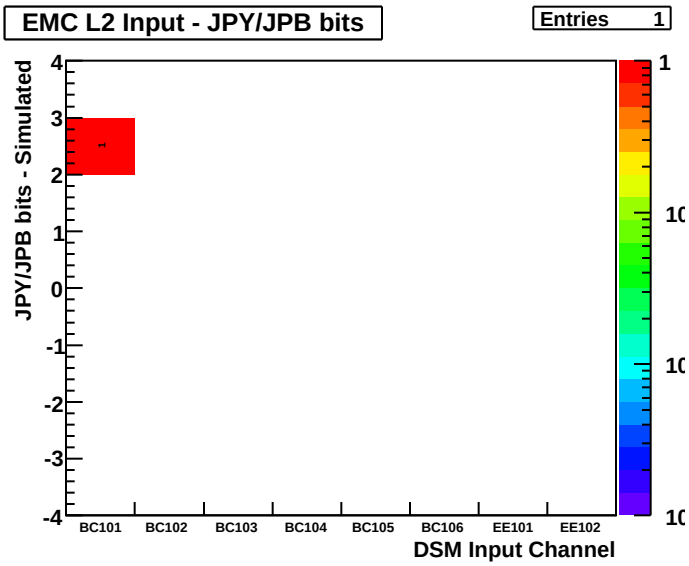

Endcap EMC L1 Input - High Tower Bits
Entries 2892072
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

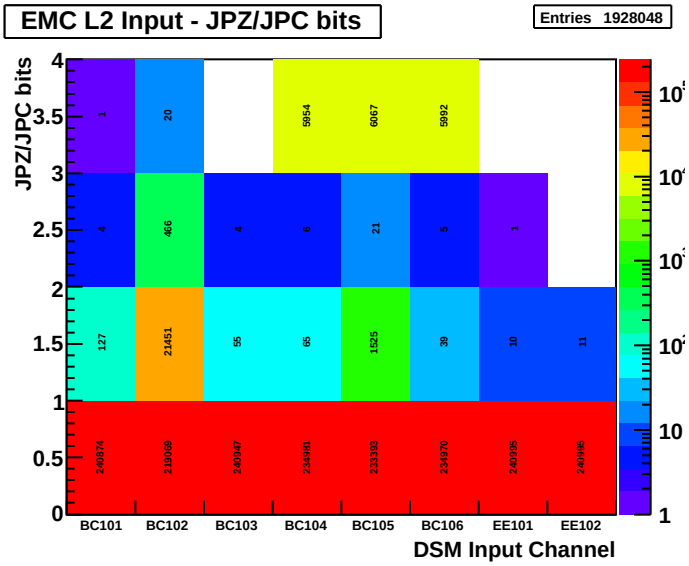

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

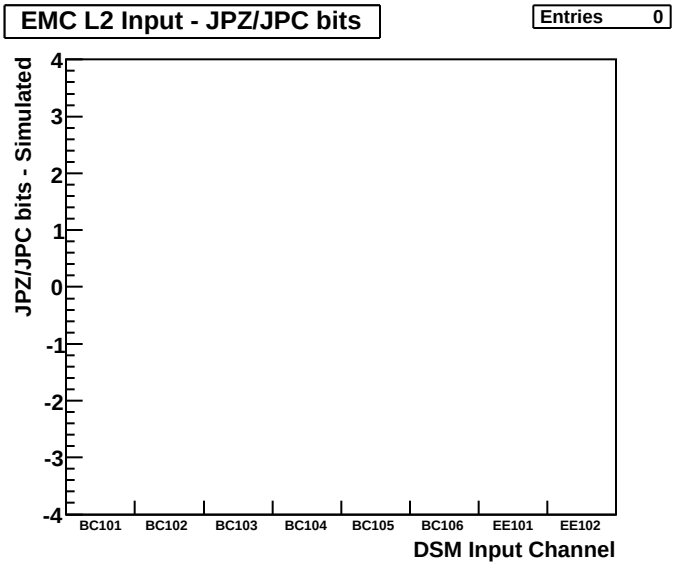

EMC L2 Input - JPX/JPA bits
Entries 1928048
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4196 4248 13587 24
7 9 2 4144 4 6 1
64 37 50 5 83 41 16 23
236739 236712 227367 236833 240919 240959 240990 240992

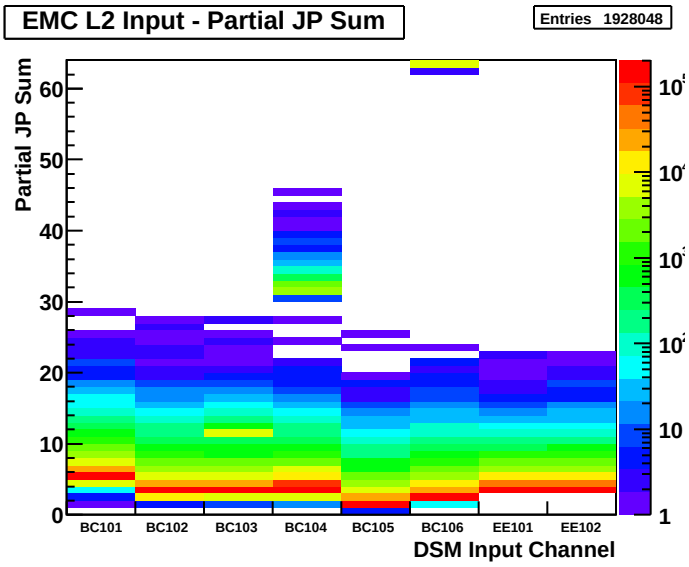

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

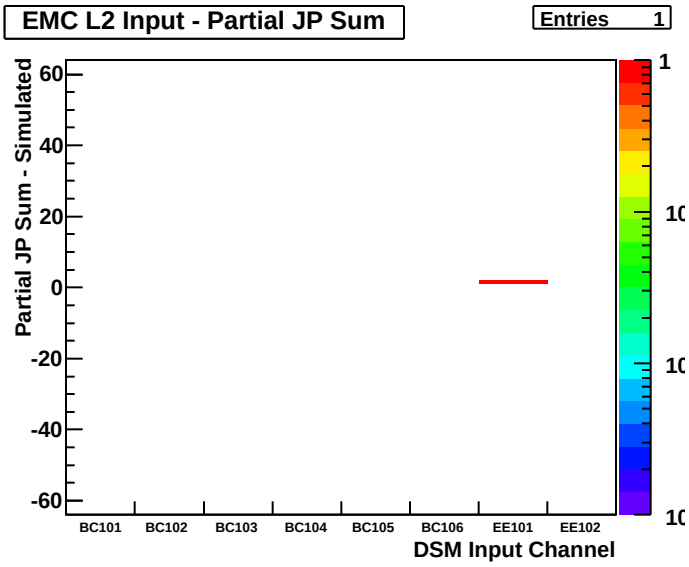

EMC L2 Input - JPY/JPB bits
Entries 1928048
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
14 1599 6017 6070 1
2 266 3 4107 67 5 1
70 11014 27 6 1483 90 13 17
240934 229712 239377 239876 233386 240910 240993 240988

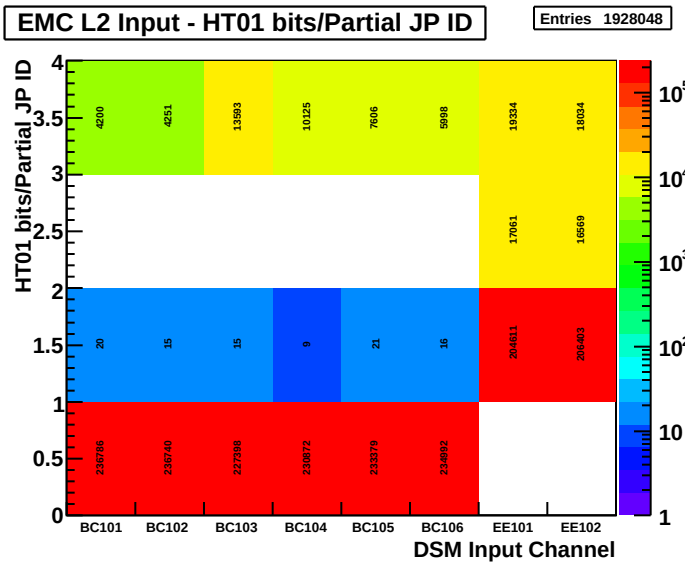

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

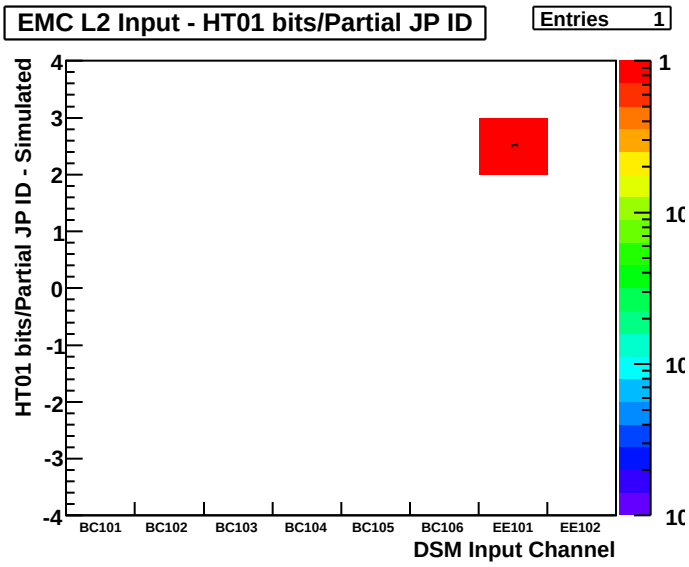

EMC L2 Input - JPZ/JPC bits
Entries 1928048
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 20 5954 6067 5992
4 466 4 6 21 5 1
127 21451 55 65 1525 39 10 11
240874 219069 240947 234981 233393 234970 240995 240995

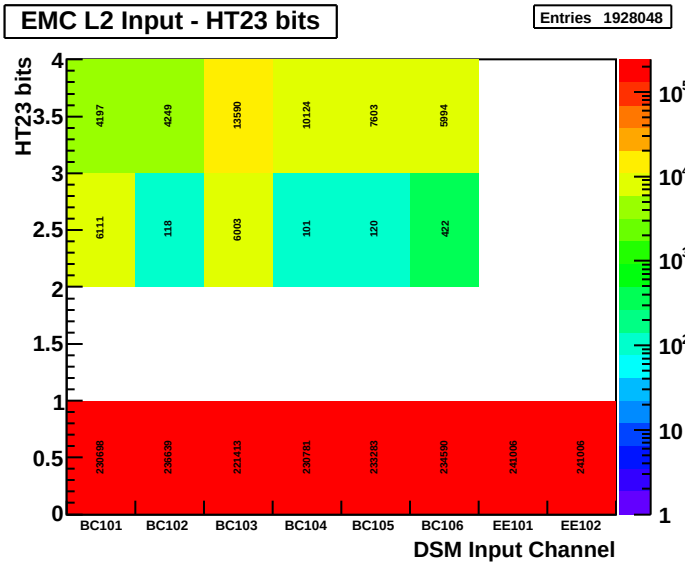

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

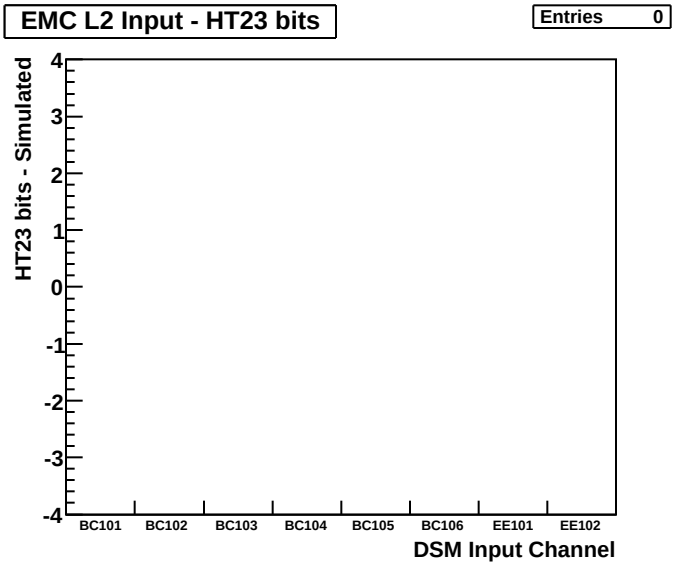

EMC L2 Input - Partial JP Sum
Entries 1928048
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

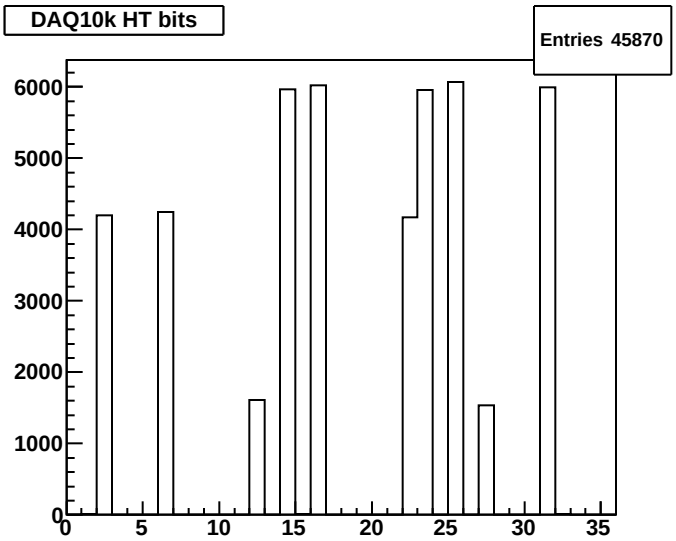

EMC L2 Input - Partial JP Sum
Entries 1
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

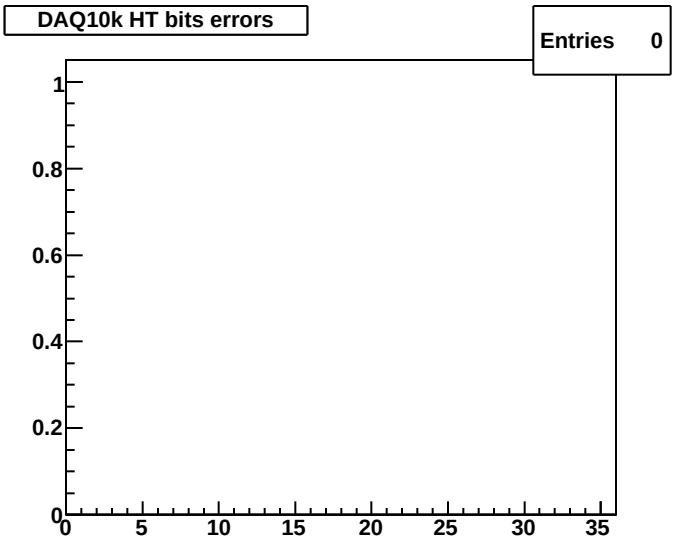

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1928048
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

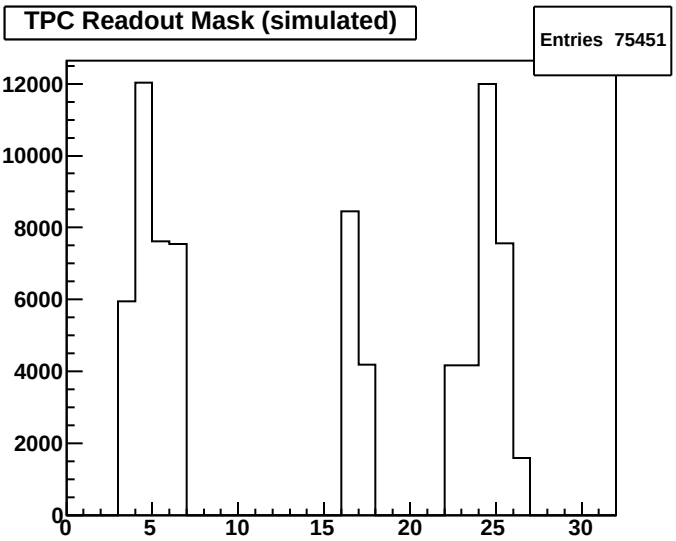

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

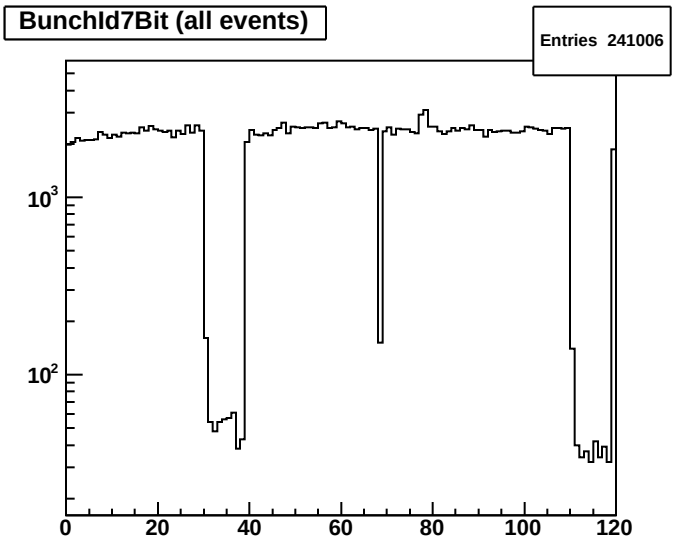

EMC L2 Input - HT23 bits
Entries 1928048
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

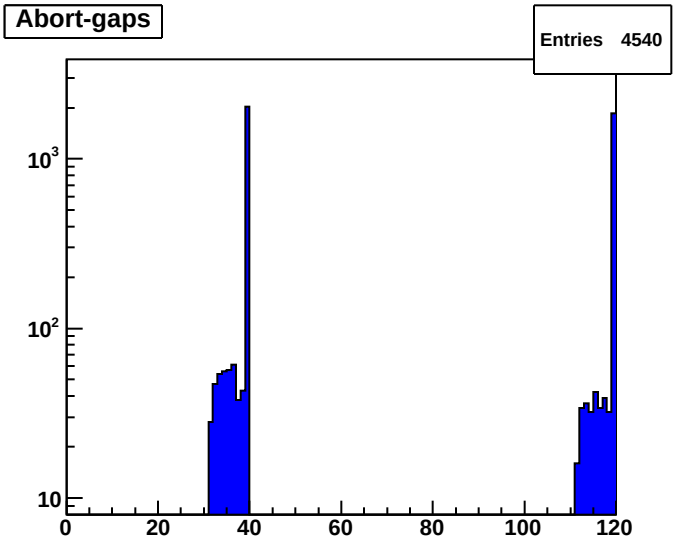

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

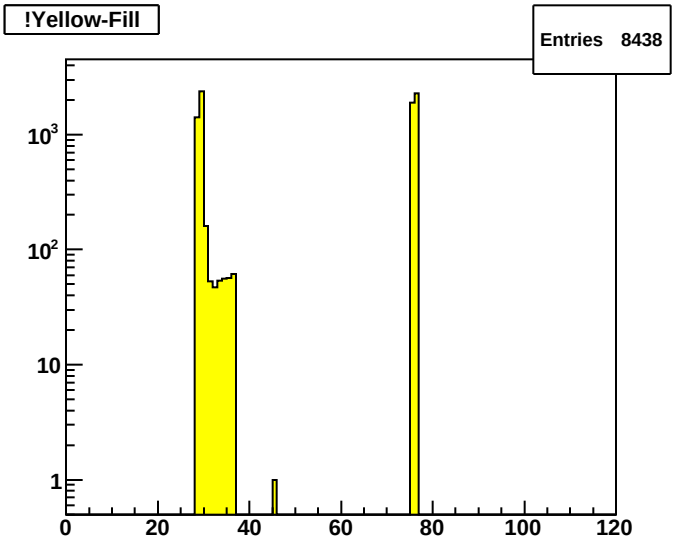

DAQ10k HT bits
Entries 45870

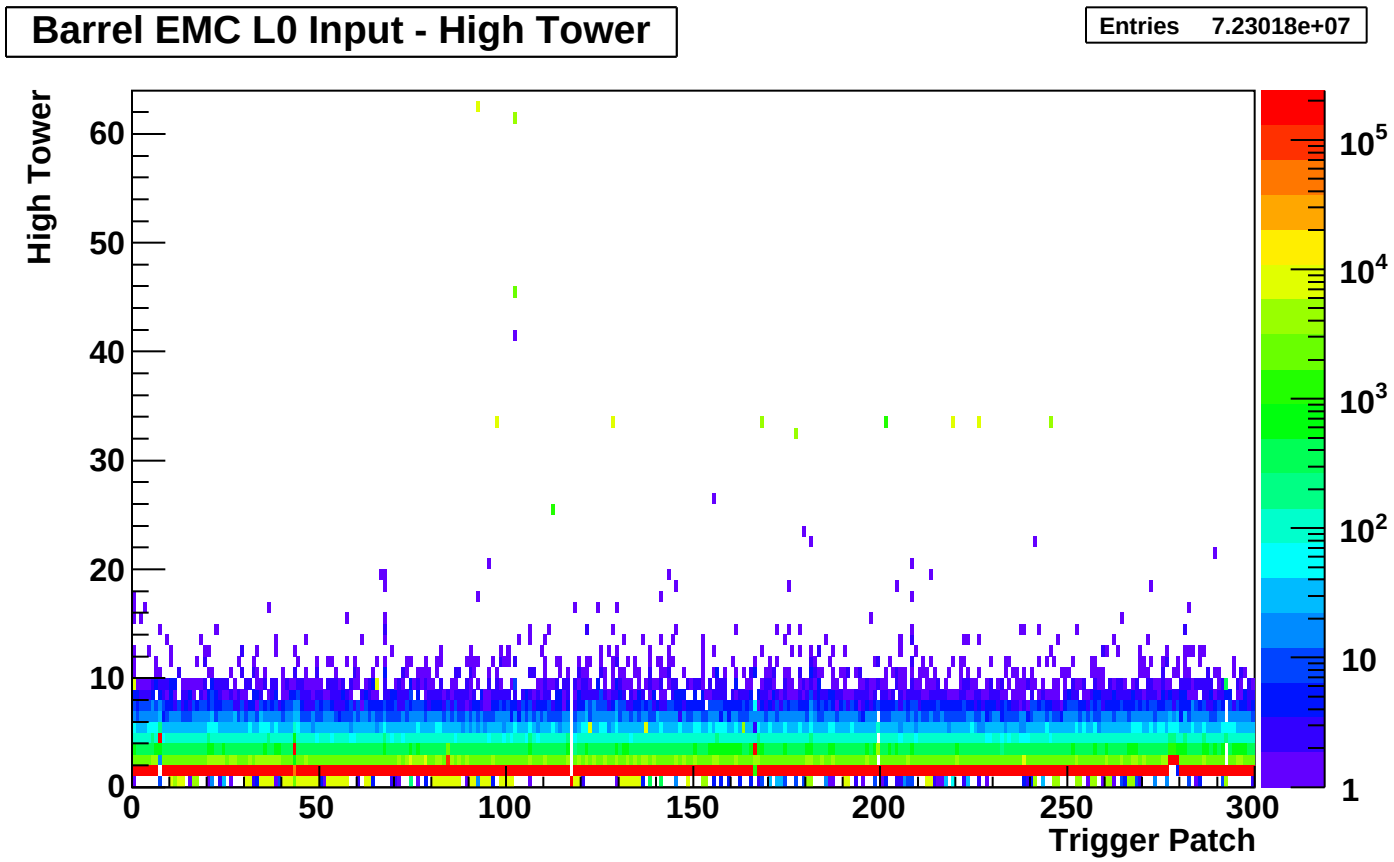

DAQ10k HT bits errors
Entries 0

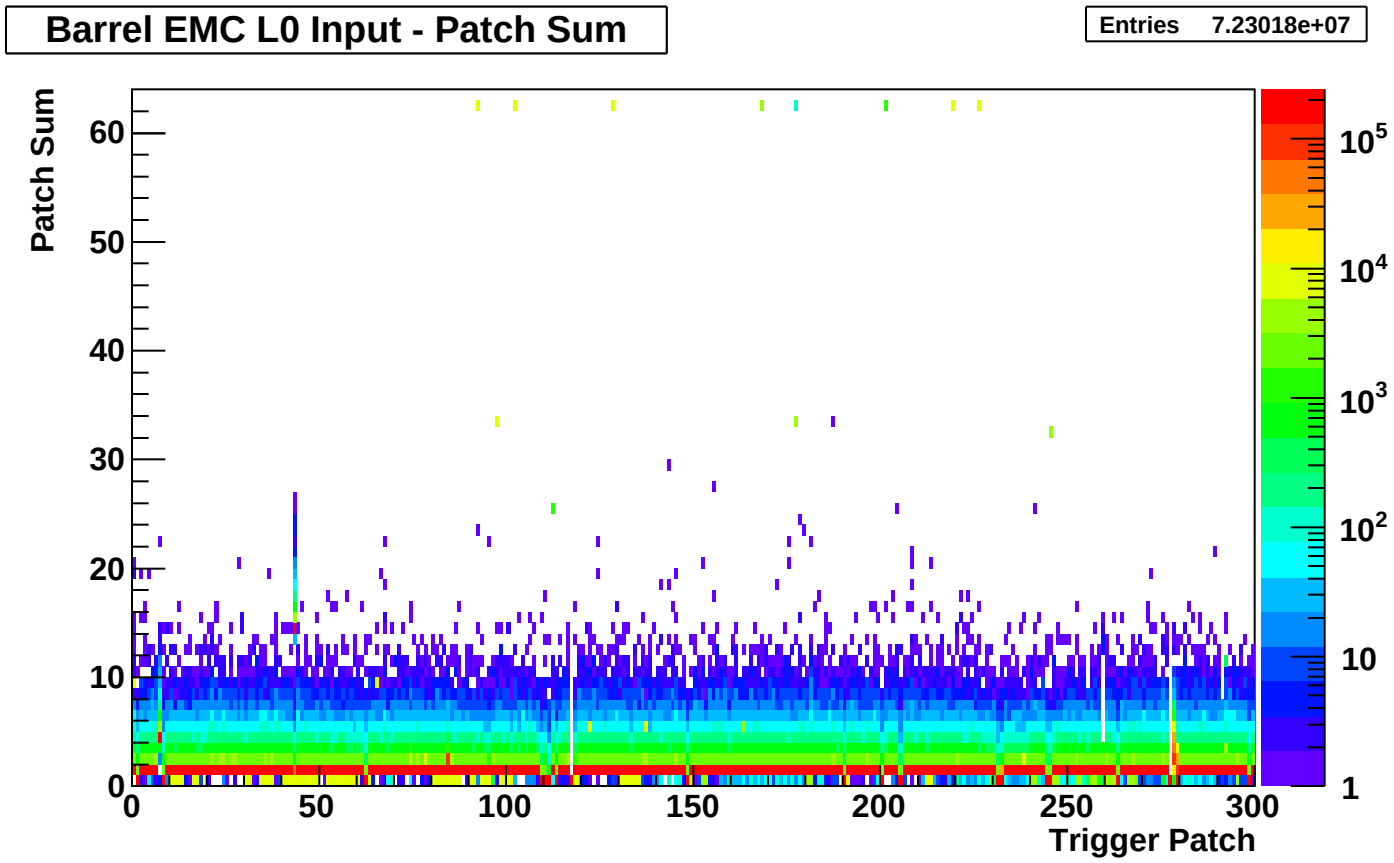

TPC Readout Mask (simulated)
Entries 75451

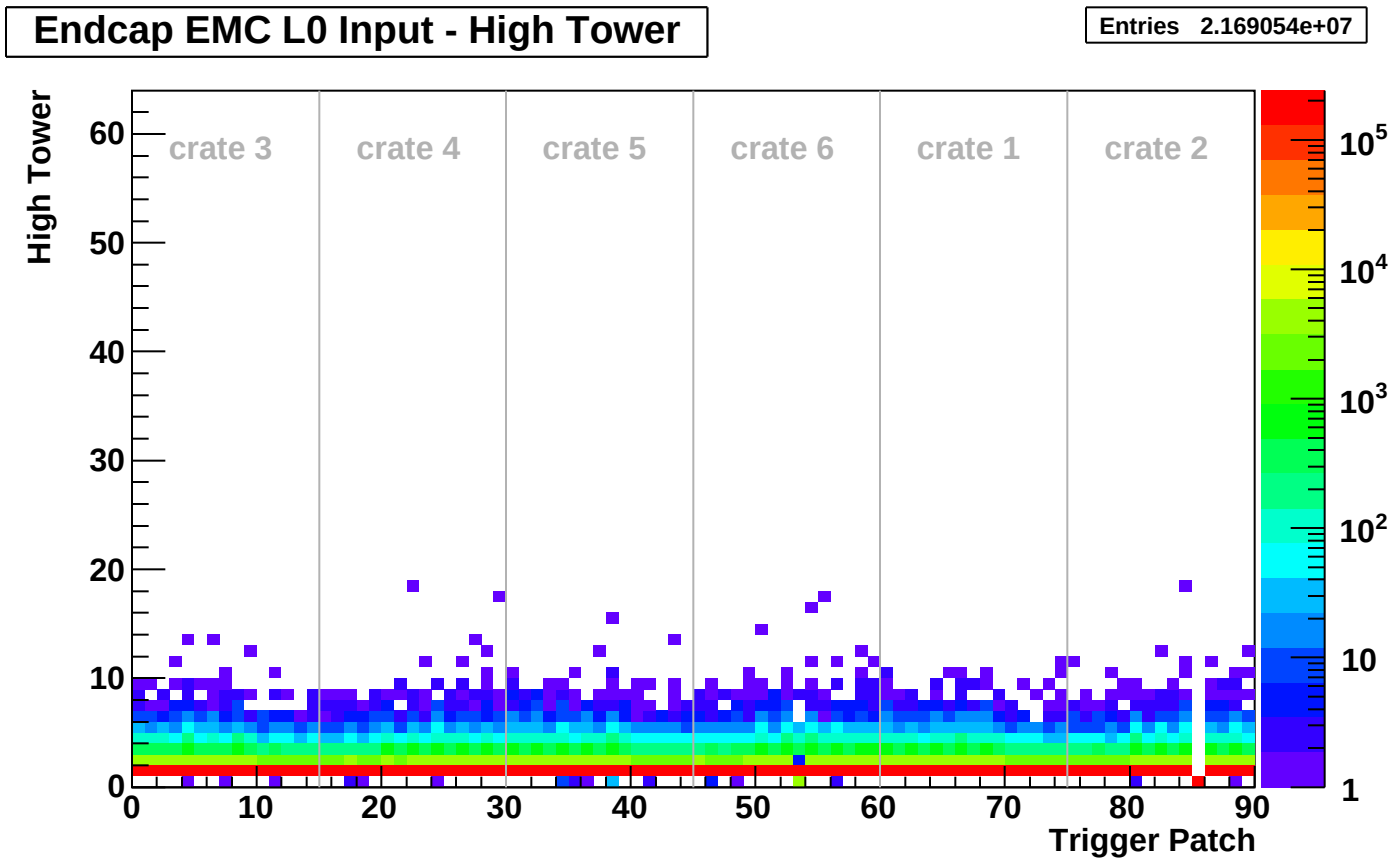

BunchId7Bit (all events)
Entries 241006

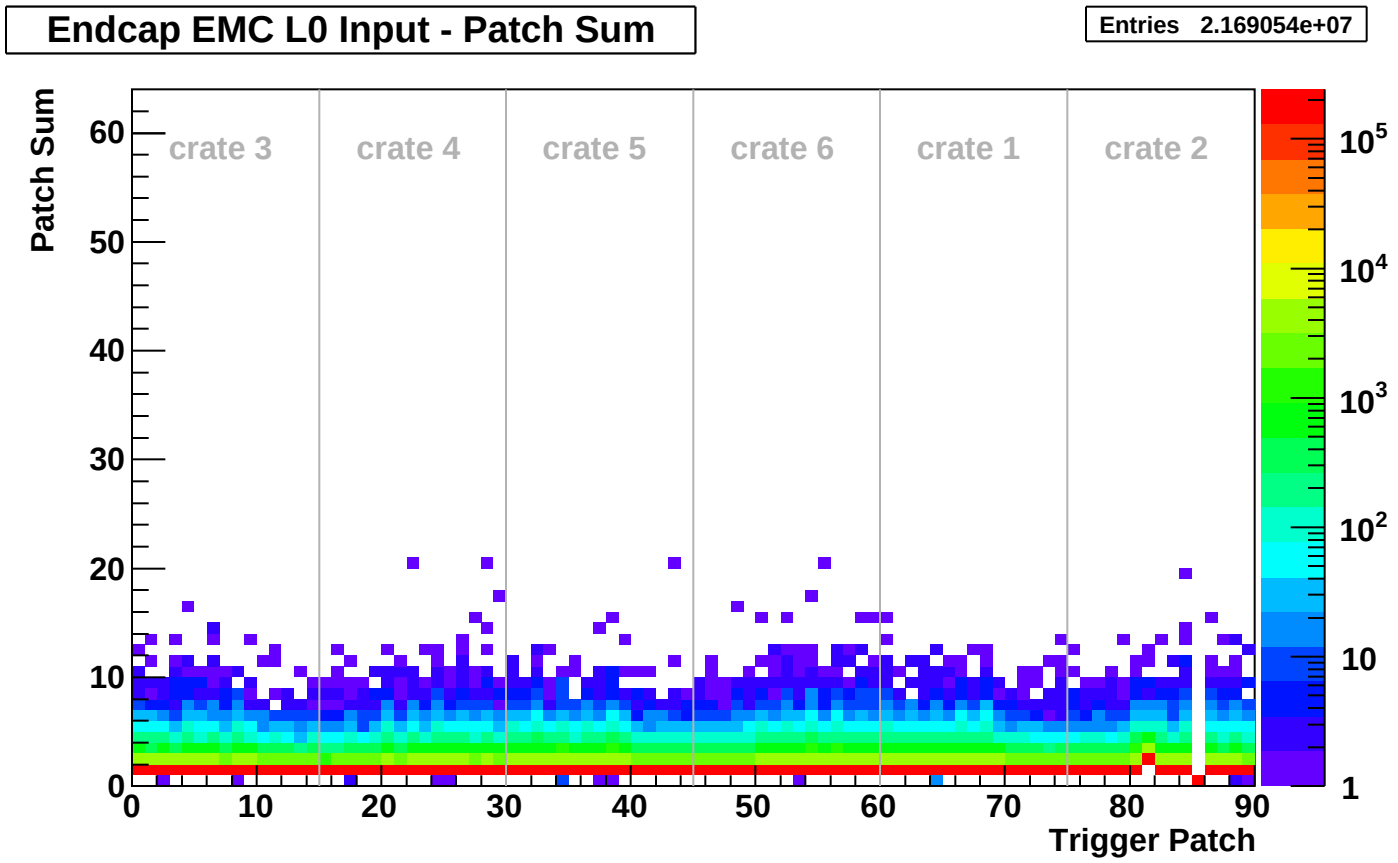

Abort-gaps
Entries 4540

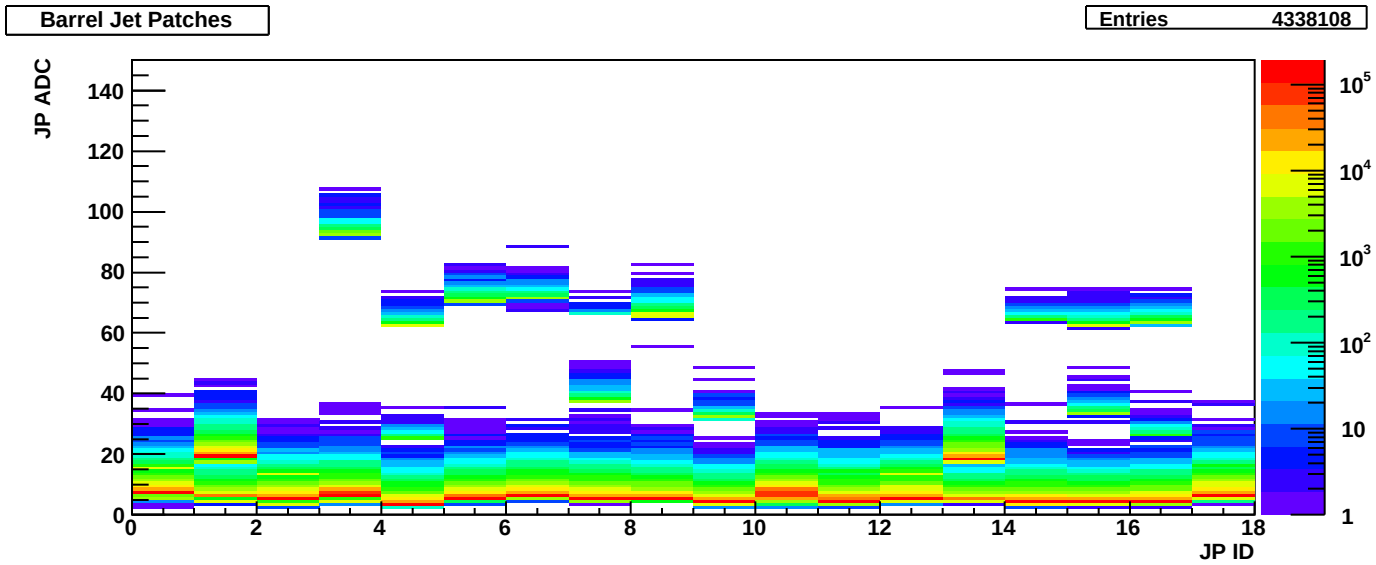

!Yellow-Fill
Entries 8438

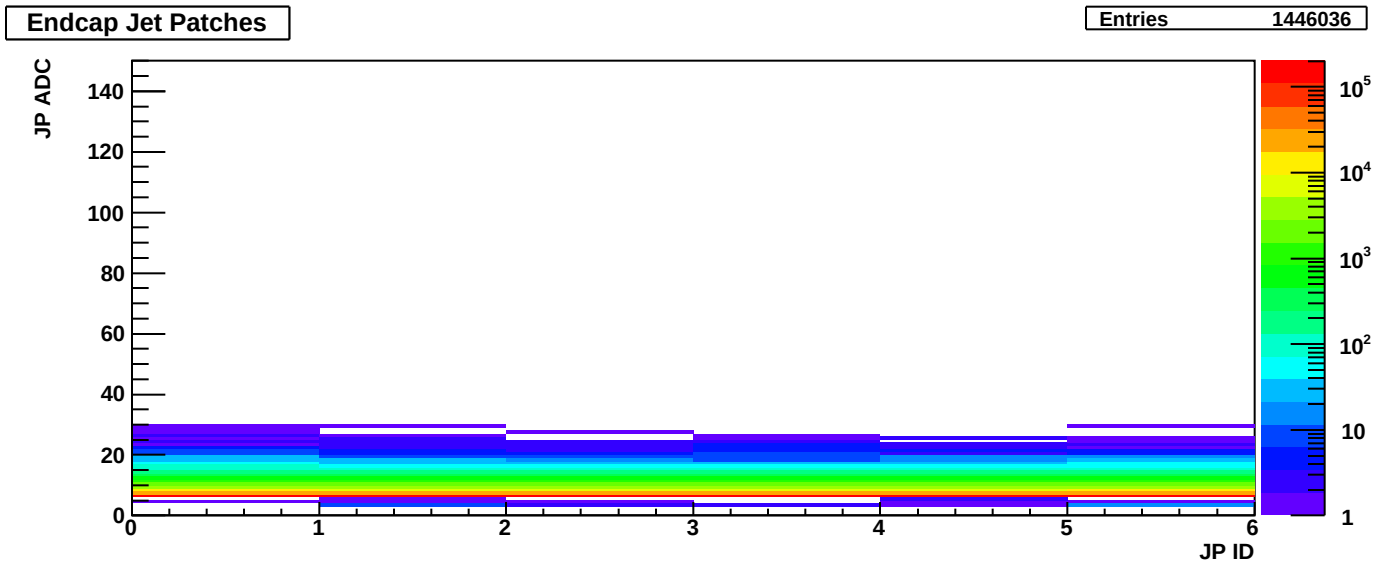

Barrel EMC L0 Input - High Tower
Entries 7.23018e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

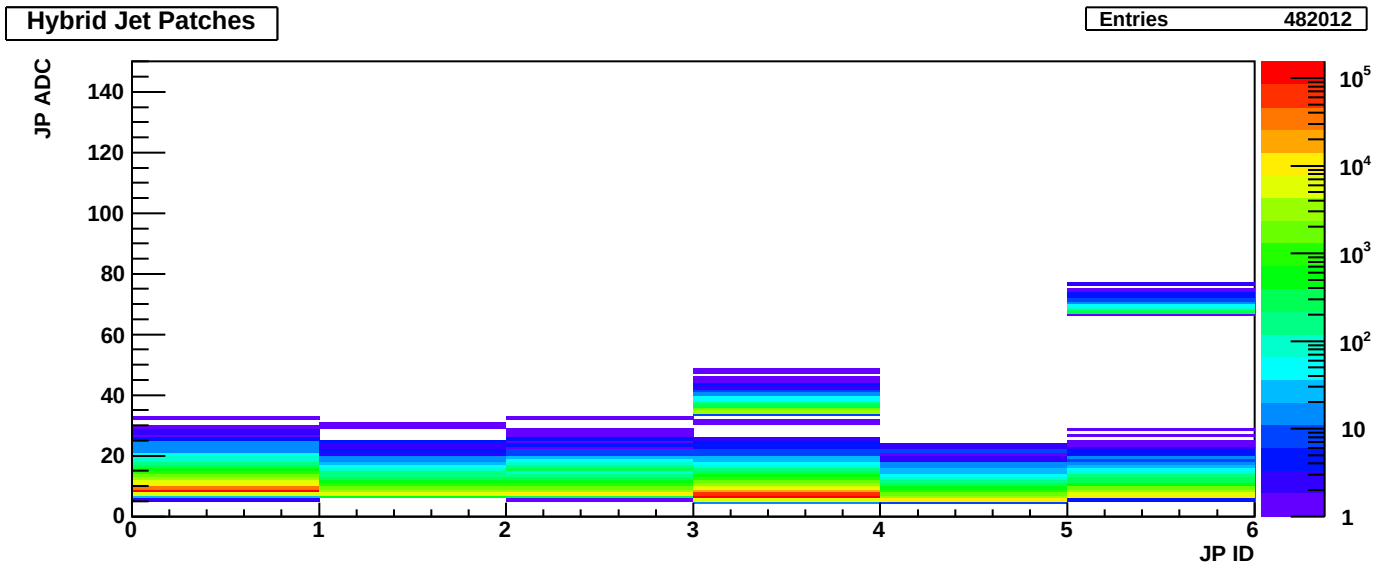

Barrel EMC L0 Input - Patch Sum
Entries 7.23018e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

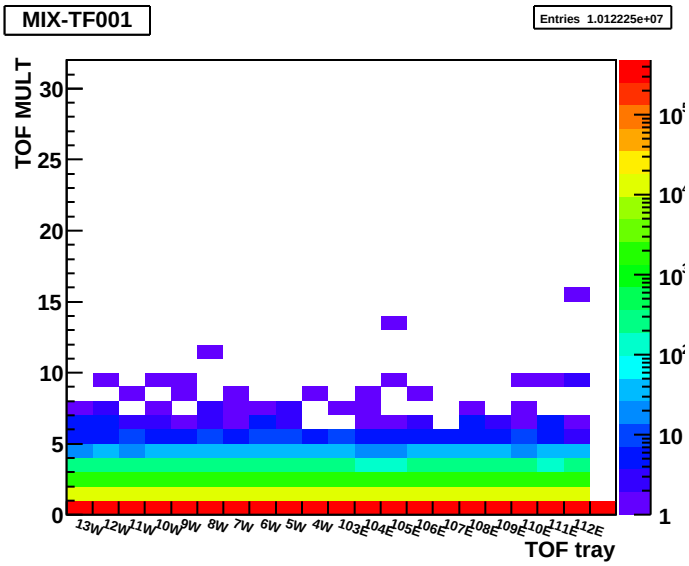

Endcap EMC L0 Input - High Tower
Entries 2.169054e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

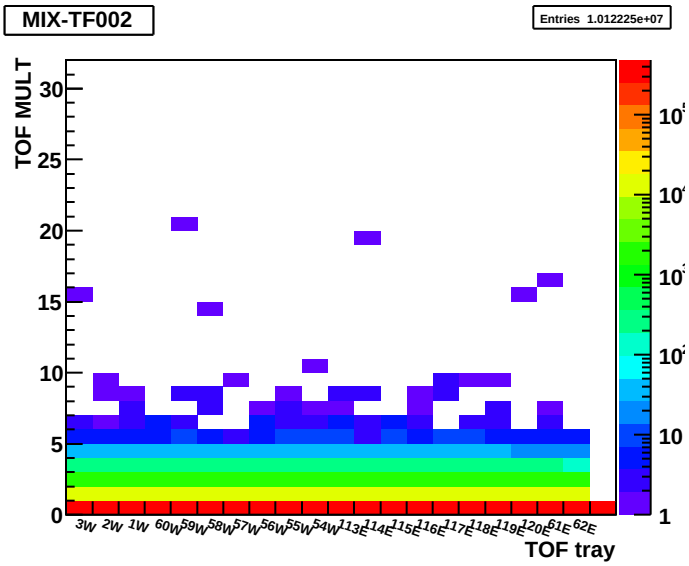

Endcap EMC L0 Input - Patch Sum
Entries 2.169054e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

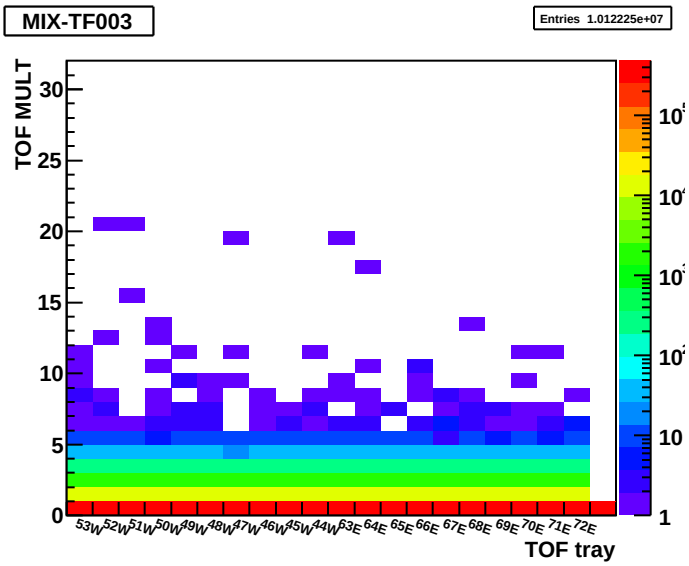

Barrel Jet Patches
Entries 4338108
JP ADC
JP ID

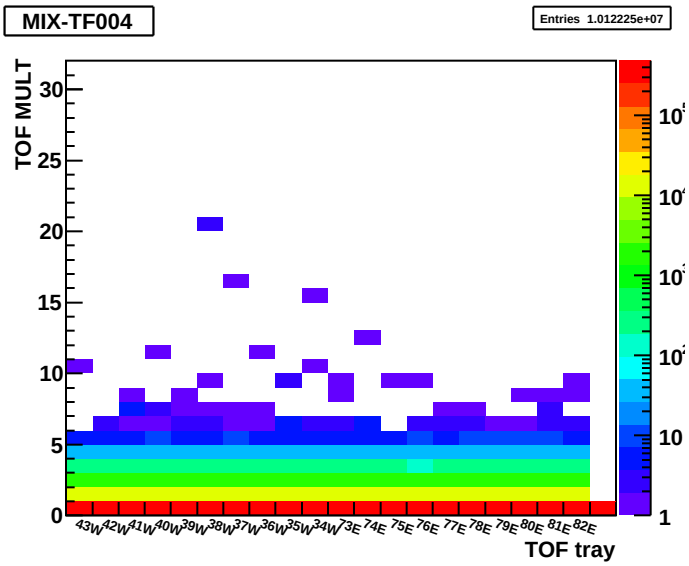

Endcap Jet Patches
Entries 1446036
JP ADC
JP ID

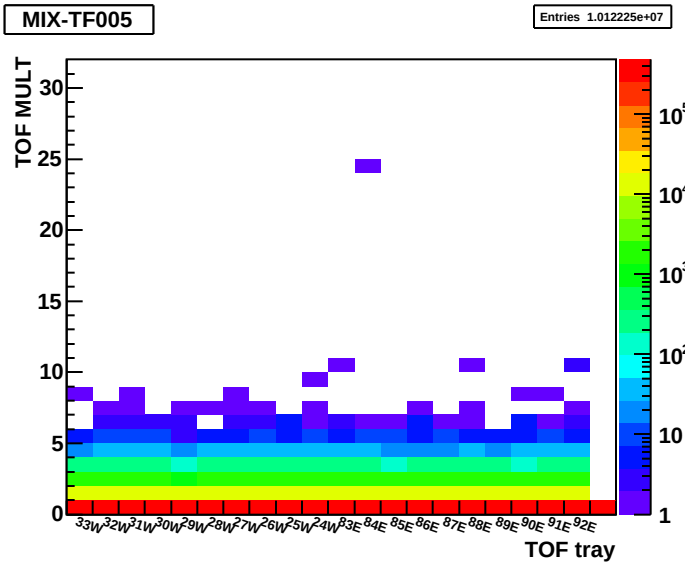

Hybrid Jet Patches
Entries 482012
JP ADC
JP ID

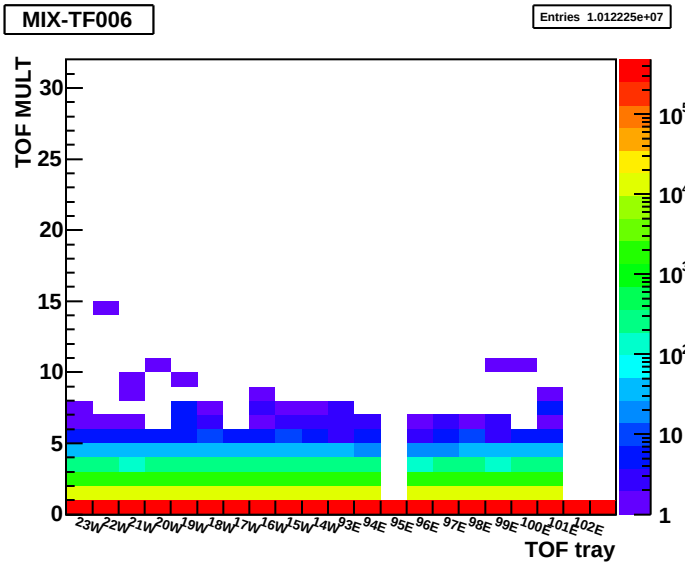

MIX-TF001
Entries 1.012225e+07
TOF MULT
TOF tray

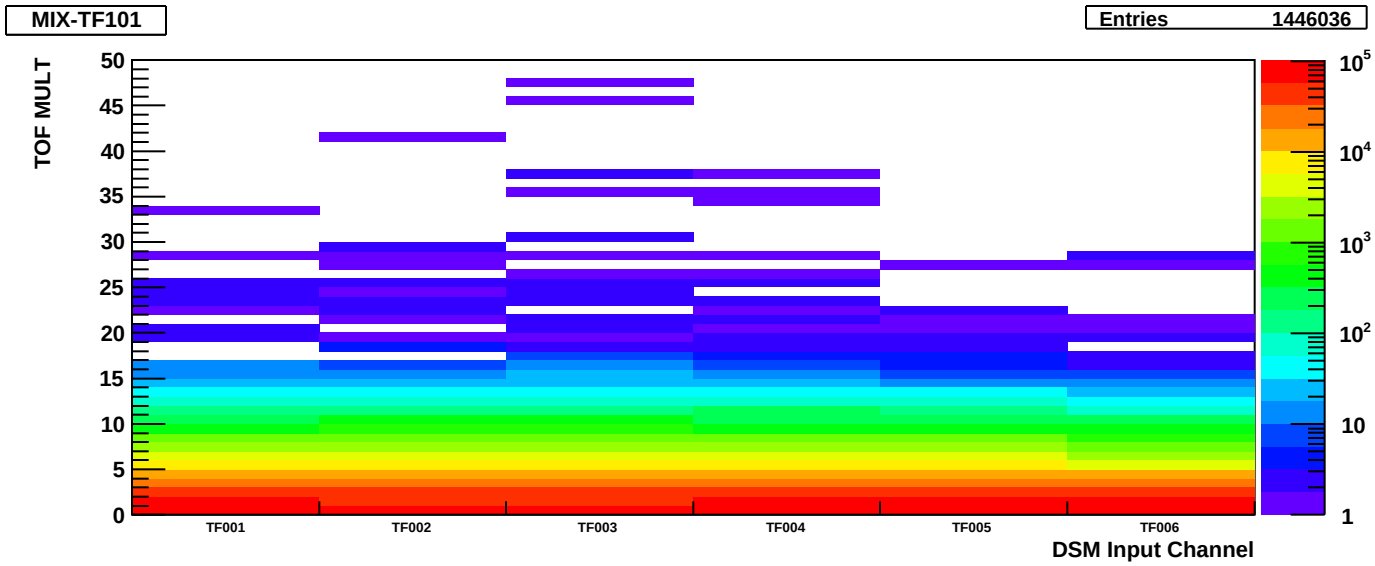

MIX-TF002
Entries 1.012225e+07
TOF MULT
TOF tray

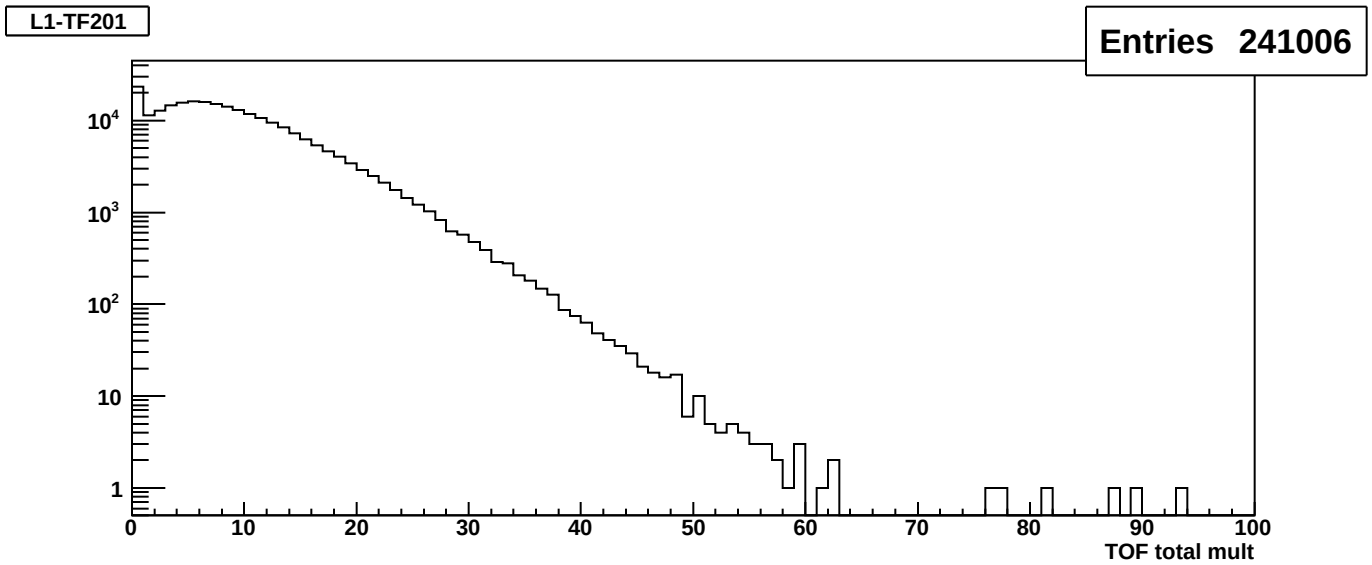

MIX-TF003
Entries 1.012225e+07
TOF MULT
TOF tray

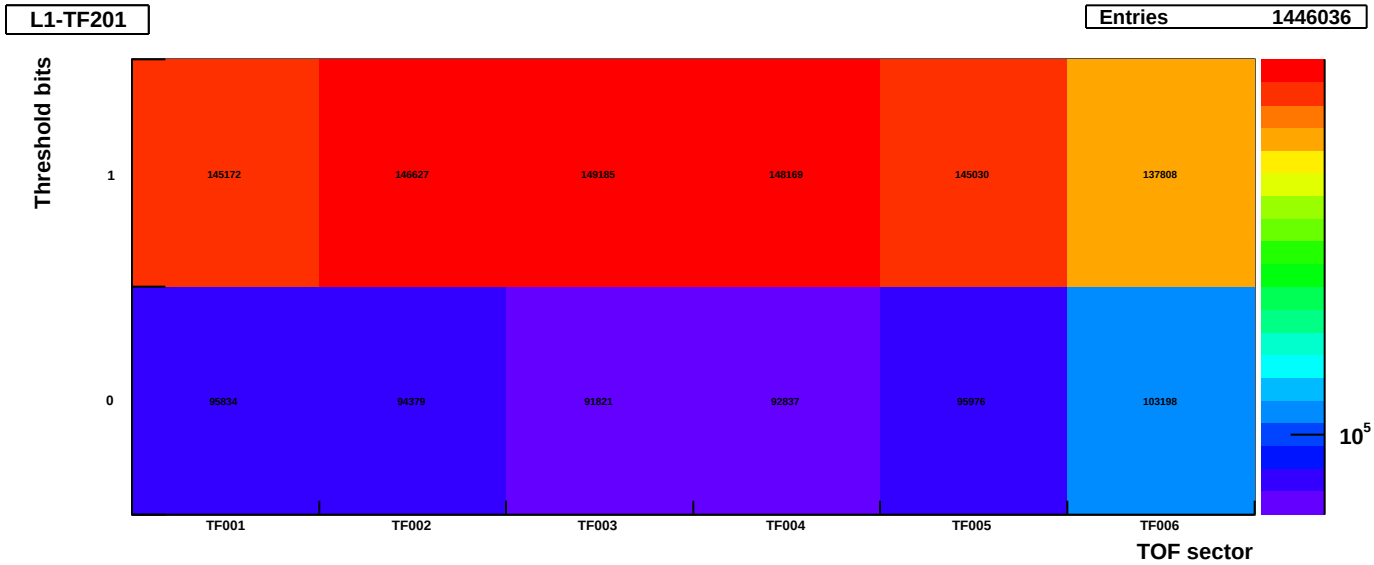

MIX-TF004
Entries 1.012225e+07
TOF MULT
TOF tray

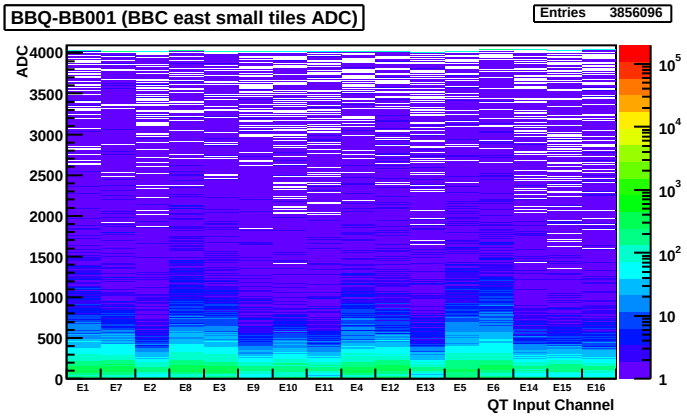

MIX-TF005
Entries 1.012225e+07
TOF MULT
TOF tray

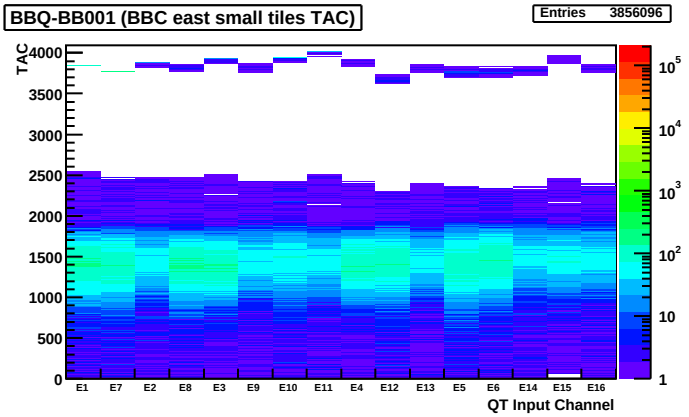

MIX-TF006
Entries 1.012225e+07
TOF MULT
TOF tray

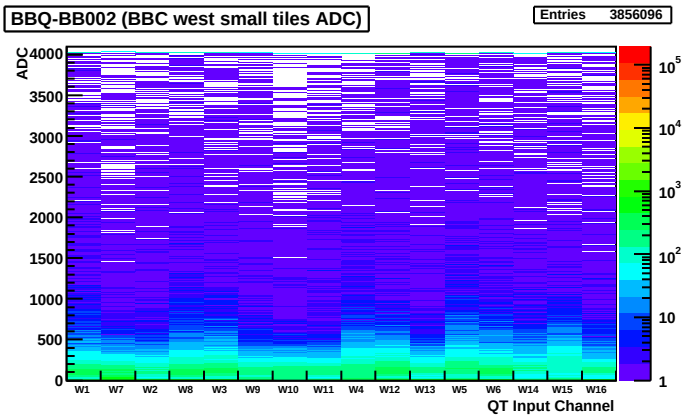

MIX-TF101
Entries 1446036
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

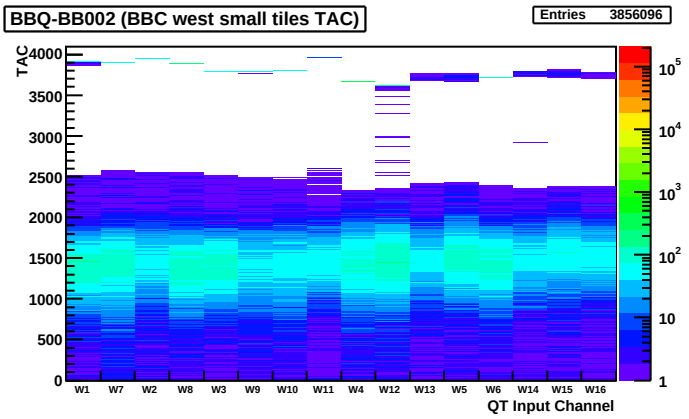

L1-TF201
Entries 241006
TOF total mult

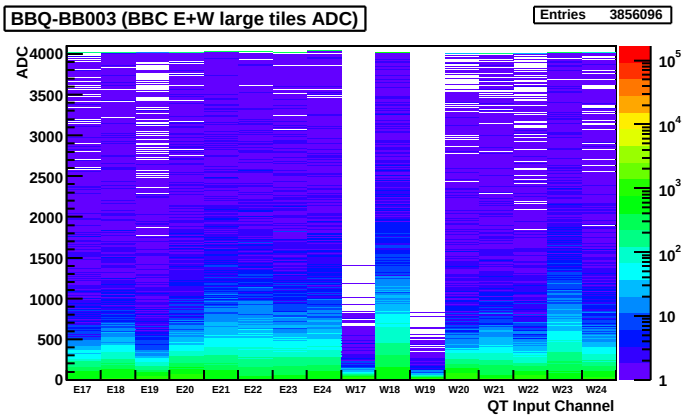

L1-TF201
Entries 1446036
Threshold bits
145172
146627
149185
148169
145030
137808
95834
94379
91821
92837
95976
103198
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

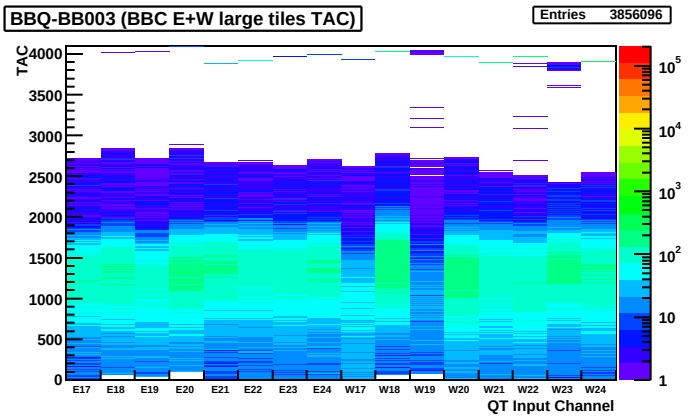

BBQ-BB001 (BBC east small tiles ADC)
Entries 3856096
ADC
QT Input Channel

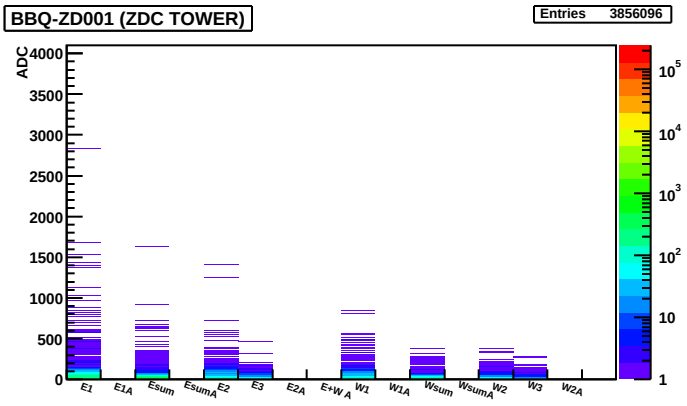

BBQ-BB001 (BBC east small tiles TAC)
Entries 3856096
TAC
QT Input Channel

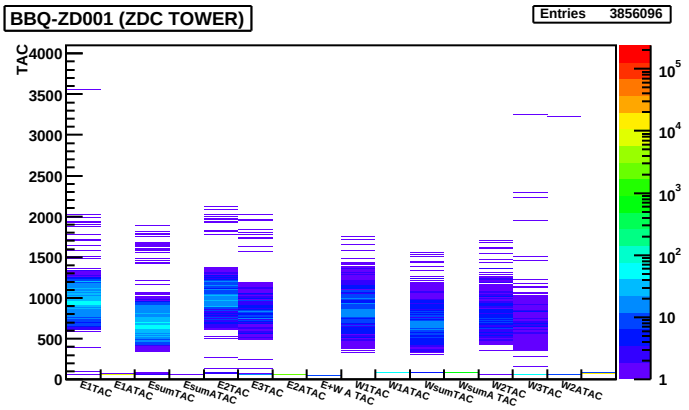

BBQ-BB002 (BBC west small tiles ADC)
Entries 3856096
ADC
QT Input Channel

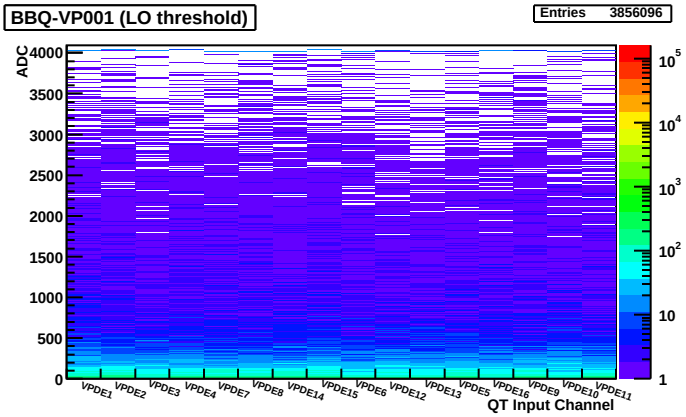

BBQ-BB002 (BBC west small tiles TAC)
Entries 3856096
TAC
QT Input Channel

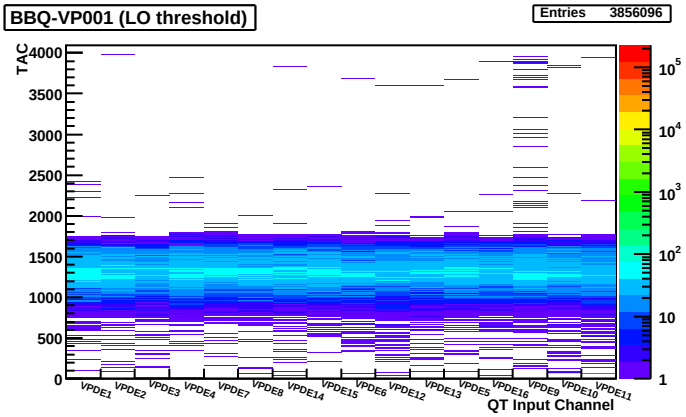

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3856096
ADC
QT Input Channel

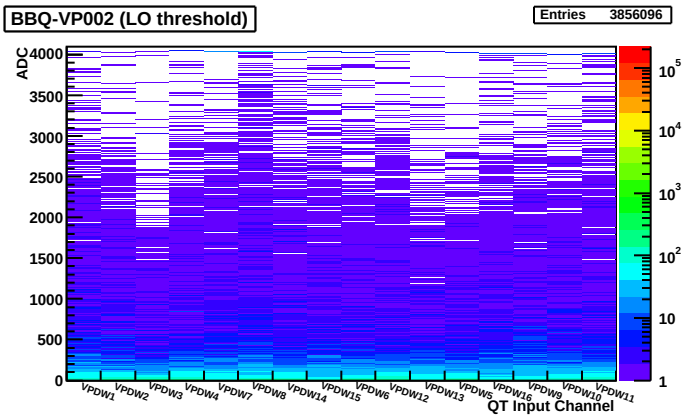

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3856096
TAC
QT Input Channel

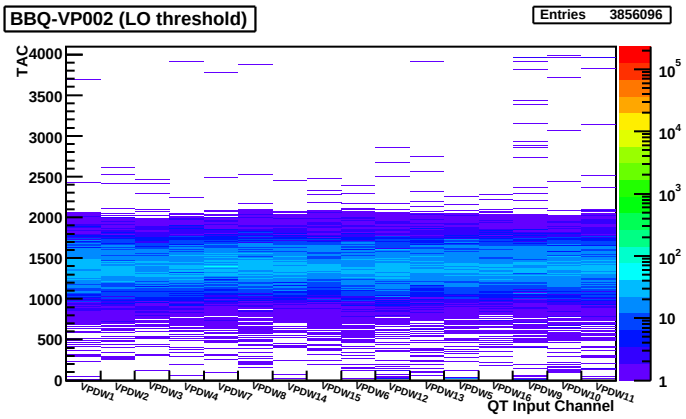

BBQ-ZD001 (ZDC TOWER)
Entries 3856096
ADC

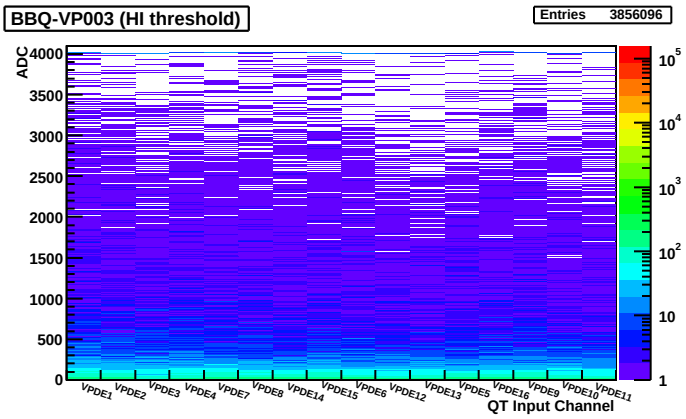

BBQ-ZD001 (ZDC TOWER)
Entries 3856096
TAC

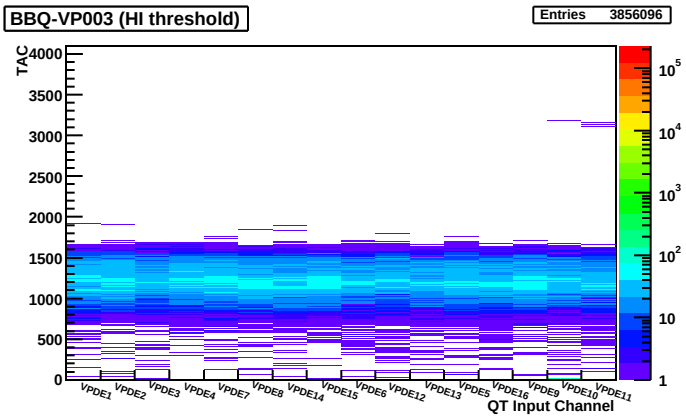

BBQ-VP001 (LO threshold)
Entries 3856096
ADC
QT Input Channel

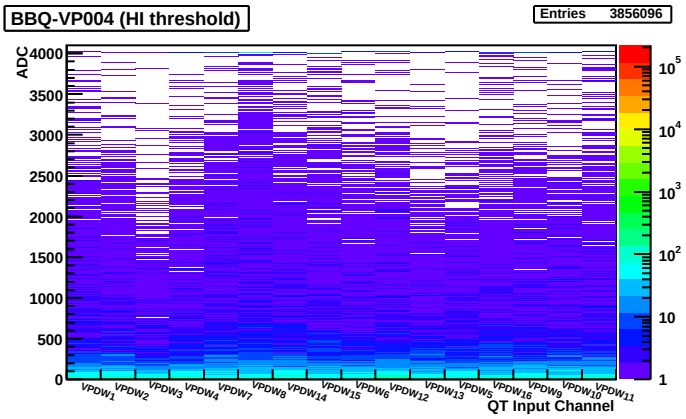

BBQ-VP001 (LO threshold)
Entries 3856096
TAC
QT Input Channel

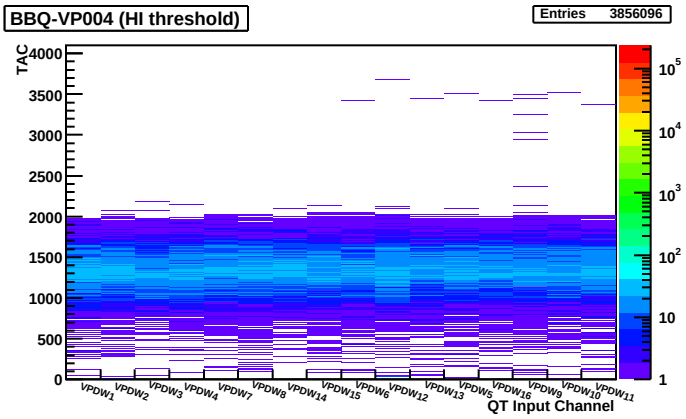

BBQ-VP002 (LO threshold)
Entries 3856096
ADC
QT Input Channel

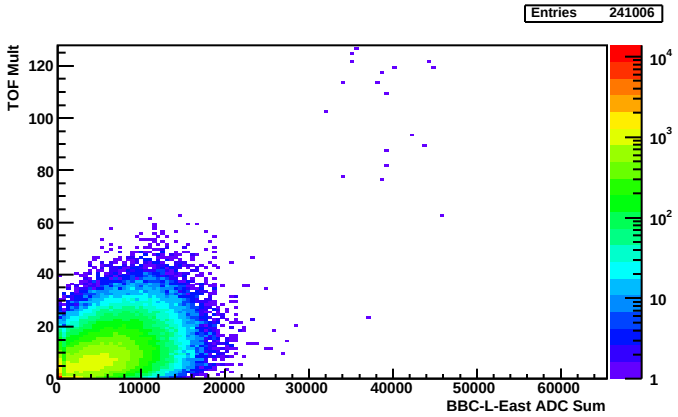

BBQ-VP002 (LO threshold)
Entries 3856096
TAC
QT Input Channel

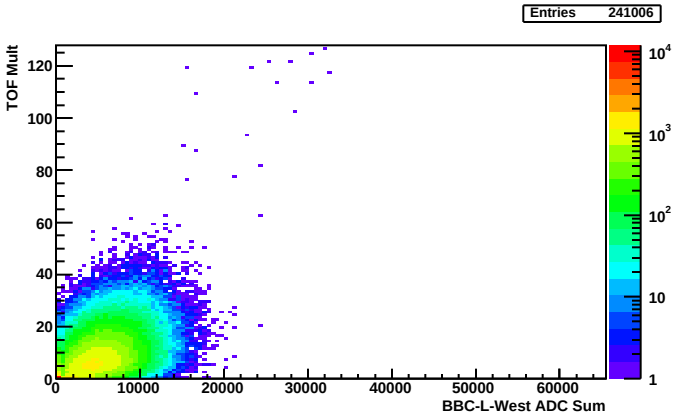

BBQ-VP003 (HI threshold)
Entries 3856096
ADC
QT Input Channel

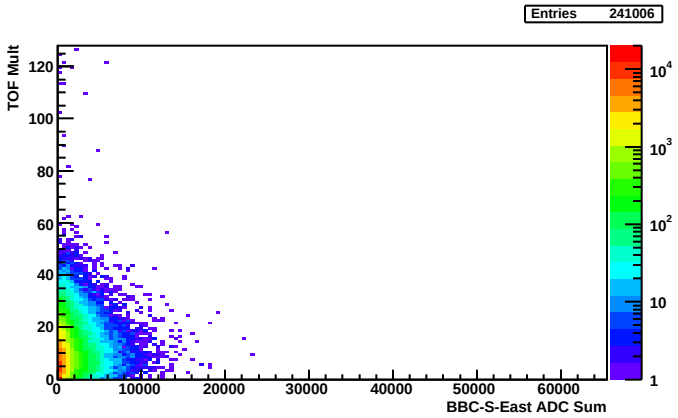

BBQ-VP003 (HI threshold)
Entries 3856096
TAC
QT Input Channel

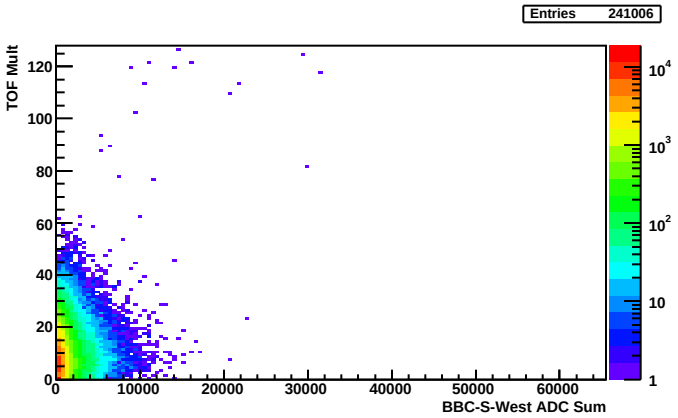

BBQ-VP004 (HI threshold)
Entries 3856096
ADC
QT Input Channel

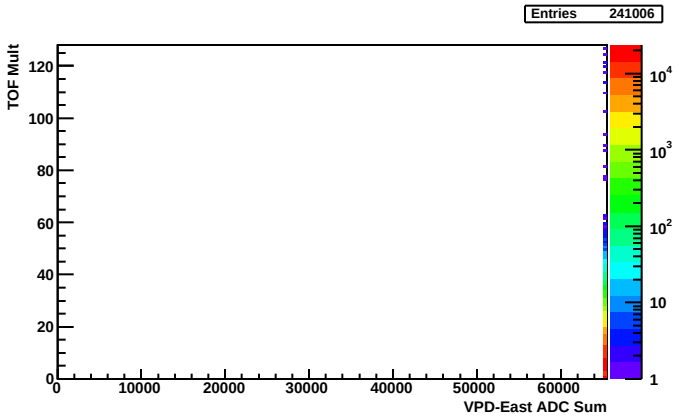

BBQ-VP004 (HI threshold)
Entries 3856096
TAC
QT Input Channel

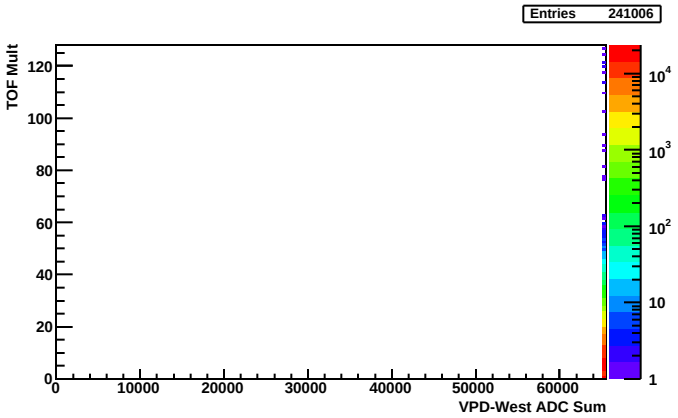

Entries 241006
TOF Mult
BBC-L-East ADC Sum

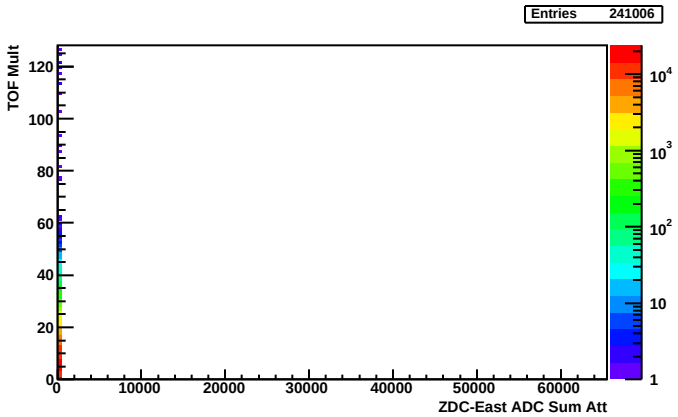

Entries 241006
TOF Mult
BBC-L-West ADC Sum

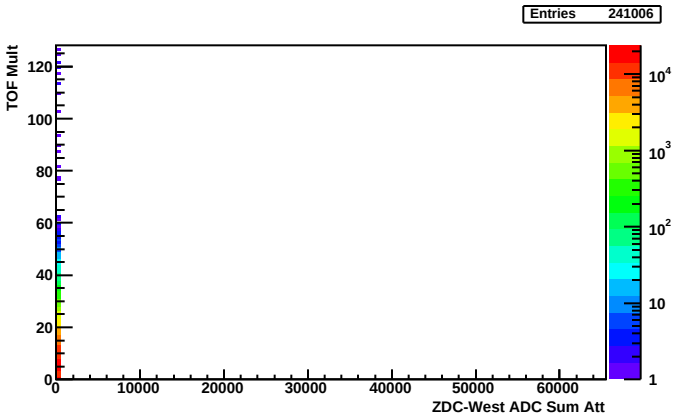

Entries 241006
TOF Mult
BBC-S-East ADC Sum

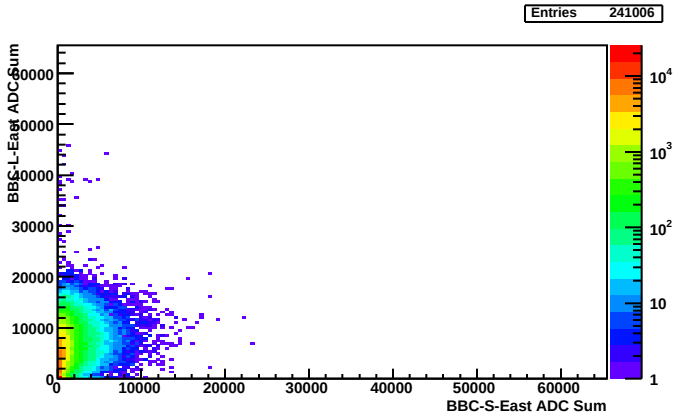

Entries 241006
TOF Mult
BBC-S-West ADC Sum

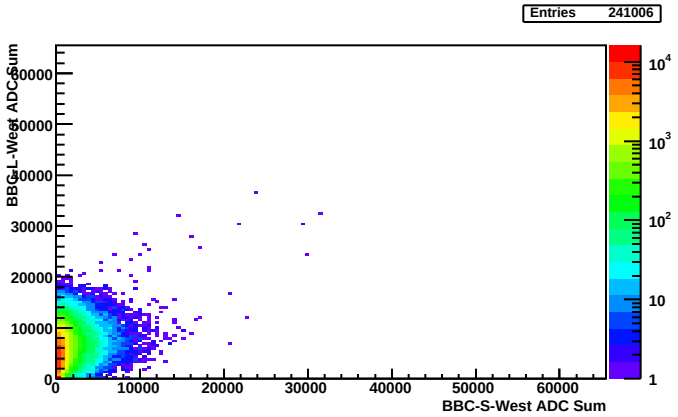

Entries 241006
TOF Mult
VPD-East ADC Sum

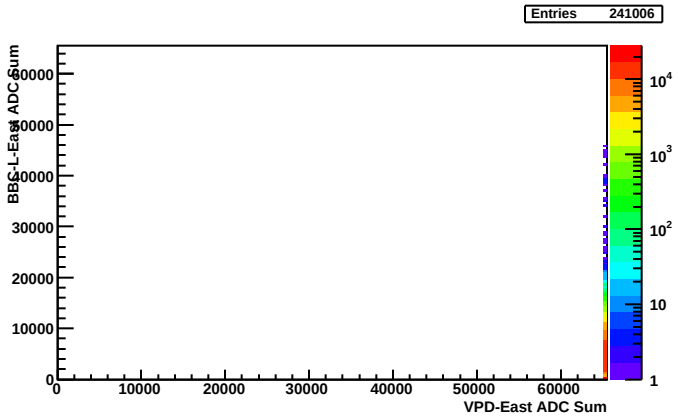

Entries 241006
TOF Mult
VPD-West ADC Sum

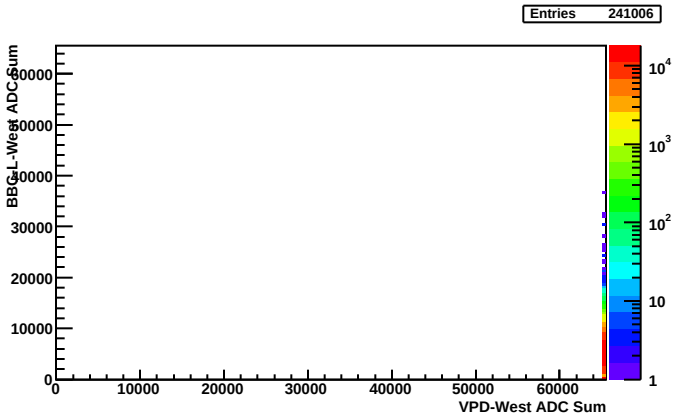

Entries 241006
TOF Mult
ZDC-East ADC Sum Att

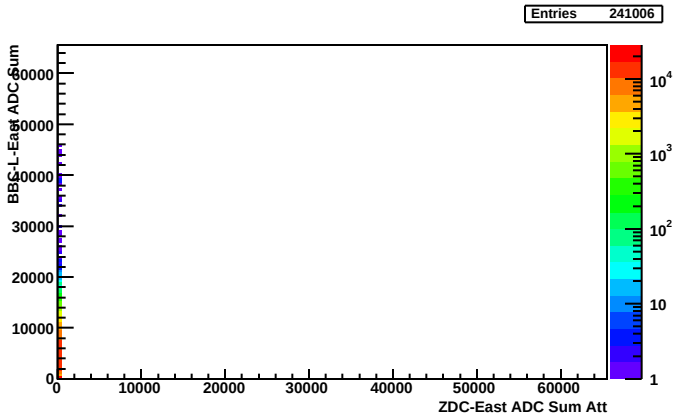

Entries 241006
TOF Mult
ZDC-West ADC Sum Att

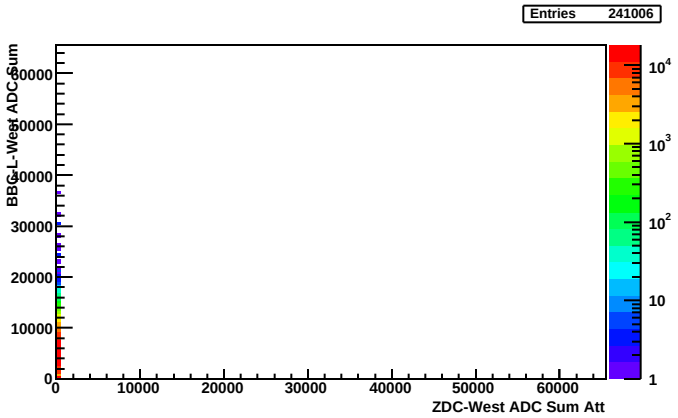

Entries 241006
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 241006
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 241006
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 241006
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 241006
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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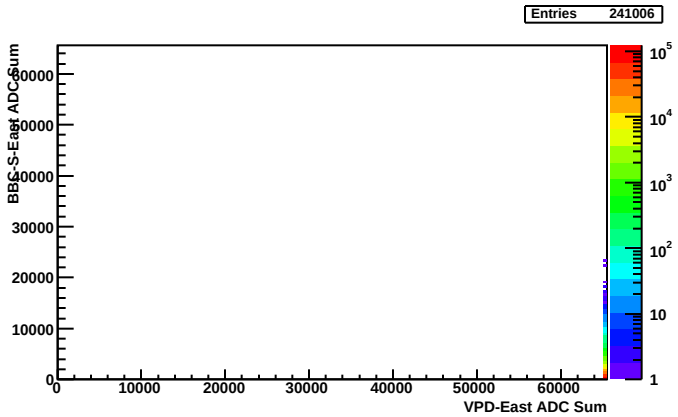
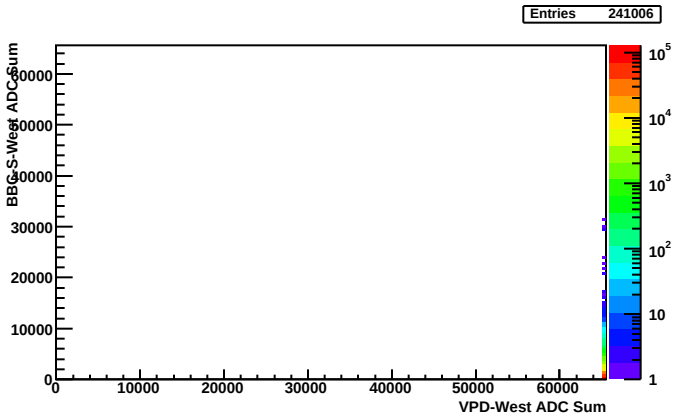
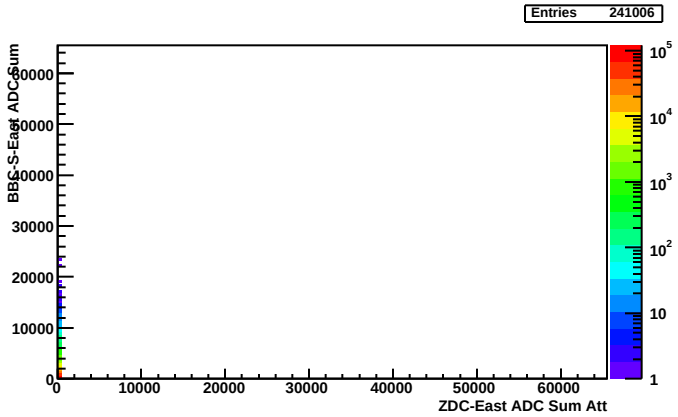
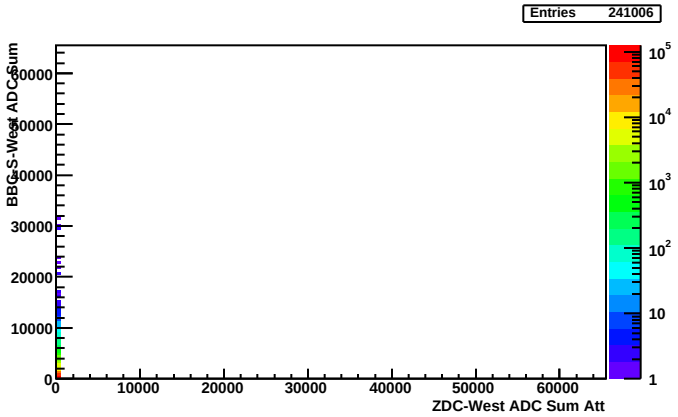
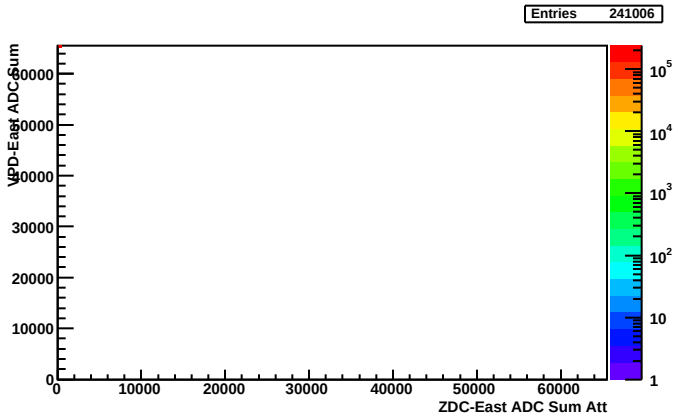
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

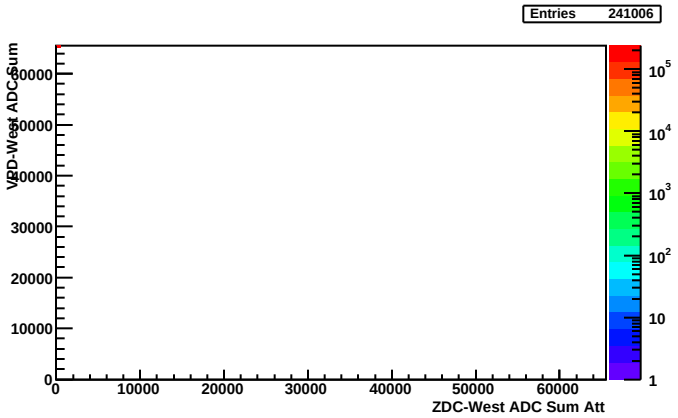

Entries 241006
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 241006
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 241006
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 241006
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 241006
VPD-East ADC Sum
ZDC-East ADC Sum Att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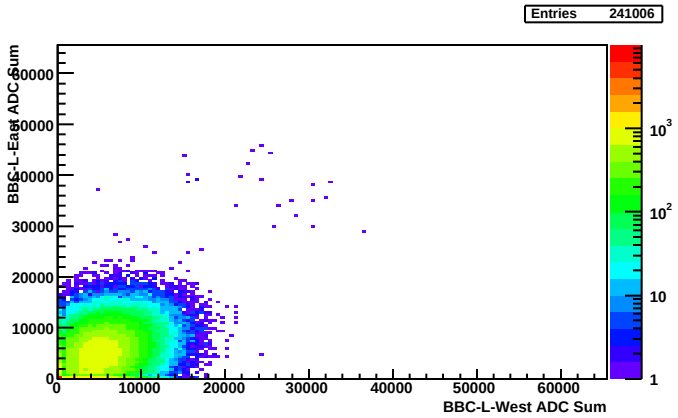
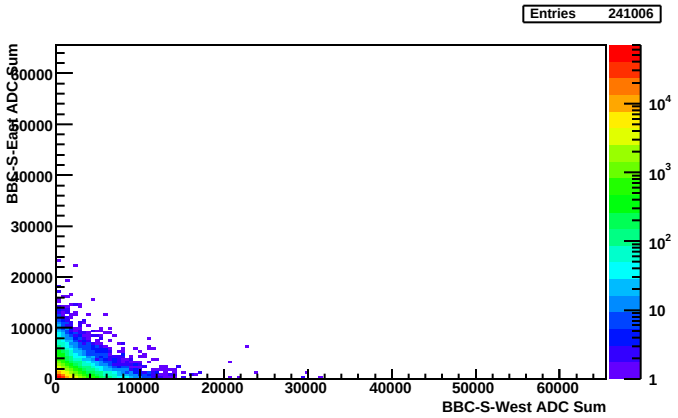
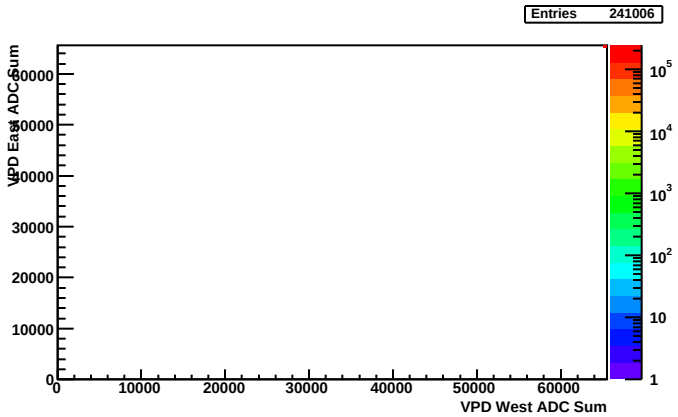
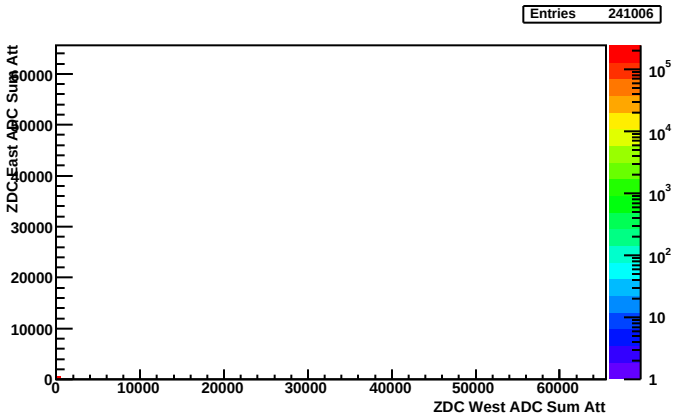
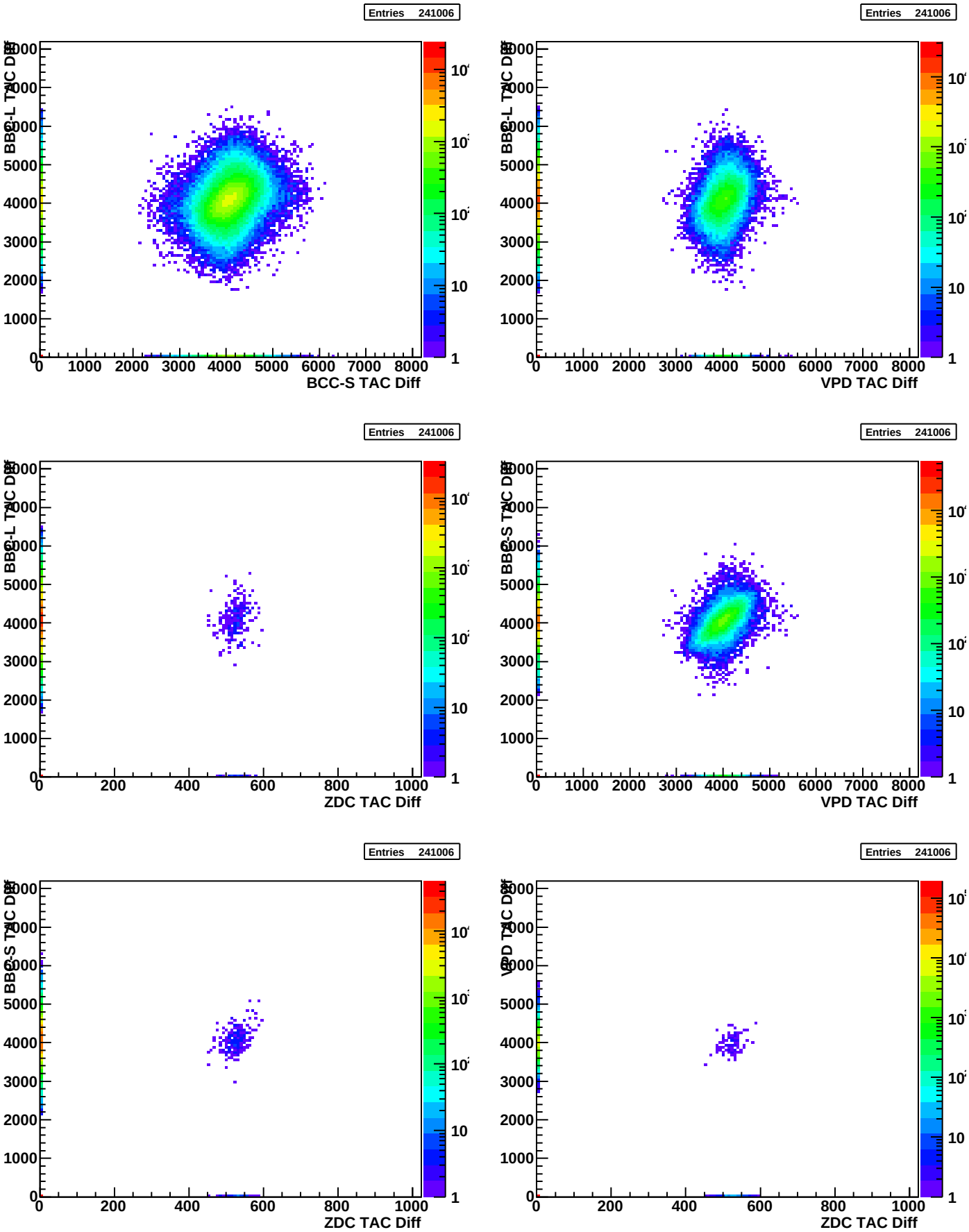
VPD-West ADC Sum
ZDC-West ADC Sum Att

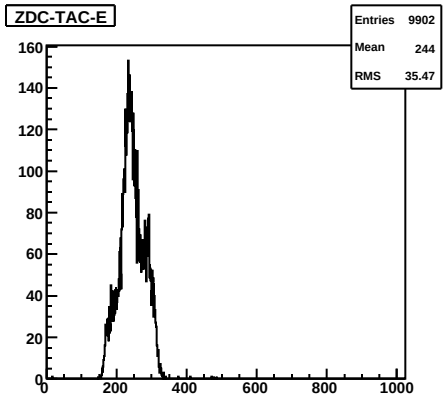

Entries 241006
BBC-L-East ADC Sum
BBC-L-West ADC Sum

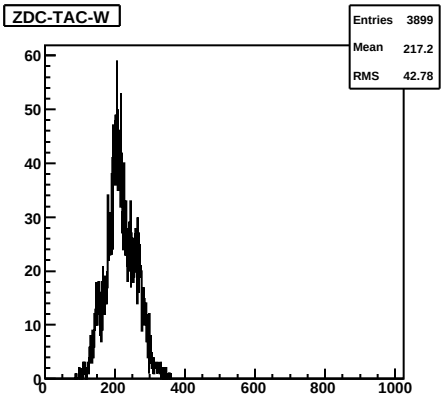

Entries 241006
BBC-S-East ADC Sum
BBC-S-West ADC Sum

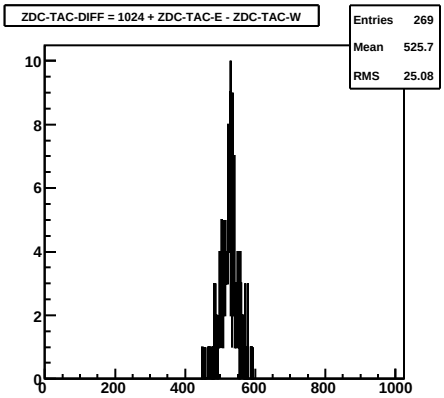

Entries 241006
VPD East ADC Sum
VPD West ADC Sum

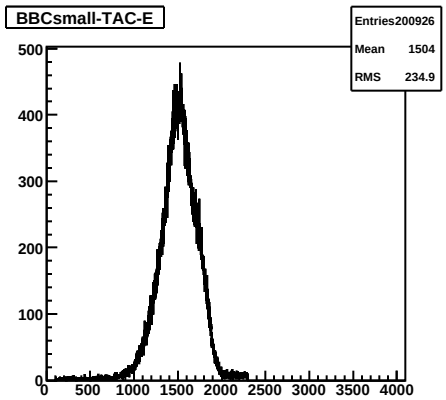

Entries 241006
ZDC East ADC Sum Att
ZDC West ADC Sum Att

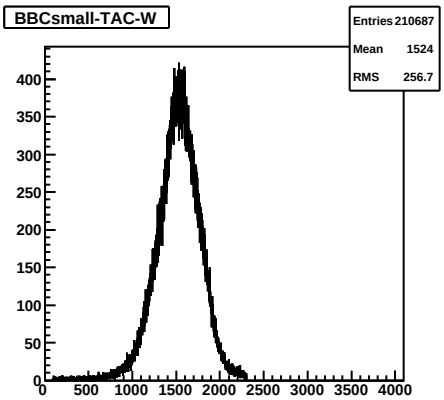

Entries 241006
BBC-L TAC Diff
BCC-S TAC Diff
Entries 241006
BBC-L TAC Diff
VPD TAC Diff
Entries 241006
BBC-L TAC Diff
ZDC TAC Diff
Entries 241006
BBC-S TAC Diff
VPD TAC Diff
Entries 241006
BBC-S TAC Diff
ZDC TAC Diff
Entries 241006
VPD TAC Diff
ZDC TAC Diff

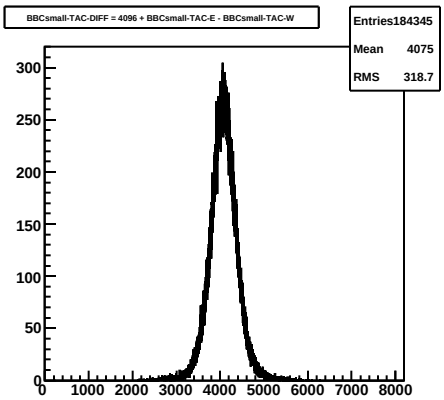

ZDC-TAC-E
Entries 9902
Mean 244
RMS 35.47

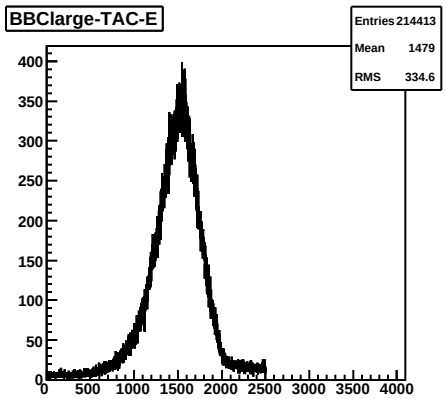

ZDC-TAC-W
Entries 3899
Mean 217.2
RMS 42.78

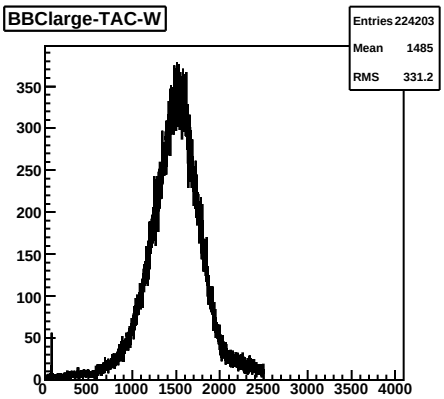

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 269
Mean 525.7
RMS 25.08

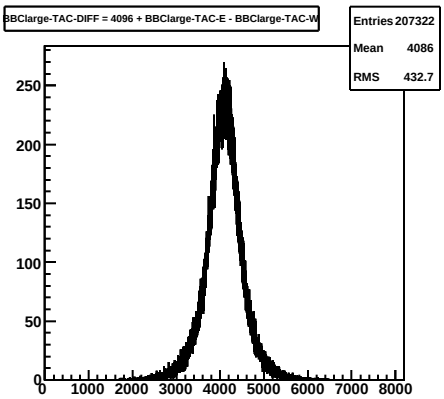

BBCsmall-TAC-E
Entries 200926
Mean 1504
RMS 234.9

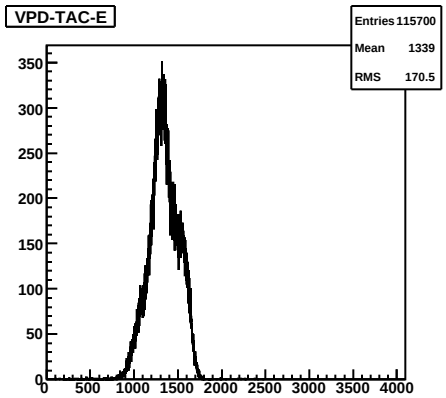

BBCsmall-TAC-W
Entries 210687
Mean 1524
RMS 256.7

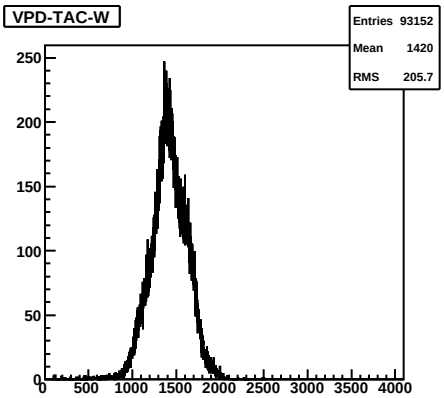

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 184345
Mean 4075
RMS 318.7

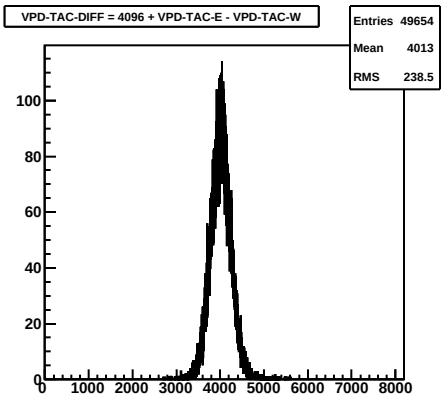

BBClarge-TAC-E
Entries 214413
Mean 1479
RMS 334.6

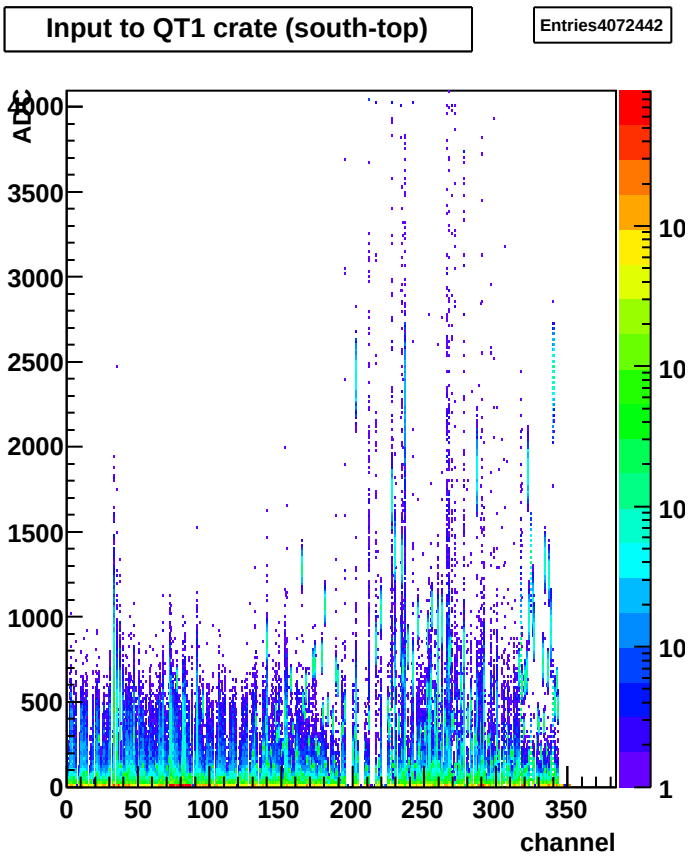

BBClarge-TAC-W
Entries 224203
Mean 1485
RMS 331.2

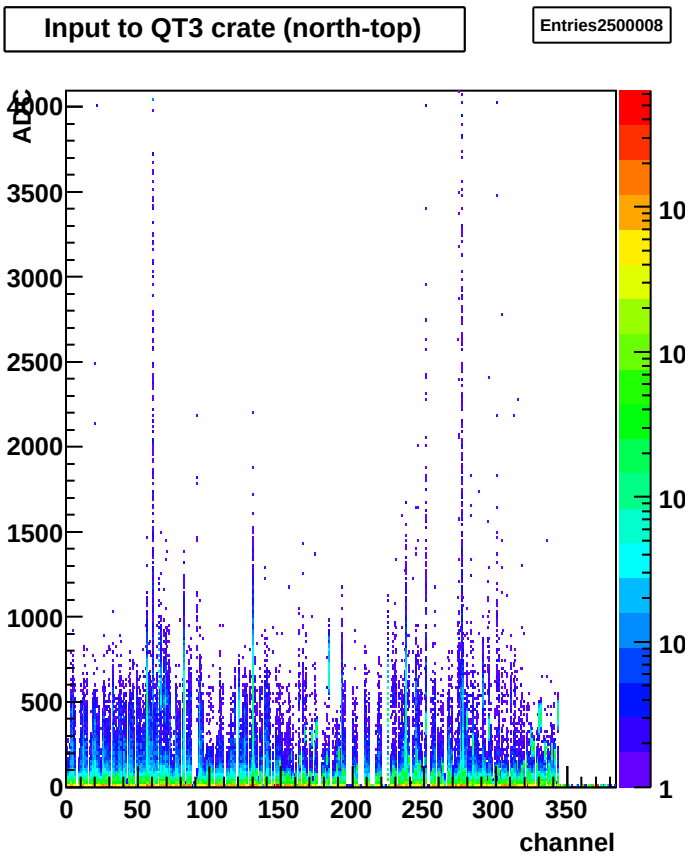

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 207322
Mean 4086
RMS 432.7

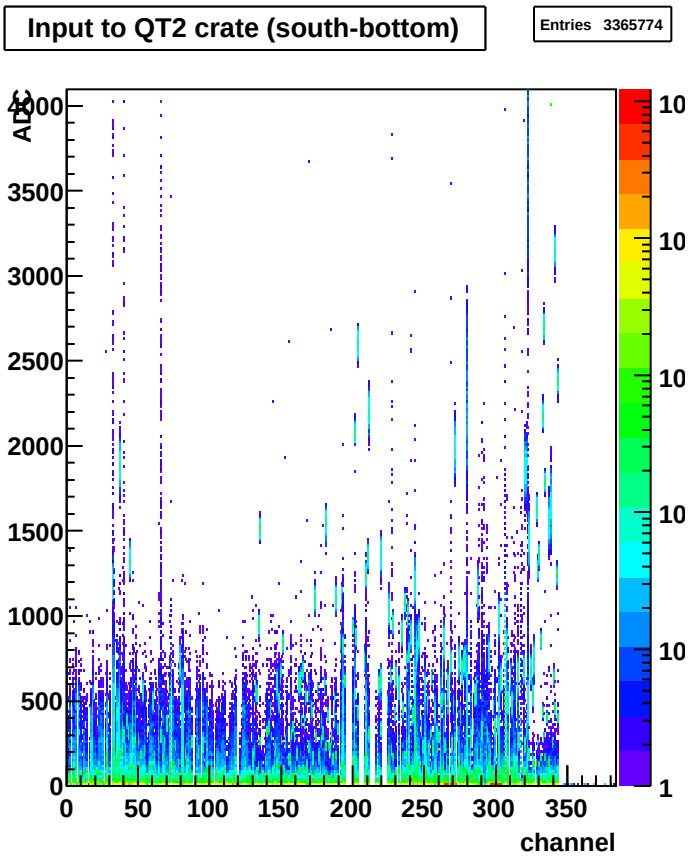

VPD-TAC-E
Entries 115700
Mean 1339
RMS 170.5

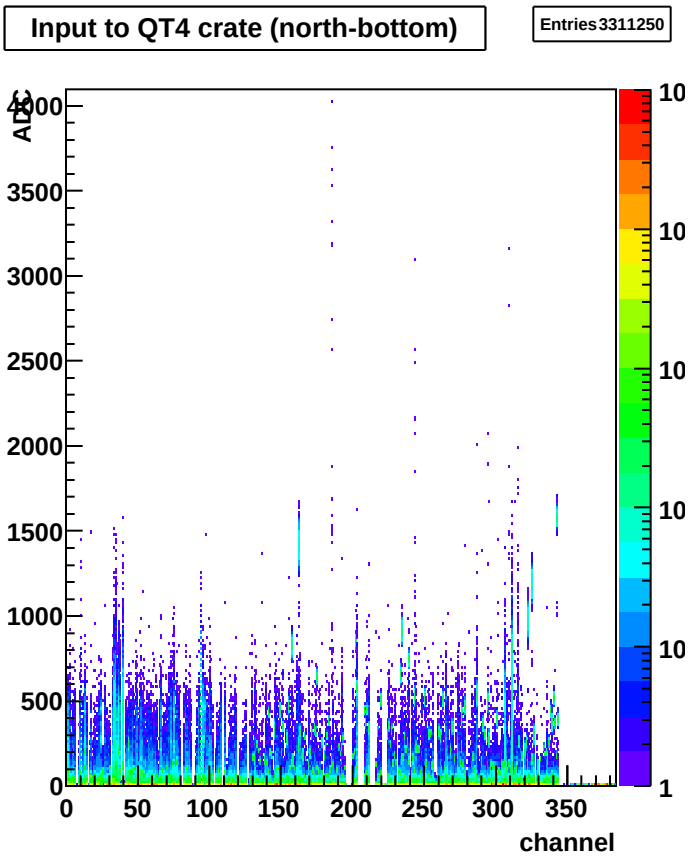

VPD-TAC-W
Entries 93152
Mean 1420
RMS 205.7

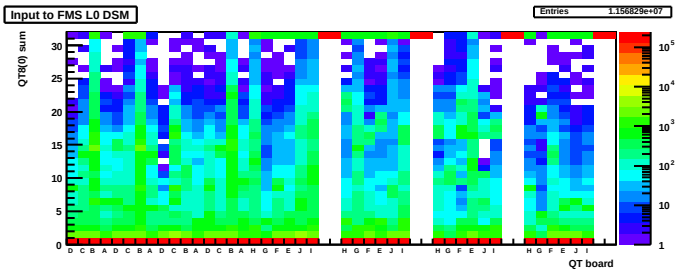

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 49654
Mean 4013
RMS 238.5

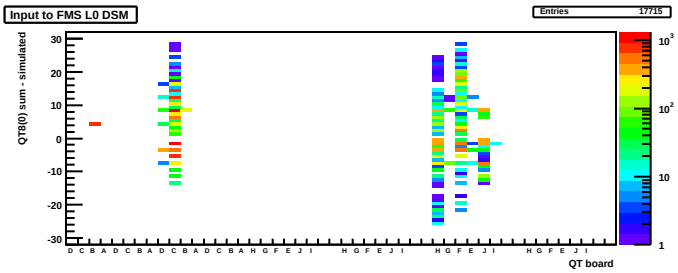

Input to QT1 crate (south-top)
Entries4072442
ADC
channel

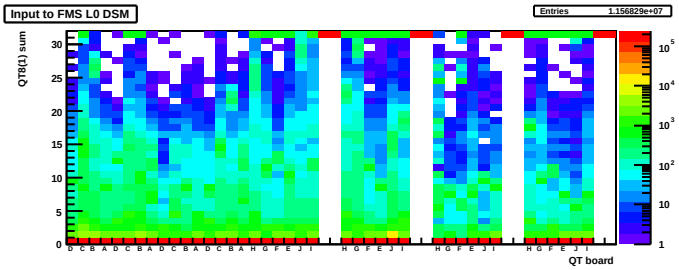

Input to QT3 crate (north-top)
Entries2500008
ADC
channel

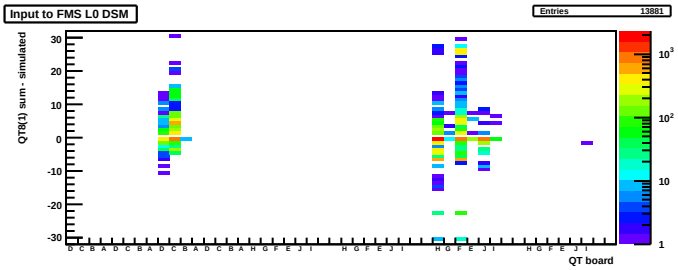

Input to QT2 crate (south-bottom)
Entries 3365774
ADC
channel

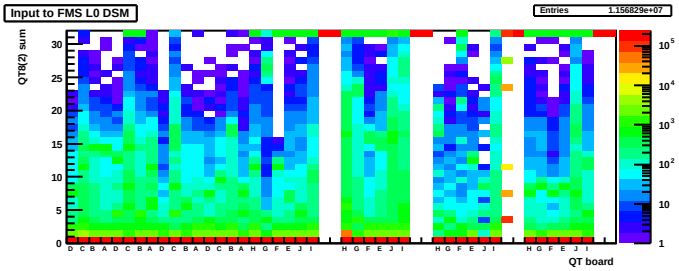

Input to QT4 crate (north-bottom)
Entries 3311250
ADC
channel

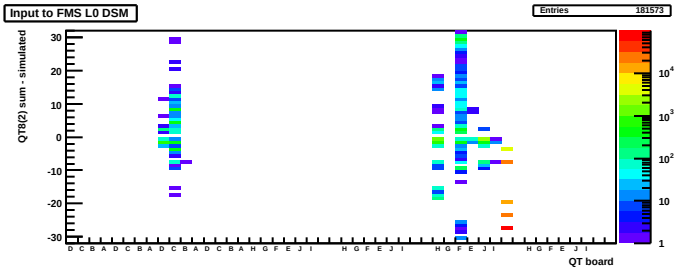

Input to FMS L0 DSM
Entries 1.156829e+07
QT8(0) sum
QT board

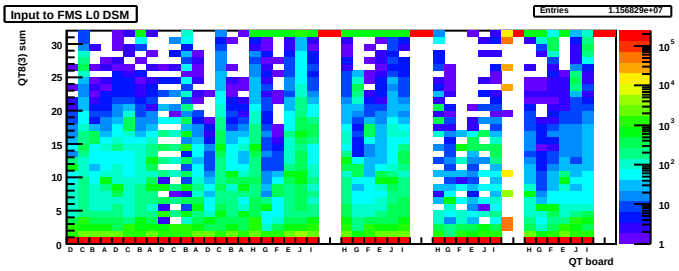

Input to FMS L0 DSM
Entries 17715
QT8(0) sum - simulated
QT board

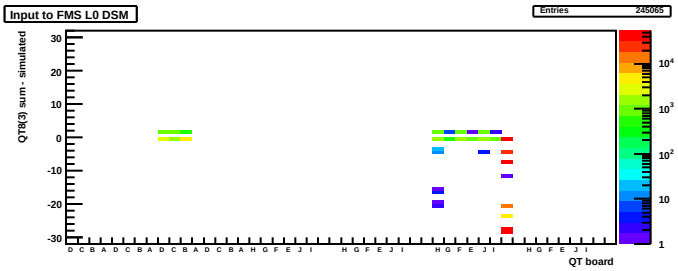

Input to FMS L0 DSM
Entries 1.156829e+07
QT8(1) sum
QT board

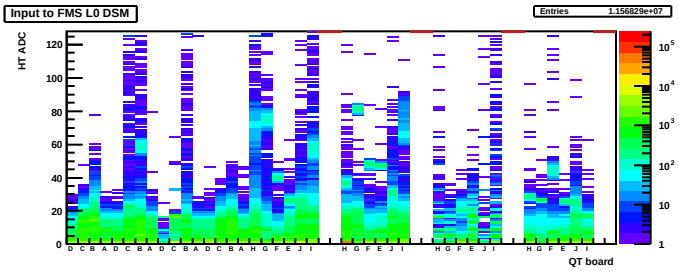

Input to FMS L0 DSM
Entries 13881
QT8(1) sum - simulated
QT board

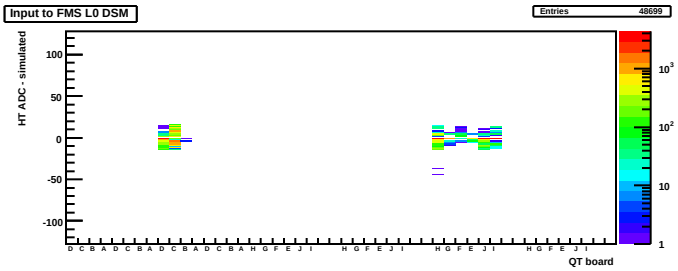

Input to FMS L0 DSM
Entries 1.156829e+07
QT8(2) sum
QT board

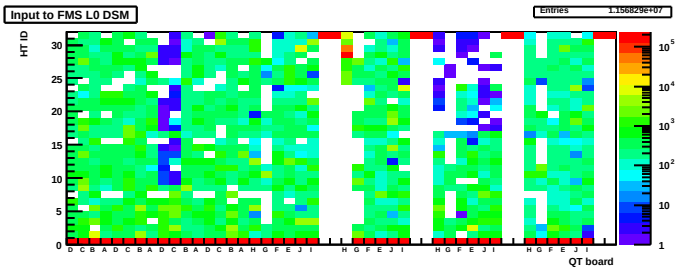

Input to FMS L0 DSM
Entries 181573
QT8(2) sum - simulated
QT board

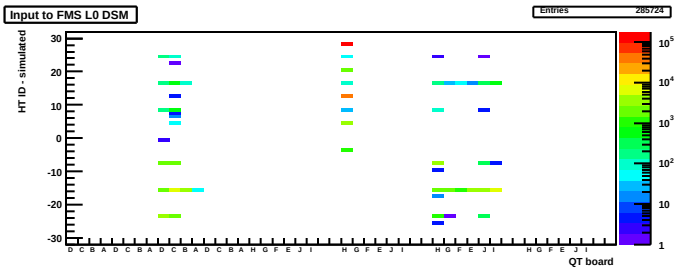

Input to FMS L0 DSM
Entries 1.156829e+07
QT8(3) sum
QT board

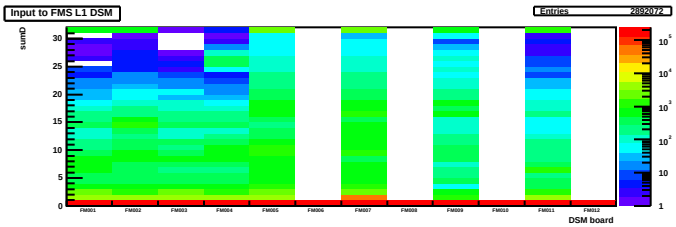

Input to FMS L0 DSM
Entries 245065
QT8(3) sum - simulated
QT board

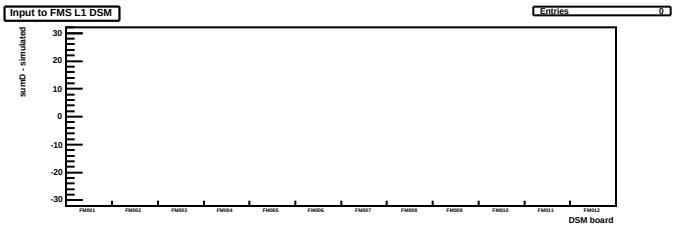

Input to FMS L0 DSM
Entries 1.156829e+07
HT ADC
QT board

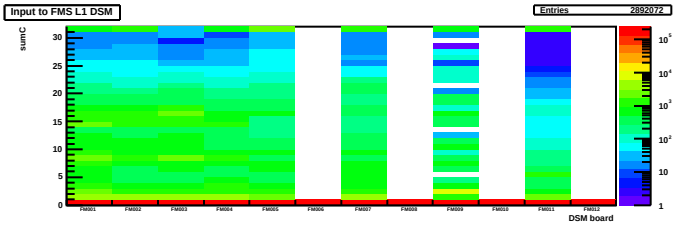

Input to FMS L0 DSM
Entries 48699
HT ADC - simulated
QT board

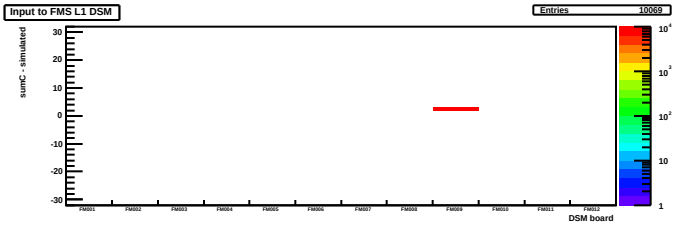

Input to FMS L0 DSM
Entries 1.156829e+07
HT ID
QT board

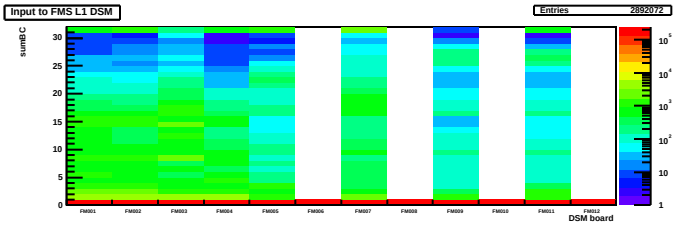

Input to FMS L0 DSM
Entries 285724
HT ID - simulated
QT board

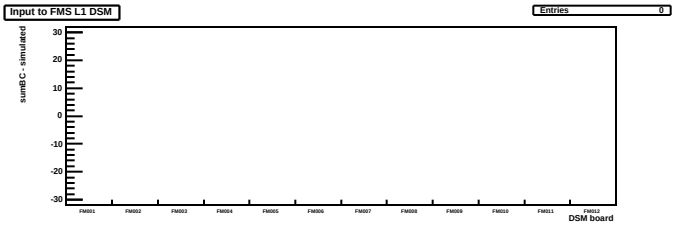

Input to FMS L1 DSM
Entries 2892072
sumD
DSM board

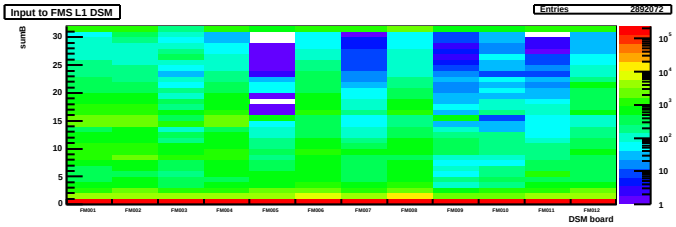

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

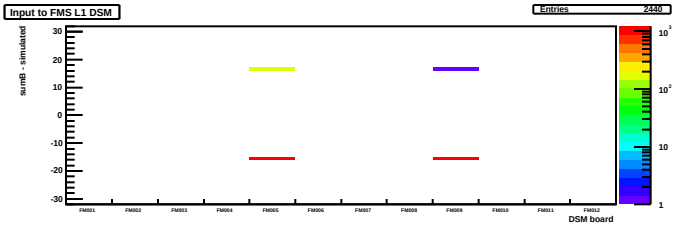

Input to FMS L1 DSM
Entries 2892072
sumC
DSM board

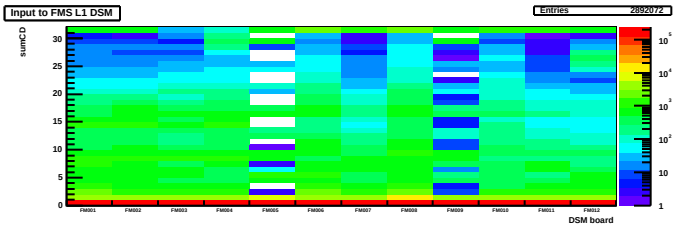

Input to FMS L1 DSM
Entries 10069
sumC - simulated
DSM board

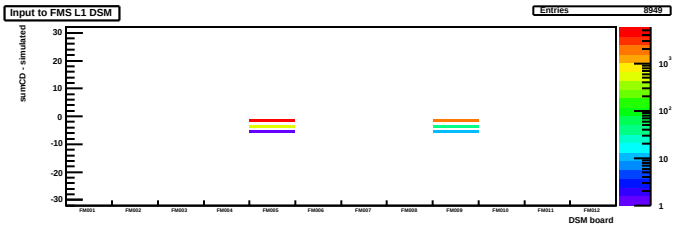

Input to FMS L1 DSM
Entries 2892072
sumBC
DSM board

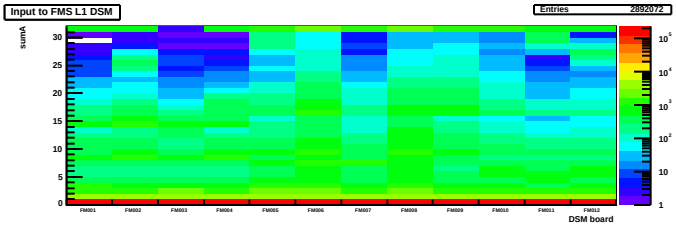

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

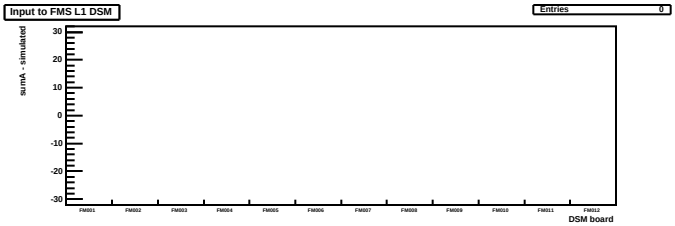

Input to FMS L1 DSM
Entries 2892072
sumB
DSM board

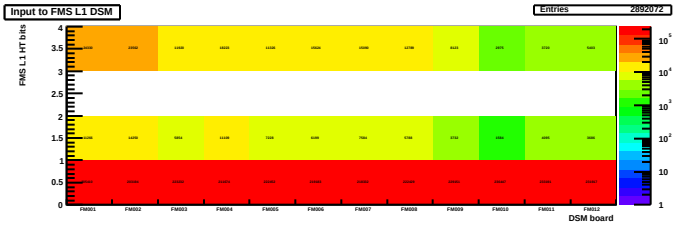

Input to FMS L1 DSM
Entries 2440
sumB - simulated
DSM board

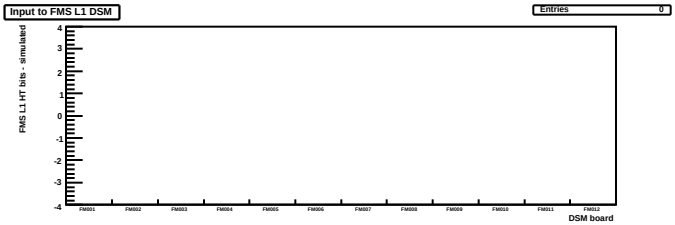

Input to FMS L1 DSM
Entries 2892072
sumCD
DSM board

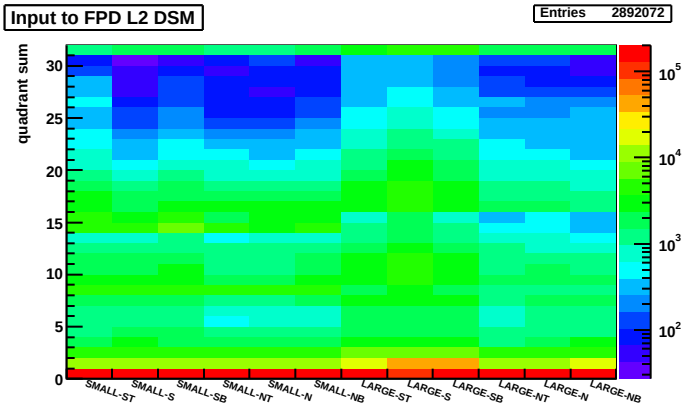

Input to FMS L1 DSM
Entries 8949
sumCD - simulated
DSM board

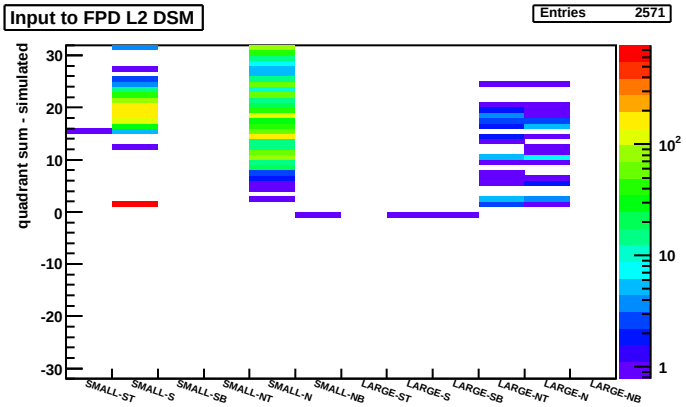

Input to FMS L1 DSM
Entries 2892072
sumA
DSM board

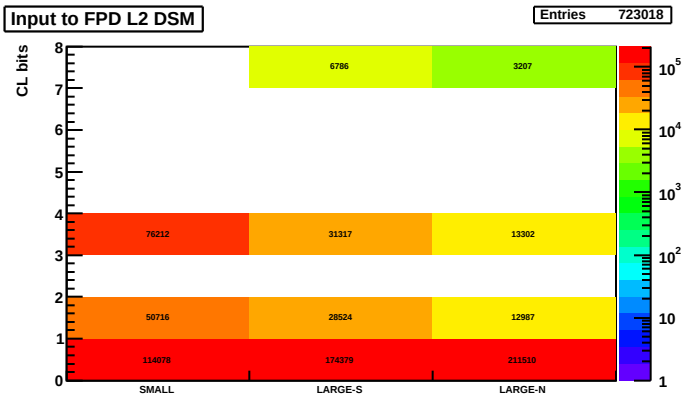

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

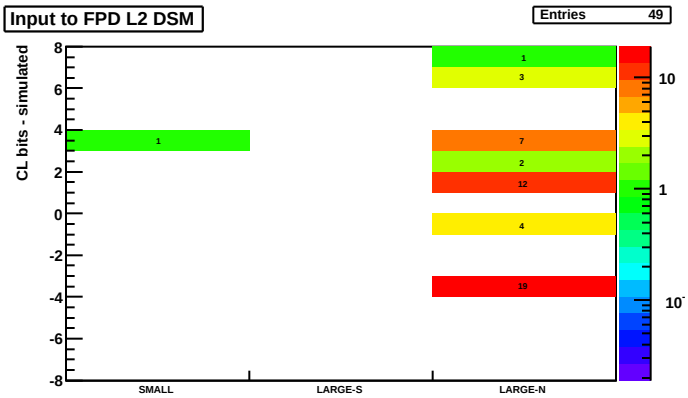

Input to FMS L1 DSM
Entries 2892072
FMS L1 HT bits
DSM board

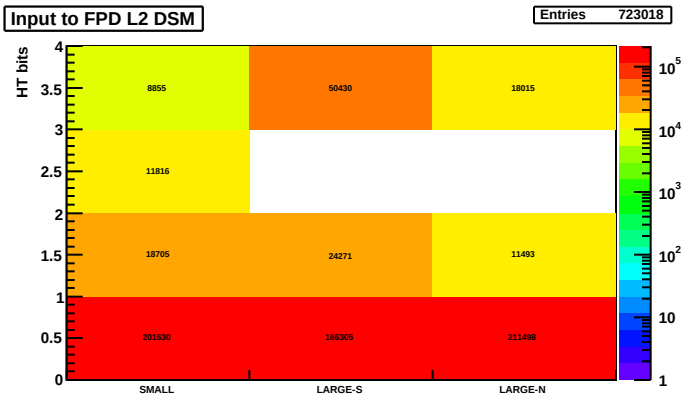

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

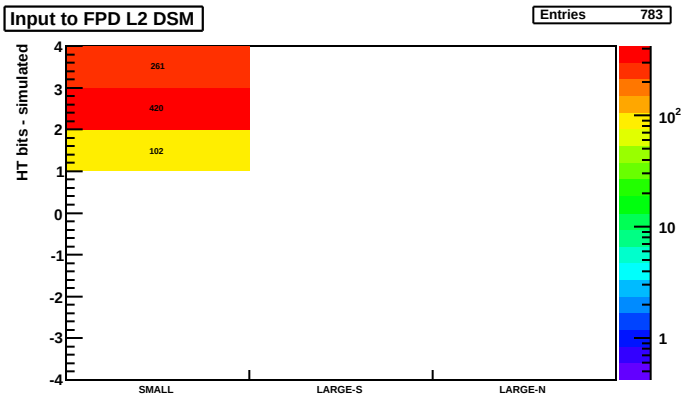

Input to FPD L2 DSM
Entries 2892072
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

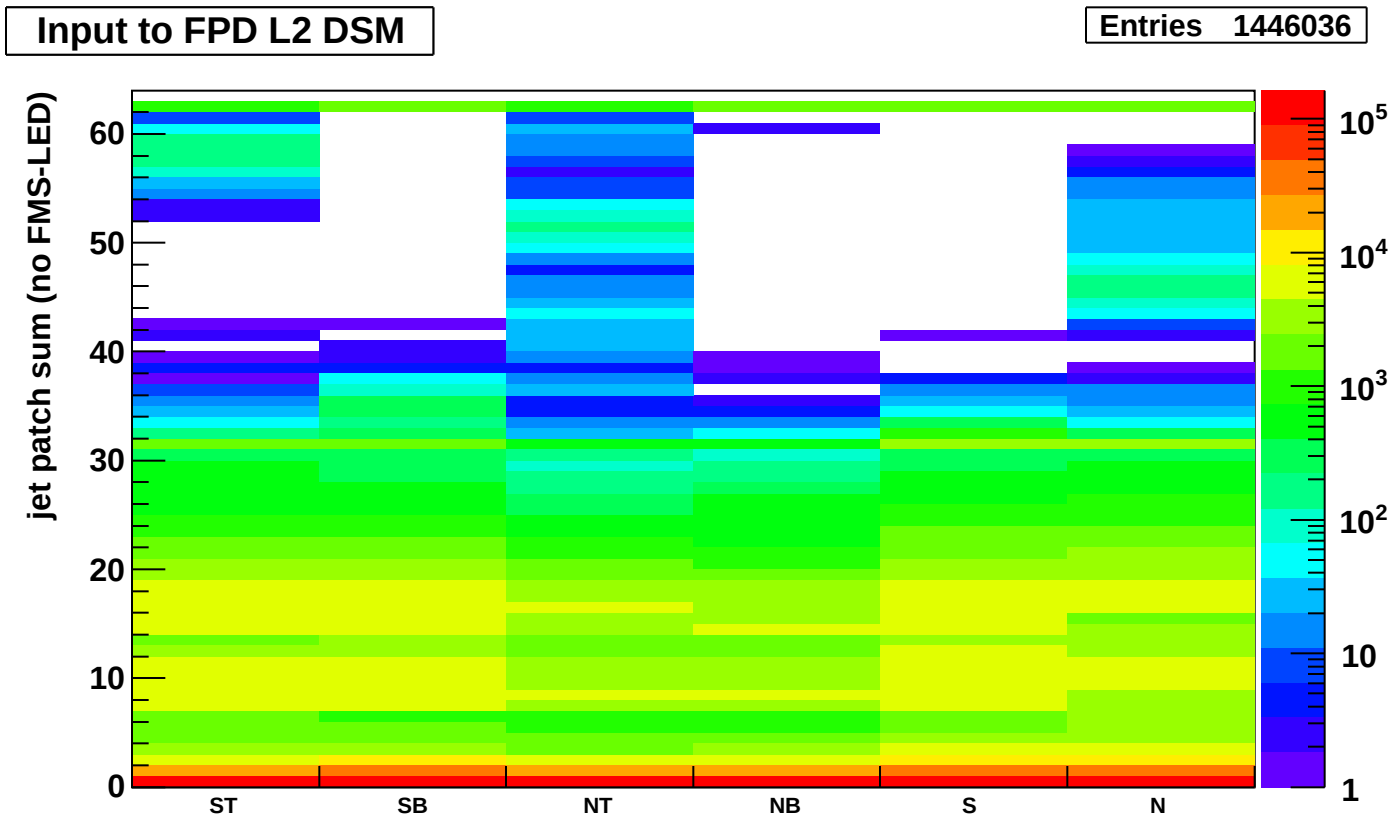

Input to FPD L2 DSM
Entries 2571
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

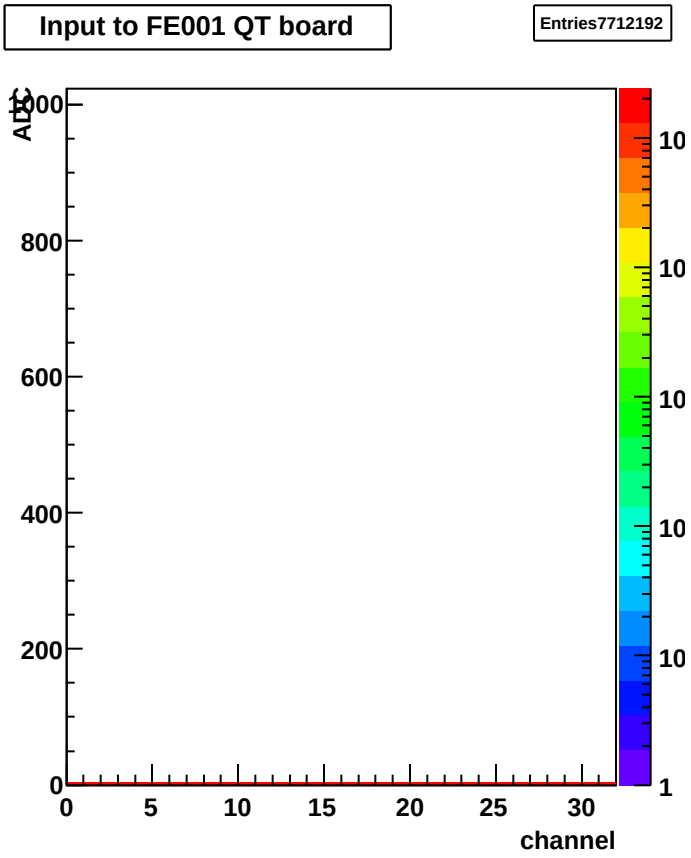

Input to FPD L2 DSM
Entries 723018
CL bits
6786
3207
76212
31317
13302
50716
28524
12987
114078
174379
211510
SMALL
LARGE-S
LARGE-N

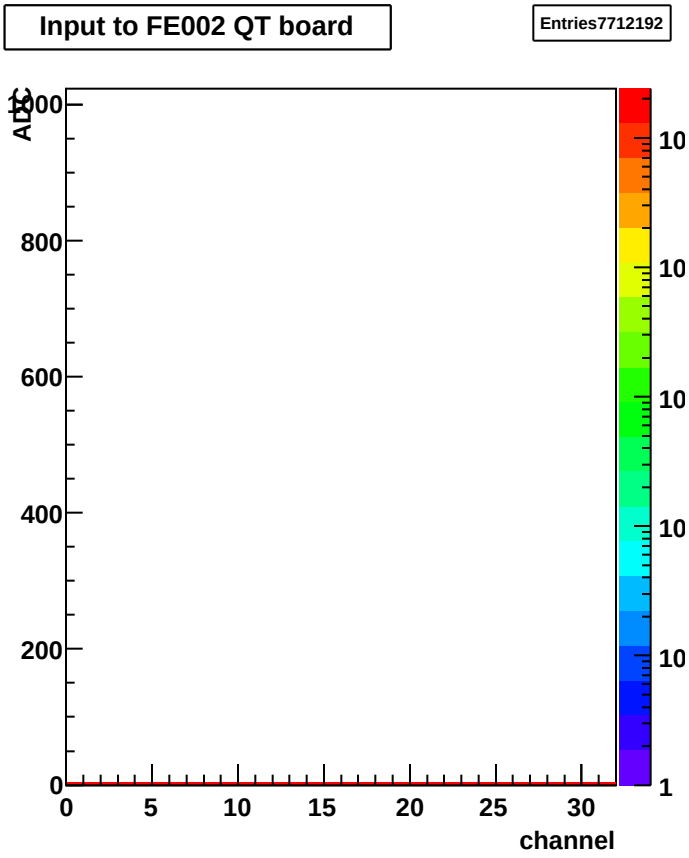

Input to FPD L2 DSM
Entries 49
CL bits - simulated
1
3
1
7
2
12
4
19
SMALL
LARGE-S
LARGE-N

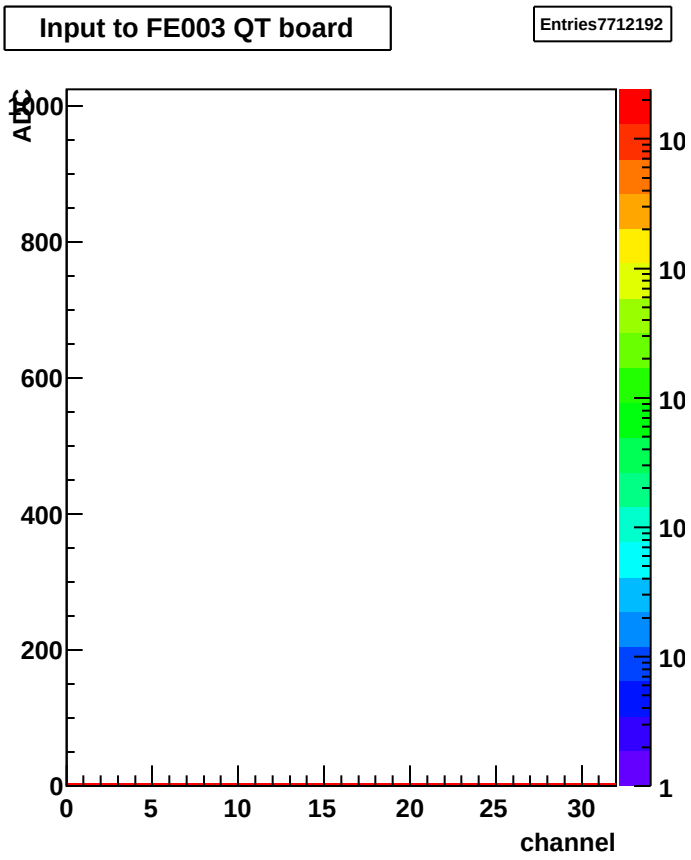

Input to FPD L2 DSM
Entries 723018
HT bits
8855
50430
18015
11816
18705
24271
11493
201630
166305
211498
SMALL
LARGE-S
LARGE-N

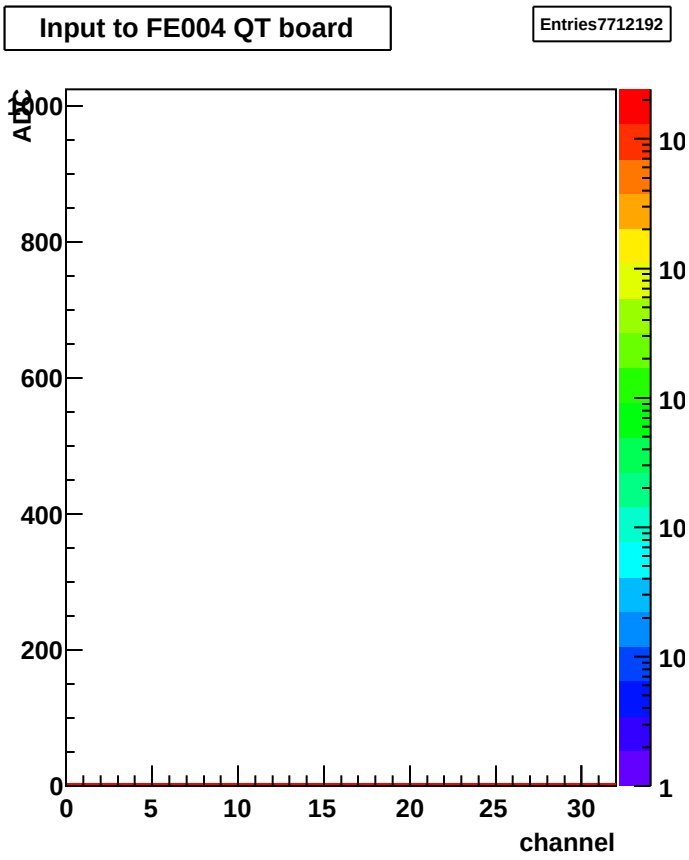

Input to FPD L2 DSM
Entries 783
HT bits - simulated
261
420
102
SMALL
LARGE-S
LARGE-N

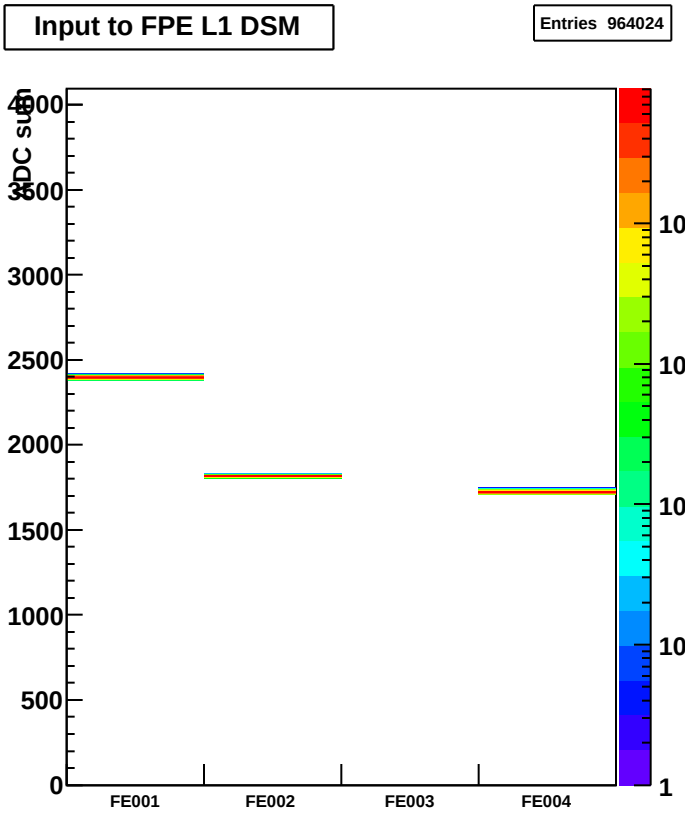

Input to FPD L2 DSM
Entries 1446036
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

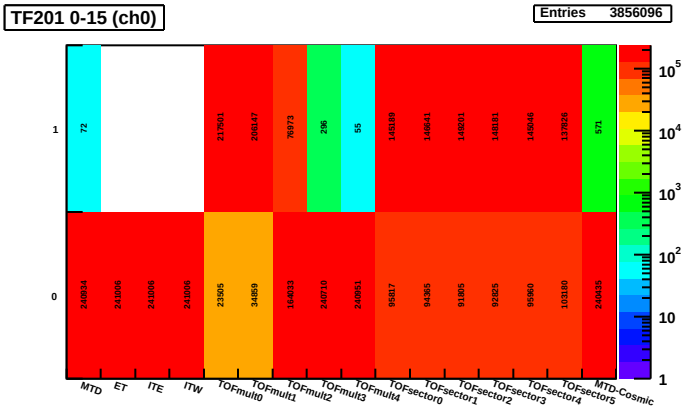

Input to FE001 QT board
Entries7712192
ADC
channel

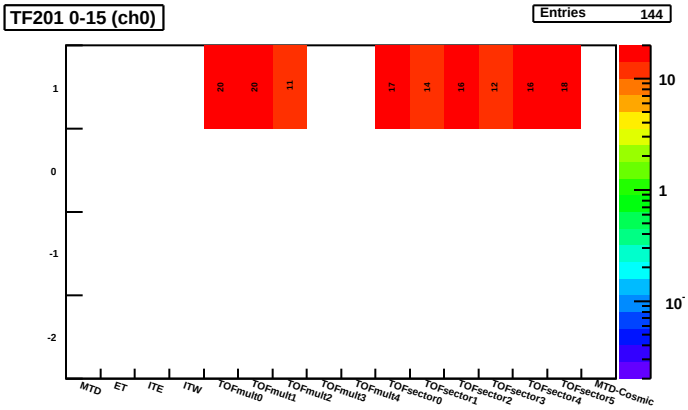

Input to FE002 QT board
Entries7712192
ADC
channel

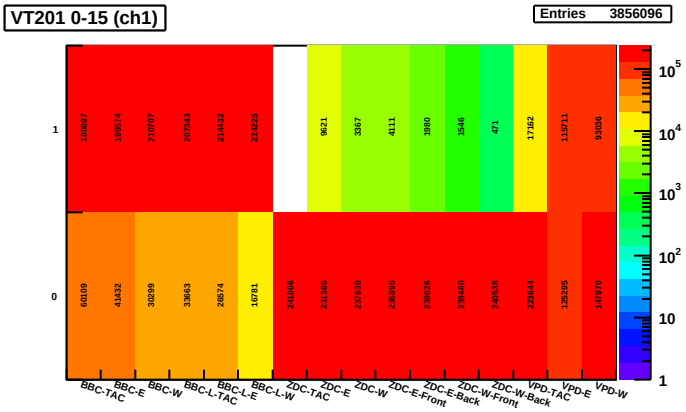

Input to FE003 QT board
Entries7712192
ADC
channel

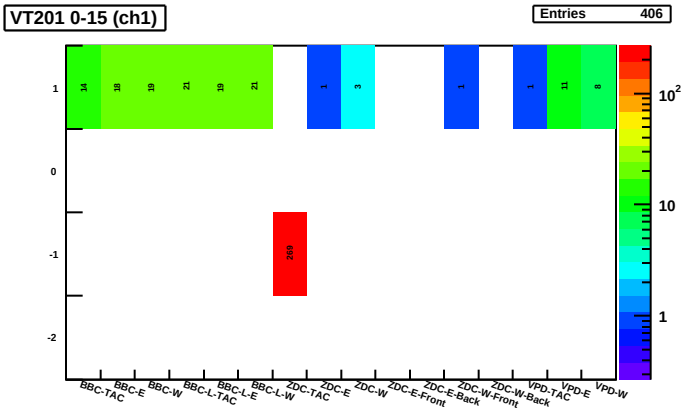

Input to FE004 QT board
Entries7712192
ADC
channel

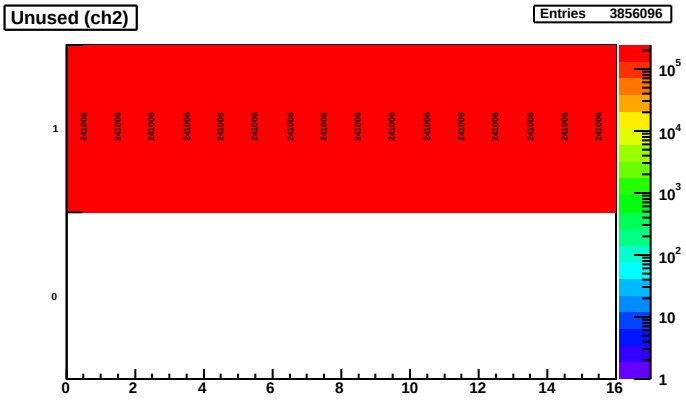

Input to FPE L1 DSM
Entries 964024
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

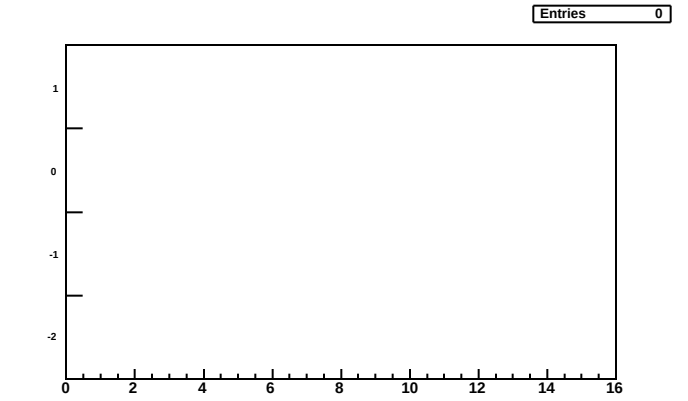

TF201 0-15 (ch0)
Entries 3856096
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 144
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3856096
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 406
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3856096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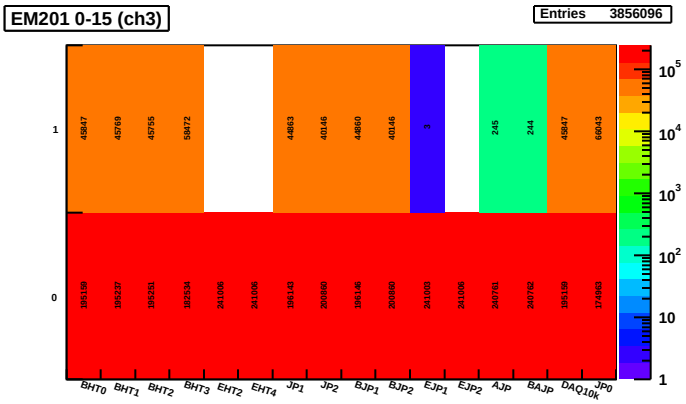
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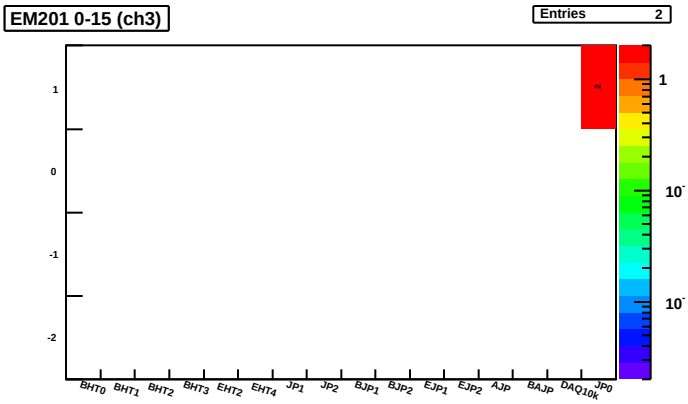
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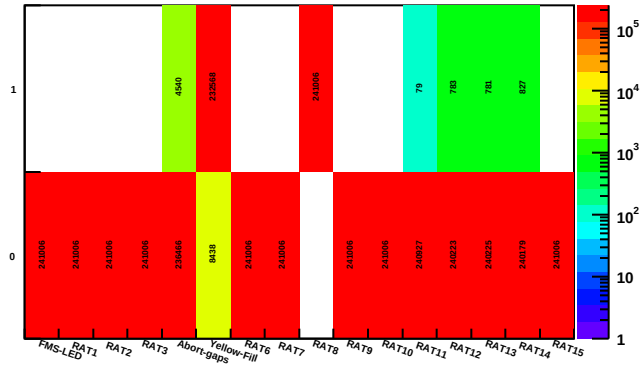
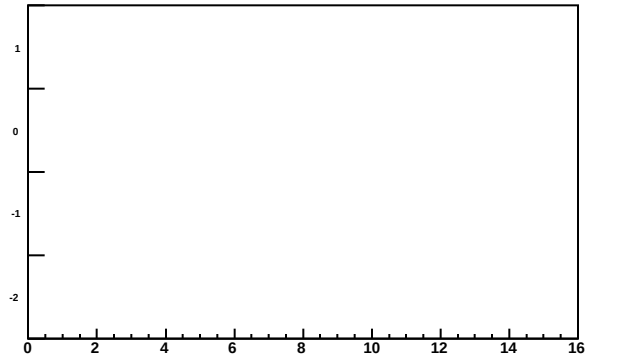
RAT board (ch4)





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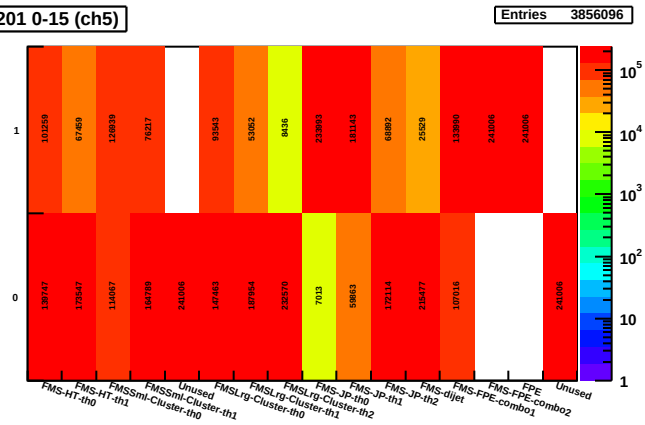

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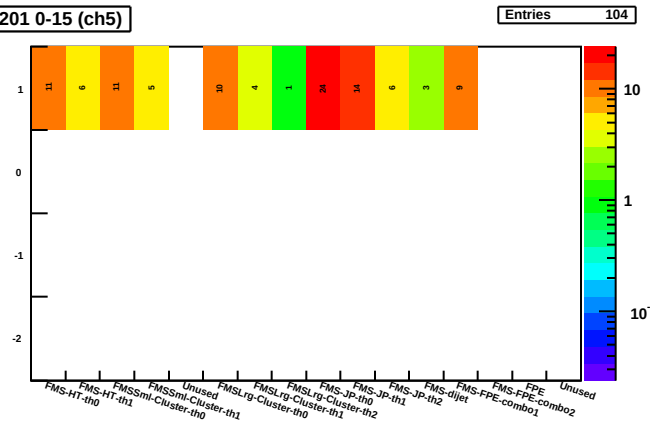

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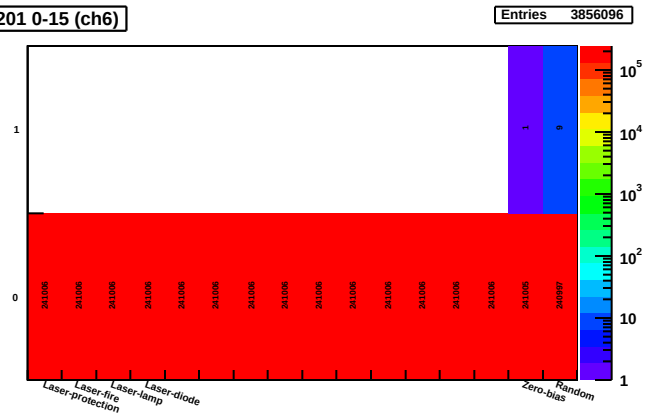

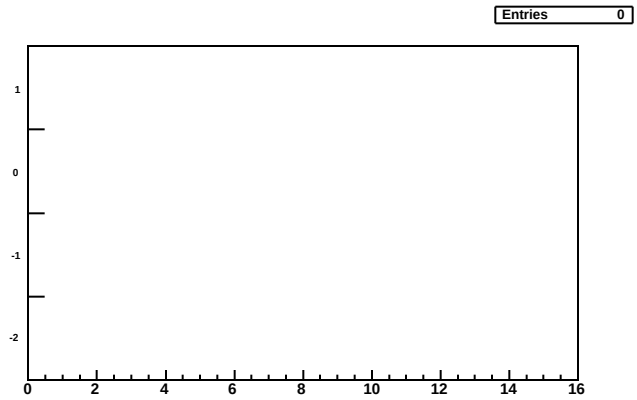
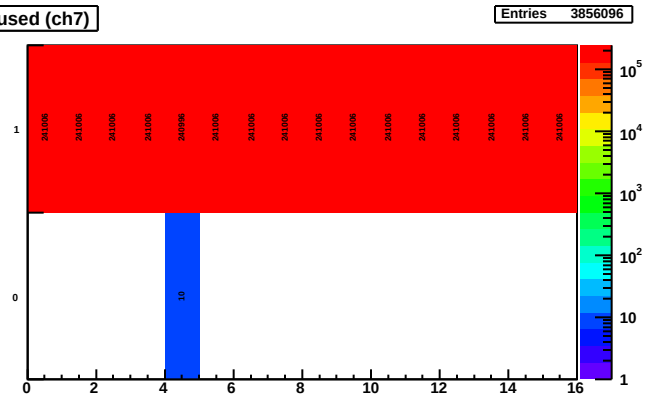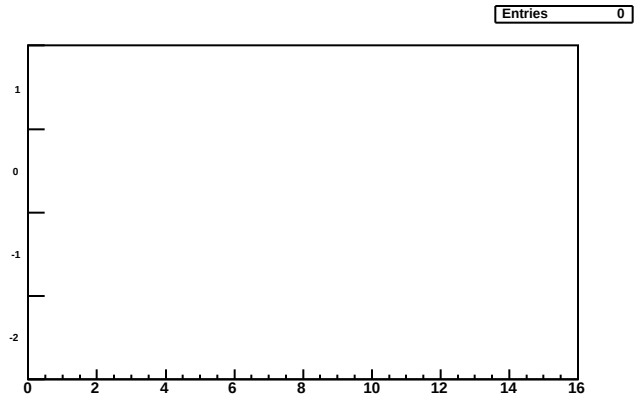
Unused (ch7)