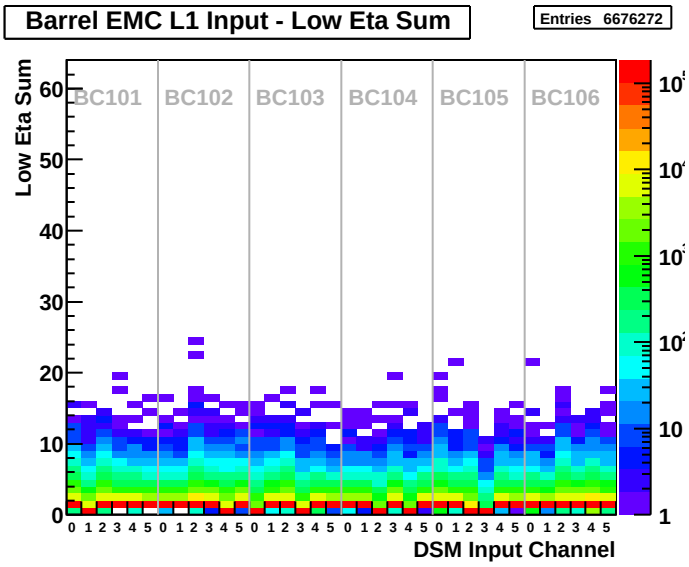
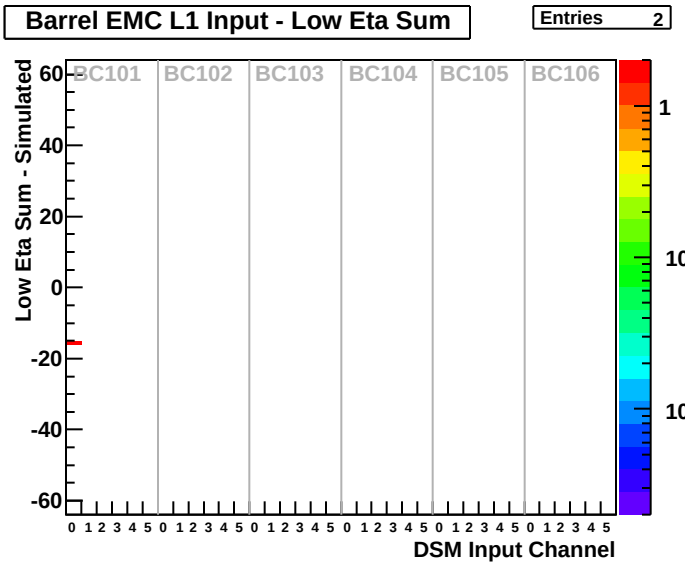
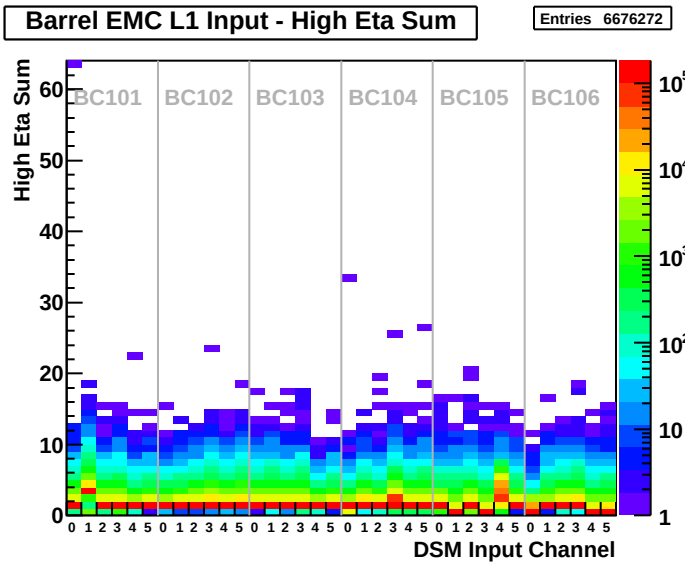
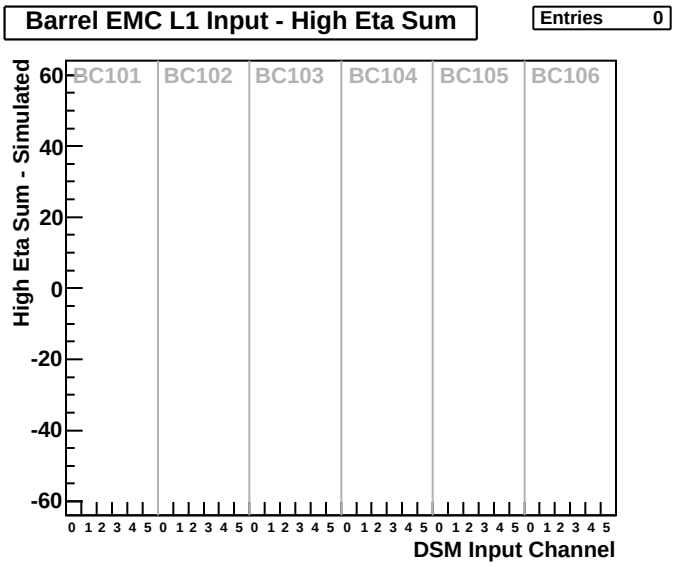
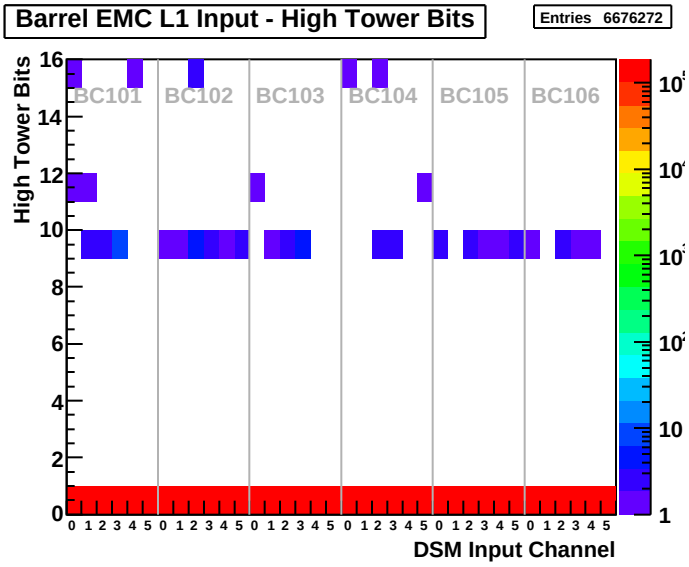
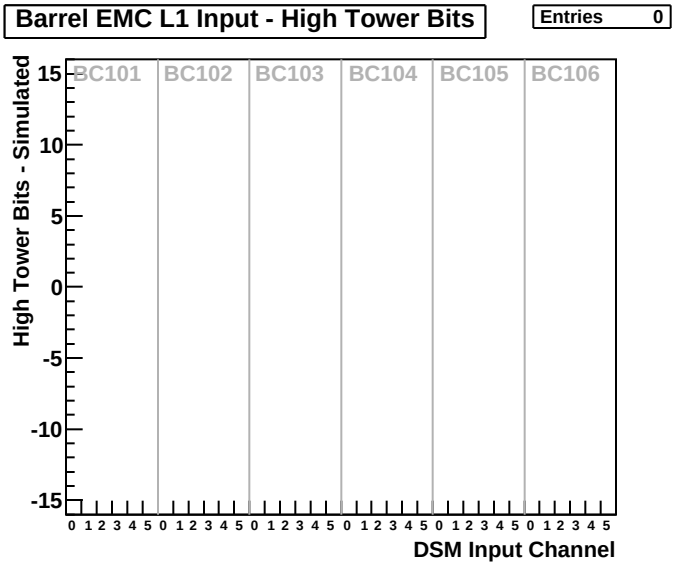

Barrel EMC L1 Input - Low Eta Sum
Entries 6676272
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

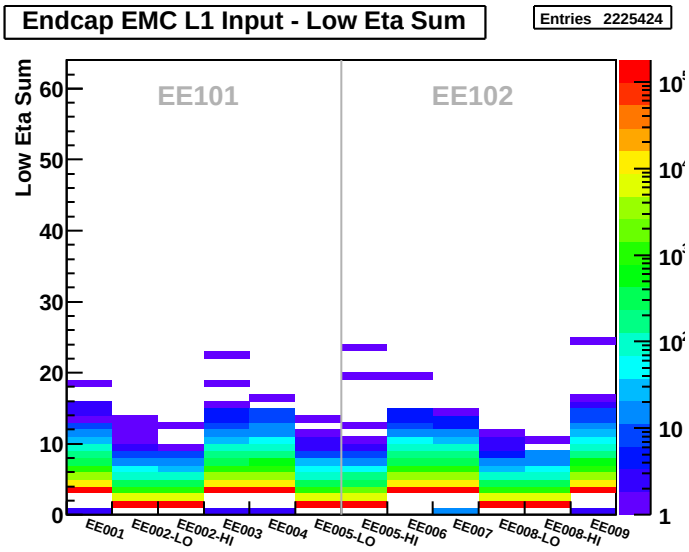

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

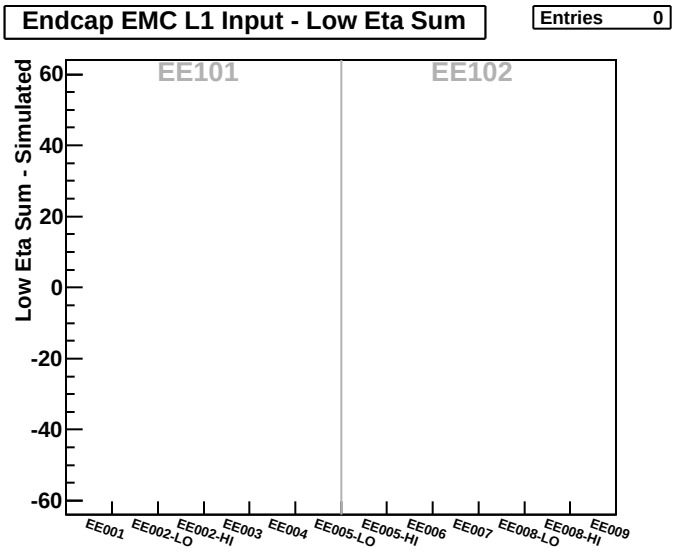

Barrel EMC L1 Input - High Eta Sum
Entries 6676272
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

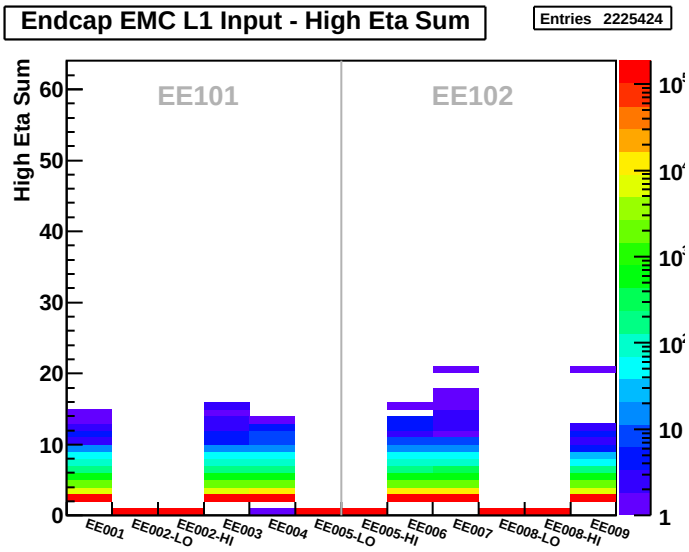

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

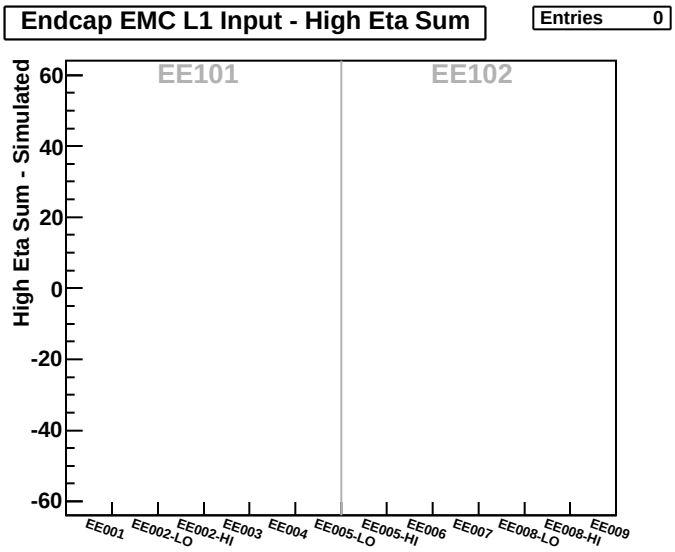

Barrel EMC L1 Input - High Tower Bits
Entries 6676272
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

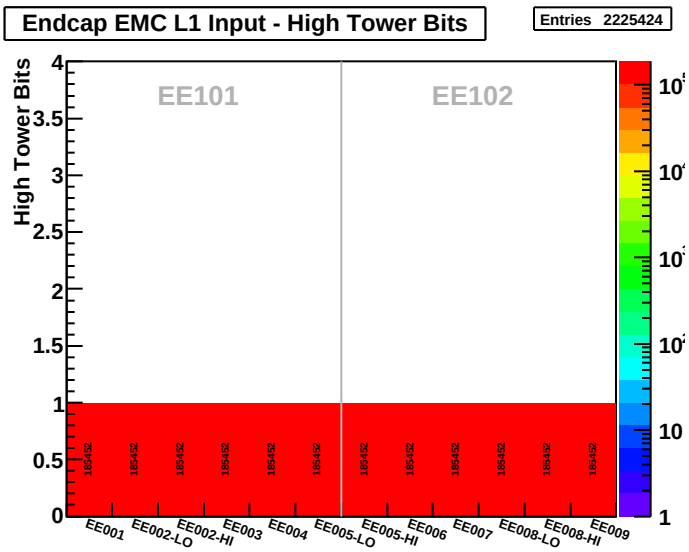

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

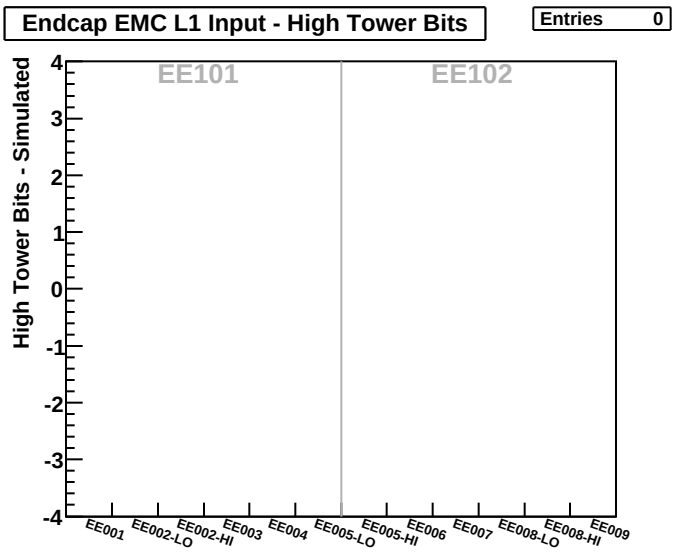

Endcap EMC L1 Input - Low Eta Sum
Entries 2225424
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

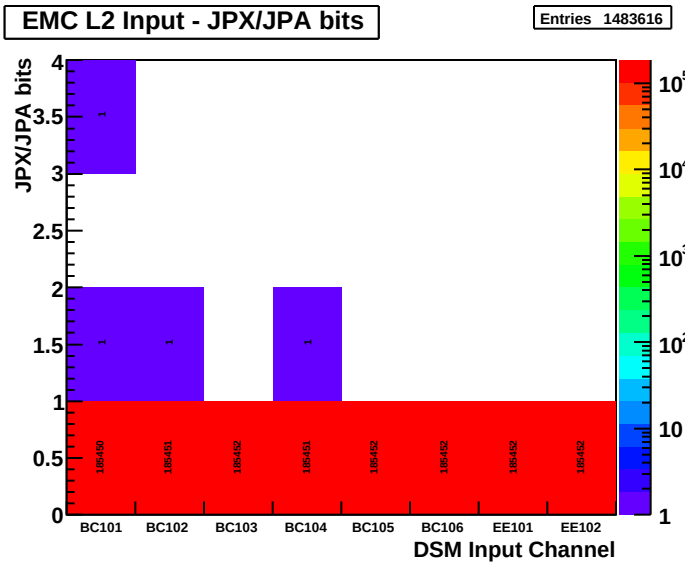

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

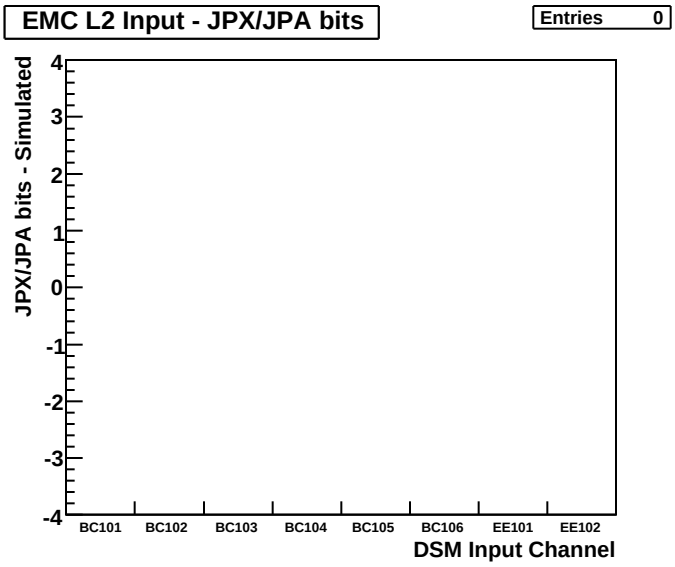

Endcap EMC L1 Input - High Eta Sum
Entries 2225424
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

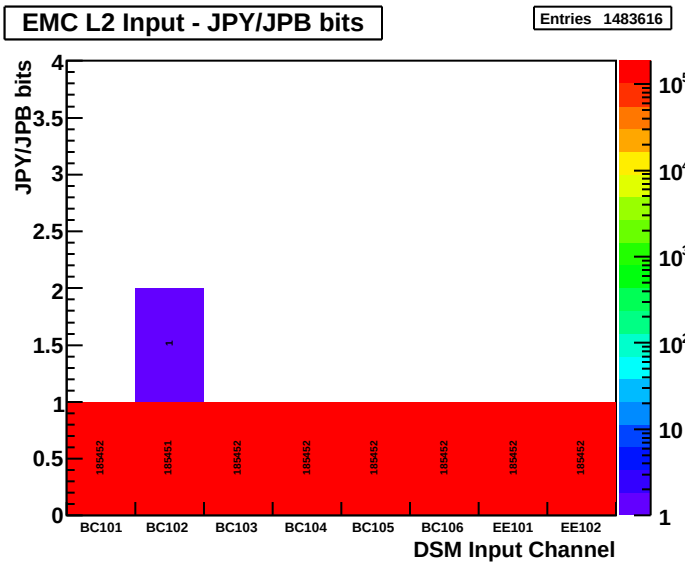

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

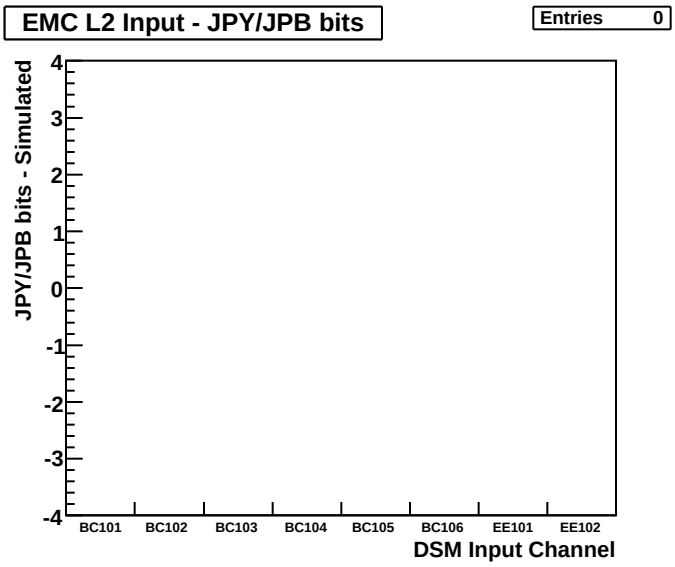

Endcap EMC L1 Input - High Tower Bits
Entries 2225424
High Tower Bits
EE101
EE102
185452
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

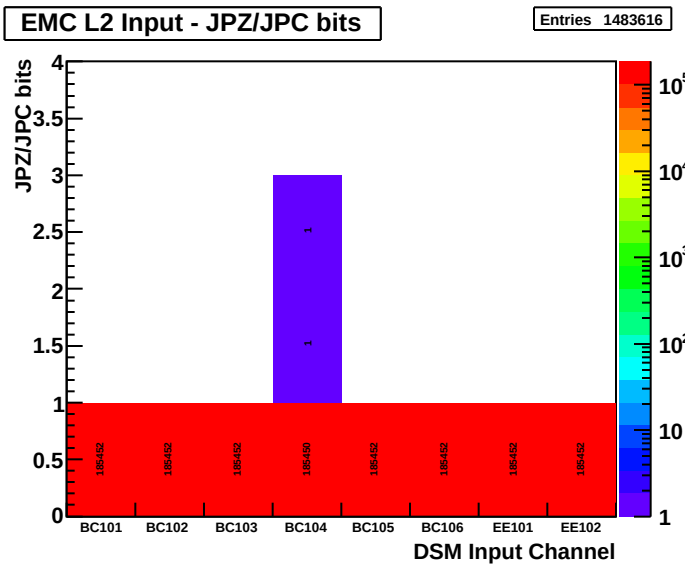

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

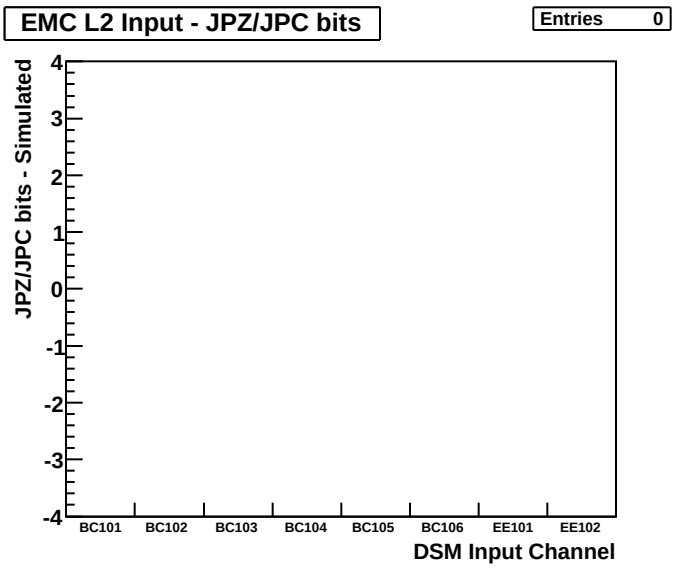

EMC L2 Input - JPX/JPA bits
Entries 1483616
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

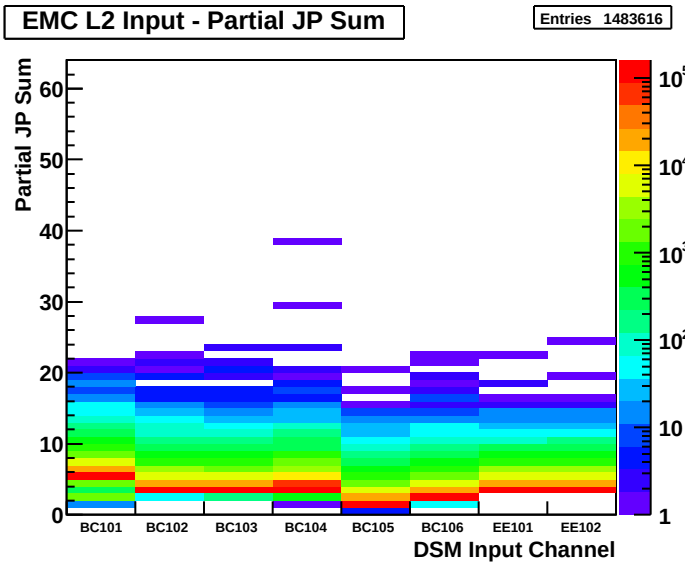

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

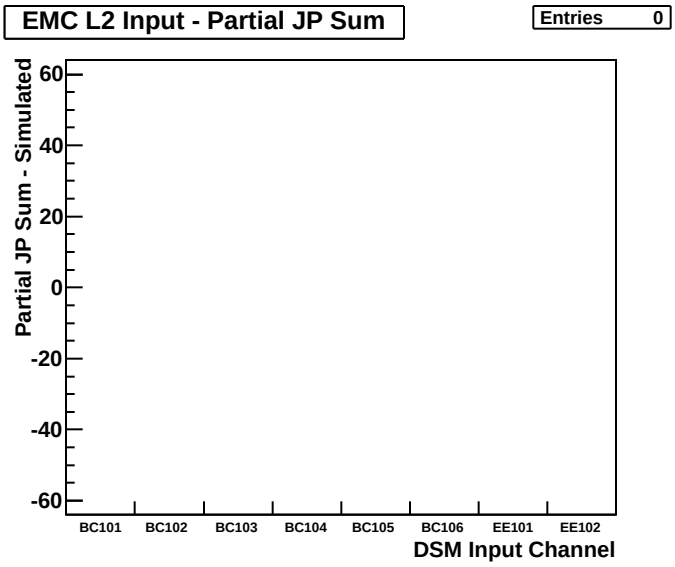

EMC L2 Input - JPY/JPB bits
Entries 1483616
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

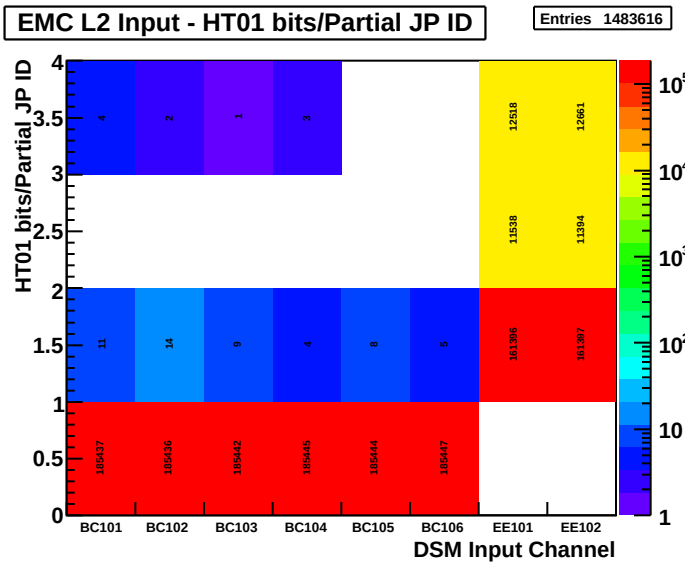

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

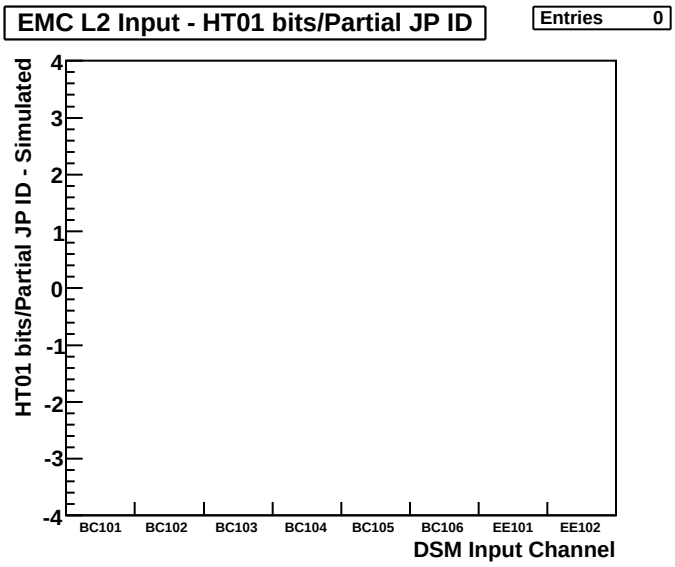

EMC L2 Input - JPZ/JPC bits
Entries 1483616
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

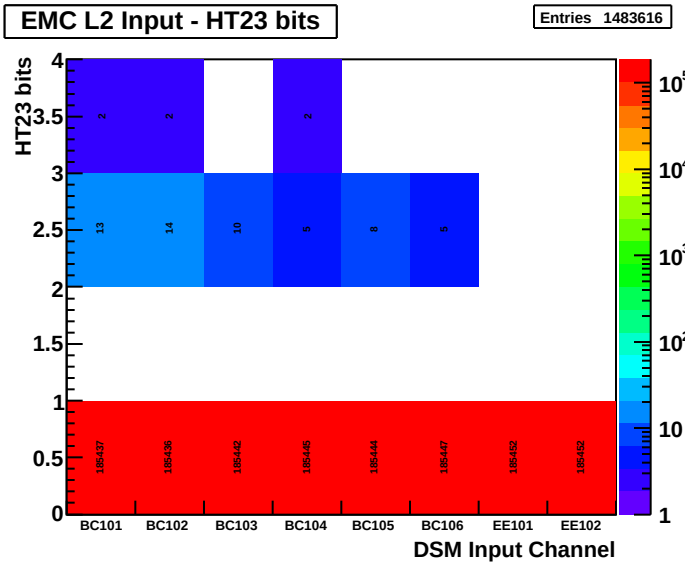

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

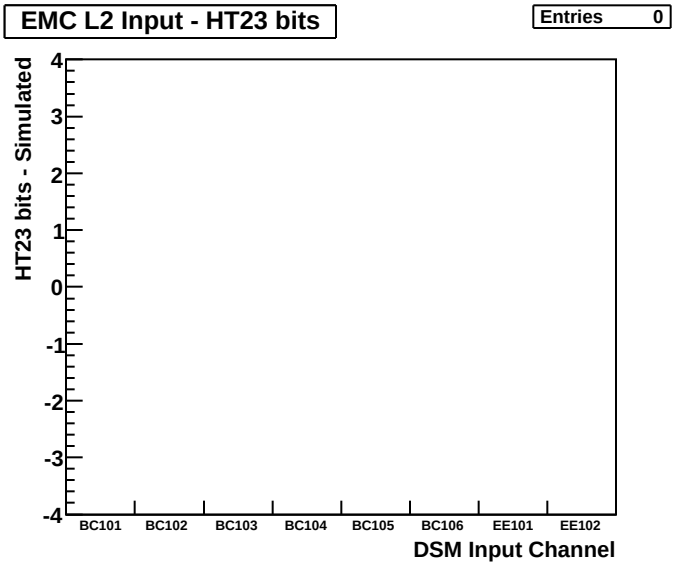

EMC L2 Input - Partial JP Sum
Entries 1483616
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

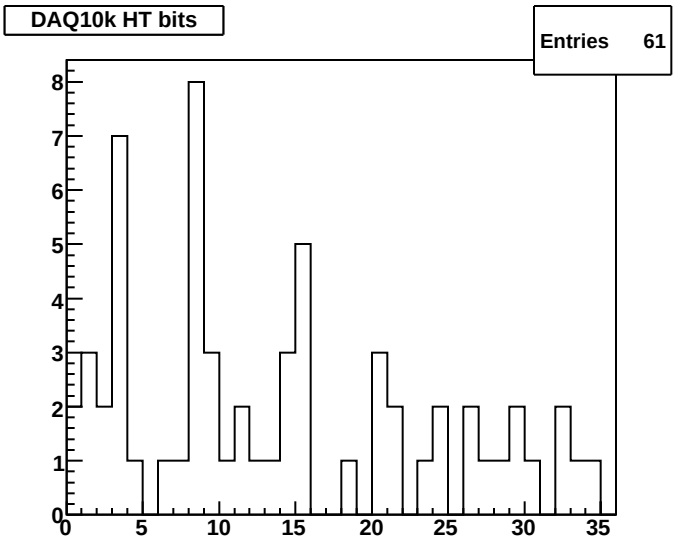

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

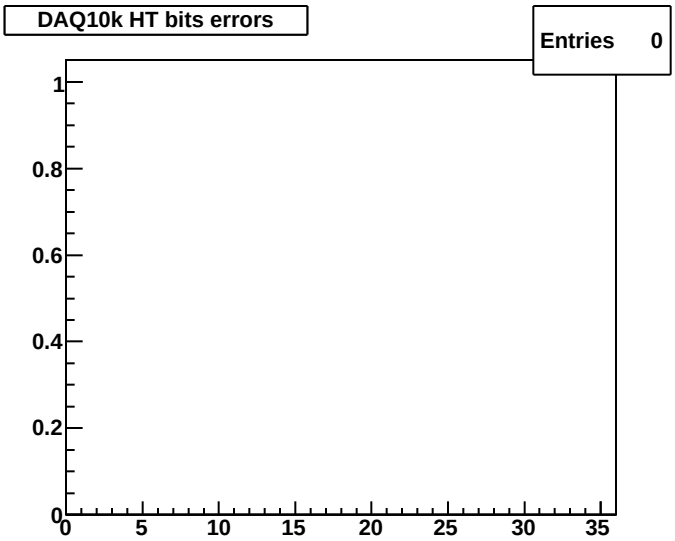

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1483616
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

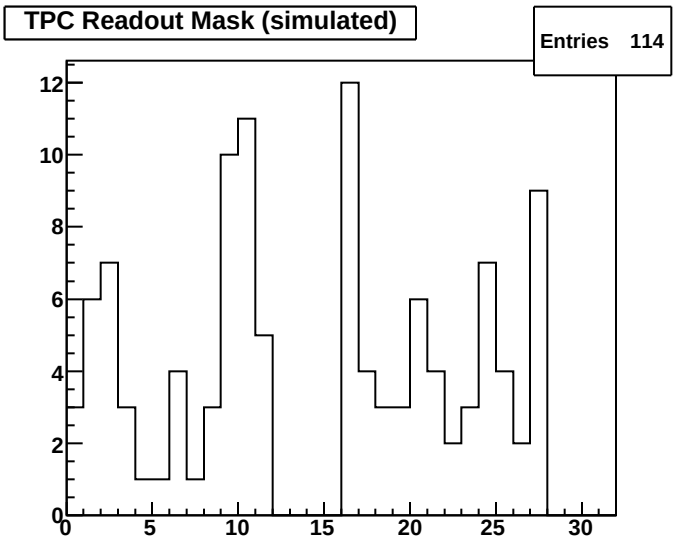

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

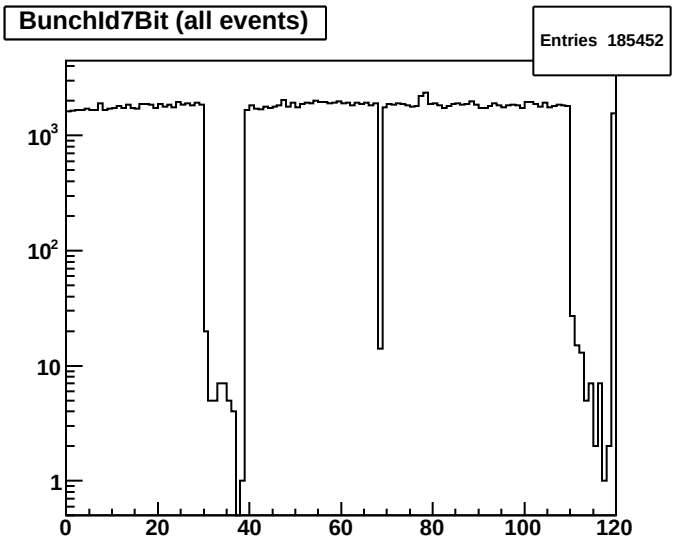

EMC L2 Input - HT23 bits
Entries 1483616
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

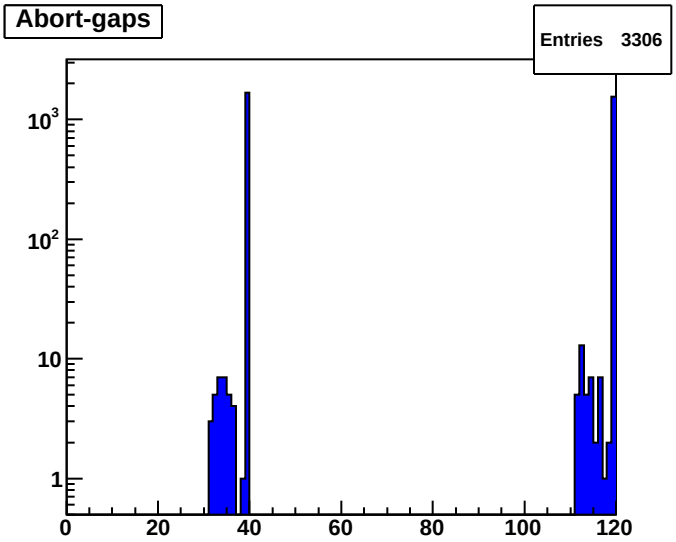

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

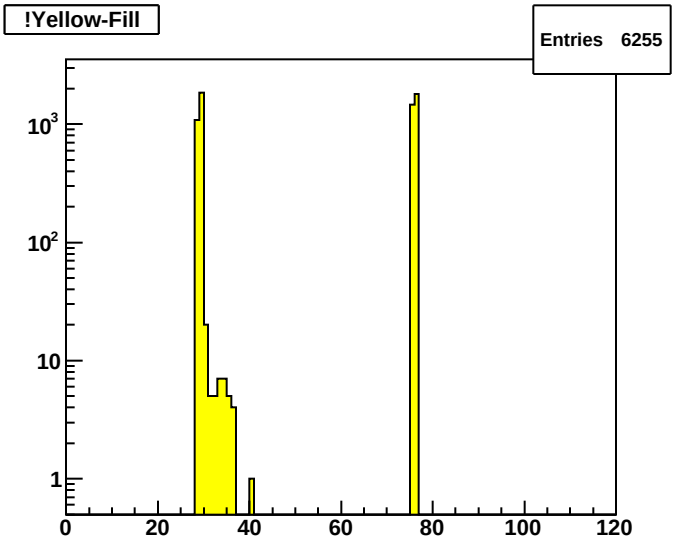

DAQ10k HT bits
Entries 61

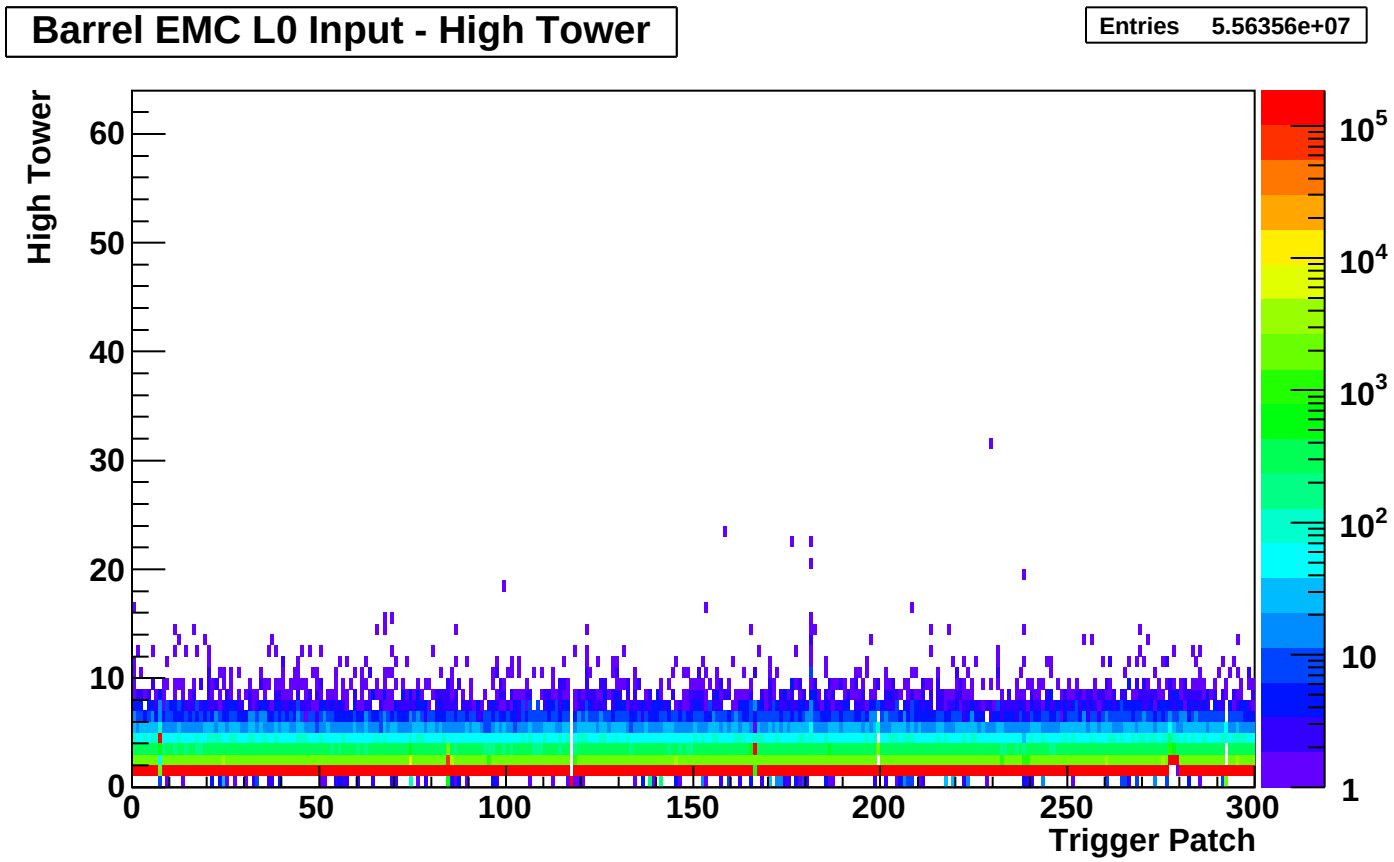

DAQ10k HT bits errors
Entries 0

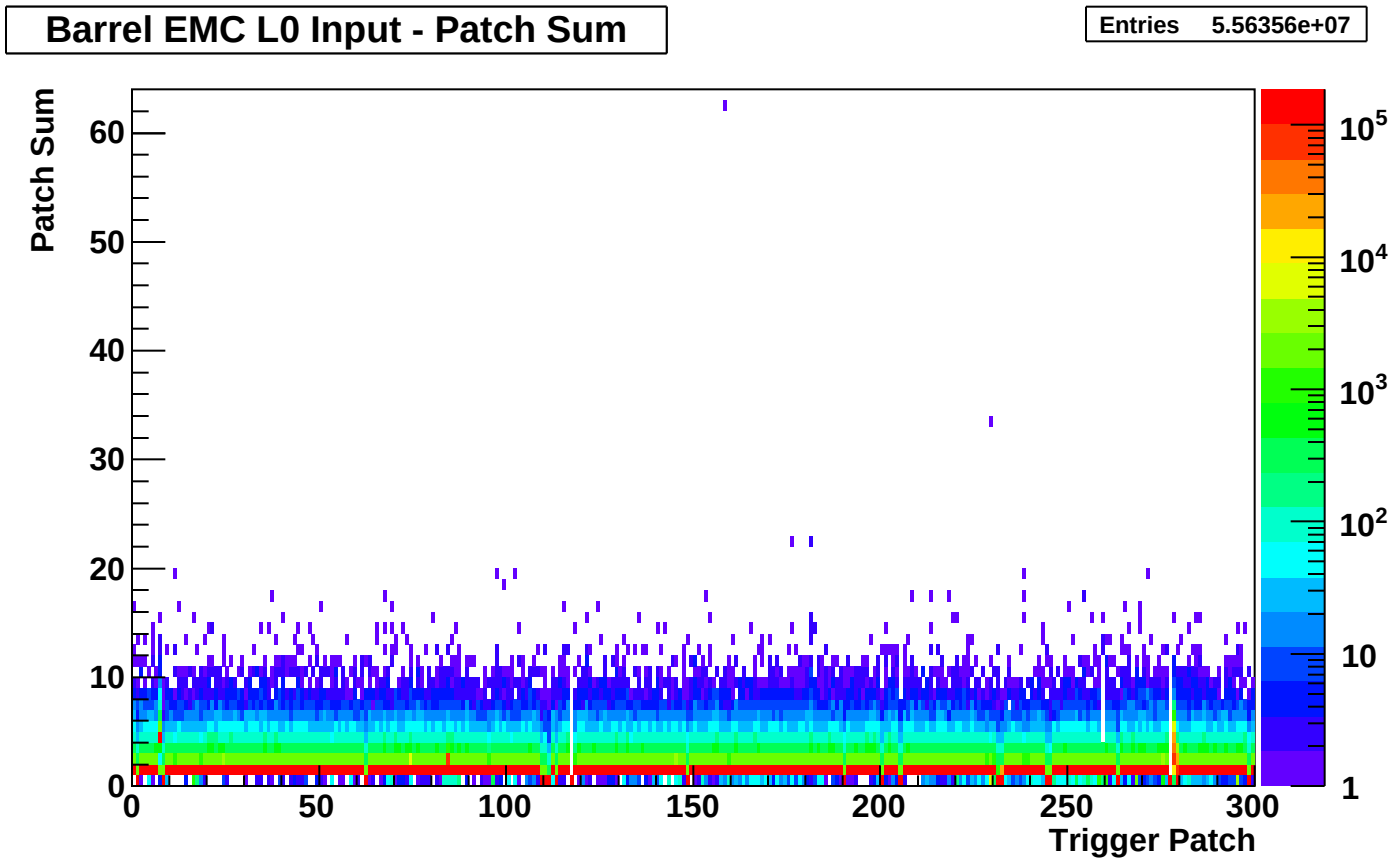

TPC Readout Mask (simulated)
Entries 114

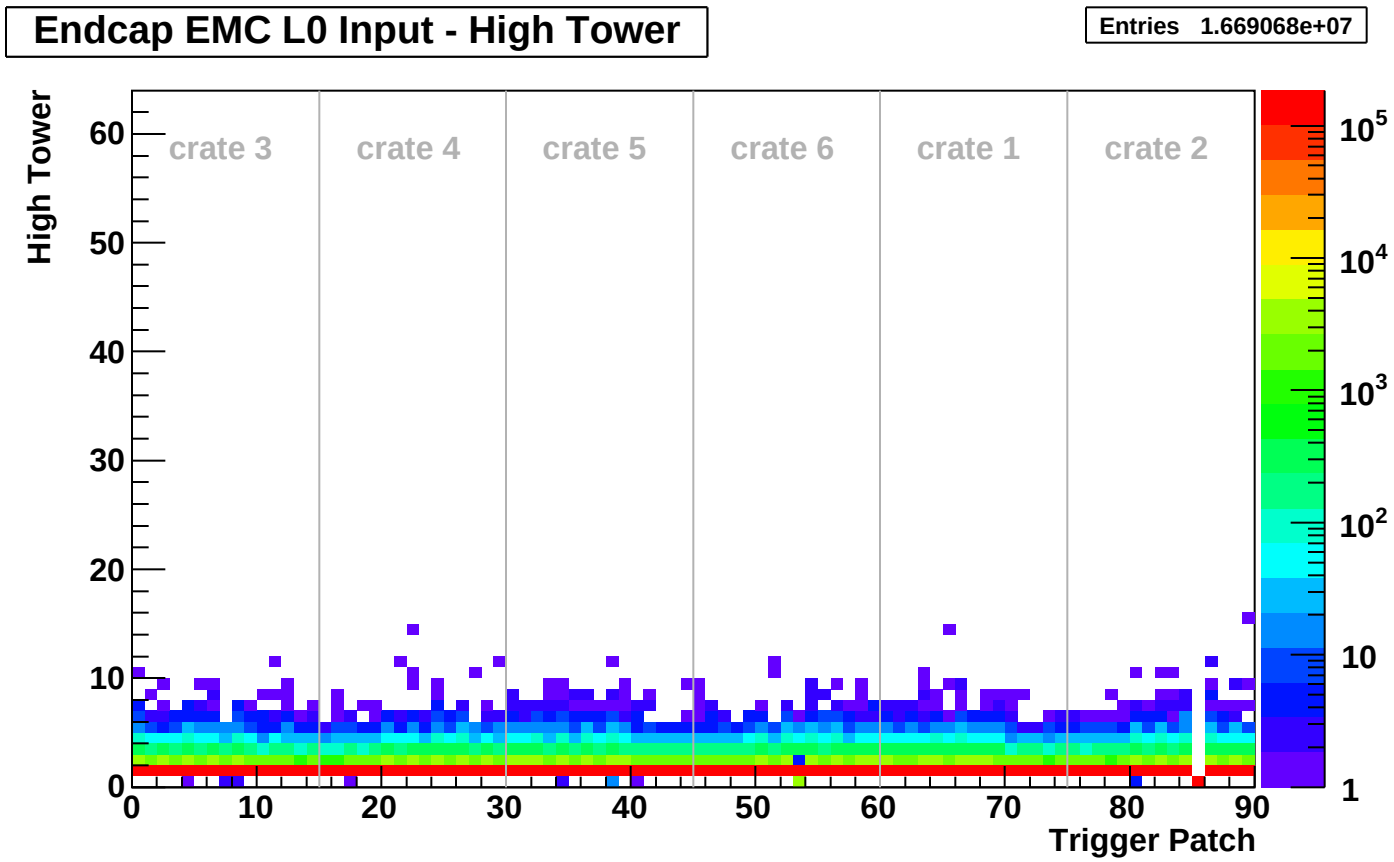

BunchId7Bit (all events)
Entries 185452

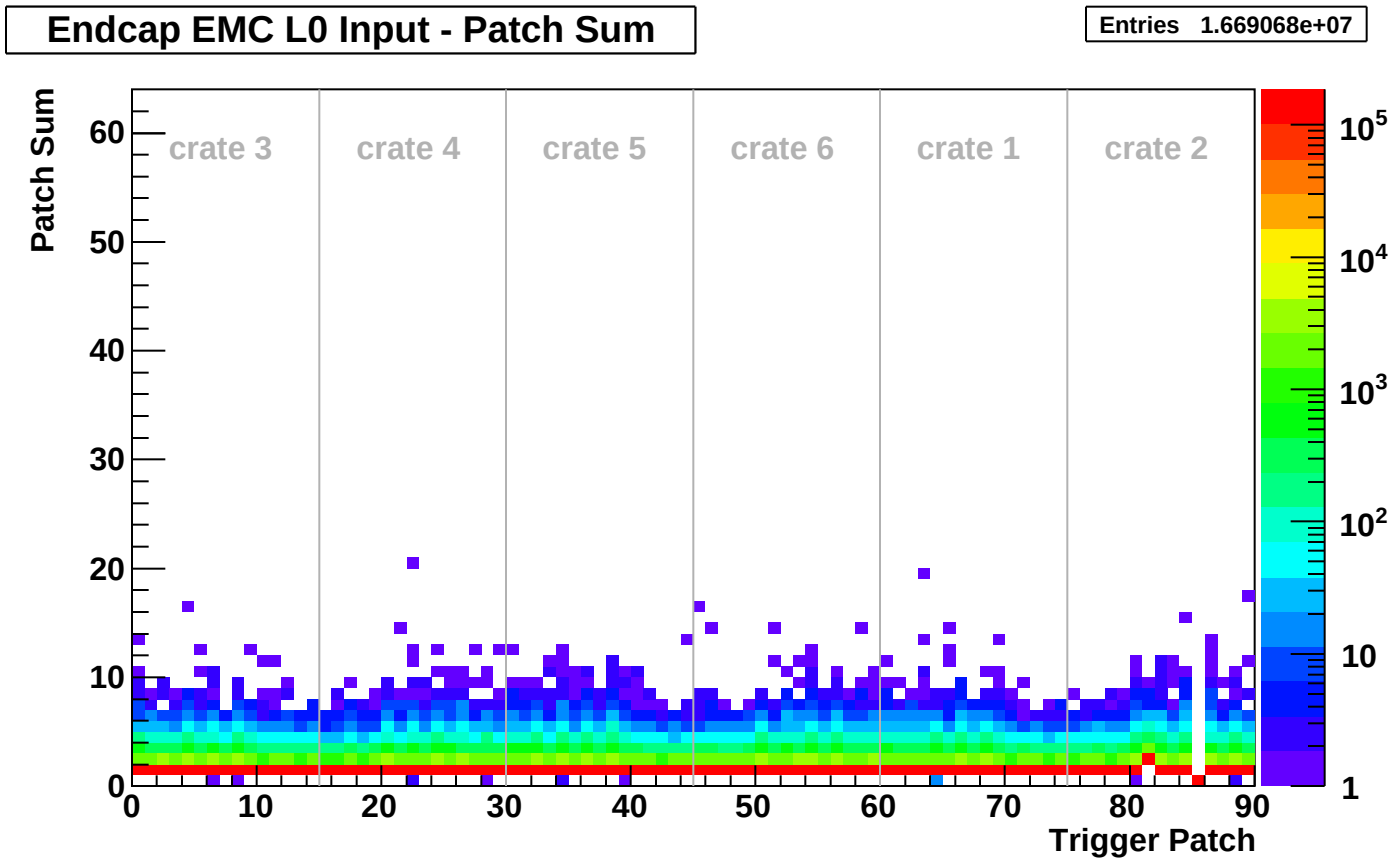

Abort-gaps
Entries 3306

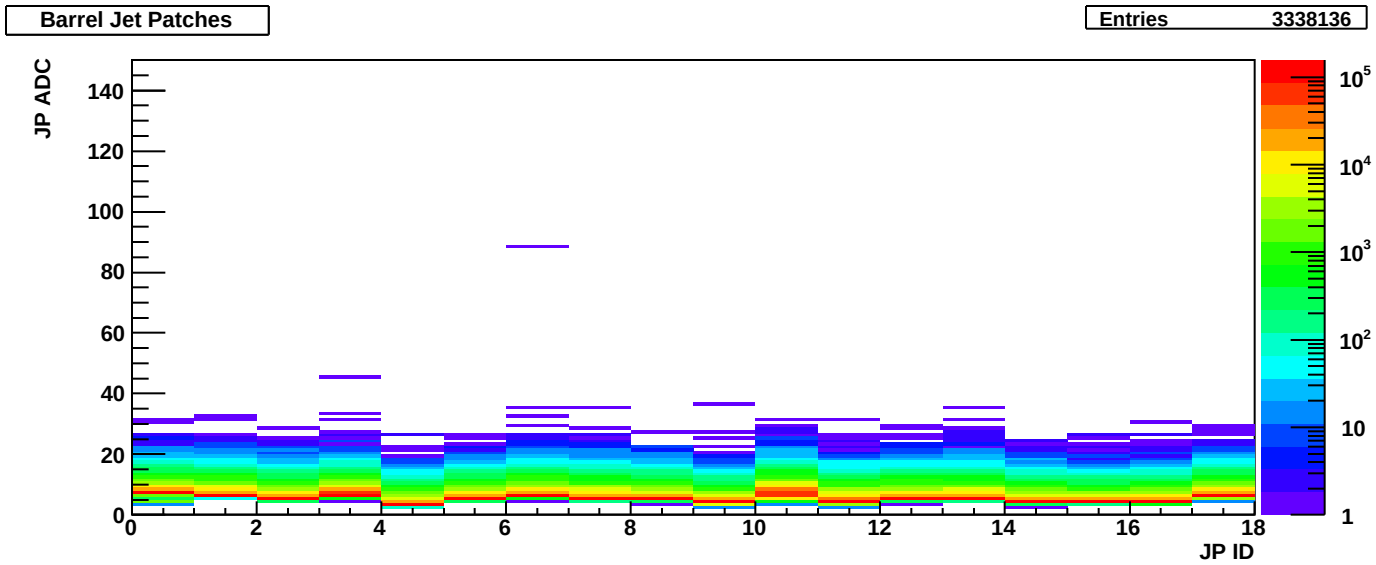

!Yellow-Fill
Entries 6255

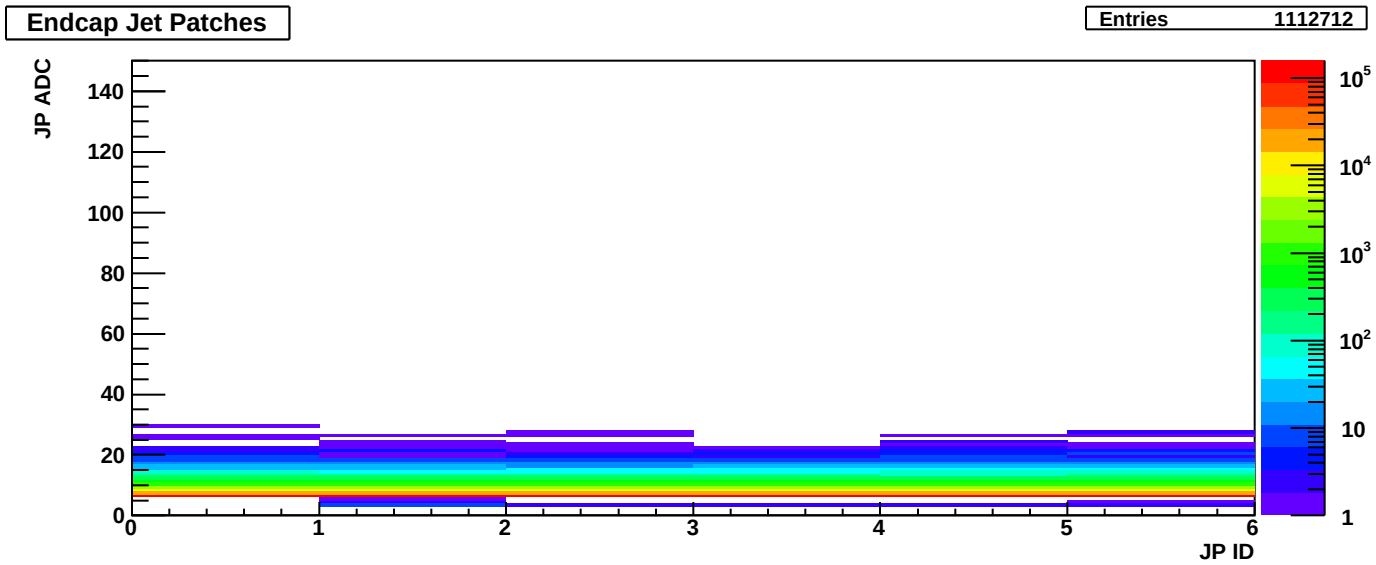

Barrel EMC L0 Input - High Tower
Entries 5.56356e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

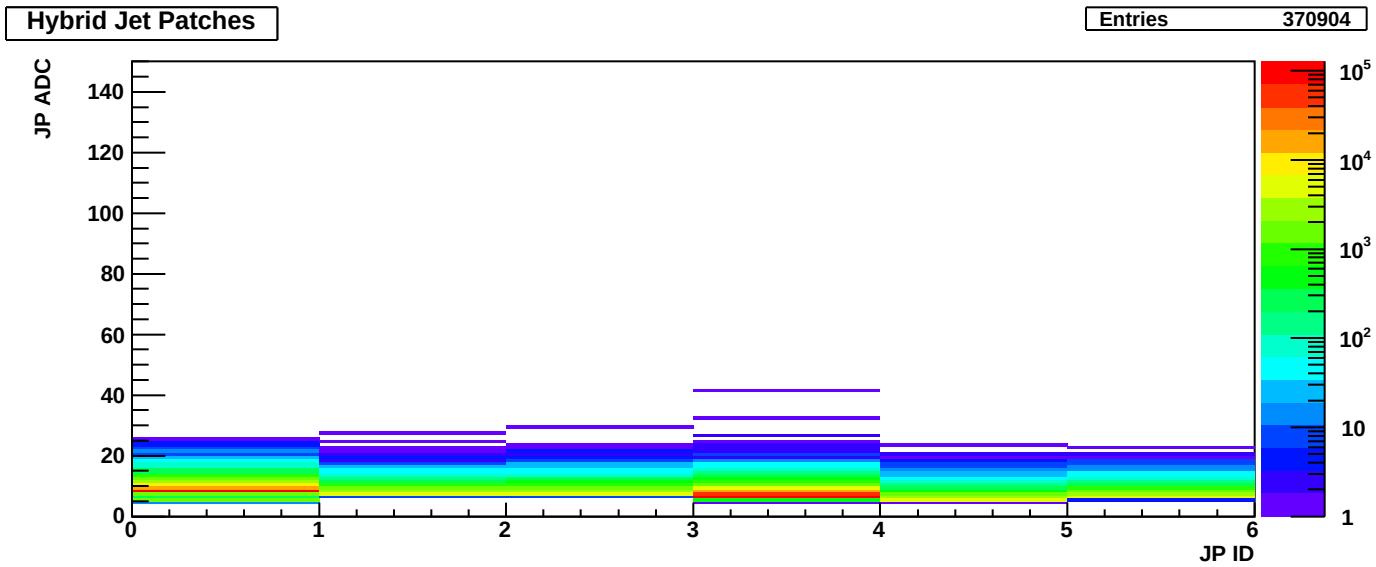

Barrel EMC L0 Input - Patch Sum
Entries 5.56356e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

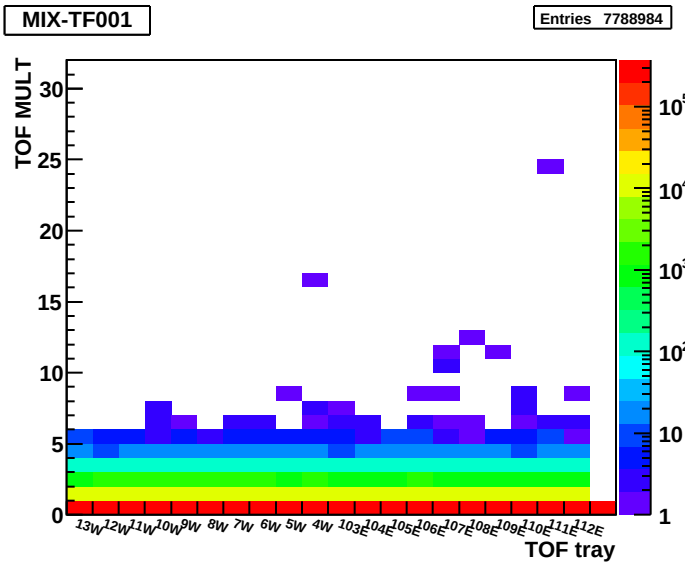

Endcap EMC L0 Input - High Tower
Entries 1.669068e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

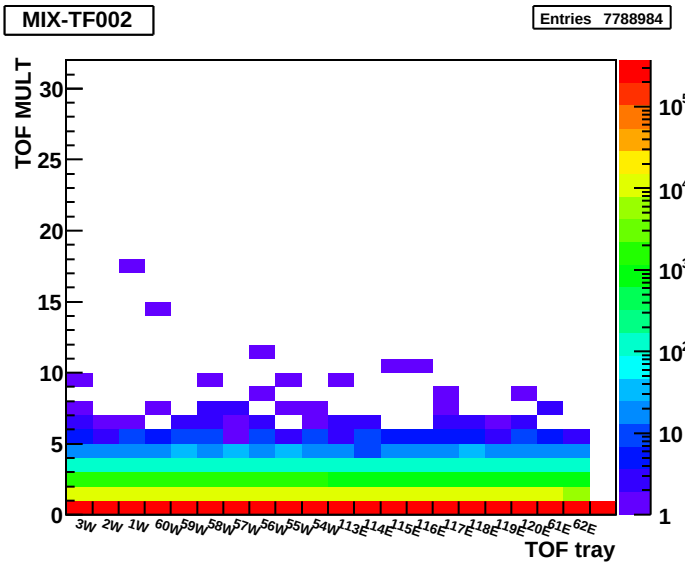

Endcap EMC L0 Input - Patch Sum
Entries 1.669068e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

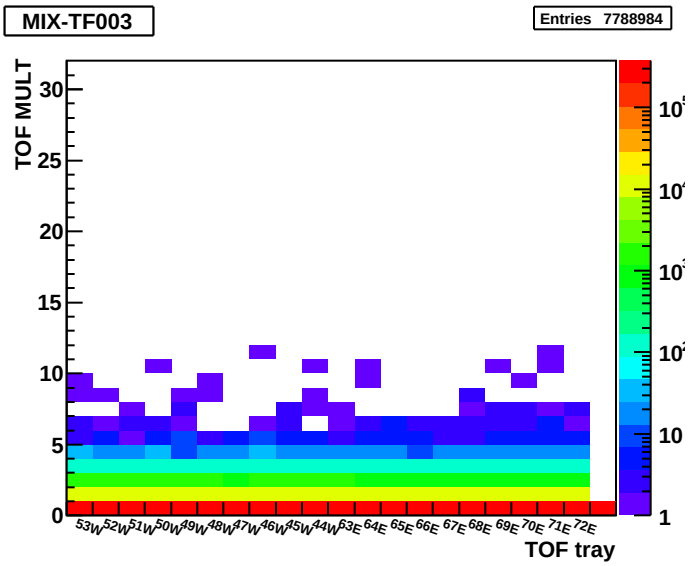

Barrel Jet Patches
Entries 3338136
JP ADC
JP ID

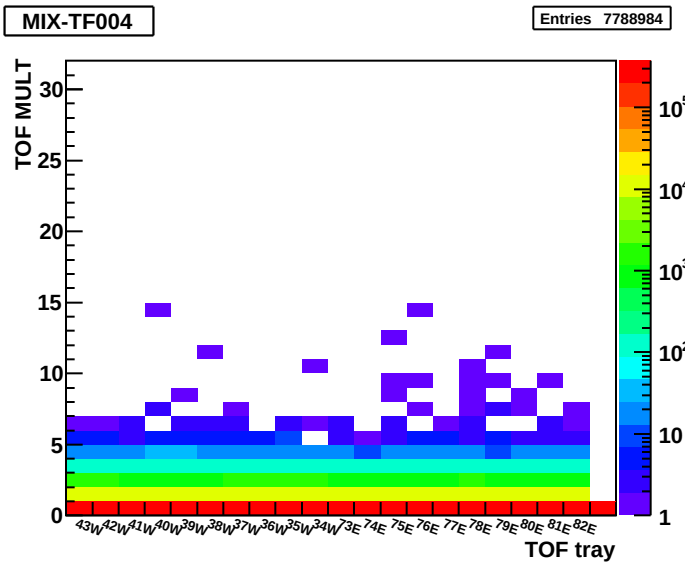

Endcap Jet Patches
Entries 1112712
JP ADC
JP ID

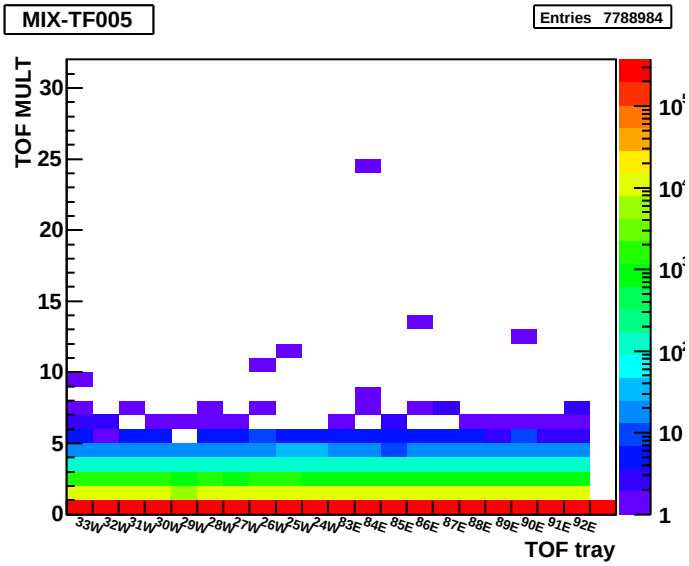

Hybrid Jet Patches
Entries 370904
JP ADC
JP ID

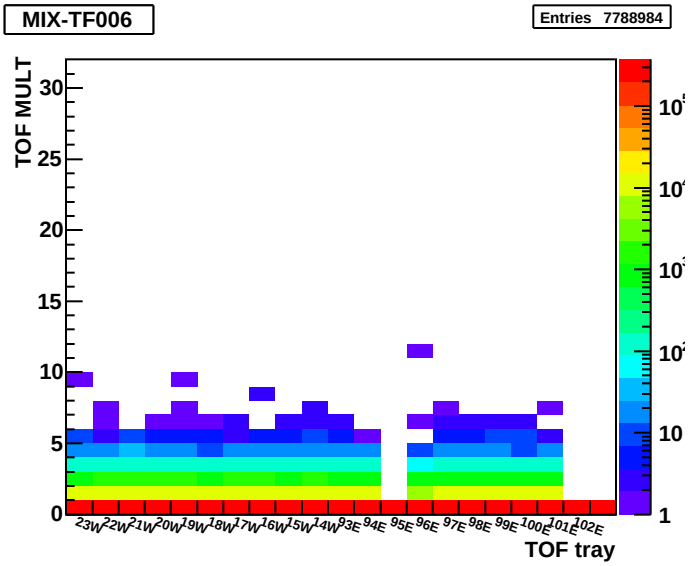

MIX-TF001
Entries 7788984
TOF MULT
TOF tray

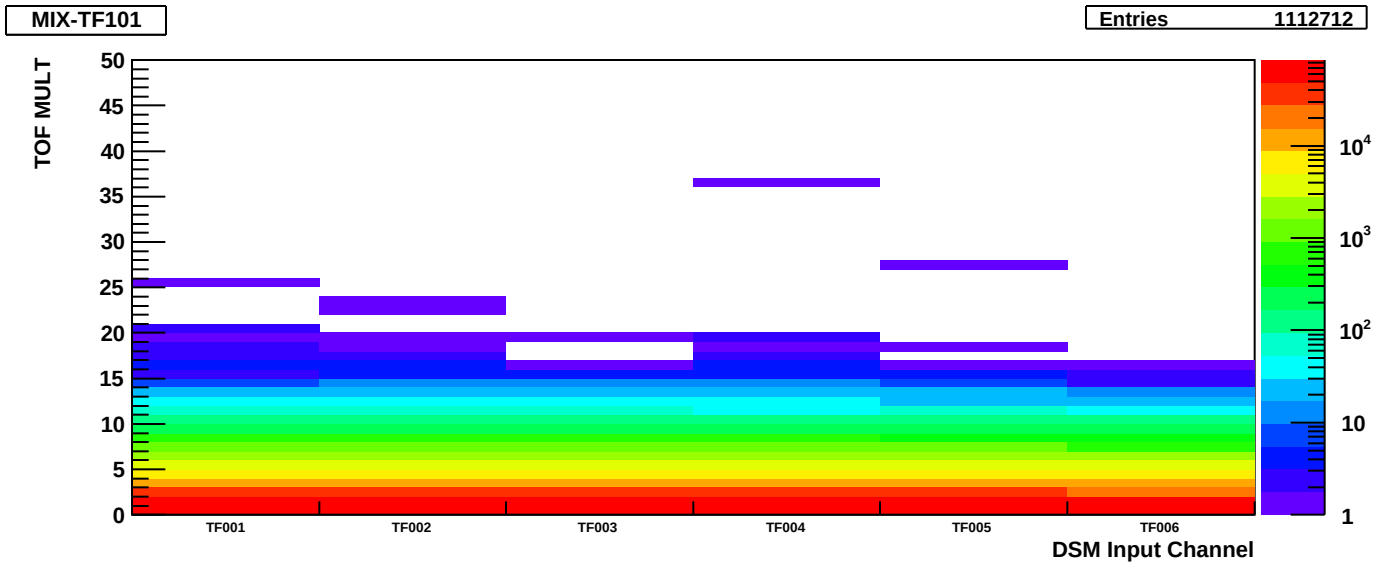

MIX-TF002
Entries 7788984
TOF MULT
TOF tray

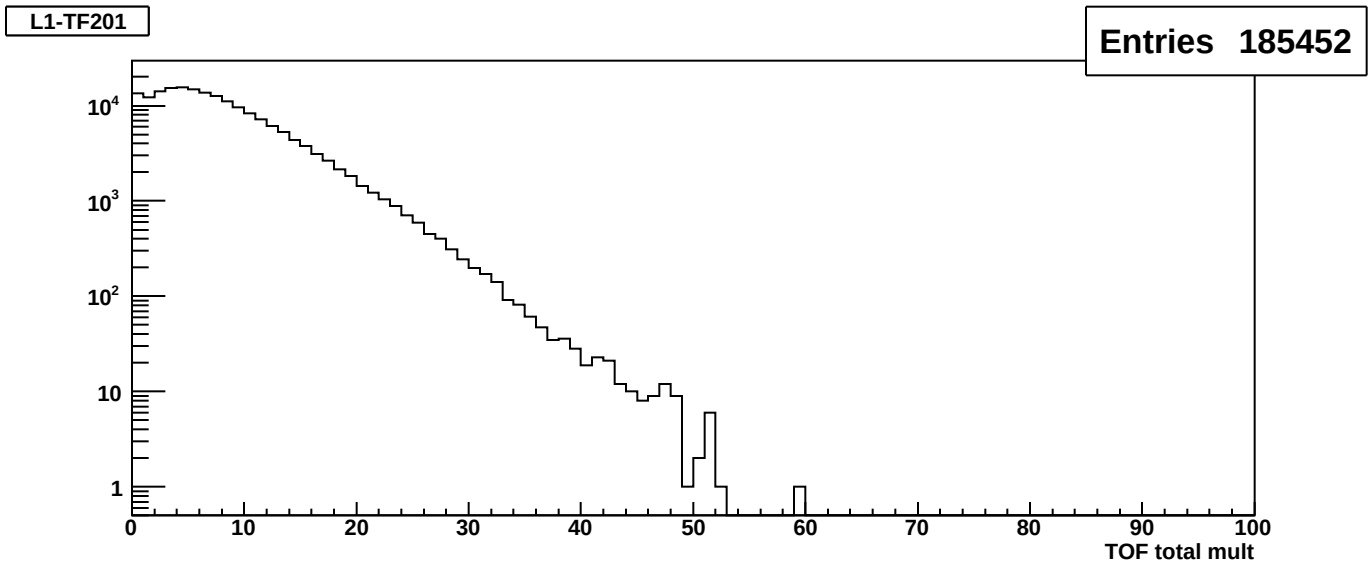

MIX-TF003
Entries 7788984
TOF MULT
TOF tray

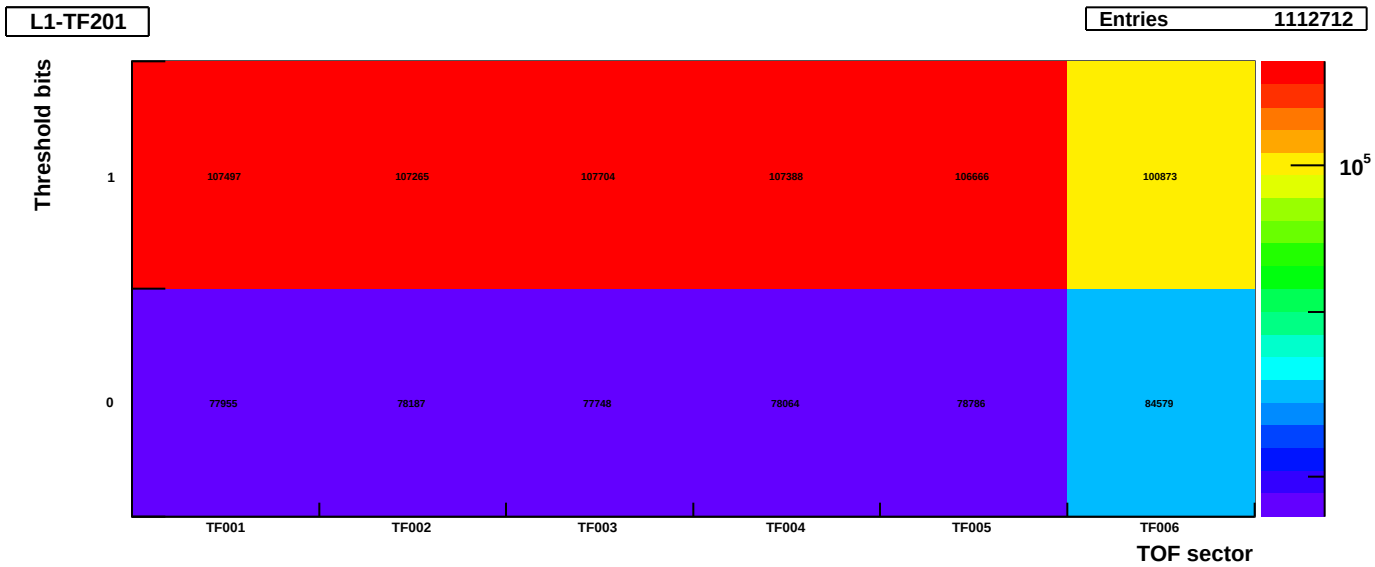

MIX-TF004
Entries 7788984
TOF MULT
TOF tray

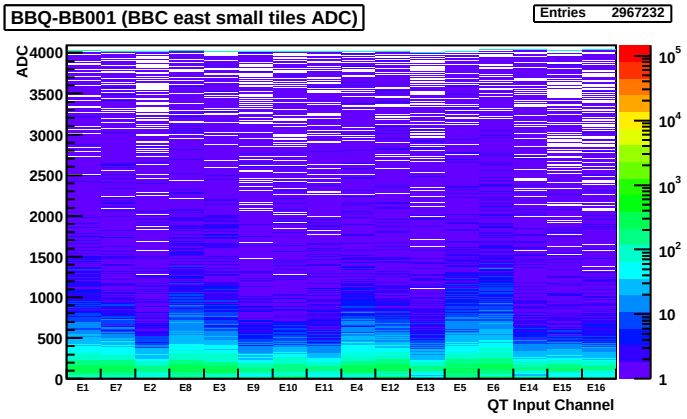

MIX-TF005
Entries 7788984
TOF MULT
TOF tray

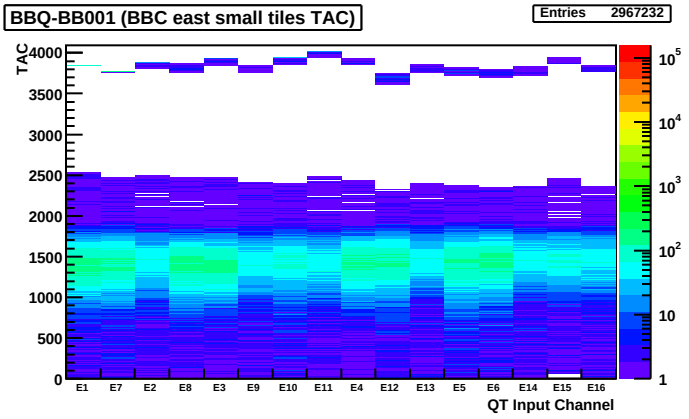

MIX-TF006
Entries 7788984
TOF MULT
TOF tray

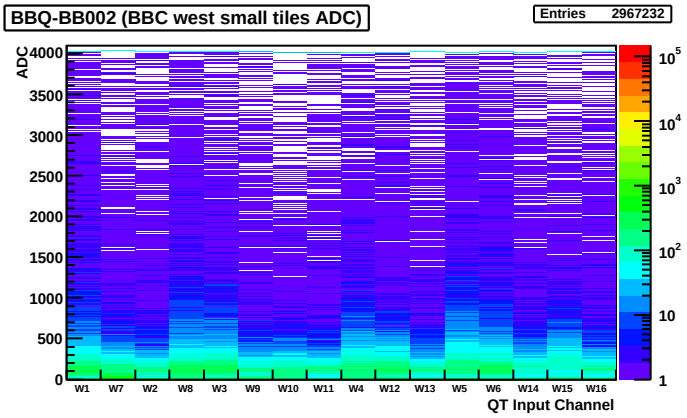

MIX-TF101
Entries 1112712
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

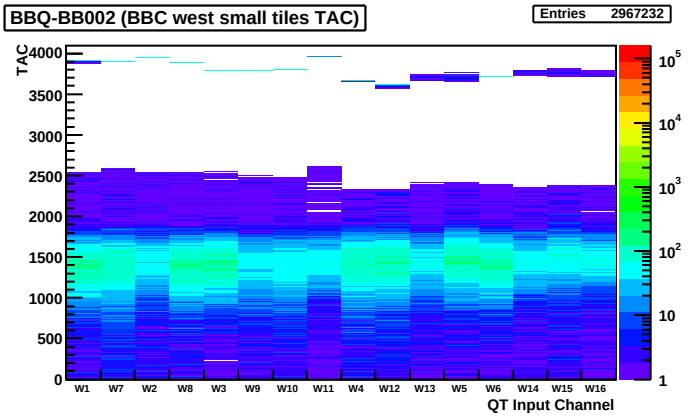

L1-TF201
Entries 185452
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

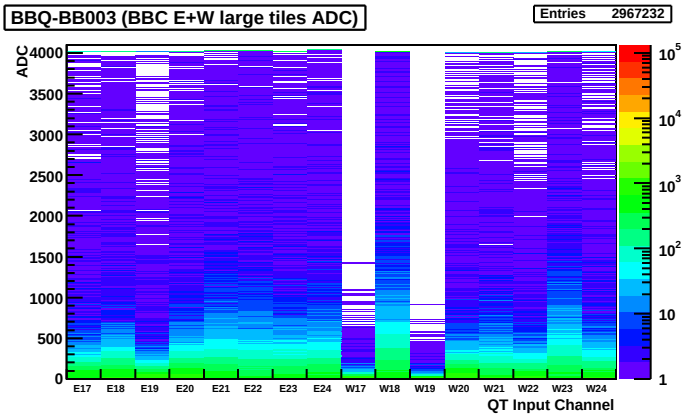

L1-TF201
Entries 1112712
Threshold bits
1
0
107497
107265
107704
107388
106666
100873
77955
78187
77748
78064
78786
84579
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

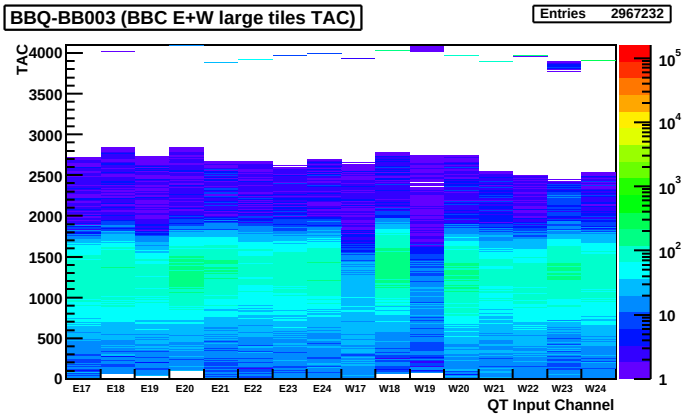

BBQ-BB001 (BBC east small tiles ADC)
Entries 2967232
ADC
QT Input Channel

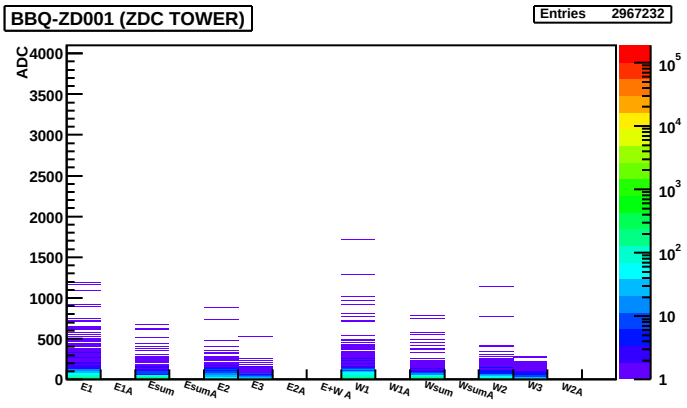

BBQ-BB001 (BBC east small tiles TAC)
Entries 2967232
TAC
QT Input Channel

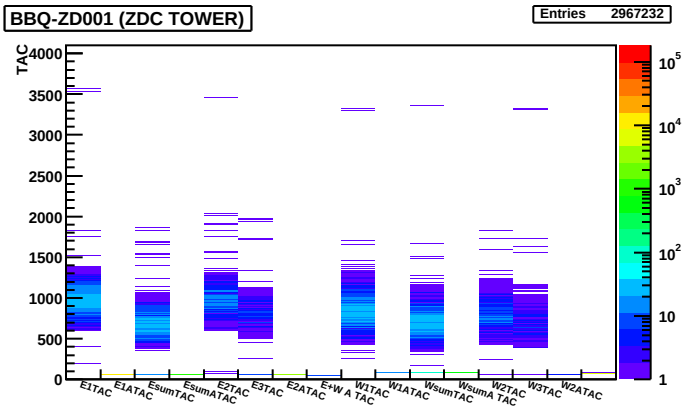

BBQ-BB002 (BBC west small tiles ADC)
Entries 2967232
ADC
QT Input Channel

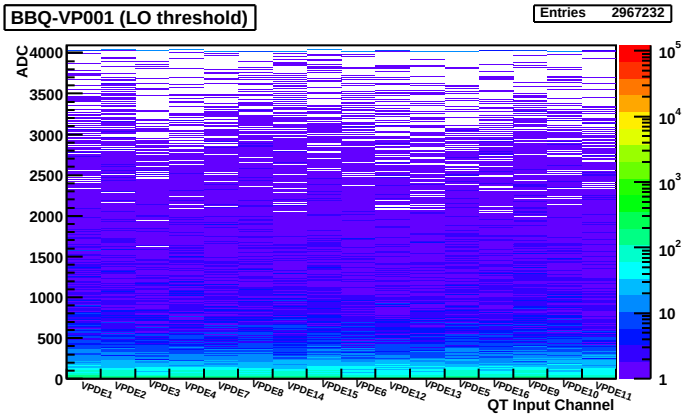

BBQ-BB002 (BBC west small tiles TAC)
Entries 2967232
TAC
QT Input Channel

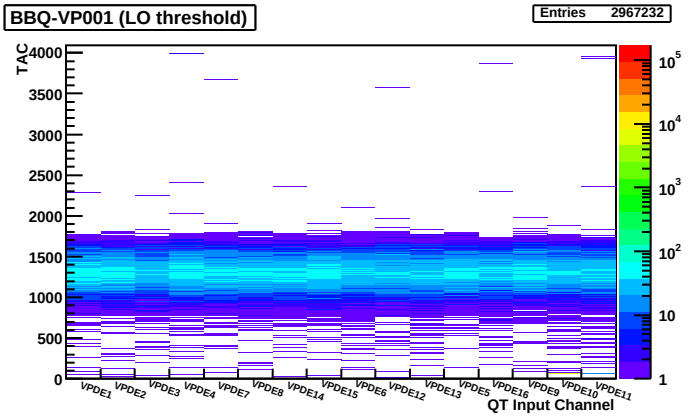

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2967232
ADC
QT Input Channel

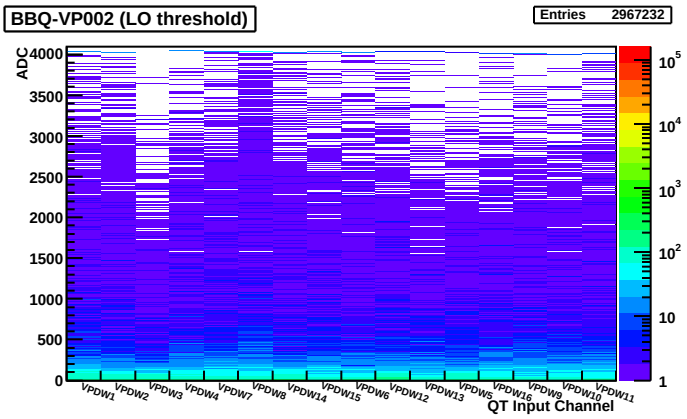

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2967232
TAC
QT Input Channel

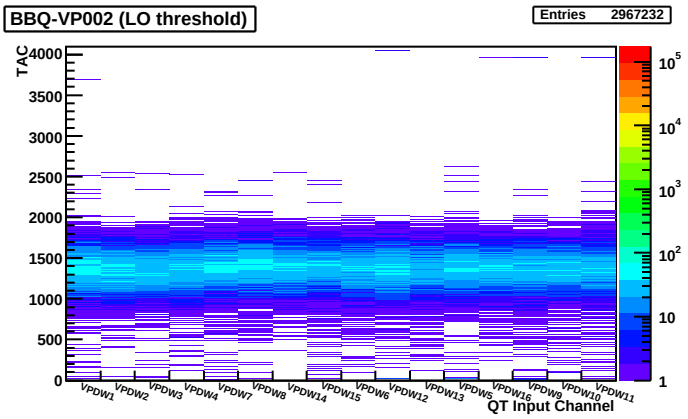

BBQ-ZD001 (ZDC TOWER)
Entries 2967232
ADC

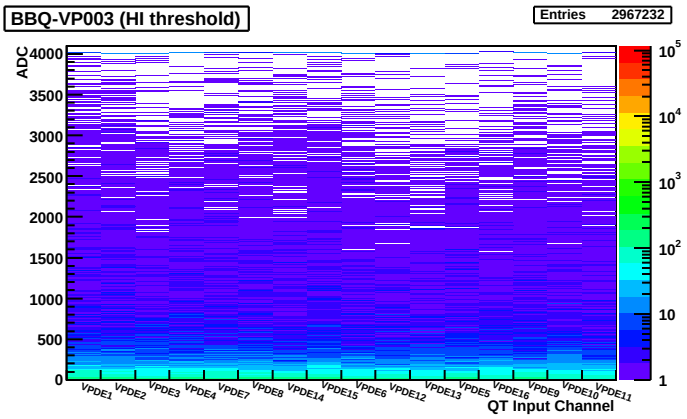

BBQ-ZD001 (ZDC TOWER)
Entries 2967232
TAC

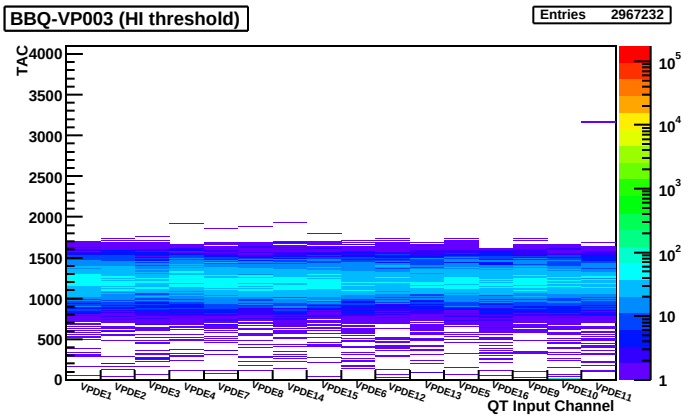

BBQ-VP001 (LO threshold)
Entries 2967232
ADC
QT Input Channel

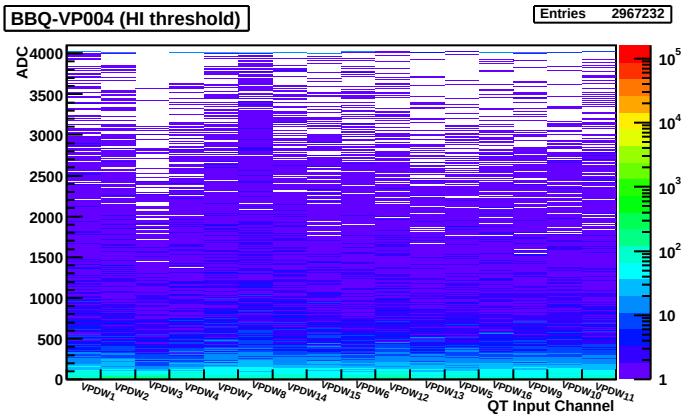

BBQ-VP001 (LO threshold)
Entries 2967232
TAC
QT Input Channel

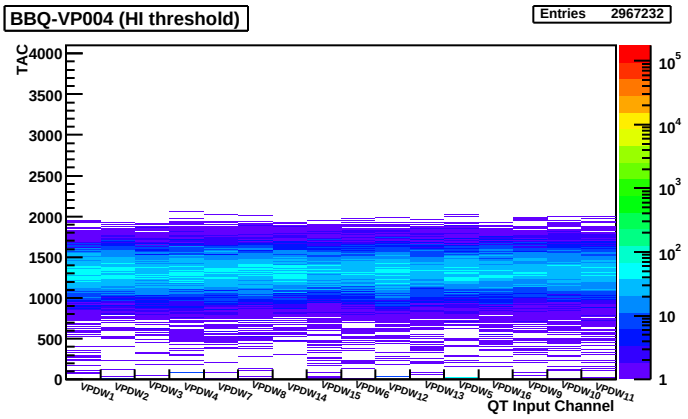

BBQ-VP002 (LO threshold)
Entries 2967232
ADC
QT Input Channel

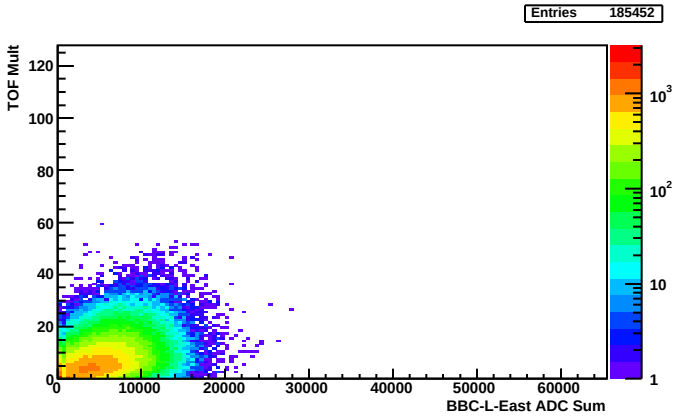

BBQ-VP002 (LO threshold)
Entries 2967232
TAC
QT Input Channel

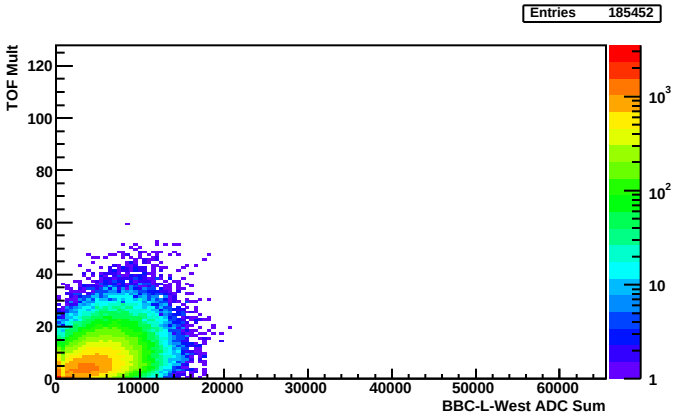

BBQ-VP003 (HI threshold)
Entries 2967232
ADC
QT Input Channel

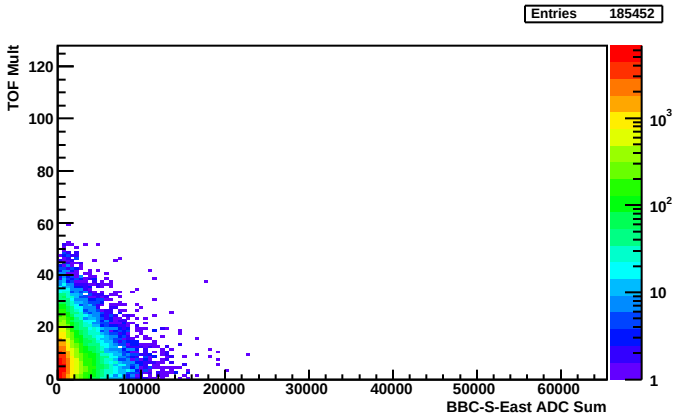

BBQ-VP003 (HI threshold)
Entries 2967232
TAC
QT Input Channel

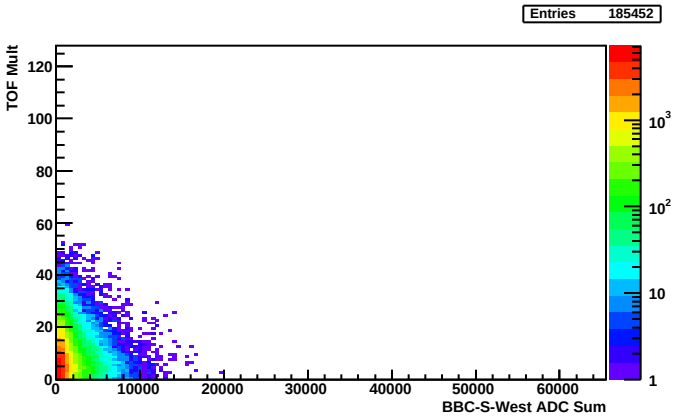

BBQ-VP004 (HI threshold)
Entries 2967232
ADC
QT Input Channel

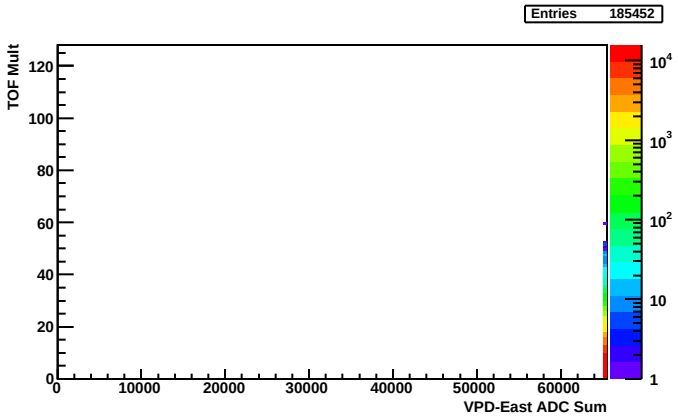

BBQ-VP004 (HI threshold)
Entries 2967232
TAC
QT Input Channel

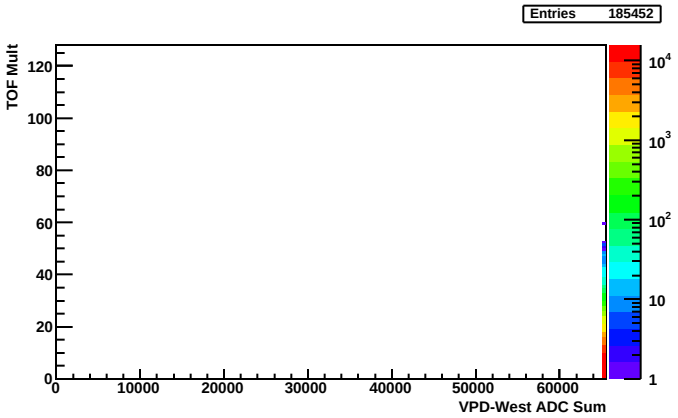

Entries 185452
TOF Mult
BBC-L-East ADC Sum

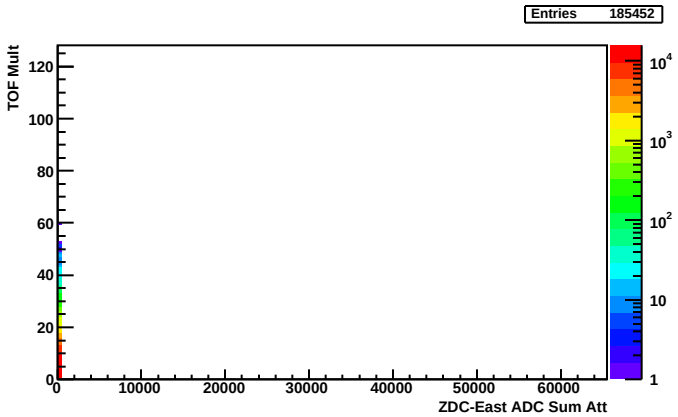

Entries 185452
TOF Mult
BBC-L-West ADC Sum

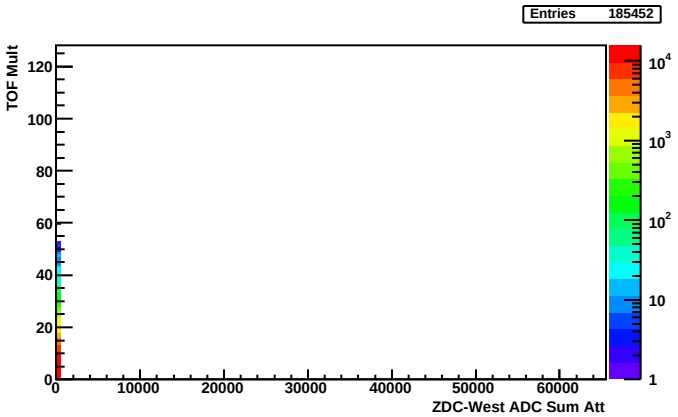

Entries 185452
TOF Mult
BBC-S-East ADC Sum

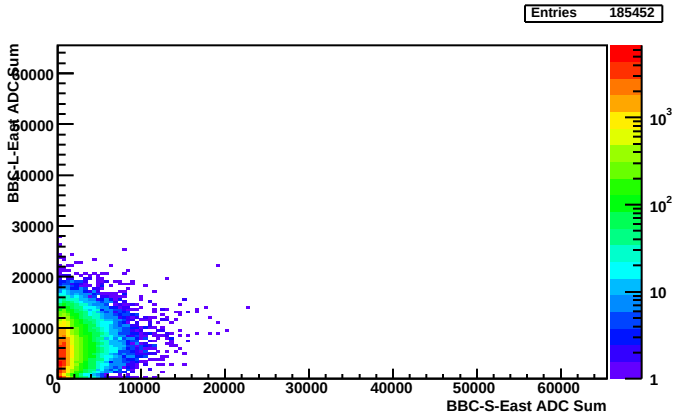

Entries 185452
TOF Mult
BBC-S-West ADC Sum

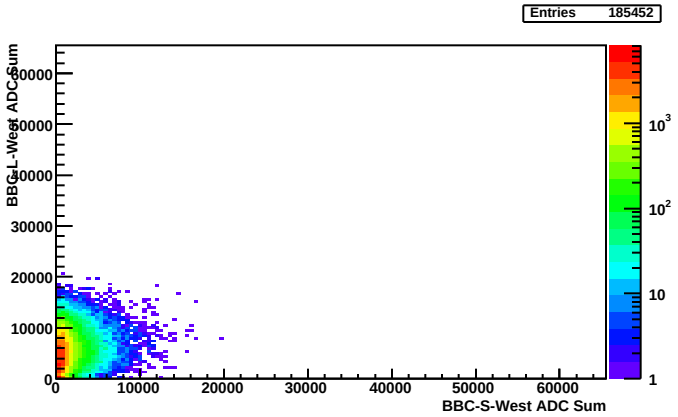

Entries 185452
TOF Mult
VPD-East ADC Sum

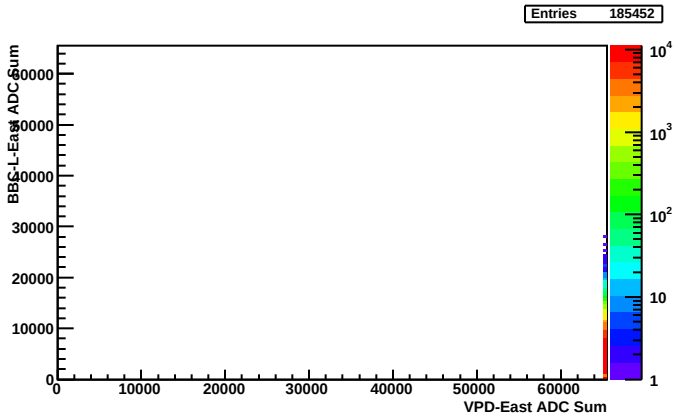

Entries 185452
TOF Mult
VPD-West ADC Sum

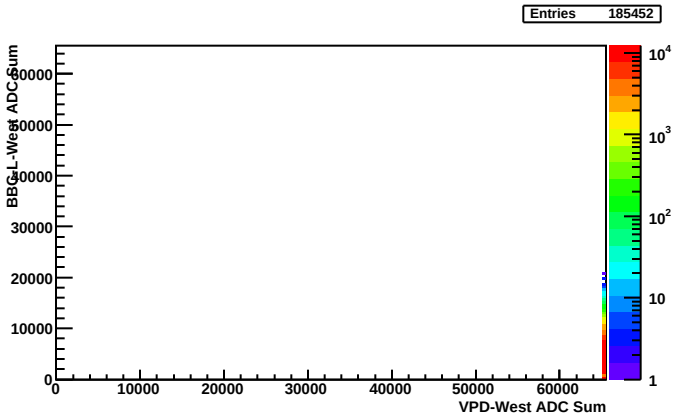

Entries 185452
TOF Mult
ZDC-East ADC Sum Att

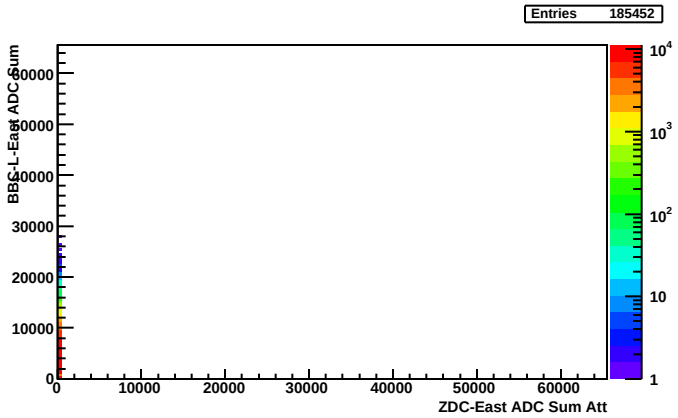

Entries 185452
TOF Mult
ZDC-West ADC Sum Att

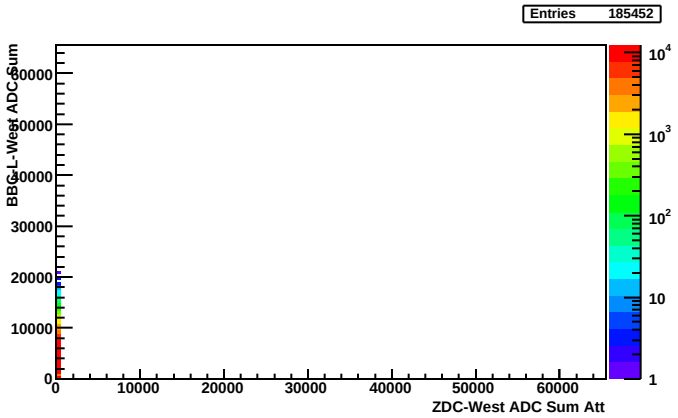

Entries 185452
BBC-L-East ADC Sum
BBC-S-East ADC Sum

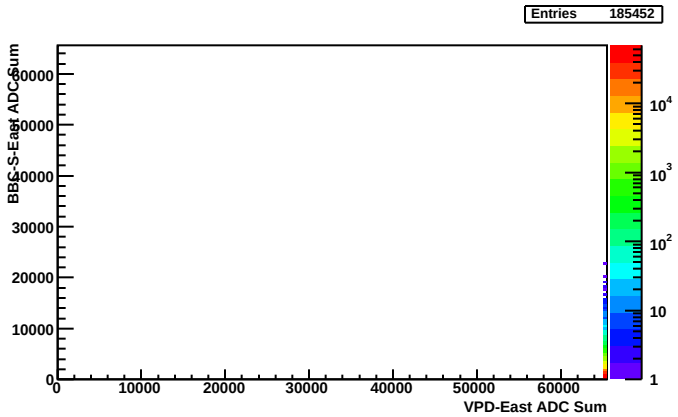

Entries 185452
BBC-L-West ADC Sum
BBC-S-West ADC Sum

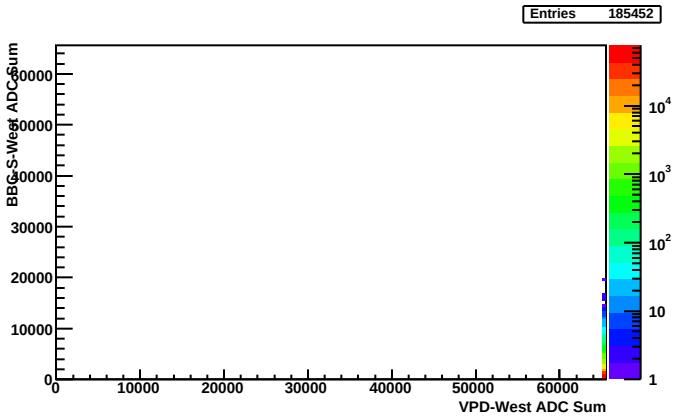

Entries 185452
BBC-L-East ADC Sum
VPD-East ADC Sum

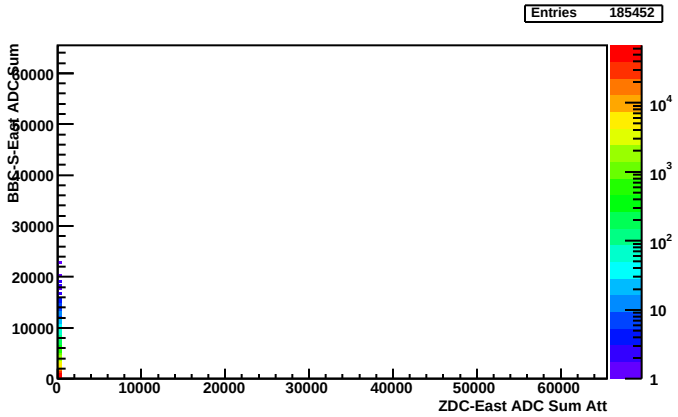

Entries 185452
BBC-L-West ADC Sum
VPD-West ADC Sum

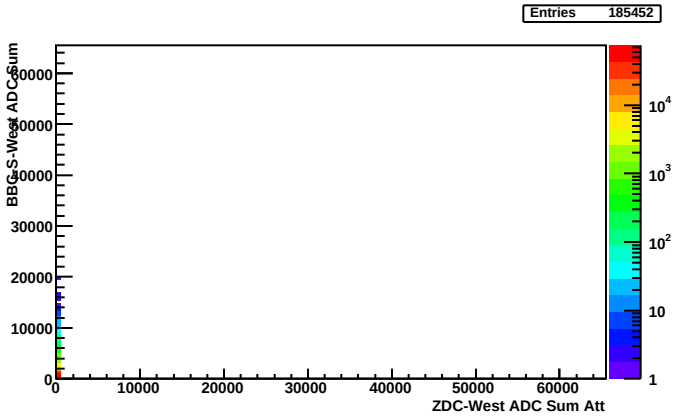

Entries 185452
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

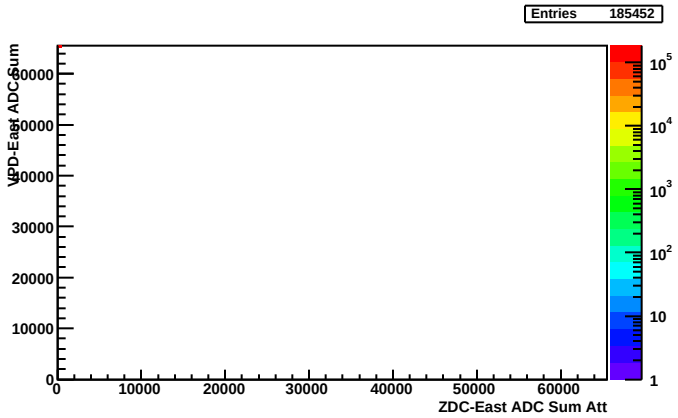

Entries 185452
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

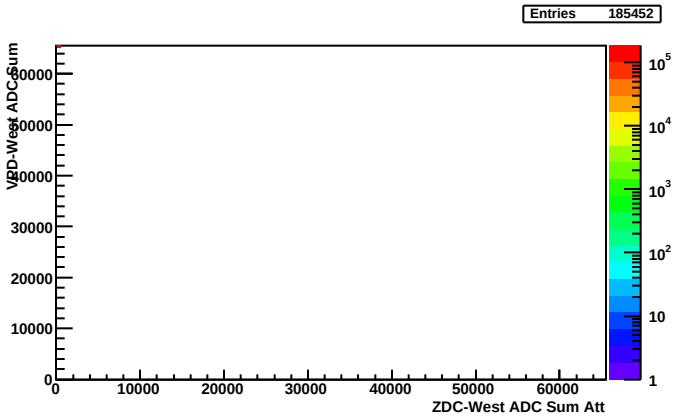

Entries 185452
BBC-S-East ADC Sum
VPD-East ADC Sum

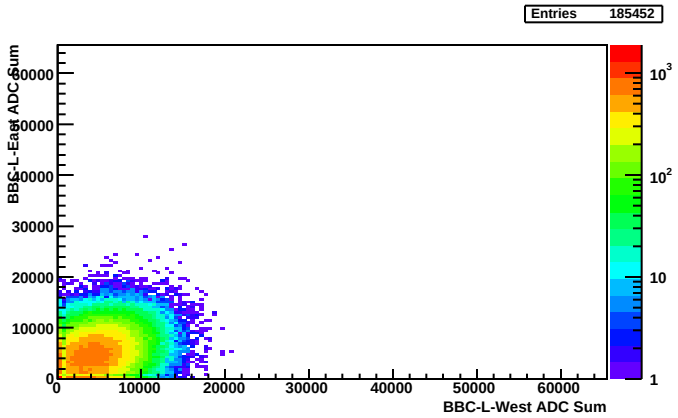

Entries 185452
BBC-S-West ADC Sum
VPD-West ADC Sum

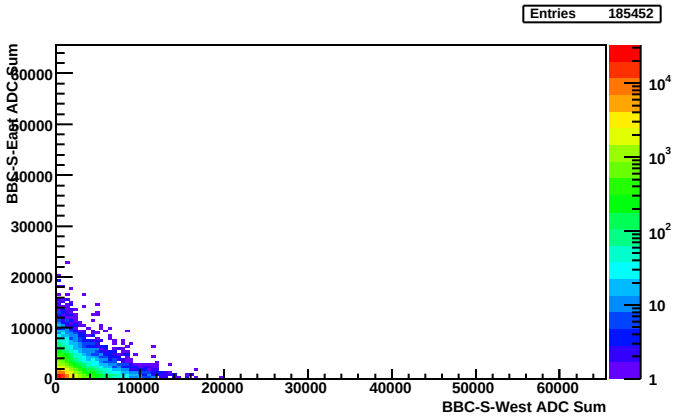

Entries 185452
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

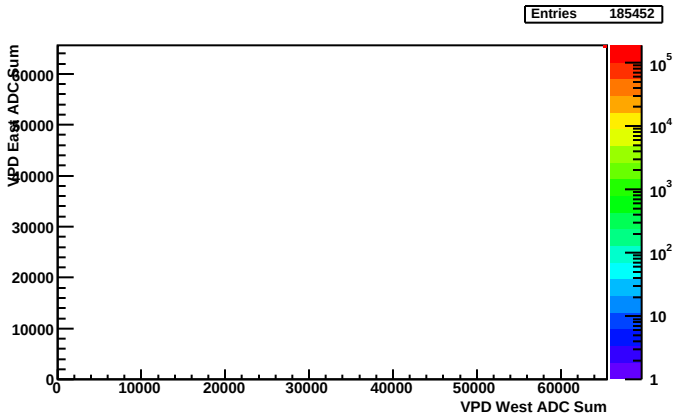

Entries 185452
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

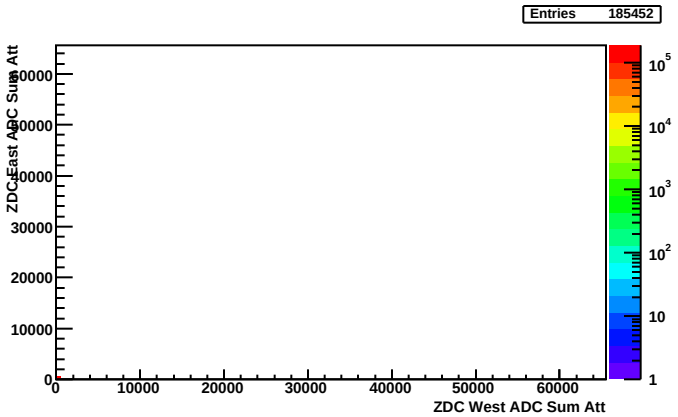

Entries 185452
VPD-East ADC Sum
ZDC-East ADC Sum Att

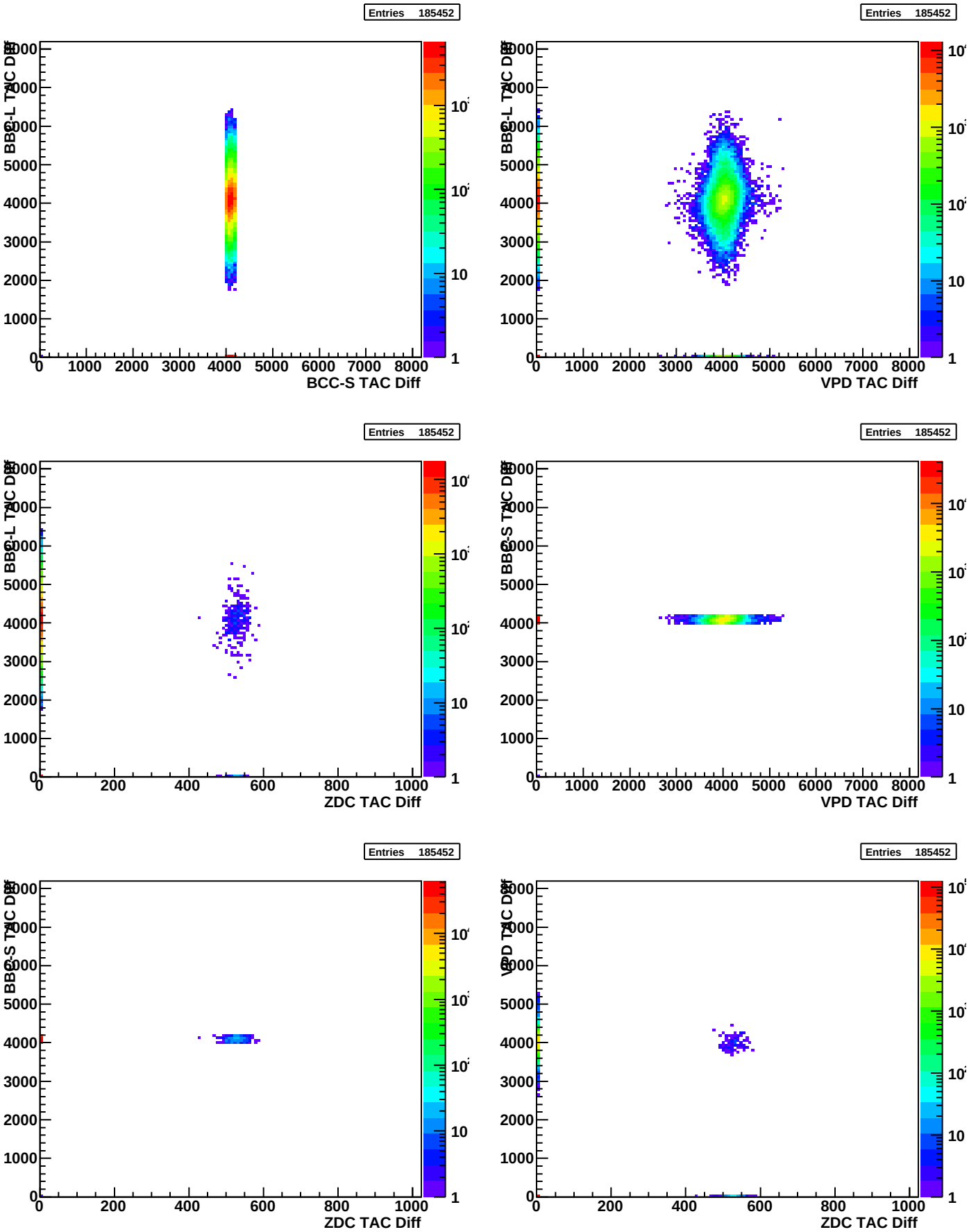

Entries 185452
VPD-West ADC Sum
ZDC-West ADC Sum Att

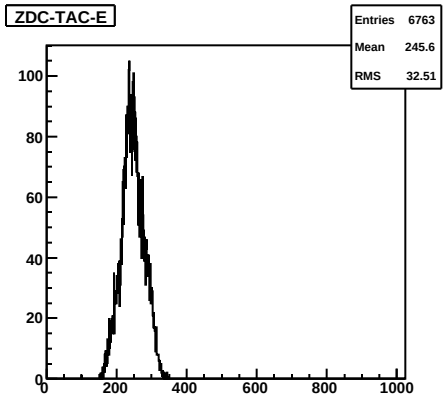

Entries 185452
BBC-L-East ADC Sum
BBC-L-West ADC Sum

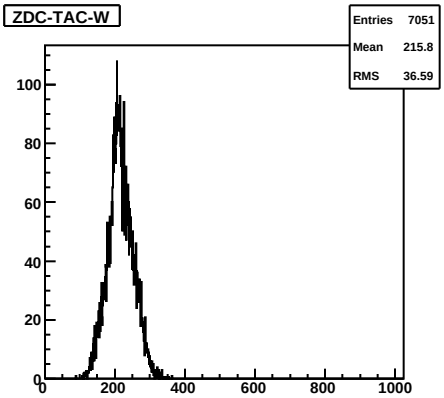

Entries 185452
BBC-S-East ADC Sum
BBC-S-West ADC Sum

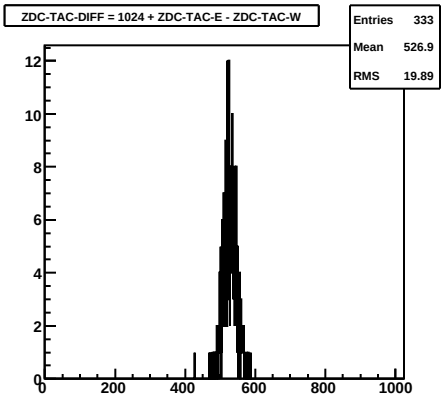

Entries 185452
VPD East ADC Sum
VPD West ADC Sum

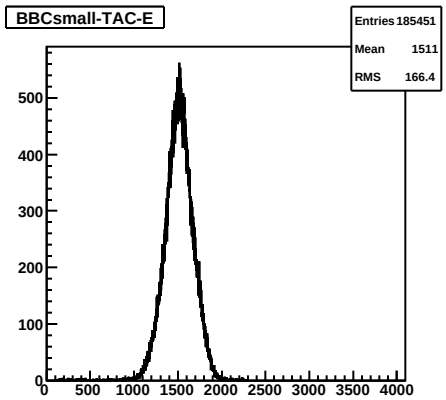

Entries 185452
ZDC East ADC Sum Att
ZDC West ADC Sum Att

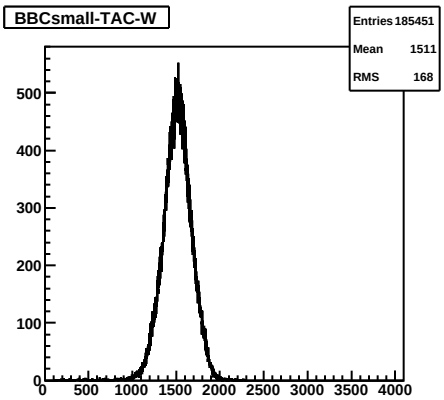

Entries 185452
BBC-L TAC Diff
BCC-S TAC Diff
Entries 185452
BBC-L TAC Diff
VPD TAC Diff
Entries 185452
BBC-L TAC Diff
ZDC TAC Diff
Entries 185452
BBC-S TAC Diff
VPD TAC Diff
Entries 185452
BBC-S TAC Diff
ZDC TAC Diff
Entries 185452
VPD TAC Diff
ZDC TAC Diff

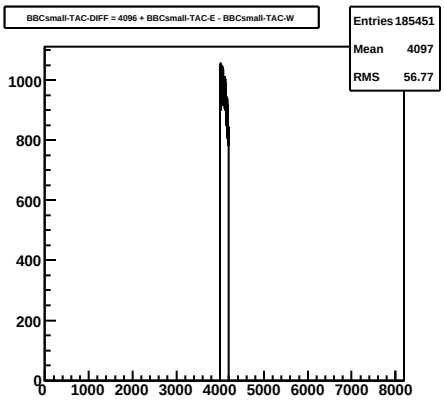

ZDC-TAC-E
Entries 6763
Mean 245.6
RMS 32.51

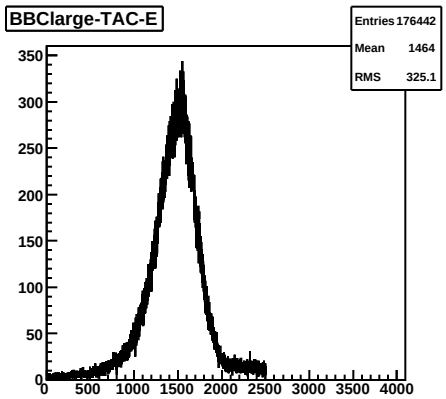

ZDC-TAC-W
Entries 7051
Mean 215.8
RMS 36.59

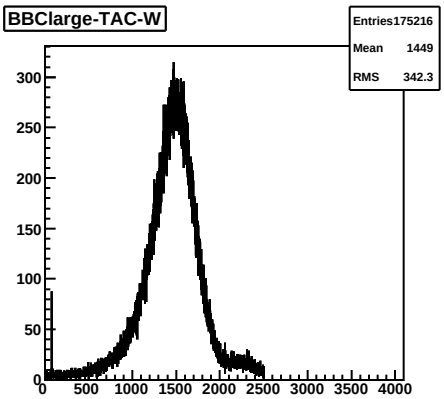

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 333
Mean 526.9
RMS 19.89

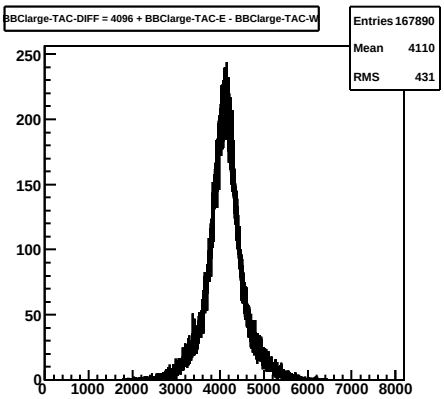

BBCsmall-TAC-E
Entries 185451
Mean 1511
RMS 166.4

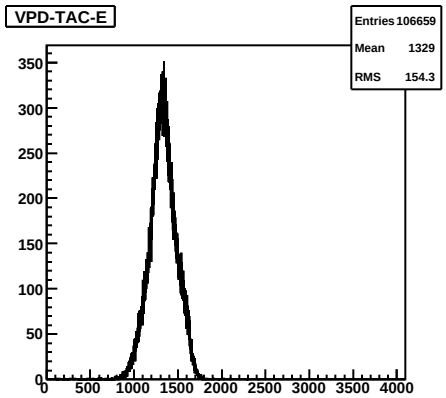

BBCsmall-TAC-W
Entries 185451
Mean 1511
RMS 168

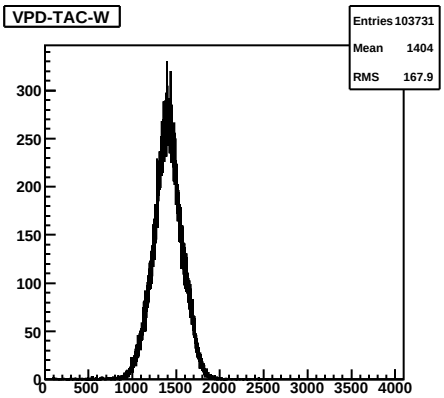

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 185451
Mean 4097
RMS 56.77

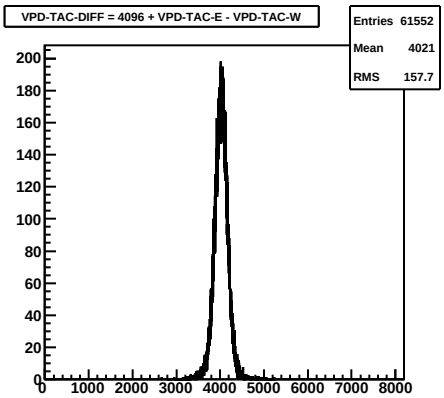

BBClarge-TAC-E
Entries 176442
Mean 1464
RMS 325.1

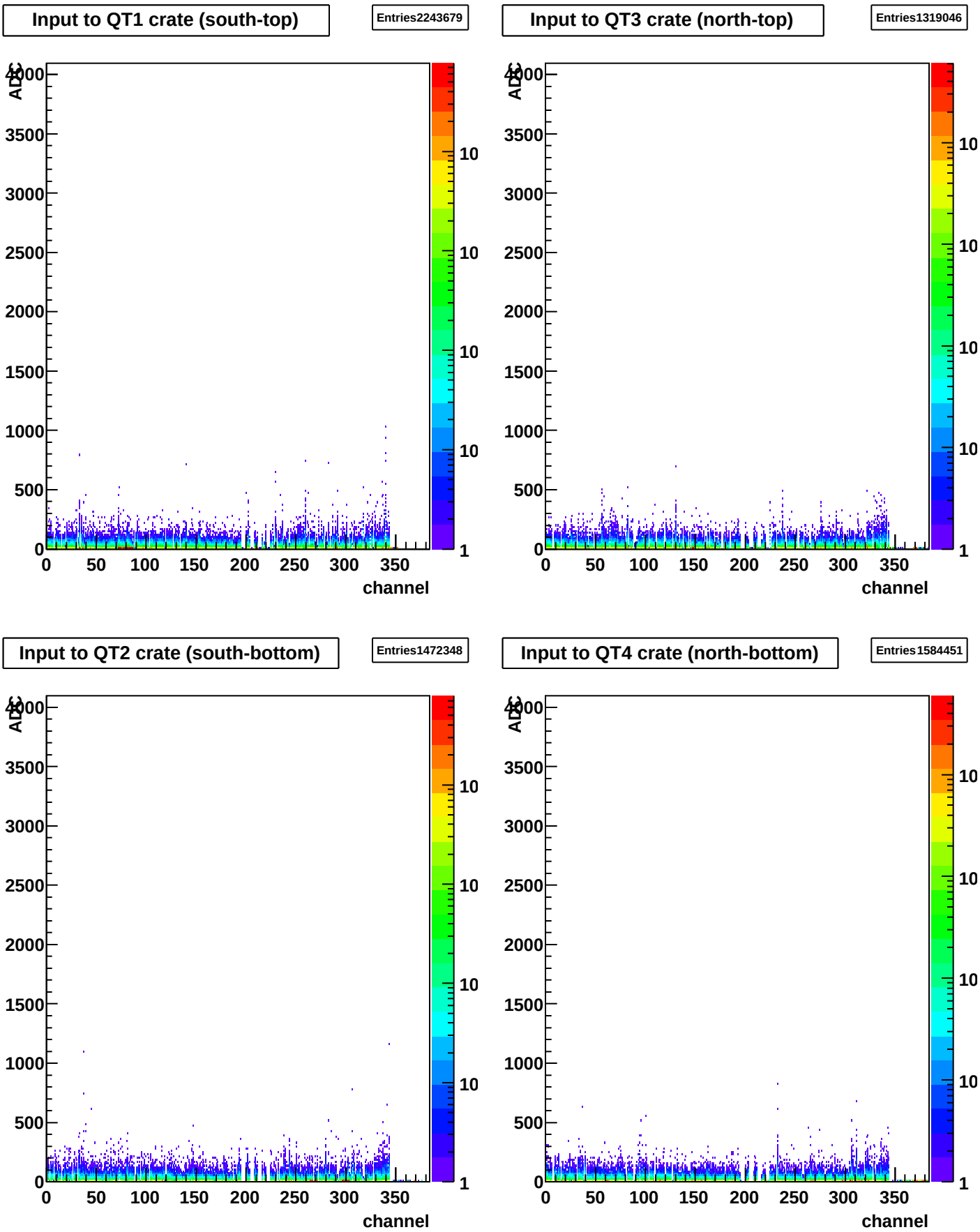

BBClarge-TAC-W
Entries 175216
Mean 1449
RMS 342.3

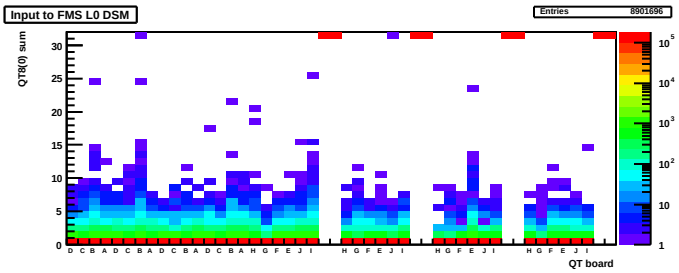

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 167890
Mean 4110
RMS 431

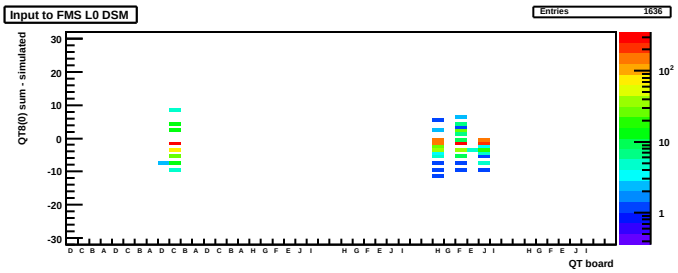

VPD-TAC-E
Entries 106659
Mean 1329
RMS 154.3

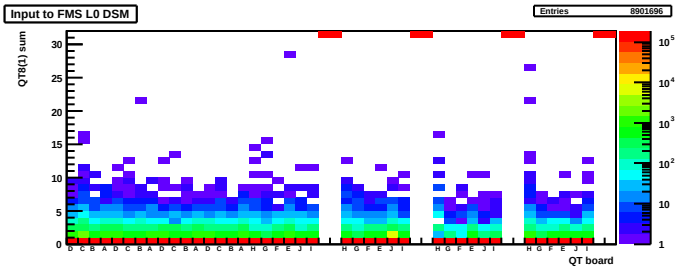

VPD-TAC-W
Entries 103731
Mean 1404
RMS 167.9

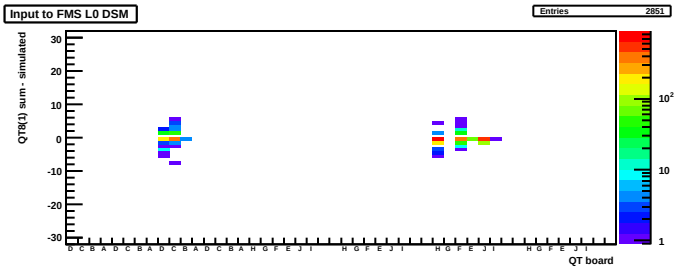

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 61552
Mean 4021
RMS 157.7

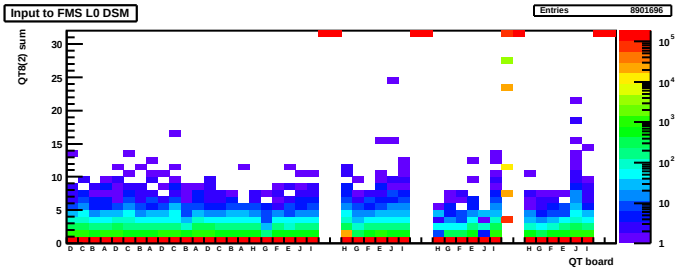

Input to QT1 crate (south-top)
Entries2243679
Input to QT3 crate (north-top)
Entries1319046
Input to QT2 crate (south-bottom)
Entries1472348
Input to QT4 crate (north-bottom)
Entries1584451
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

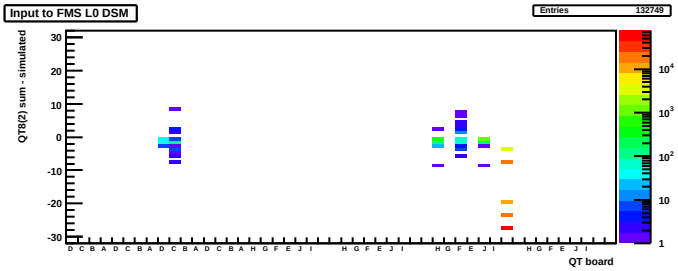

Input to FMS L0 DSM
Entries 8901696
QT8(0) sum
QT board

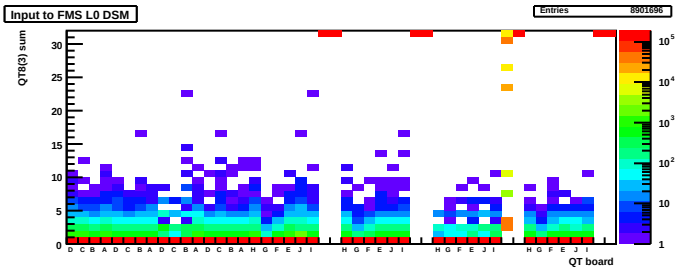

Input to FMS L0 DSM
Entries 1636
QT8(0) sum - simulated
QT board

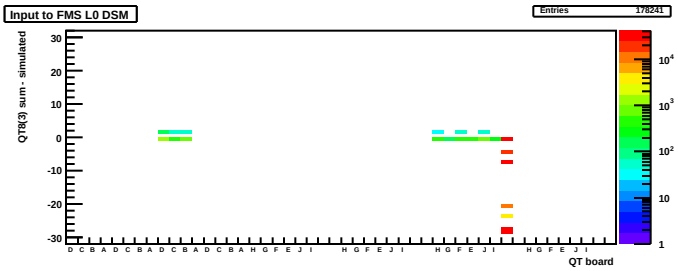

Input to FMS L0 DSM
Entries 8901696
QT8(1) sum
QT board

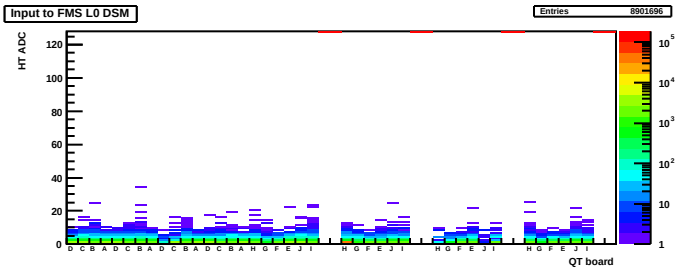

Input to FMS L0 DSM
Entries 2851
QT8(1) sum - simulated
QT board

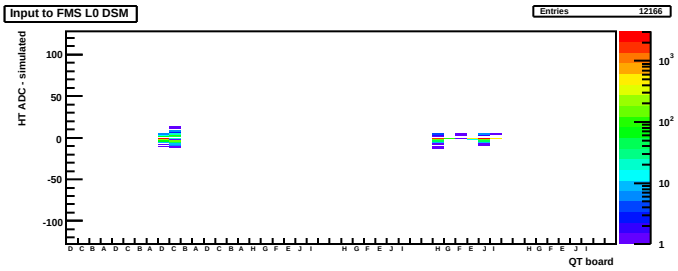

Input to FMS L0 DSM
Entries 8901696
QT8(2) sum
QT board

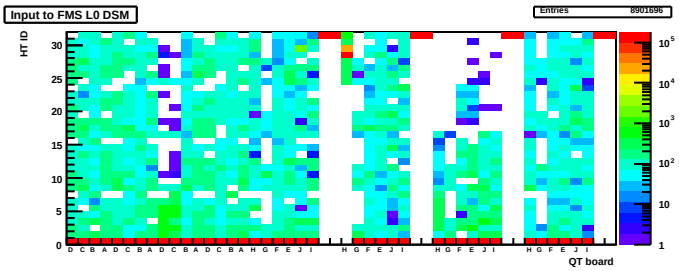

Input to FMS L0 DSM
Entries 132749
QT8(2) sum - simulated
QT board

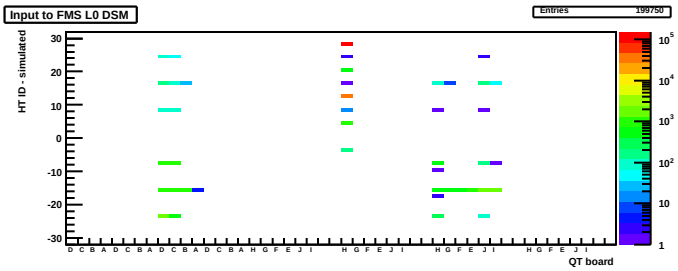

Input to FMS L0 DSM
Entries 8901696
QT8(3) sum
QT board

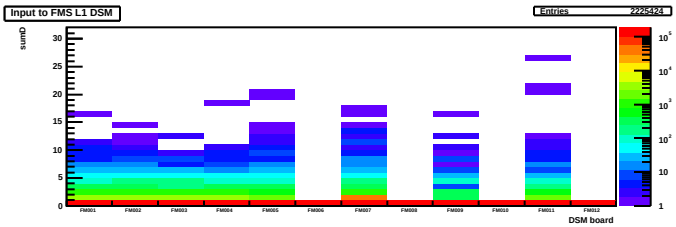

Input to FMS L0 DSM
Entries 178241
QT8(3) sum - simulated
QT board

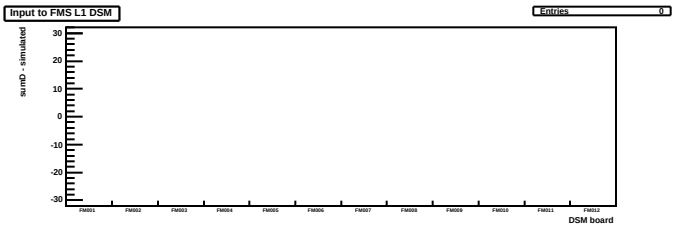

Input to FMS L0 DSM
Entries 8901696
HT ADC
QT board

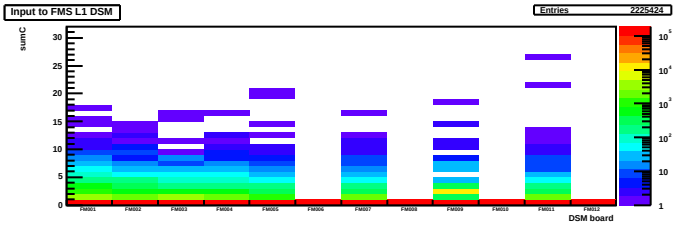

Input to FMS L0 DSM
Entries 12166
HT ADC - simulated
QT board

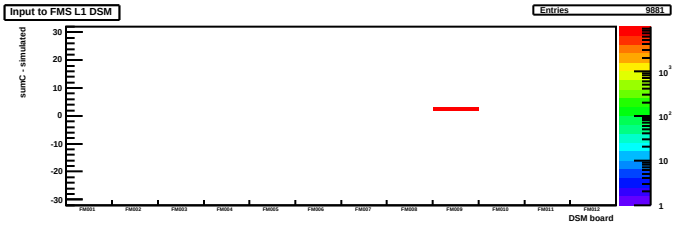

Input to FMS L0 DSM
Entries 8901696
HT ID
QT board

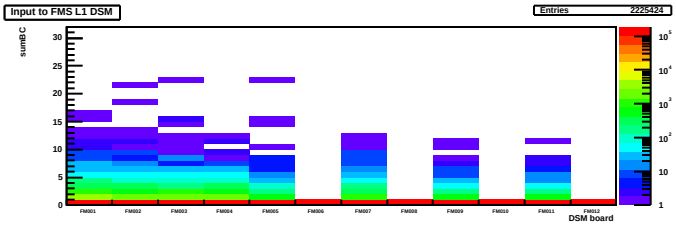

Input to FMS L0 DSM
Entries 199750
HT ID - simulated
QT board

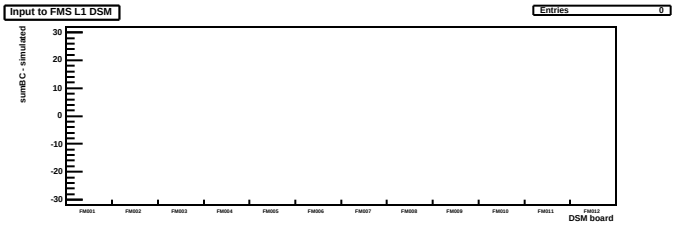

Input to FMS L1 DSM
Entries 2225424
sumD
DSM board

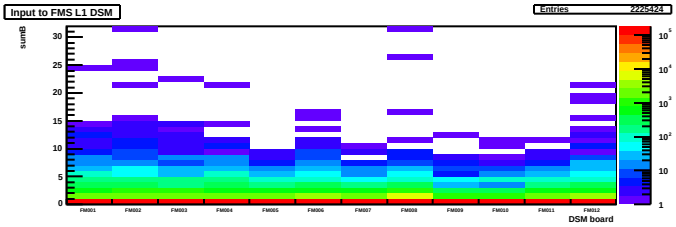

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

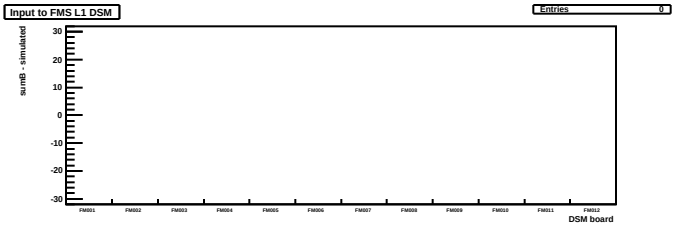

Input to FMS L1 DSM
Entries 2225424
sumC
DSM board

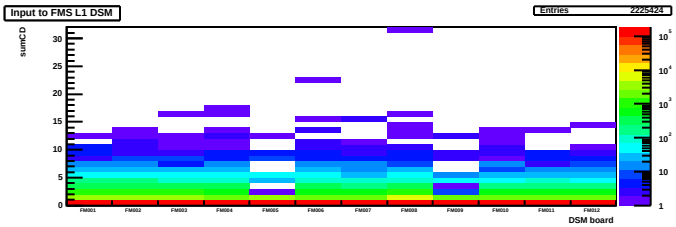

Input to FMS L1 DSM
Entries 9881
sumC - simulated
DSM board

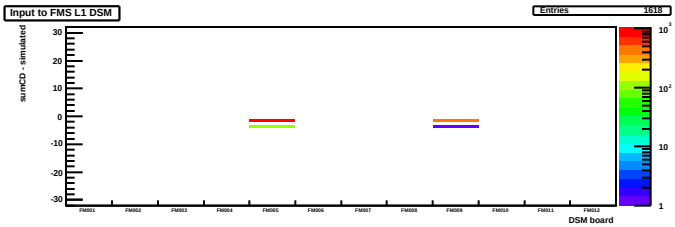

Input to FMS L1 DSM
Entries 2225424
sumBC
DSM board

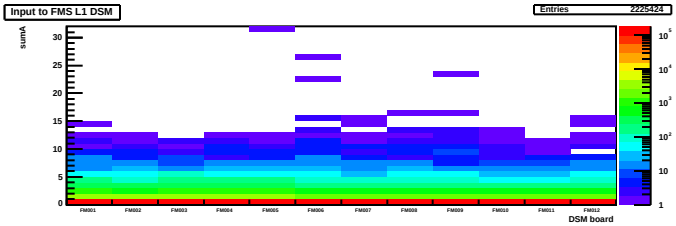

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

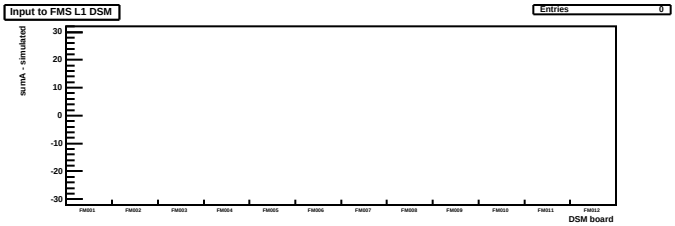

Input to FMS L1 DSM
Entries 2225424
sumB
DSM board

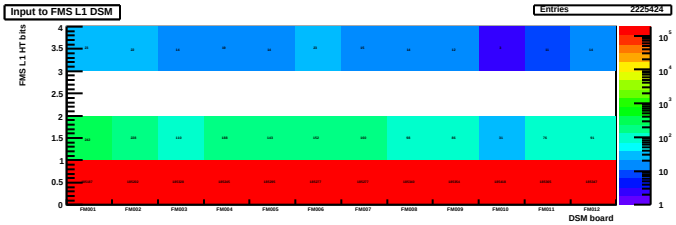

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

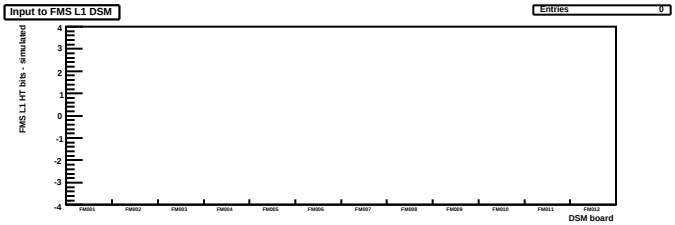

Input to FMS L1 DSM
Entries 2225424
sumCD
DSM board

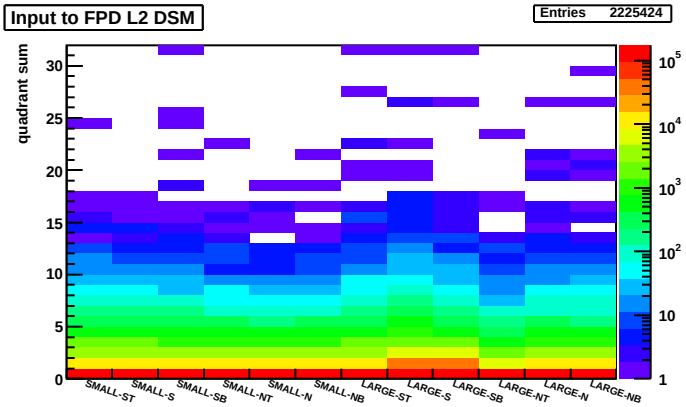

Input to FMS L1 DSM
Entries 1618
sumCD - simulated
DSM board

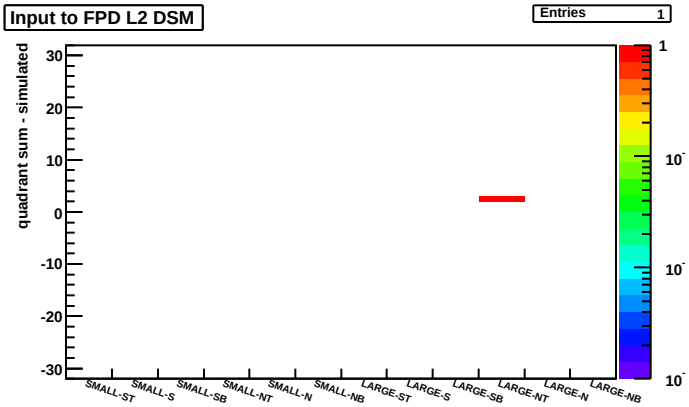

Input to FMS L1 DSM
Entries 2225424
sumA
DSM board

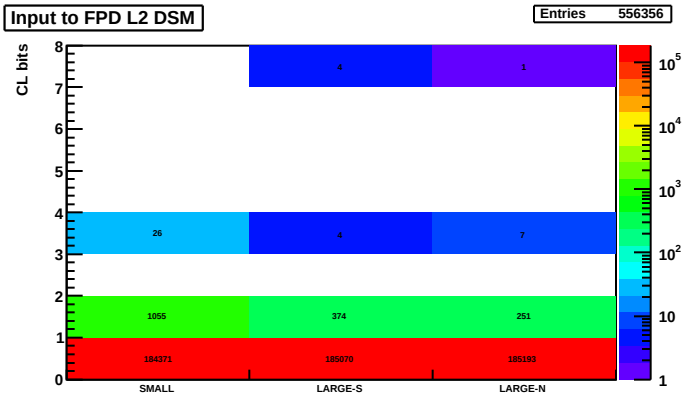

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

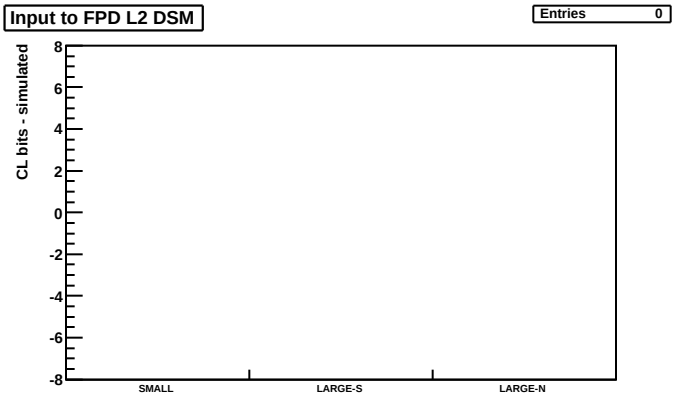

Input to FMS L1 DSM
Entries 2225424
FMS L1 HT bits
DSM board

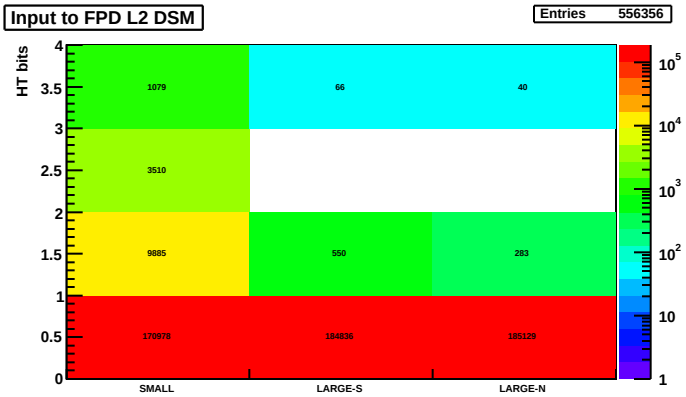

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

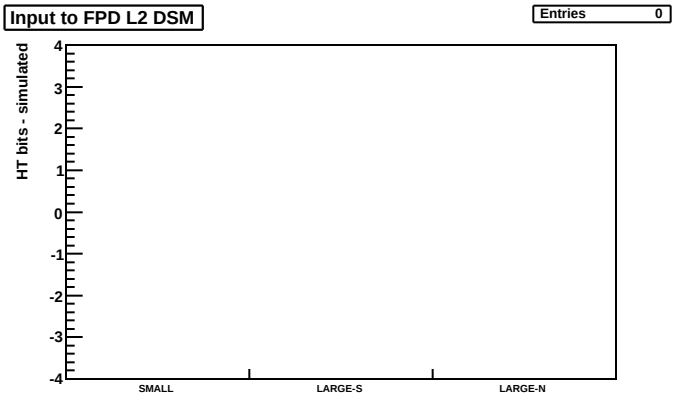

Input to FPD L2 DSM
Entries 2225424
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

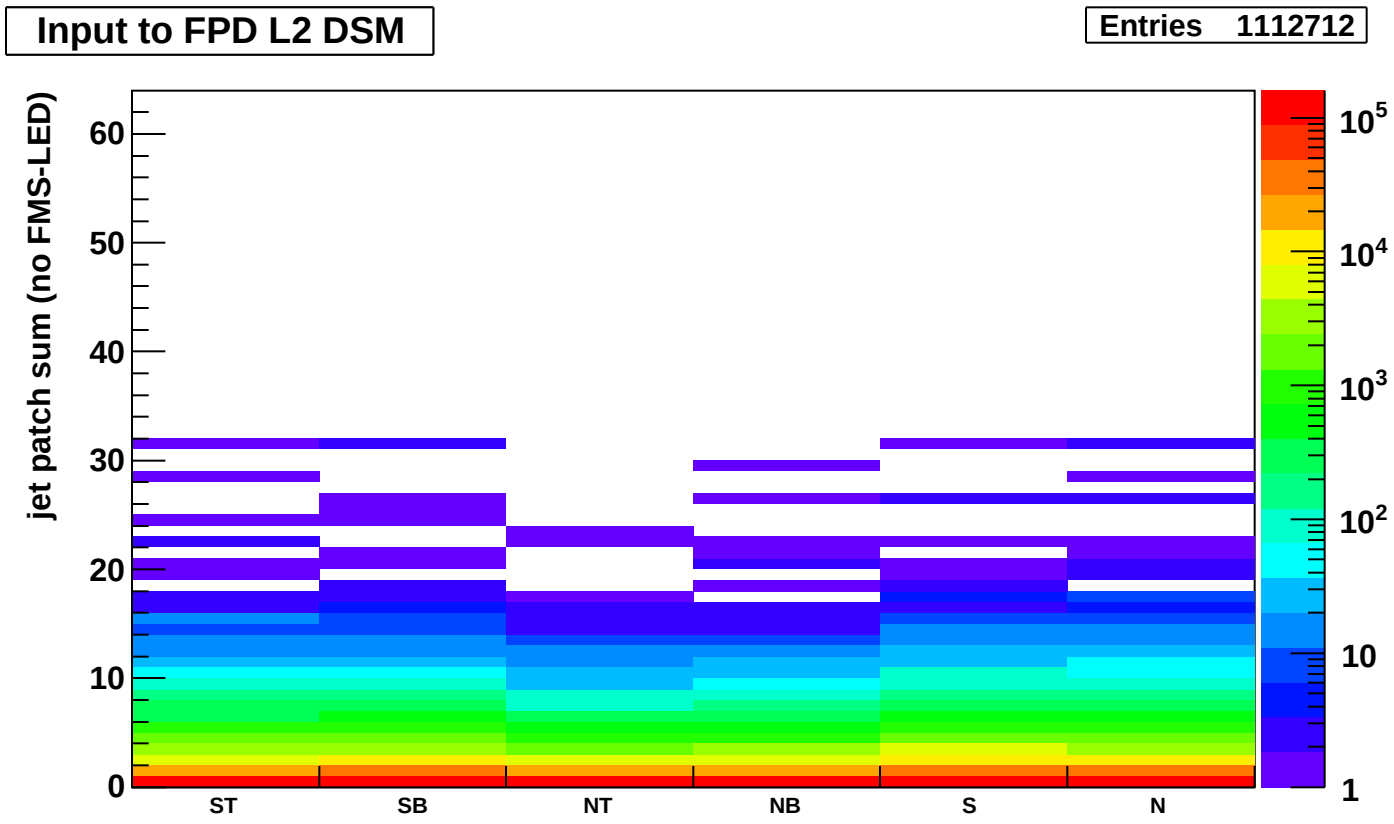

Input to FPD L2 DSM
Entries 1
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

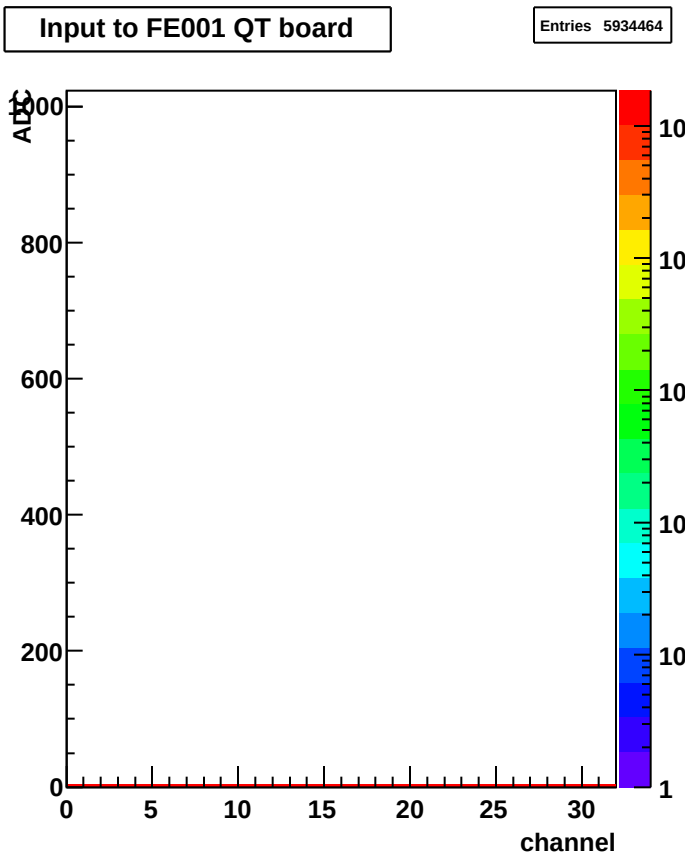

Input to FPD L2 DSM
Entries 556356
CL bits
4
1
26
4
7
1055
374
251
184371
185070
185193
SMALL
LARGE-S
LARGE-N

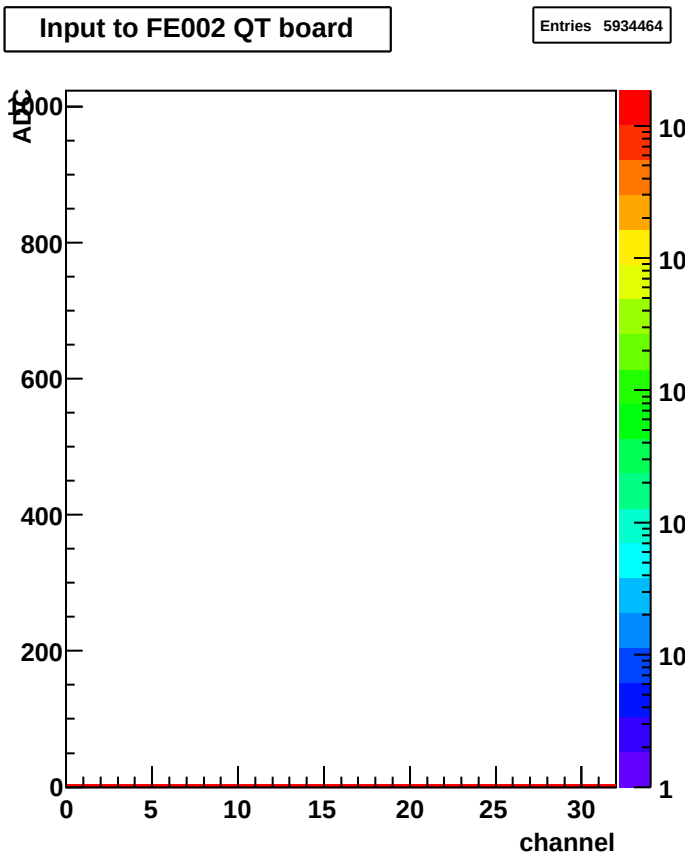

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

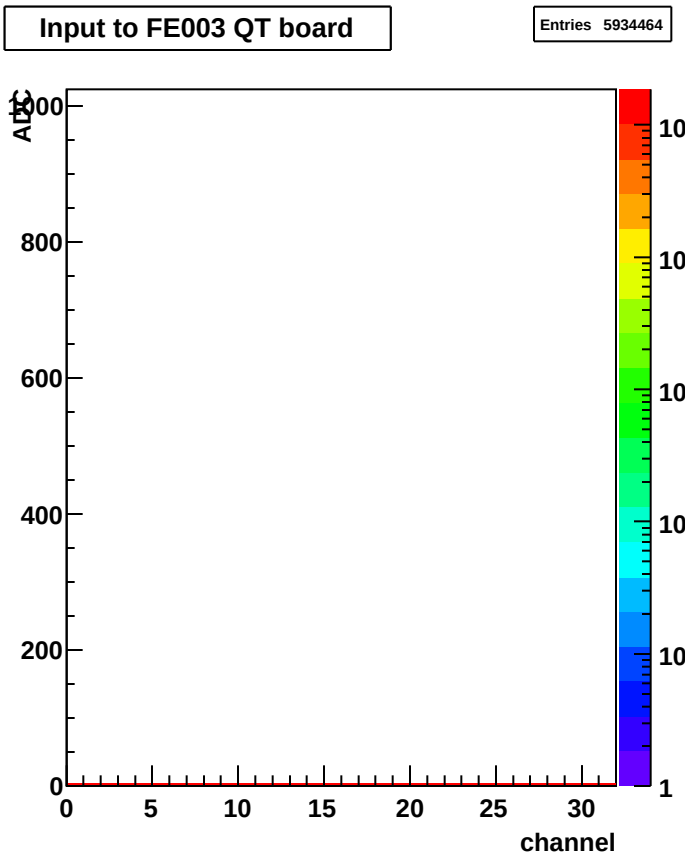

Input to FPD L2 DSM
Entries 556356
HT bits
1079
66
40
3510
9885
550
283
170978
184836
185129
SMALL
LARGE-S
LARGE-N

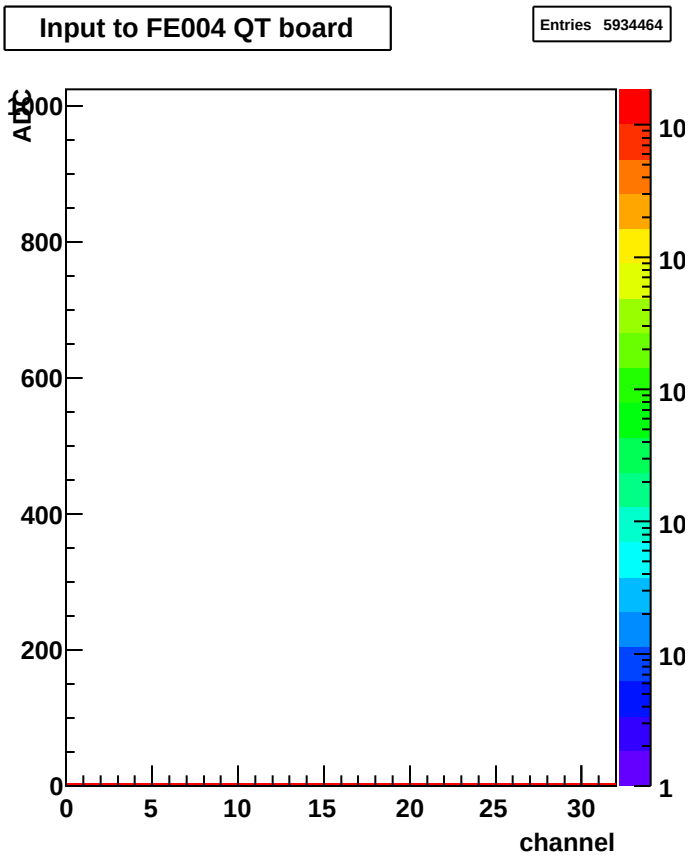

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

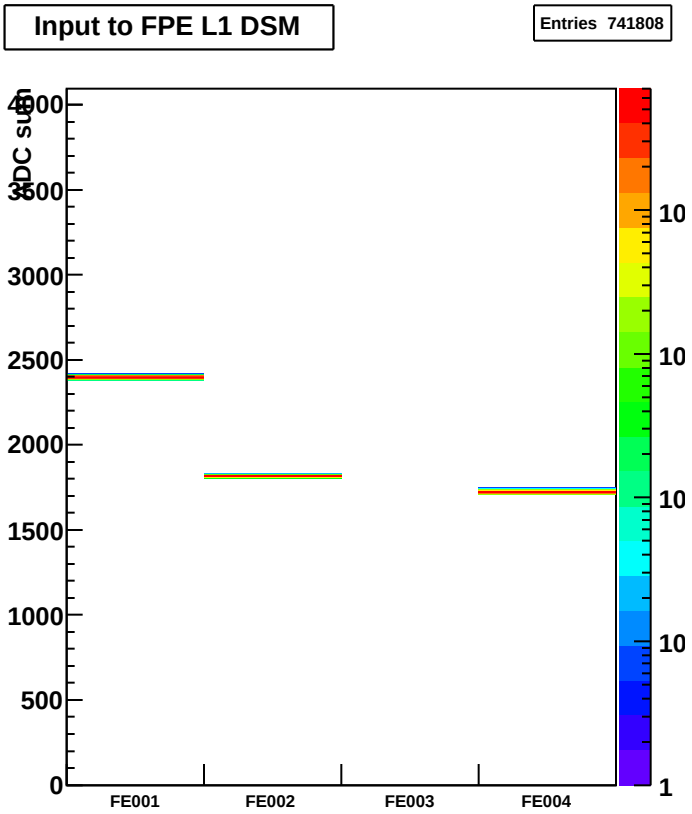

Input to FPD L2 DSM
Entries 1112712
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 5934464
ADC
channel

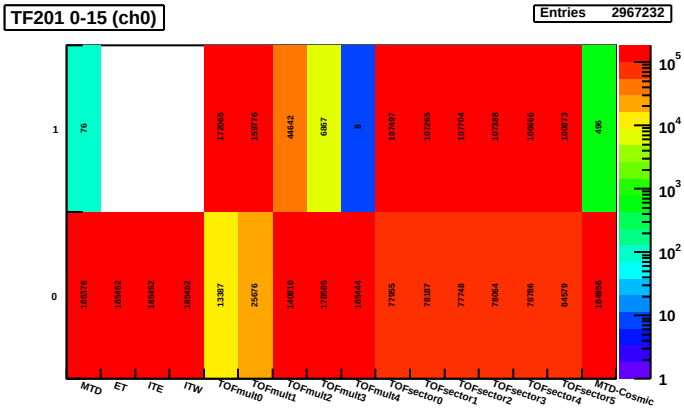

Input to FE002 QT board
Entries 5934464
ADC
channel


Input to FE003 QT board
Entries 5934464
ADC
channel

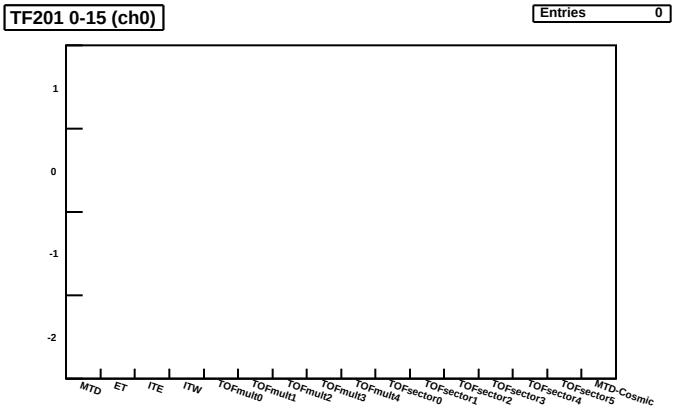

Input to FE004 QT board
Entries 5934464
ADC
channel


Input to FPE L1 DSM
Entries 741808
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

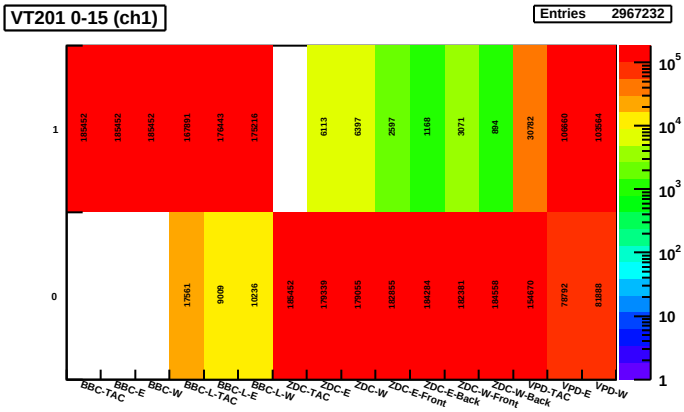
TF201 0-15 (ch0)



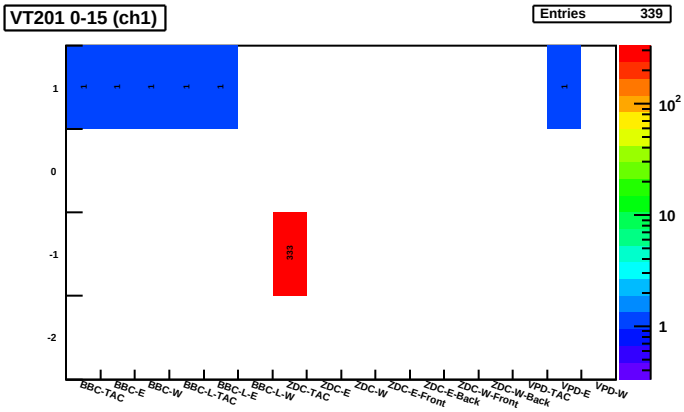
TF201 0-15 (ch0)



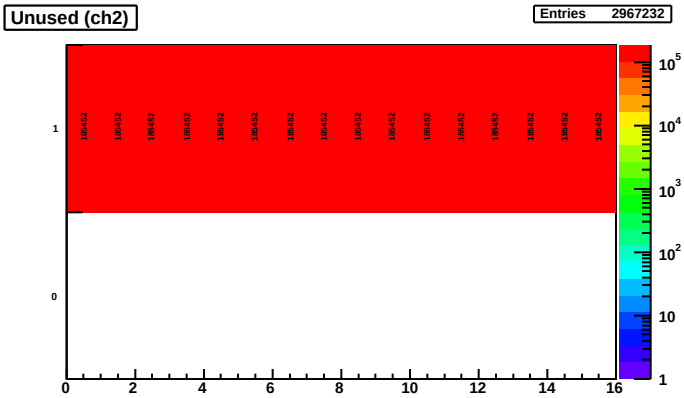
VT201 0-15 (ch1)

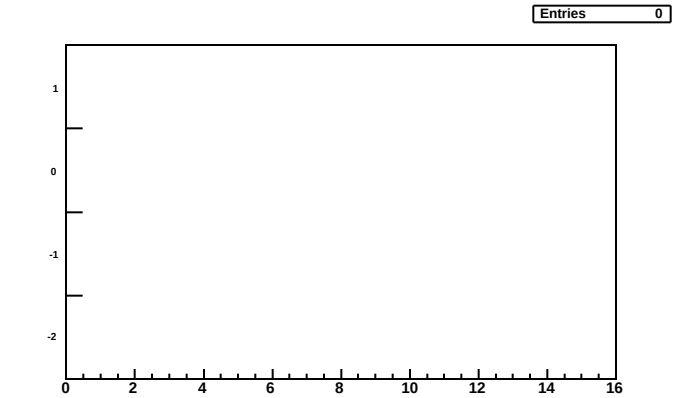


VT201 0-15 (ch1)

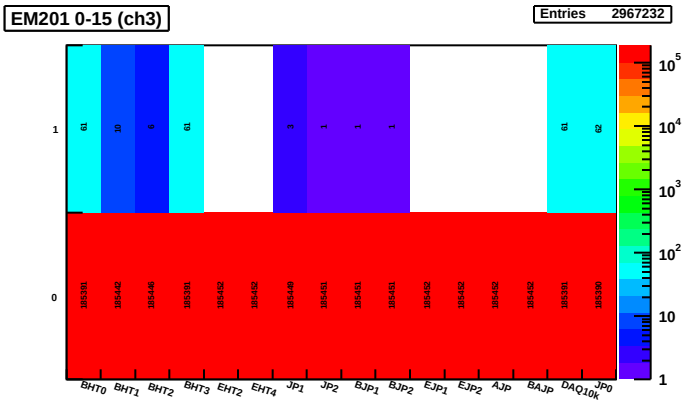


Unused (ch2)

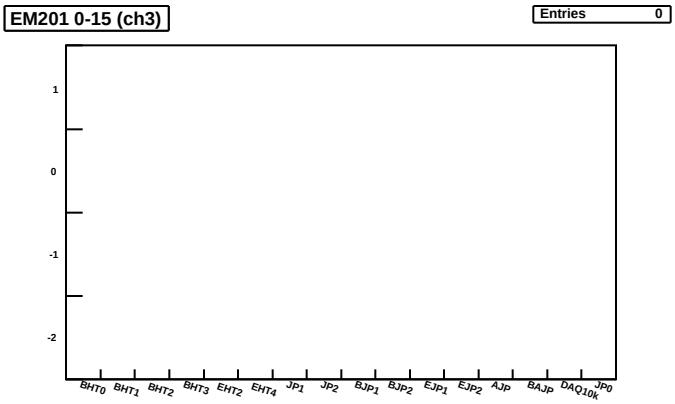




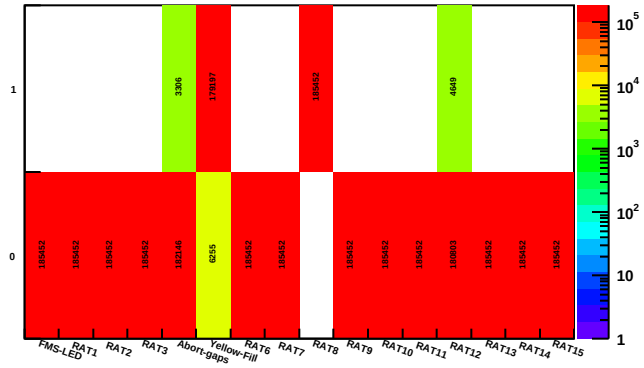
EM201 0-15 (ch3)

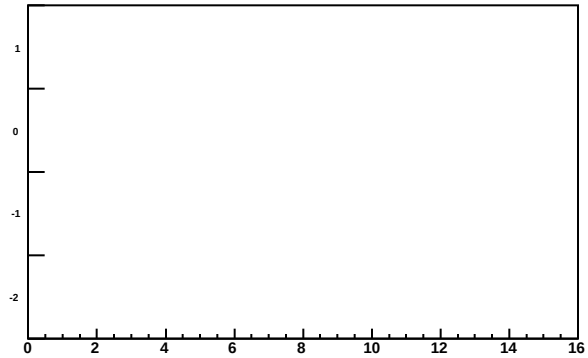


EM201 0-15 (ch3)

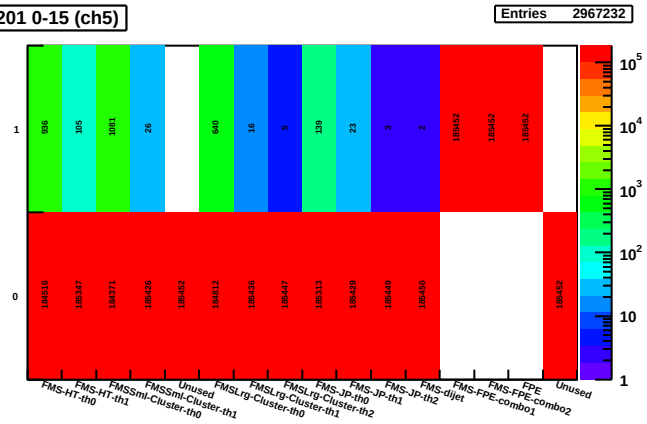


RAT board (ch4)

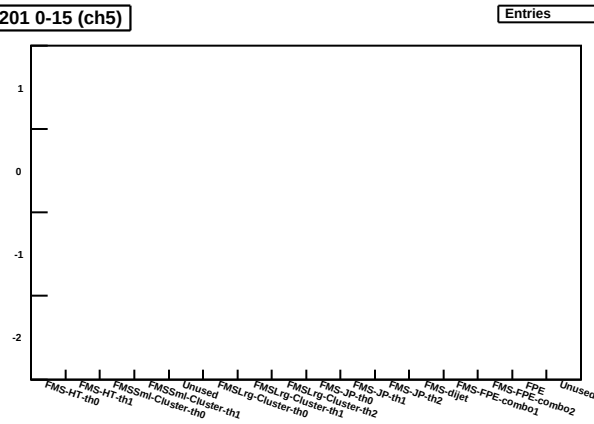




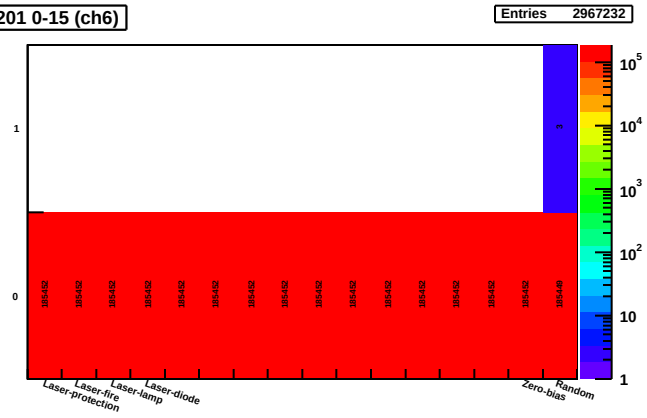
FP201 0-15 (ch5)

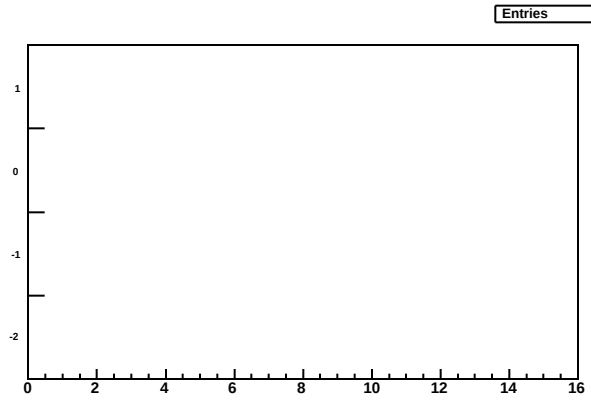
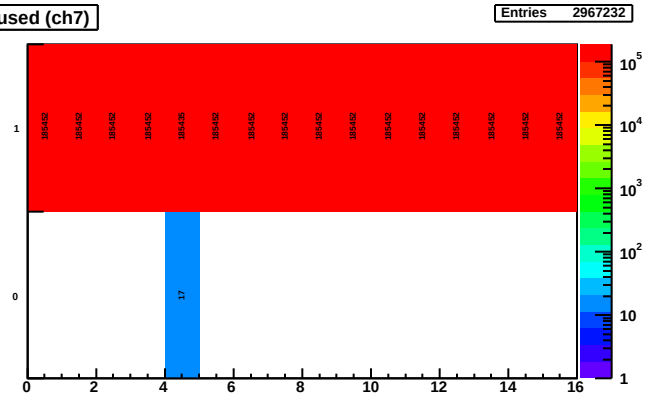
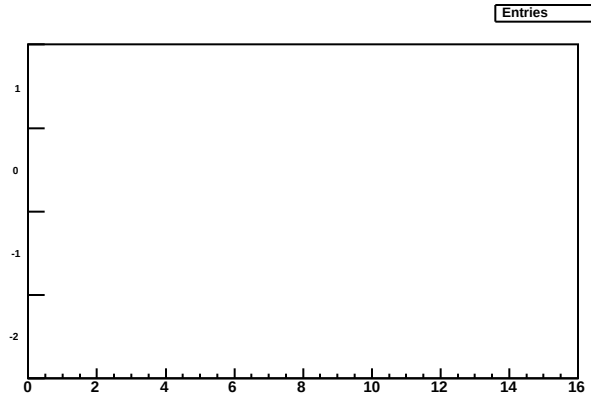


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)