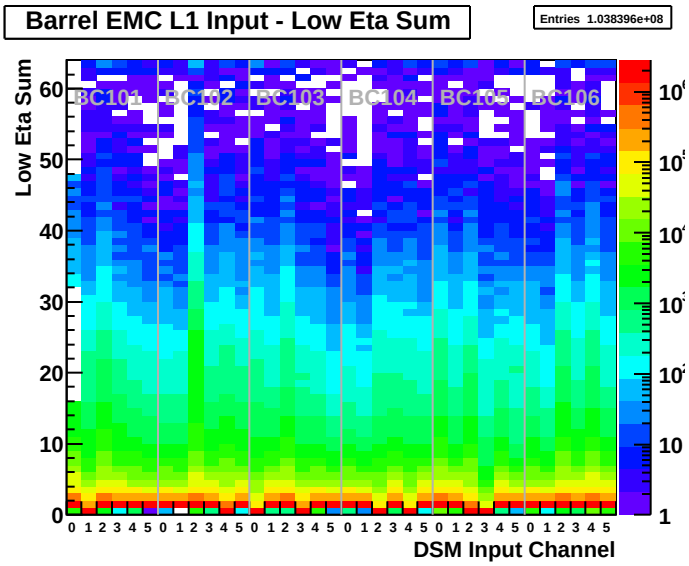
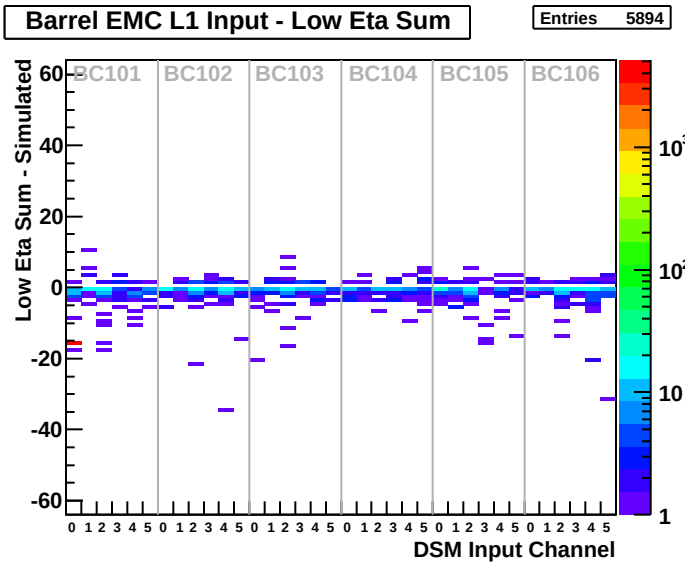
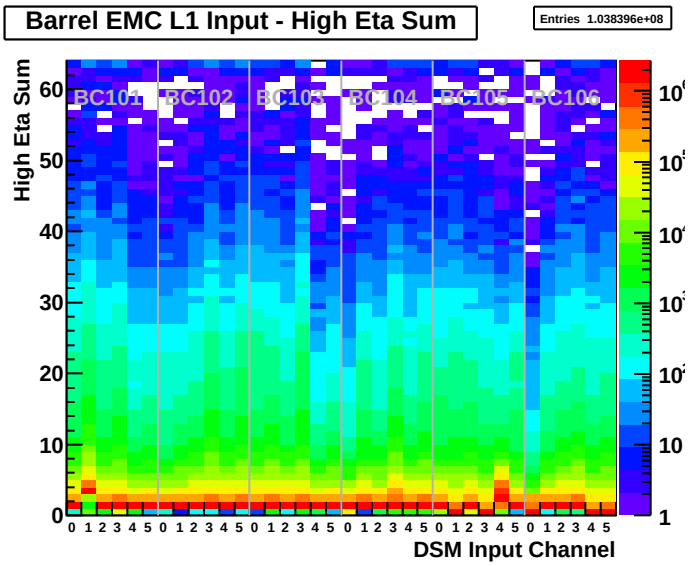
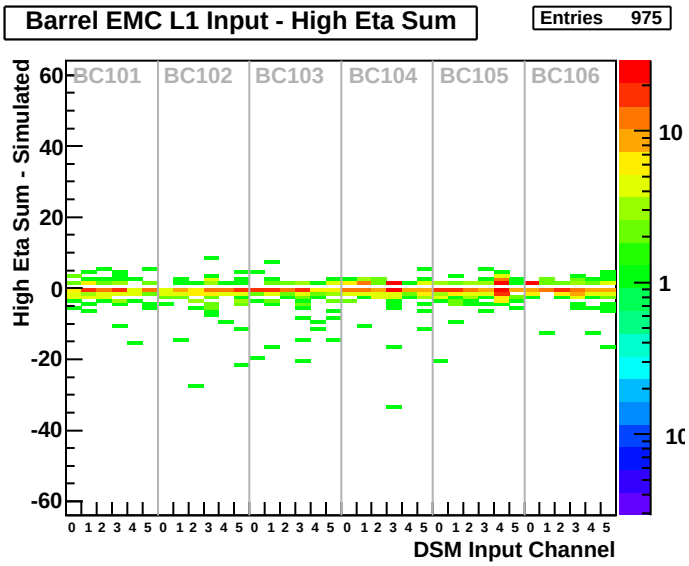
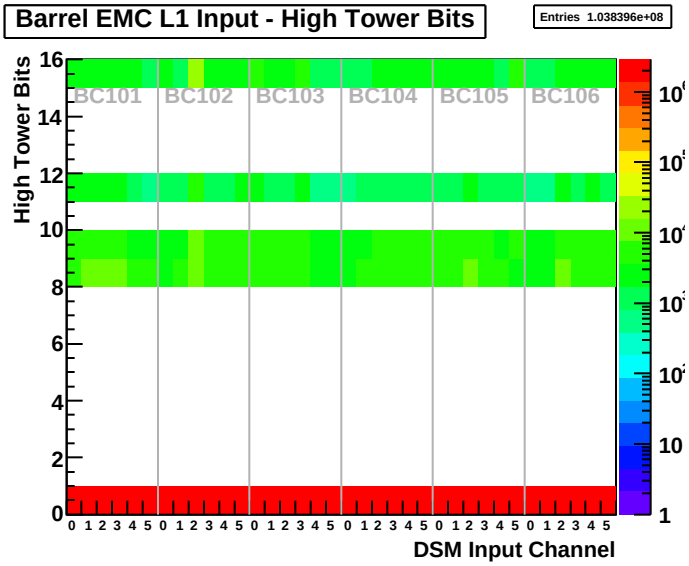
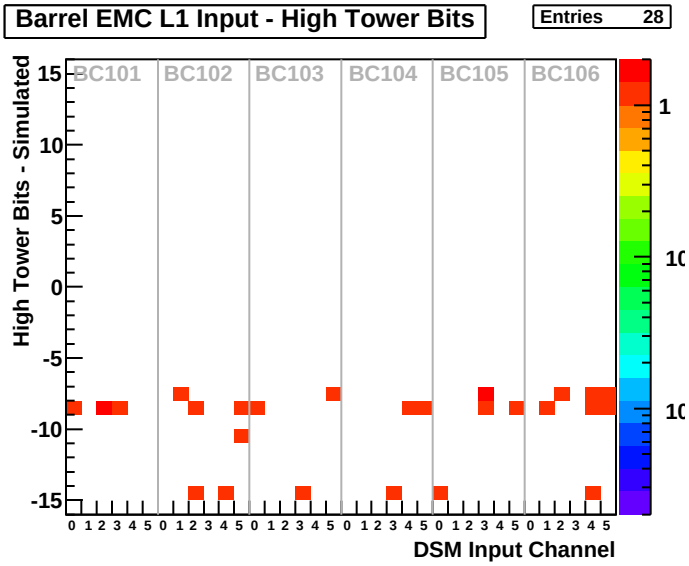

Barrel EMC L1 Input - Low Eta Sum
Entries 1.038396e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

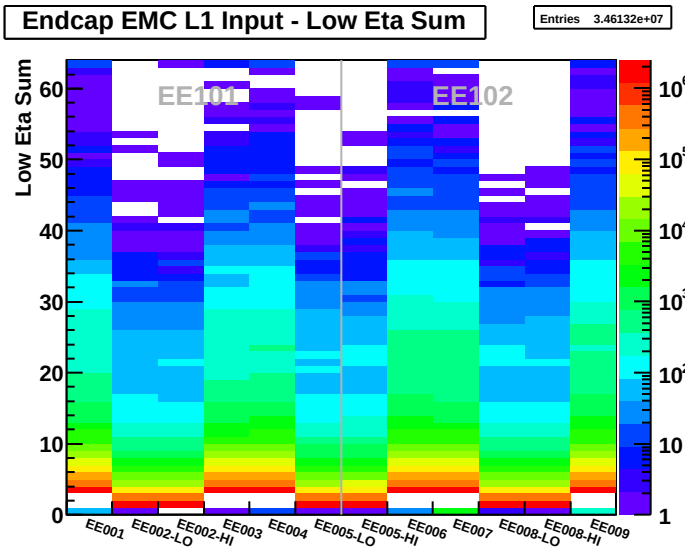

Barrel EMC L1 Input - Low Eta Sum
Entries 5894
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

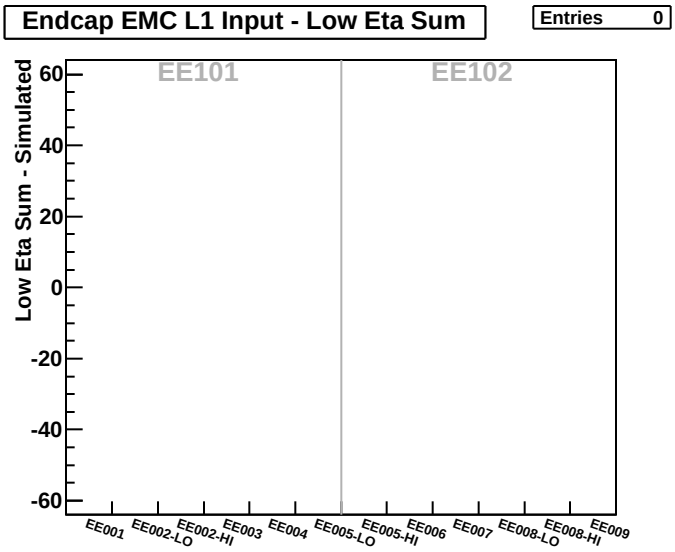

Barrel EMC L1 Input - High Eta Sum
Entries 1.038396e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

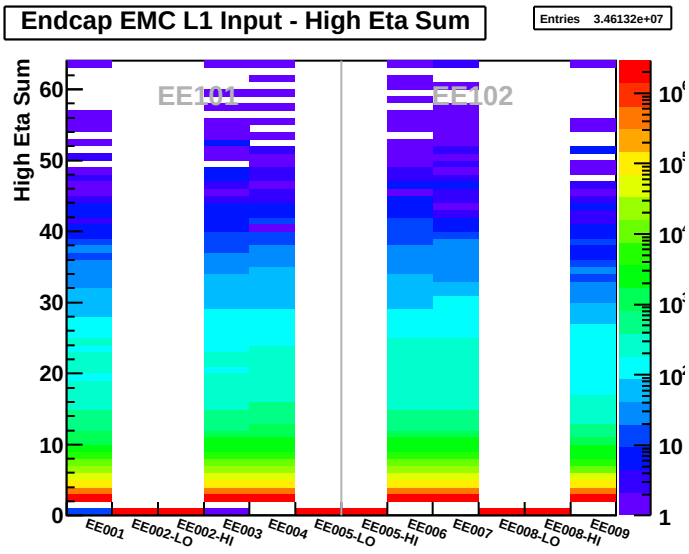

Barrel EMC L1 Input - High Eta Sum
Entries 975
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

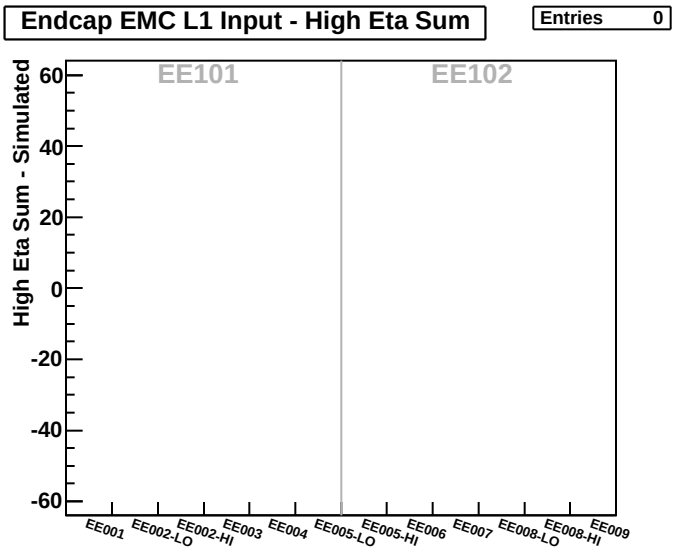

Barrel EMC L1 Input - High Tower Bits
Entries 1.038396e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

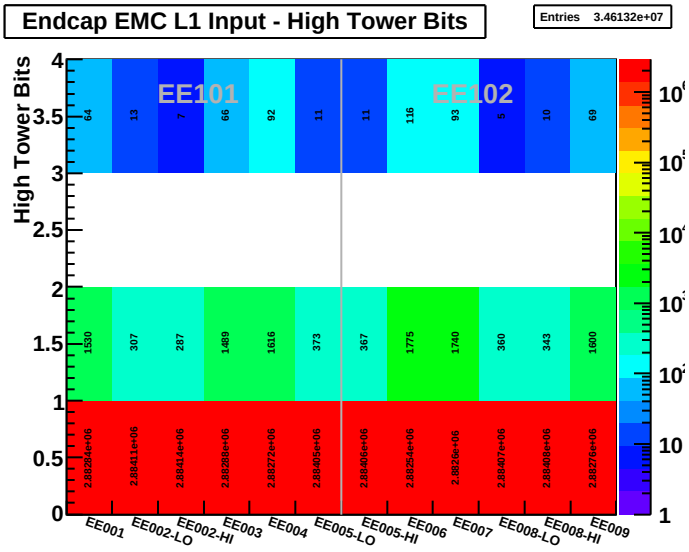

Barrel EMC L1 Input - High Tower Bits
Entries 28
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

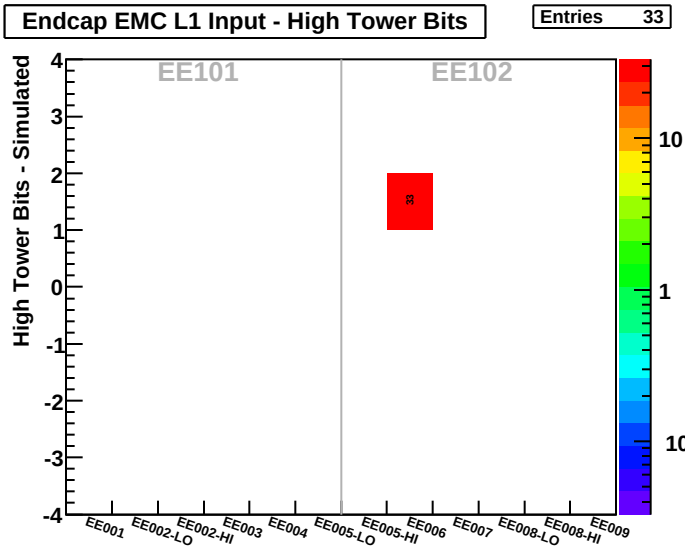

Endcap EMC L1 Input - Low Eta Sum
Entries 3.46132e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

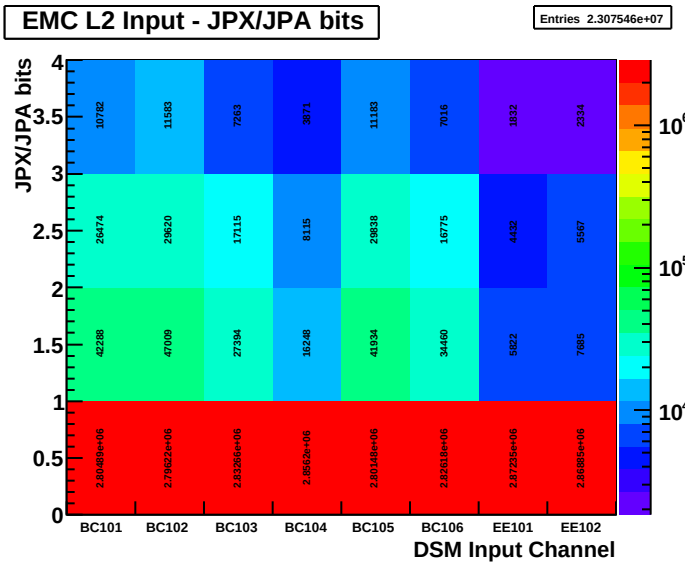

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

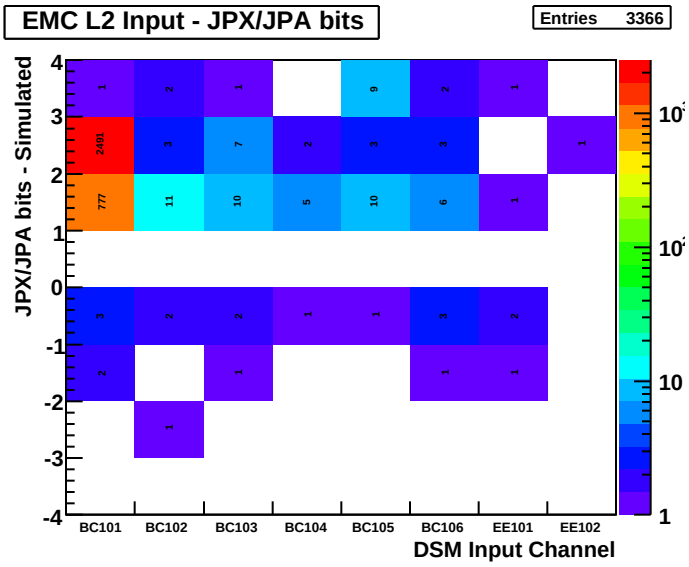

Endcap EMC L1 Input - High Eta Sum
Entries 3.46132e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

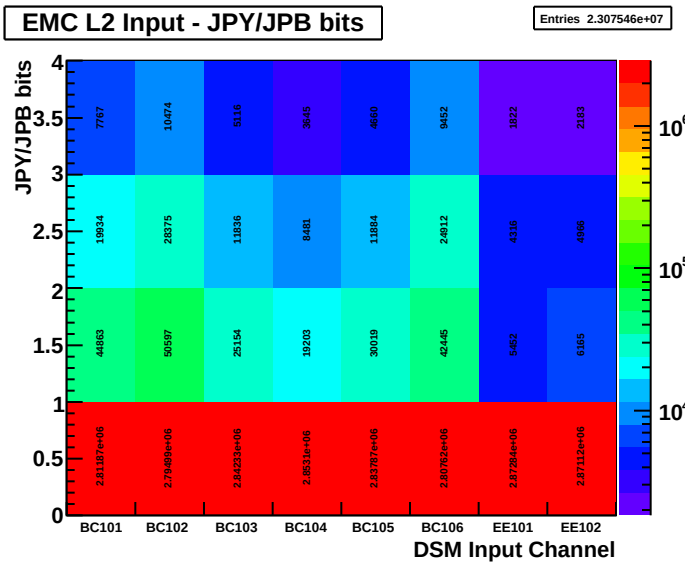

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

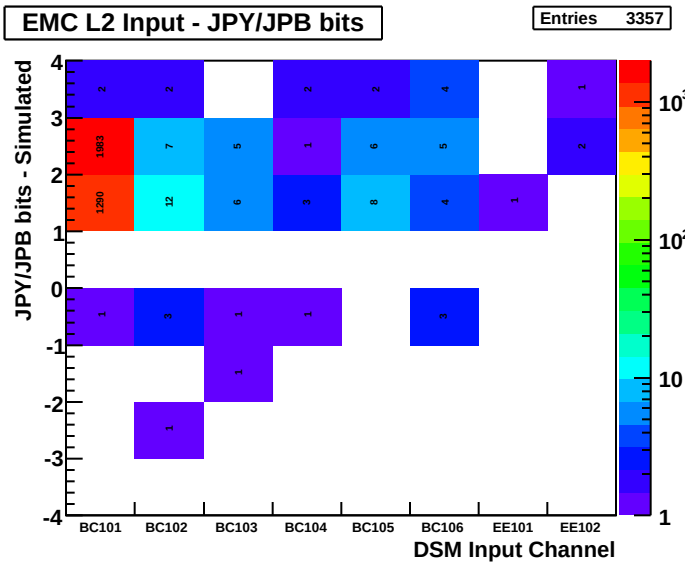

Endcap EMC L1 Input - High Tower Bits
Entries 3.46132e+07
High Tower Bits
EE101
EE102
64 13 7 66 92 11 11 116 93 5 10 69
1530 307 287 1489 1616 373 367 1775 1740 360 343 1600
2.88284e+06 2.88411e+06 2.88414e+06 2.88288e+06 2.88272e+06 2.88405e+06 2.88406e+06 2.88254e+06 2.8826e+06 2.88407e+06 2.88408e+06 2.88276e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

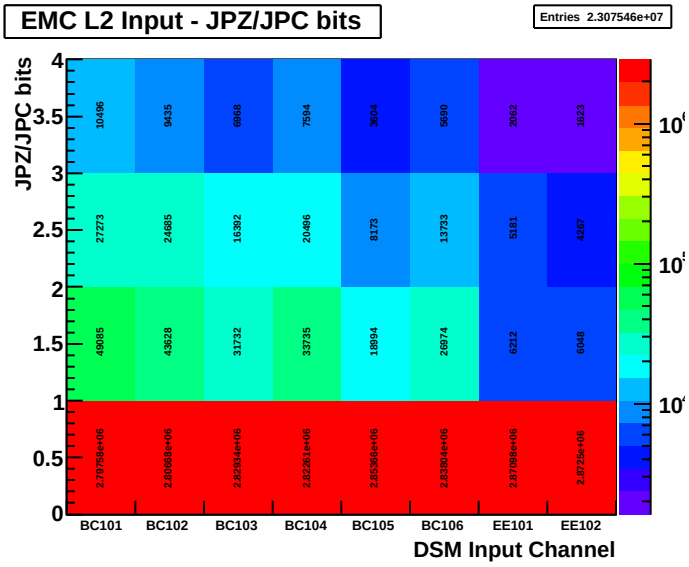

Endcap EMC L1 Input - High Tower Bits
Entries 33
High Tower Bits - Simulated
EE101
EE102
33
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

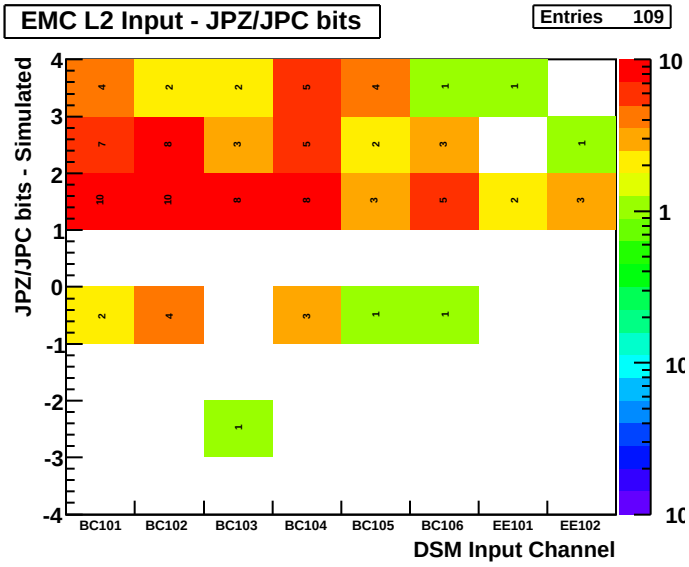

EMC L2 Input - JPX/JPA bits
Entries 2.307546e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10782 11583 7263 3871 11183 7016 1832 2334
26474 29620 17115 8115 29838 16775 4432 5567
42288 47009 27394 16248 41934 34460 5822 7685
2.80489e+06 2.79622e+06 2.83266e+06 2.8562e+06 2.80149e+06 2.82618e+06 2.87235e+06 2.86885e+06

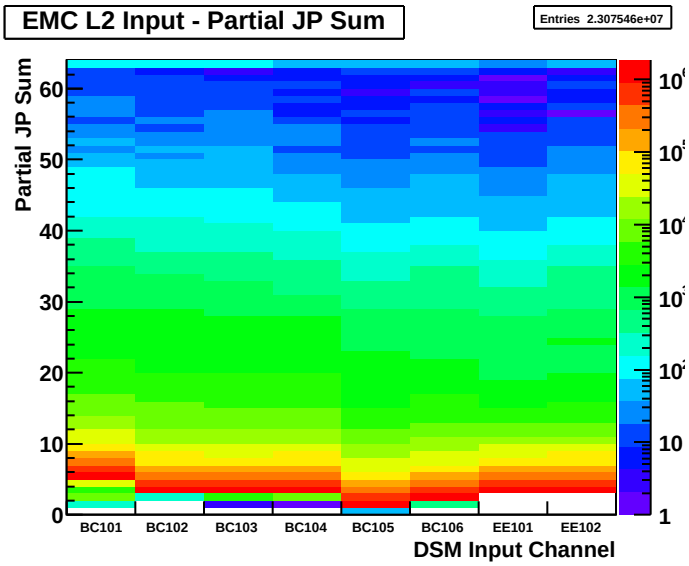

EMC L2 Input - JPX/JPA bits
Entries 3366
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

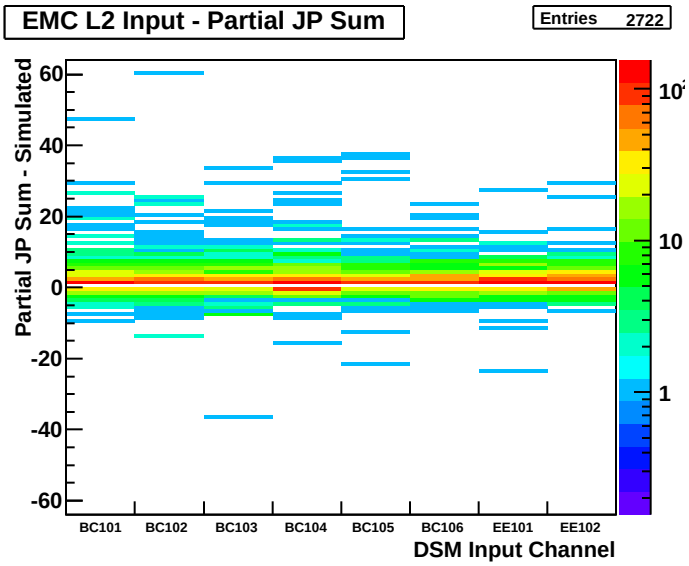

EMC L2 Input - JPY/JPB bits
Entries 2.307546e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7767 10474 5116 3645 4660 9452 1822 2183
19934 28375 11836 8481 11884 24912 4316 4966
44863 50597 25154 19203 30019 42445 5452 6165
2.81187e+06 2.79489e+06 2.84233e+06 2.8531e+06 2.83787e+06 2.80782e+06 2.87284e+06 2.87112e+06

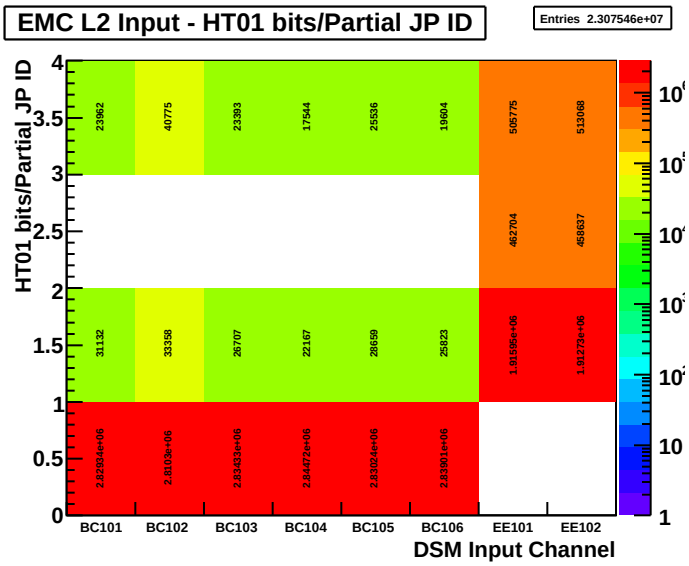

EMC L2 Input - JPY/JPB bits
Entries 3357
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

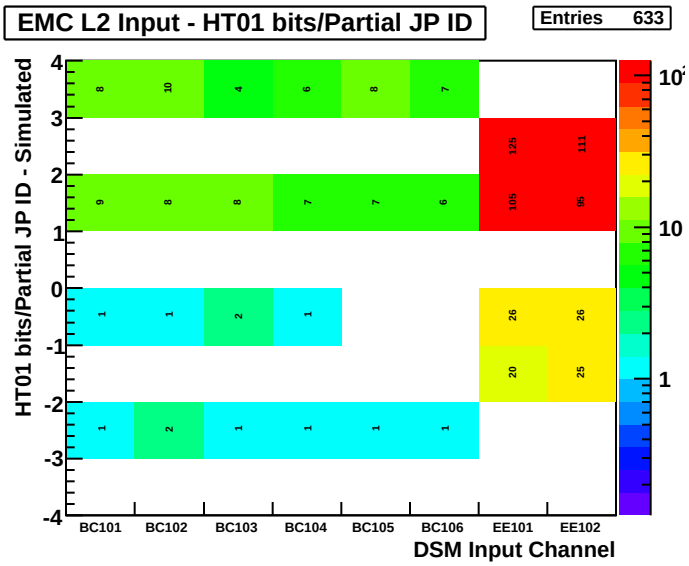

EMC L2 Input - JPZ/JPC bits
Entries 2.307546e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10496 9435 6068 7594 3604 5690 2062 1623
27273 24685 16392 20496 8173 13733 5181 4267
49085 43628 31732 33735 18994 26974 6212 6048
2.79758e+06 2.80668e+06 2.82934e+06 2.82261e+06 2.85356e+06 2.83804e+06 2.87098e+06 2.8725e+06

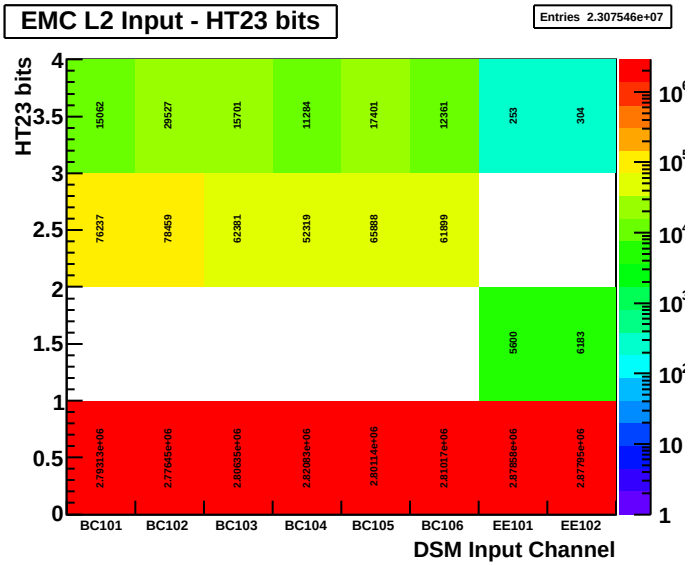

EMC L2 Input - JPZ/JPC bits
Entries 109
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

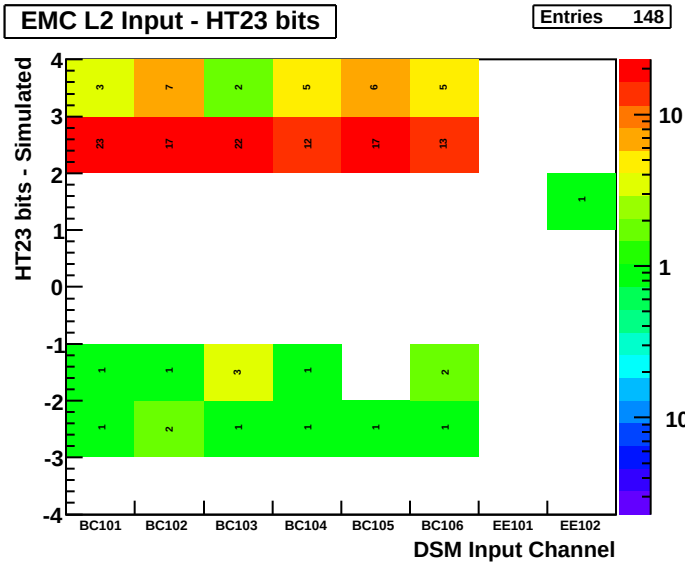

EMC L2 Input - Partial JP Sum
Entries 2.307546e+07
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

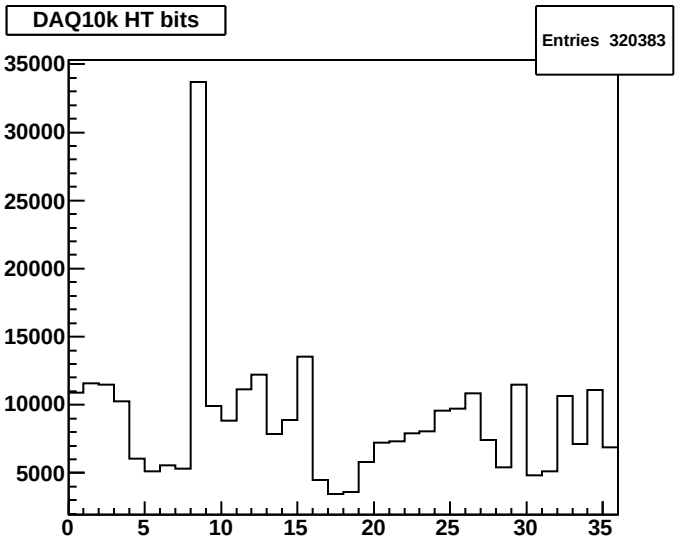

EMC L2 Input - Partial JP Sum
Entries 2722
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

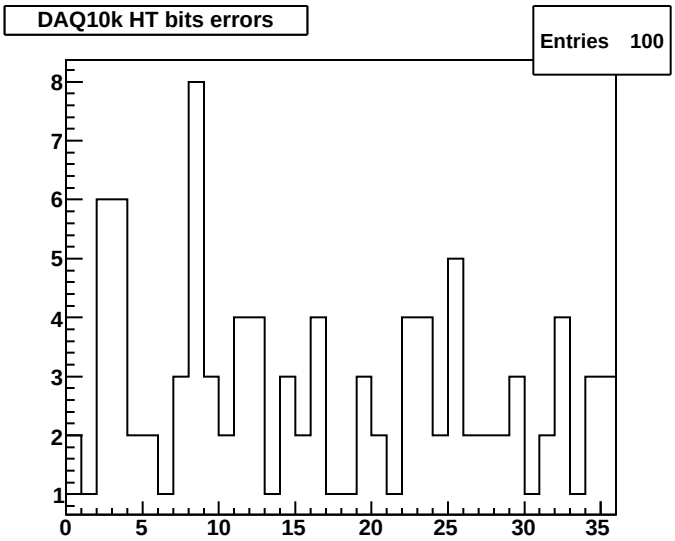

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.307546e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

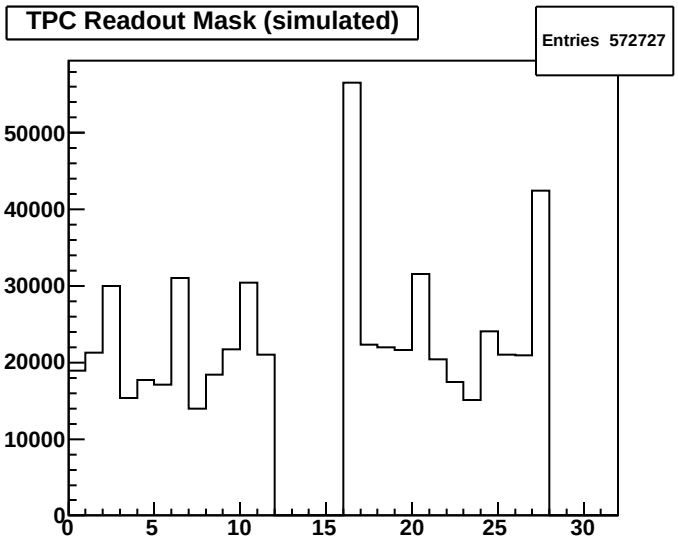

EMC L2 Input - HT01 bits/Partial JP ID
Entries 633
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

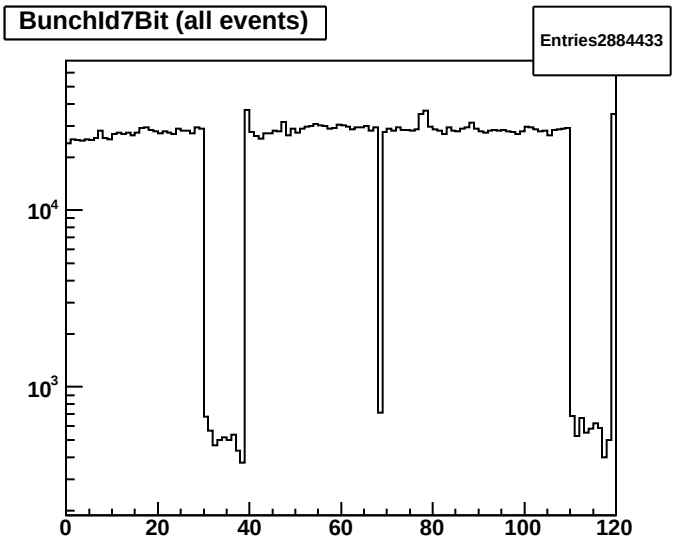

EMC L2 Input - HT23 bits
Entries 2.307546e+07
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

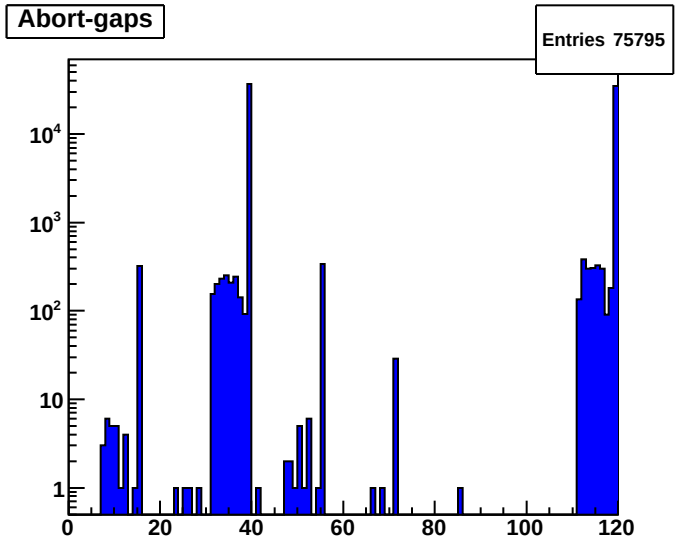

EMC L2 Input - HT23 bits
Entries 148
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

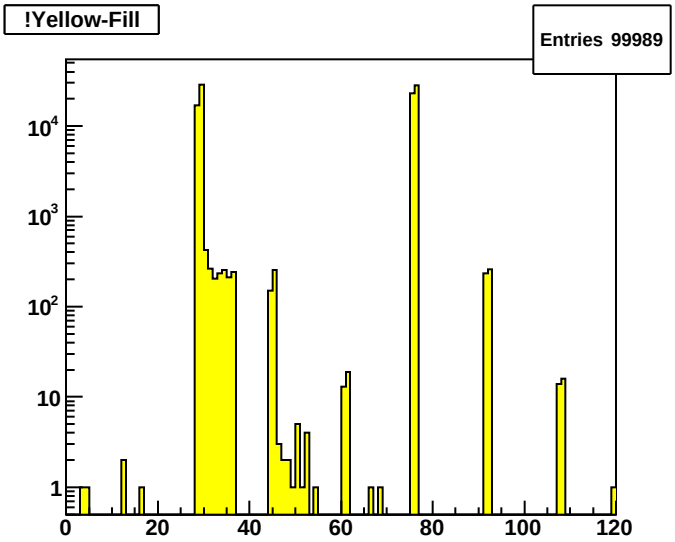

DAQ10k HT bits
Entries 320383

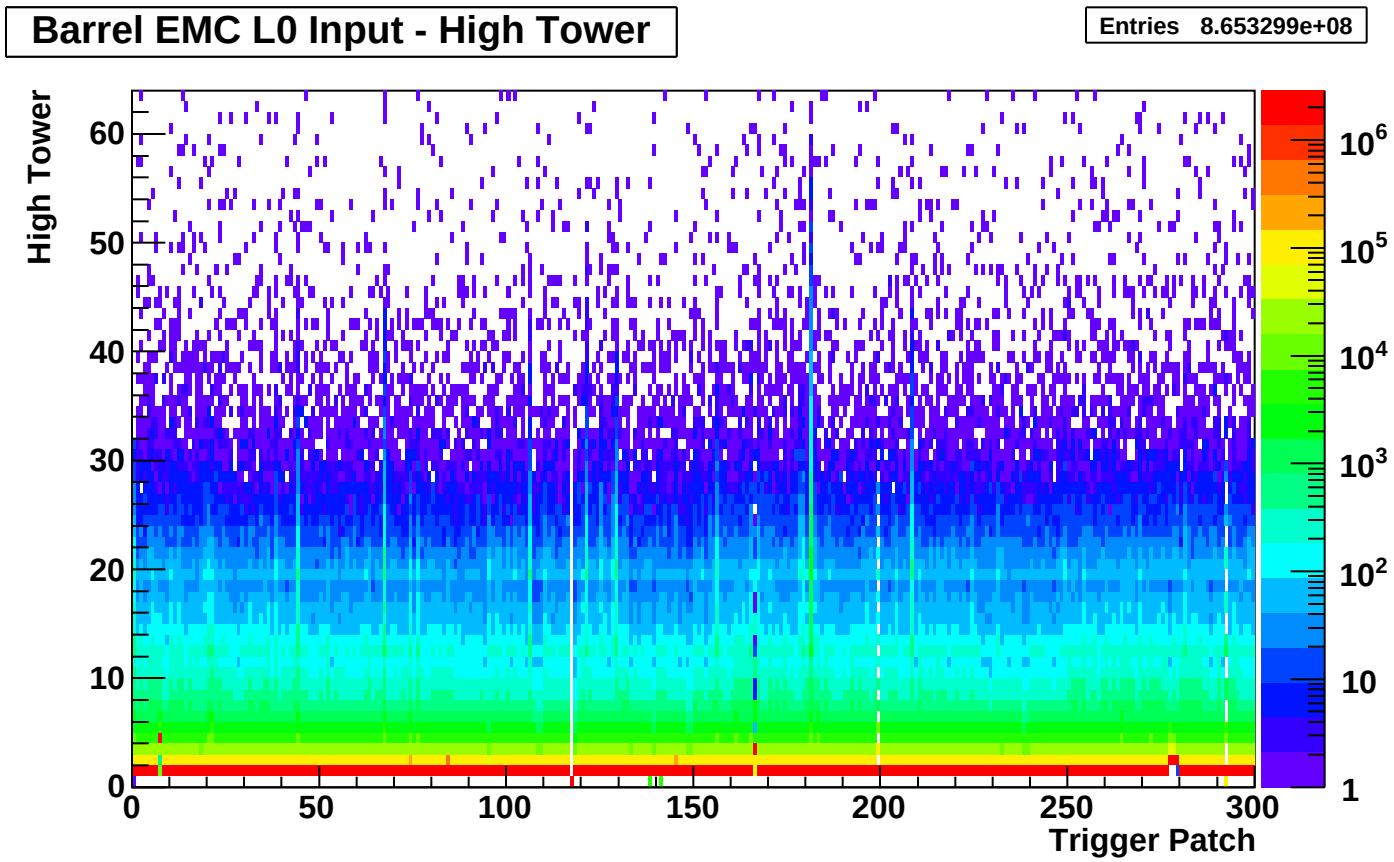

DAQ10k HT bits errors
Entries 100

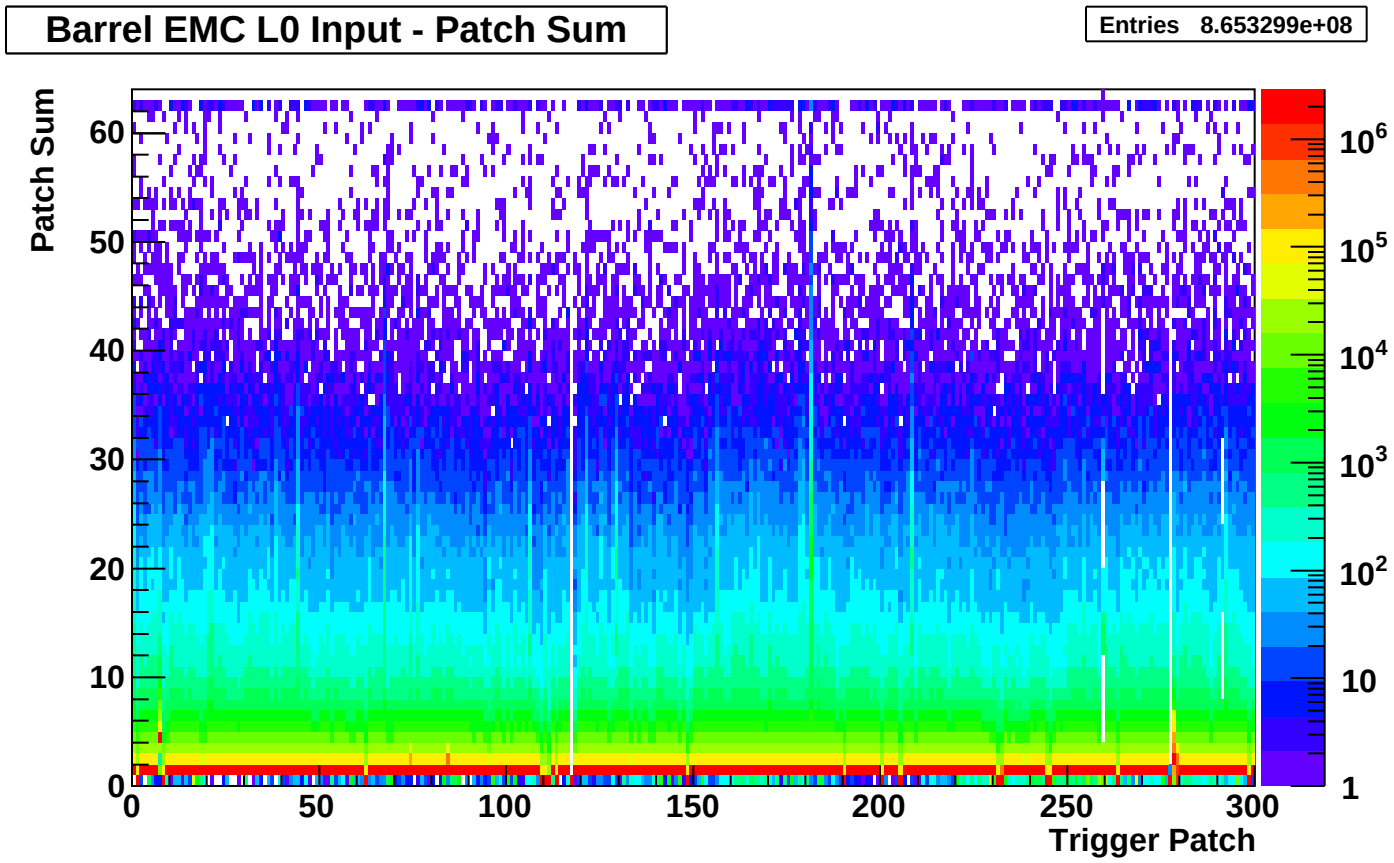

TPC Readout Mask (simulated)
Entries 572727

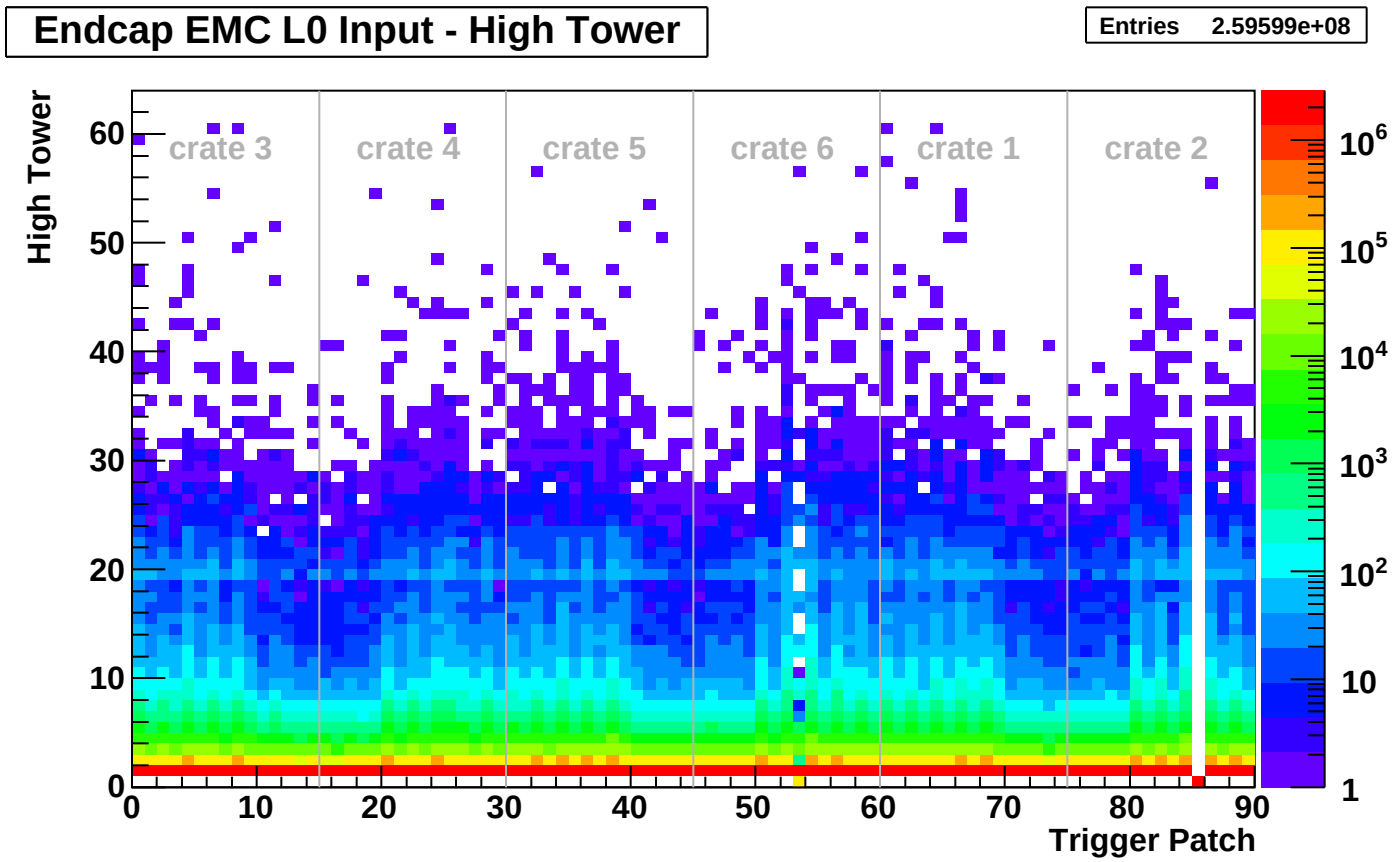

BunchId7Bit (all events)
Entries2884433

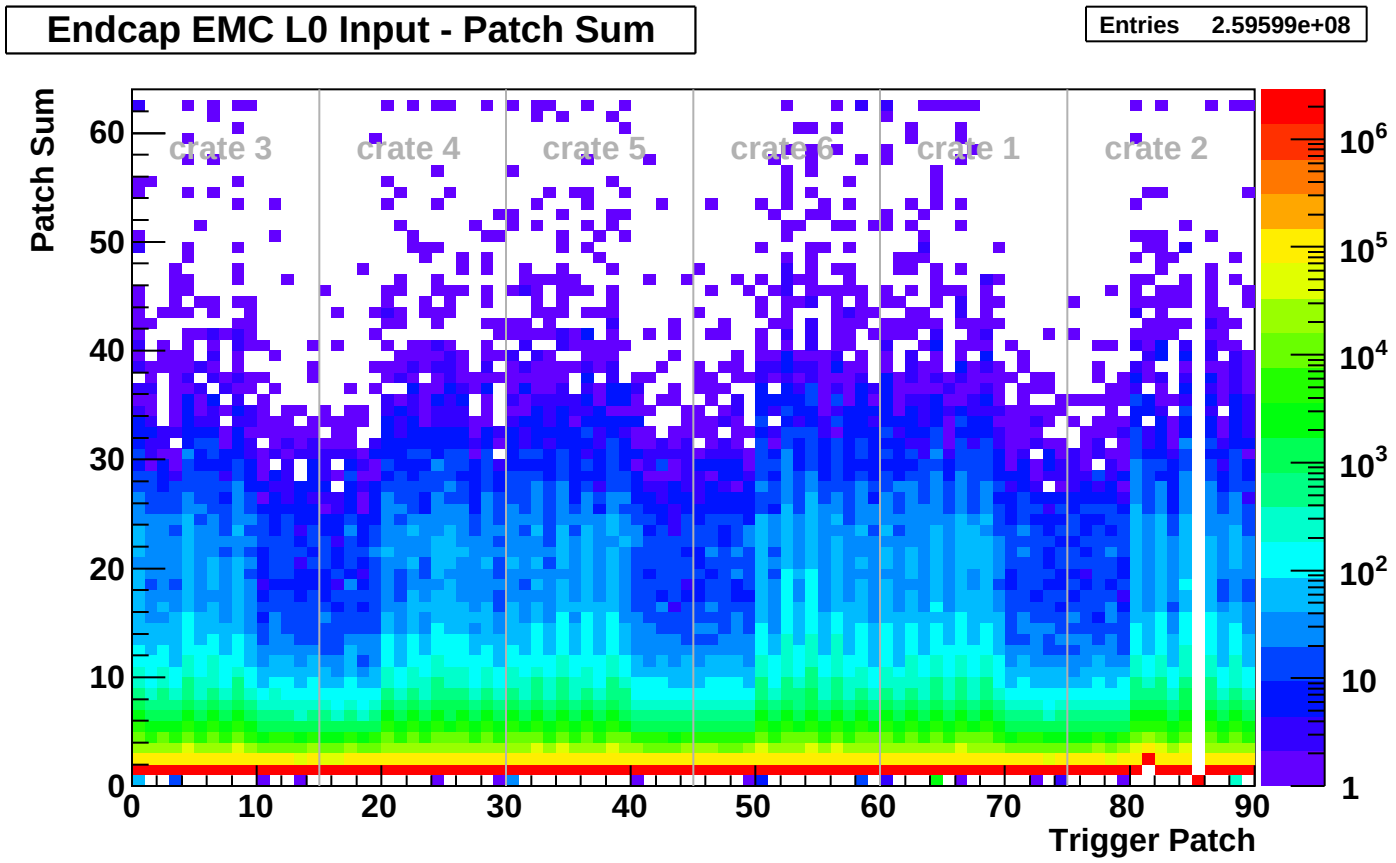

Abort-gaps
Entries 75795

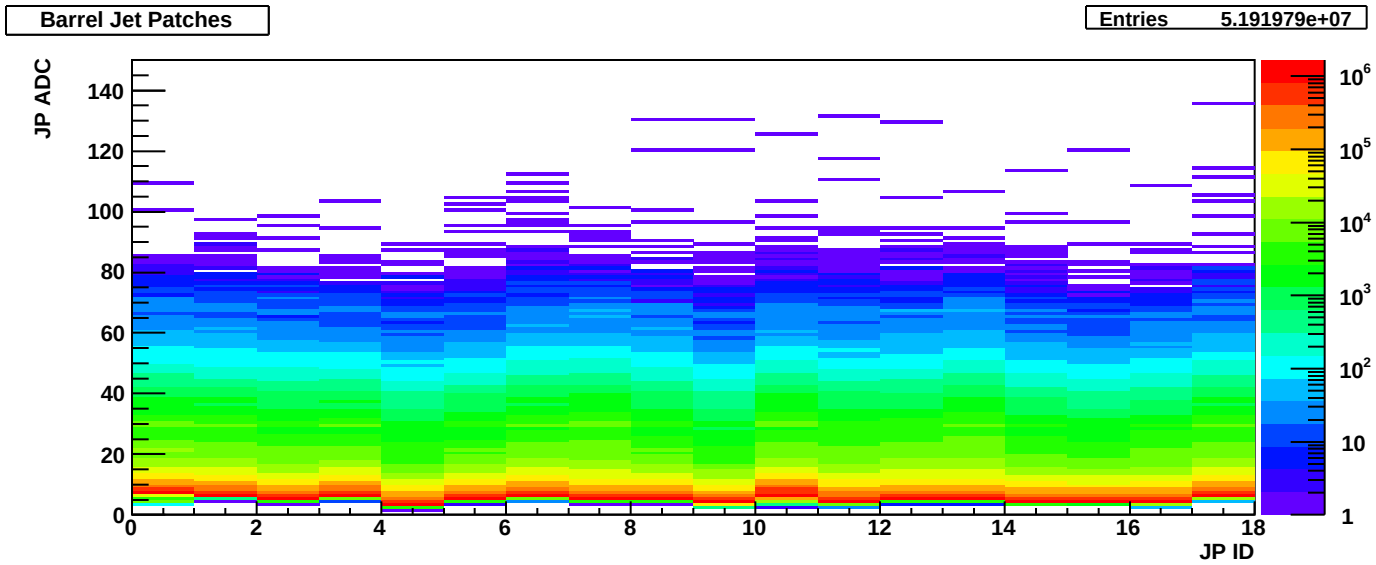

!Yellow-Fill
Entries 99989

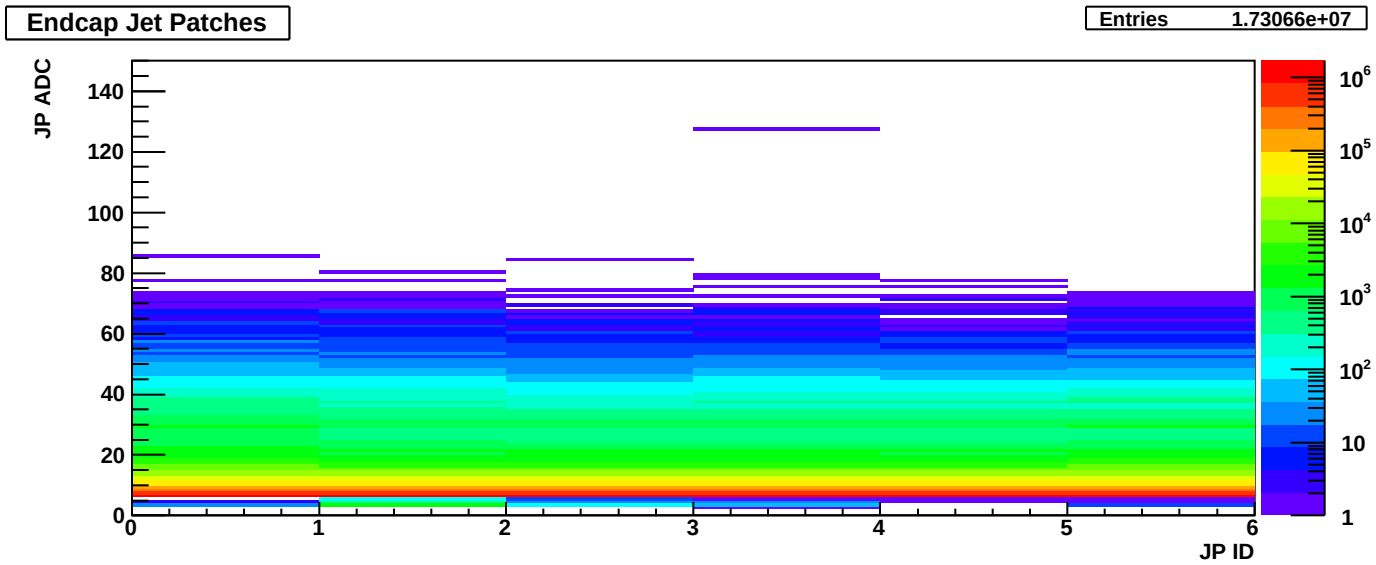

Barrel EMC L0 Input - High Tower
Entries 8.653299e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

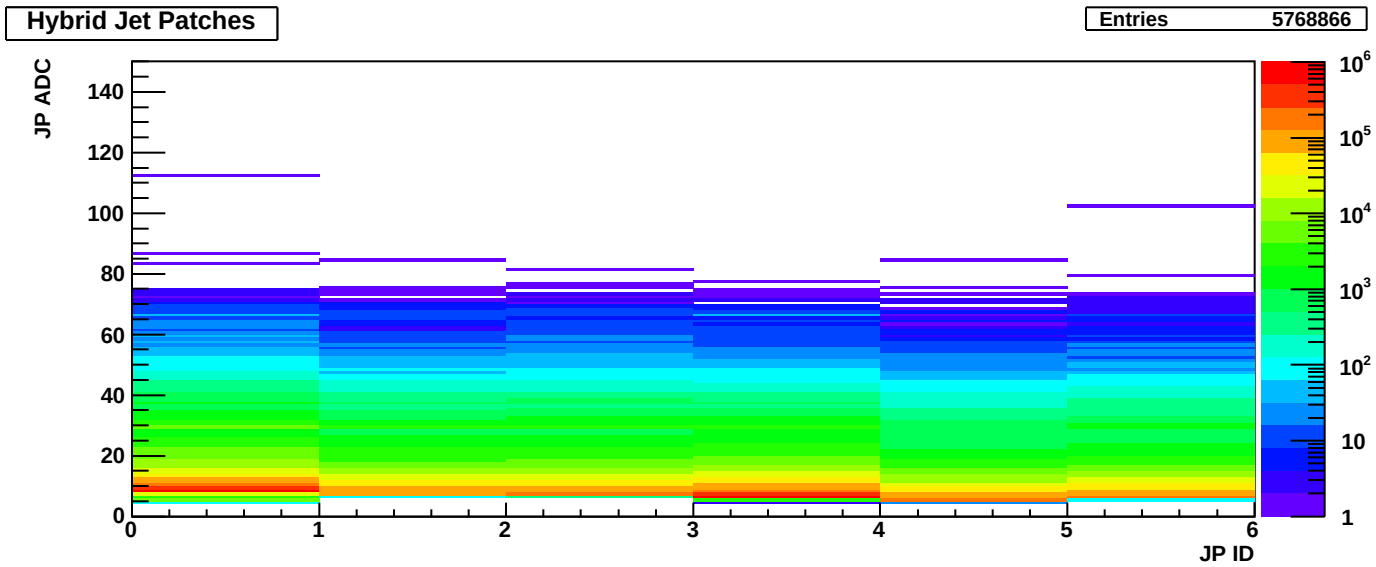

Barrel EMC L0 Input - Patch Sum
Entries 8.653299e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

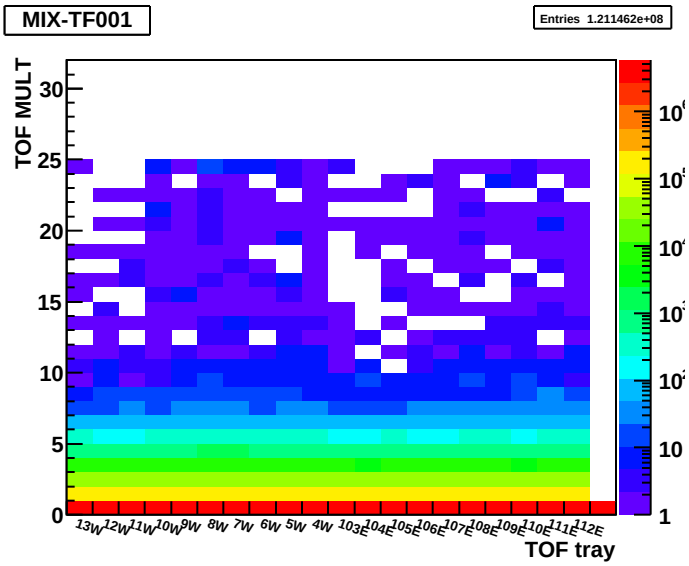

Endcap EMC L0 Input - High Tower
Entries 2.59599e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

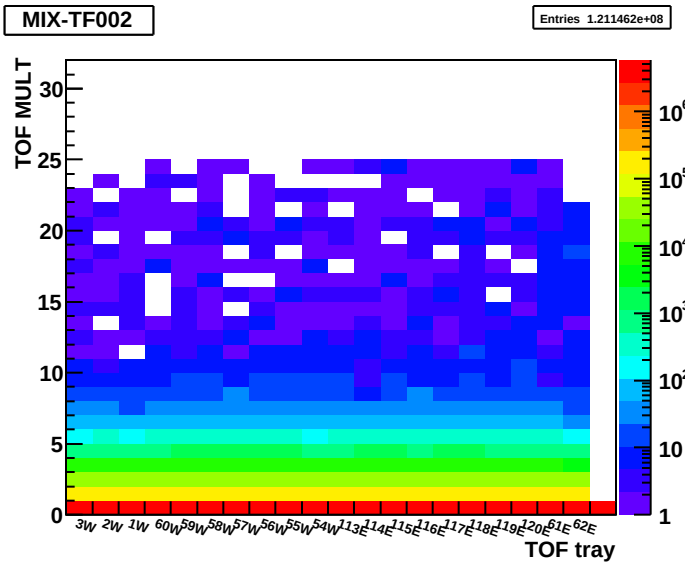

Endcap EMC L0 Input - Patch Sum
Entries 2.59599e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

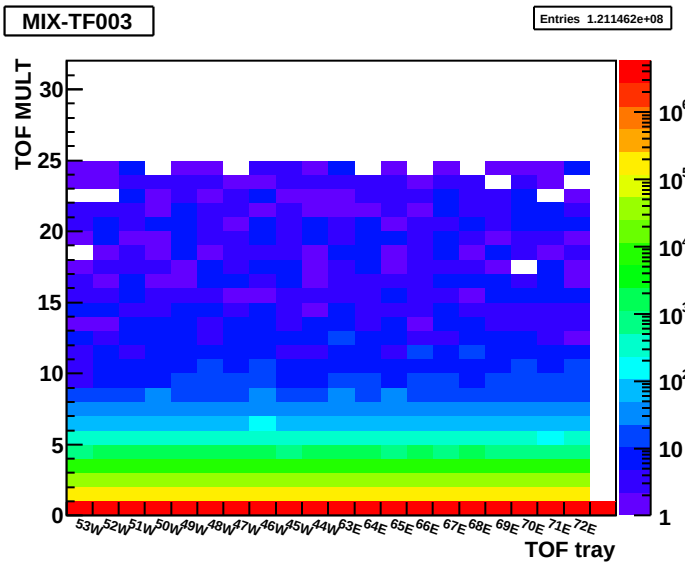

Barrel Jet Patches
Entries 5.191979e+07
JP ADC
JP ID

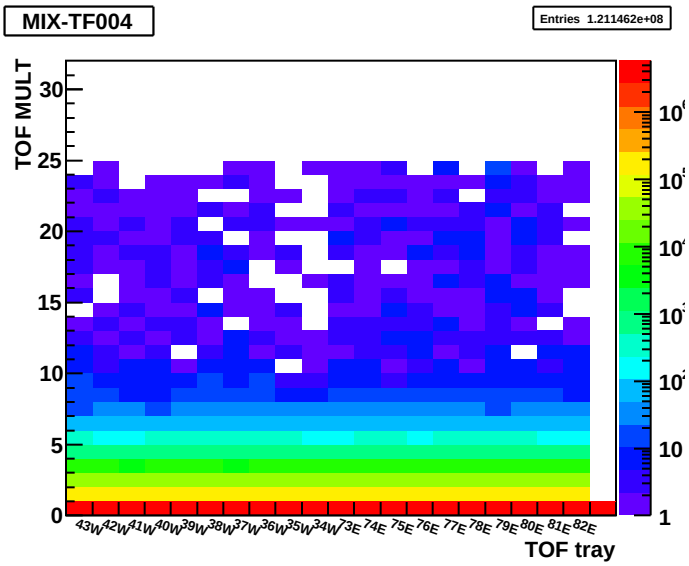

Endcap Jet Patches
Entries 1.73066e+07
JP ADC
JP ID

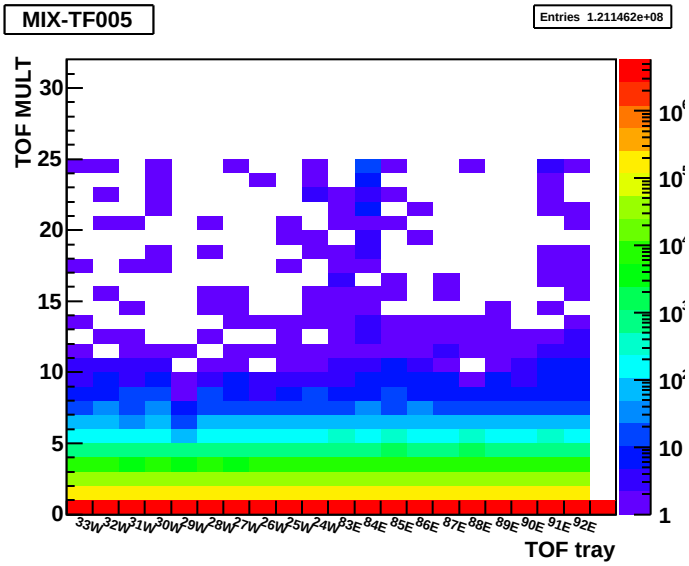

Hybrid Jet Patches
Entries 5768866
JP ADC
JP ID

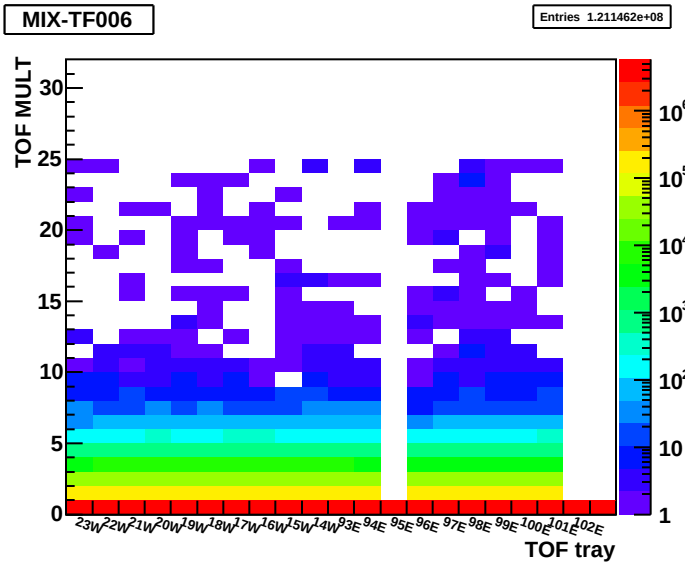

MIX-TF001
Entries 1.211462e+08
TOF MULT
TOF tray

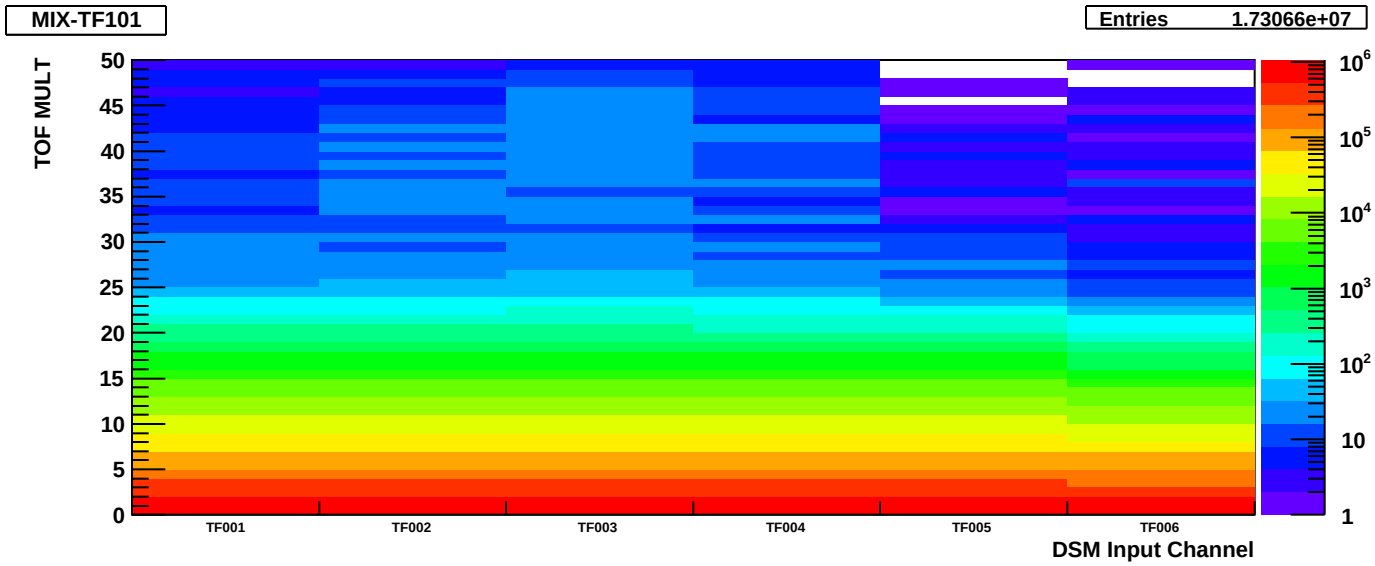

MIX-TF002
Entries 1.211462e+08
TOF MULT
TOF tray

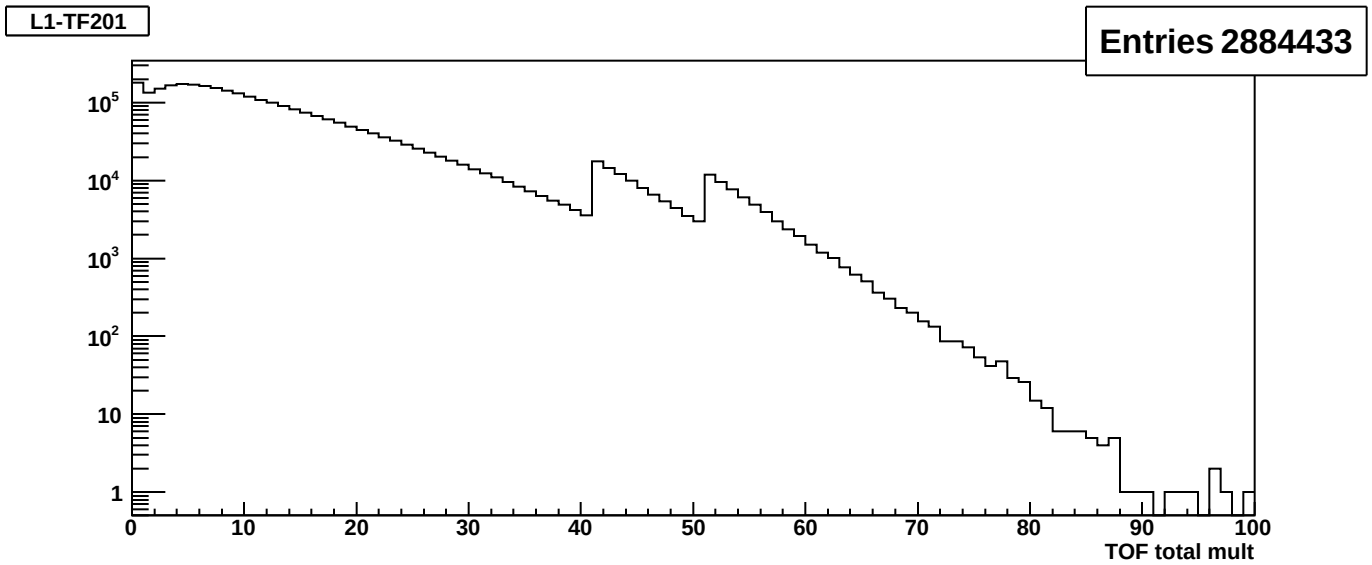

MIX-TF003
Entries 1.211462e+08
TOF MULT
TOF tray

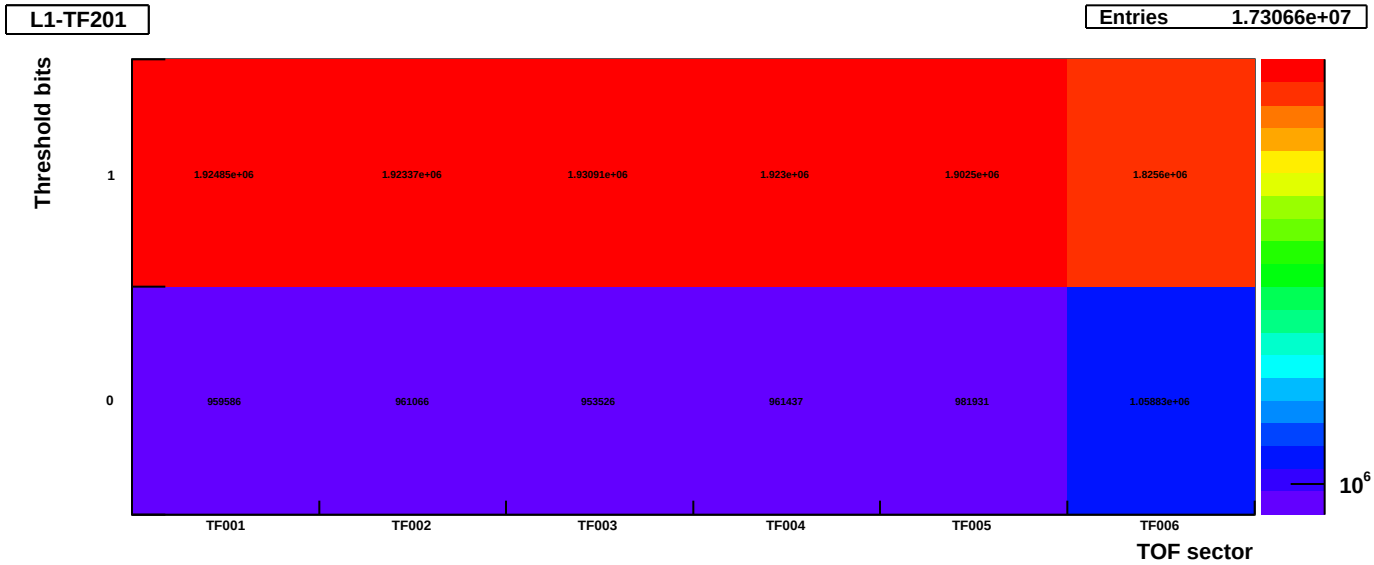

MIX-TF004
Entries 1.211462e+08
TOF MULT
TOF tray

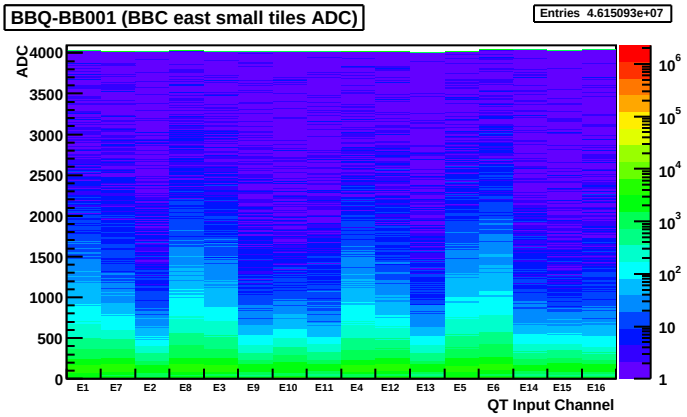

MIX-TF005
Entries 1.211462e+08
TOF MULT
TOF tray

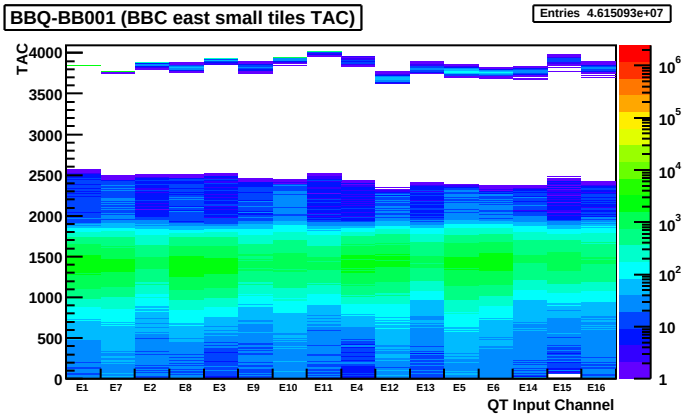

MIX-TF006
Entries 1.211462e+08
TOF MULT
TOF tray

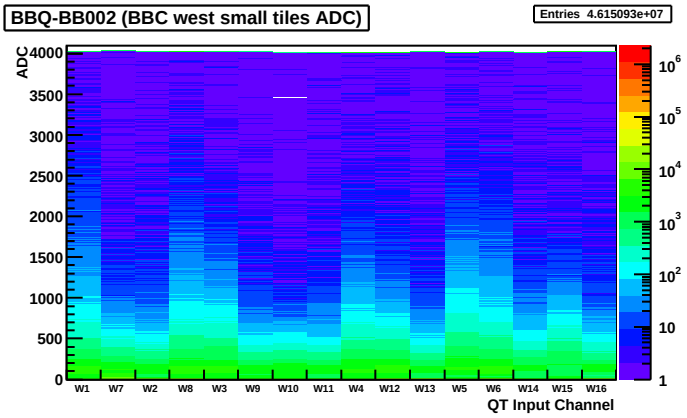

MIX-TF101
Entries 1.73066e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

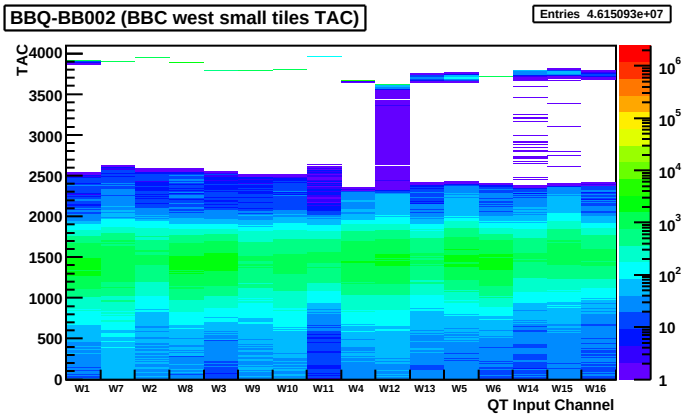

L1-TF201
Entries 2884433
TOF total mult

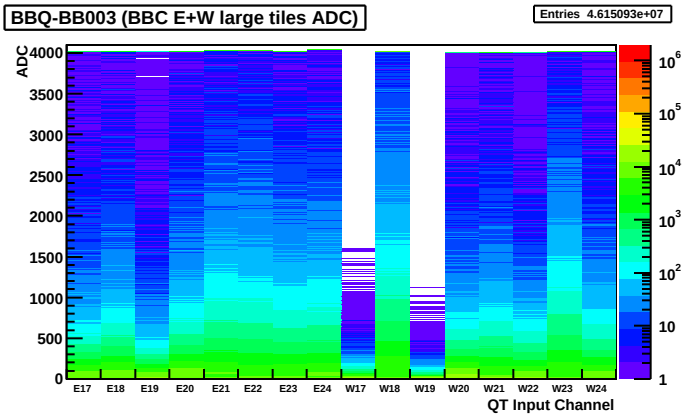

L1-TF201
Entries 1.73066e+07
Threshold bits
1.92485e+06
1.92337e+06
1.93091e+06
1.923e+06
1.9025e+06
1.8256e+06
959586
961066
953526
961437
981931
1.05883e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

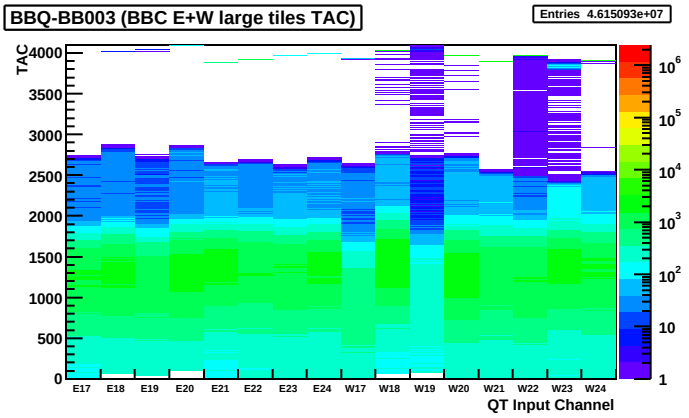

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.615093e+07
ADC
QT Input Channel

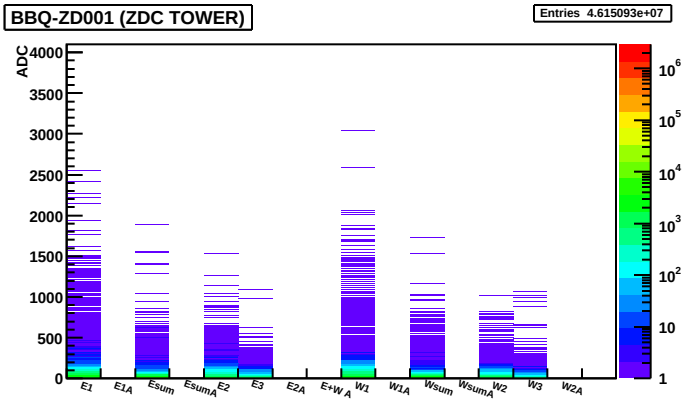

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.615093e+07
TAC
QT Input Channel

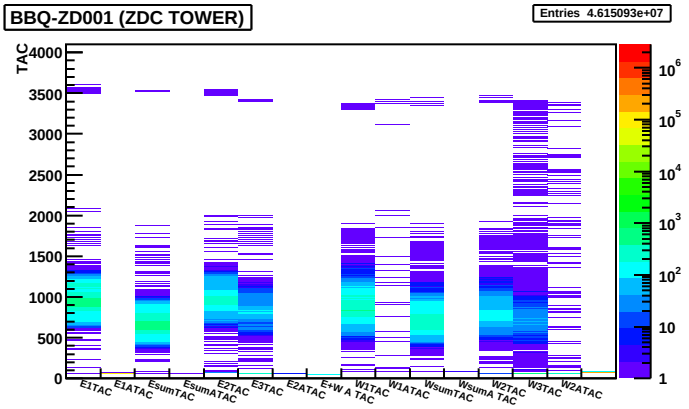

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.615093e+07
ADC
QT Input Channel

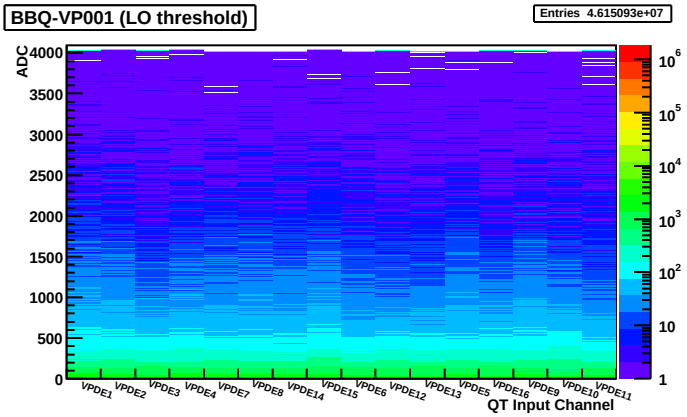

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.615093e+07
TAC
QT Input Channel

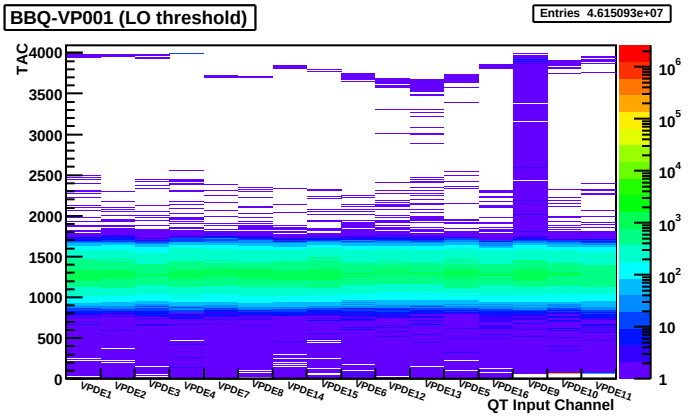

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.615093e+07
ADC
QT Input Channel

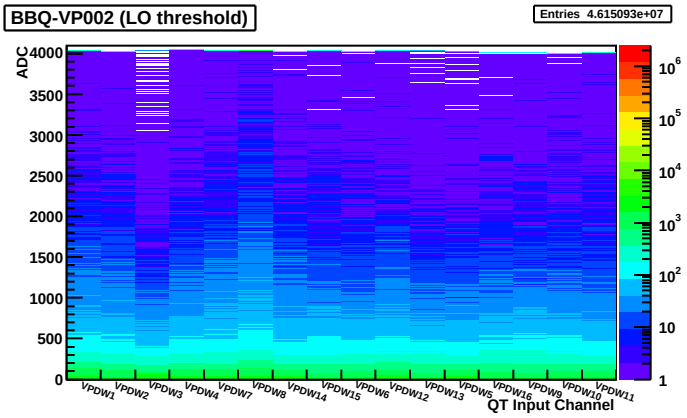

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.615093e+07
TAC
QT Input Channel

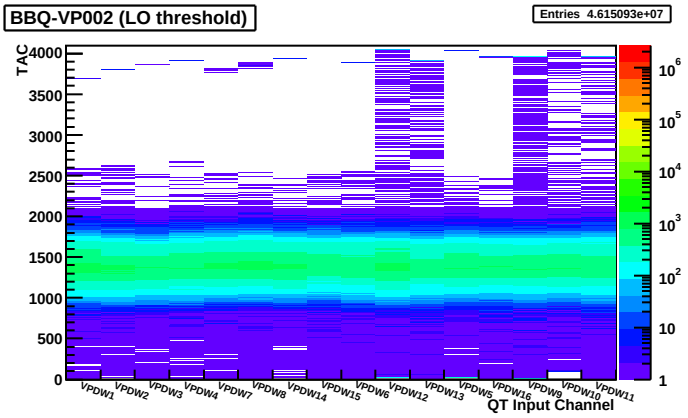

BBQ-ZD001 (ZDC TOWER)
Entries 4.615093e+07
ADC

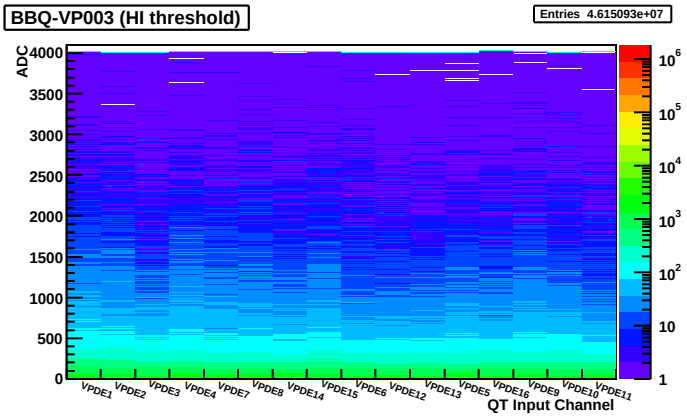

BBQ-ZD001 (ZDC TOWER)
Entries 4.615093e+07
TAC

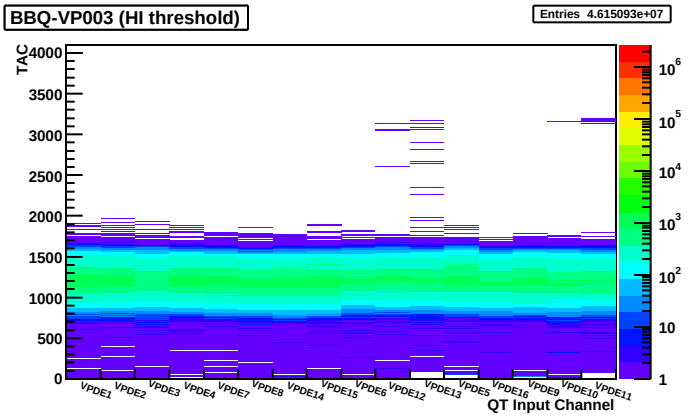

BBQ-VP001 (LO threshold)
Entries 4.615093e+07
ADC
QT Input Channel

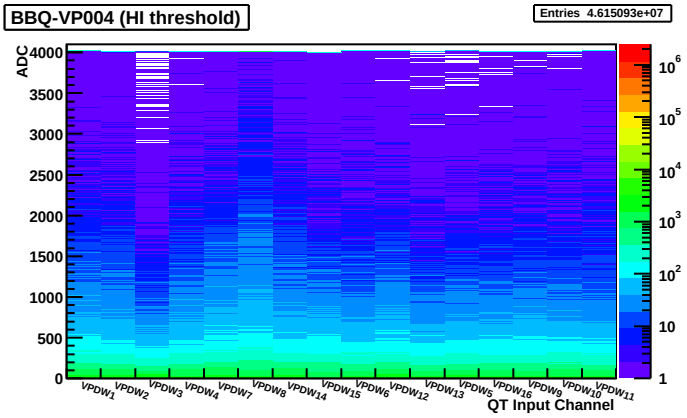

BBQ-VP001 (LO threshold)
Entries 4.615093e+07
TAC
QT Input Channel

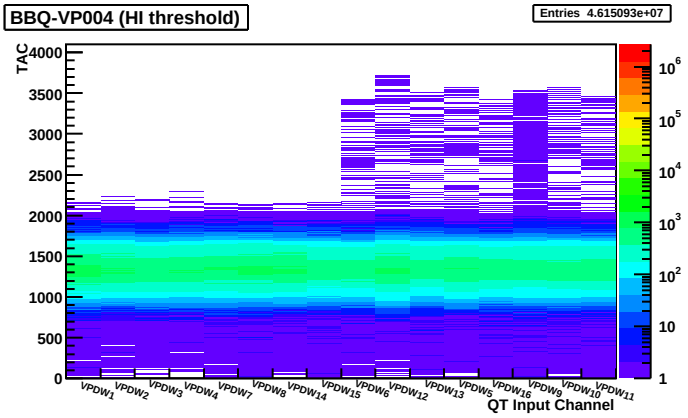

BBQ-VP002 (LO threshold)
Entries 4.615093e+07
ADC
QT Input Channel

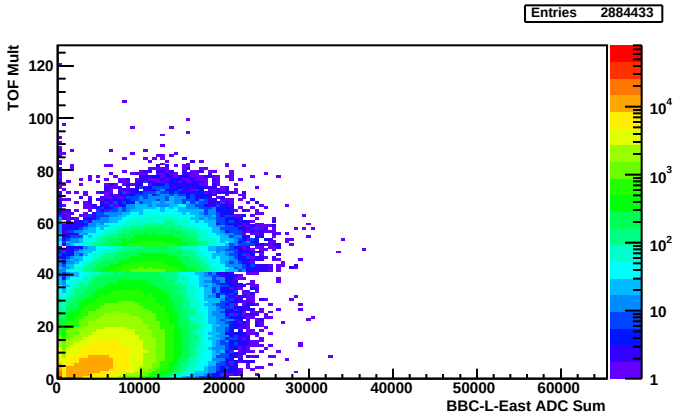

BBQ-VP002 (LO threshold)
Entries 4.615093e+07
TAC
QT Input Channel

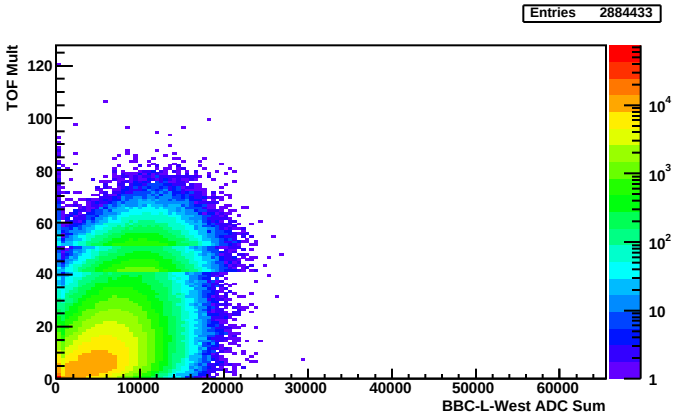

BBQ-VP003 (HI threshold)
Entries 4.615093e+07
ADC
QT Input Channel

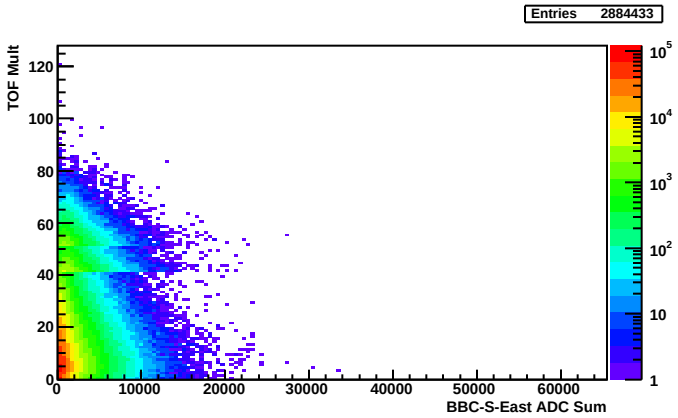

BBQ-VP003 (HI threshold)
Entries 4.615093e+07
TAC
QT Input Channel

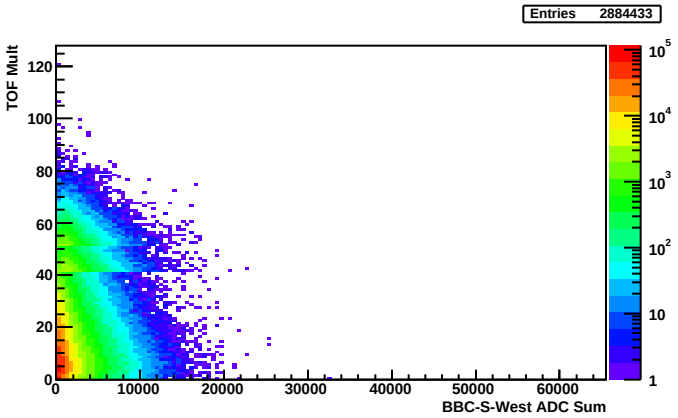

BBQ-VP004 (HI threshold)
Entries 4.615093e+07
ADC
QT Input Channel

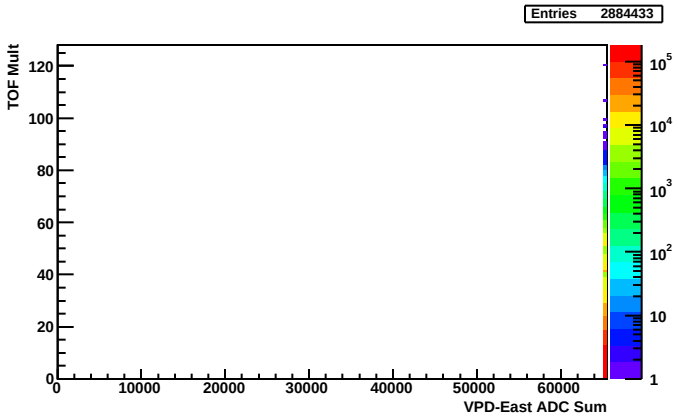

BBQ-VP004 (HI threshold)
Entries 4.615093e+07
TAC
QT Input Channel

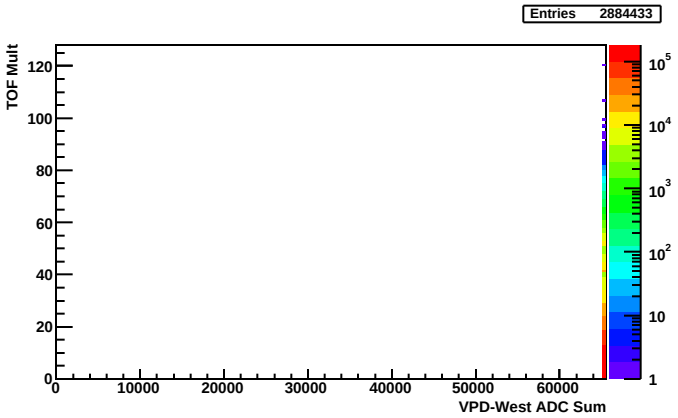

Entries 2884433
TOF Mult
BBC-L-East ADC Sum

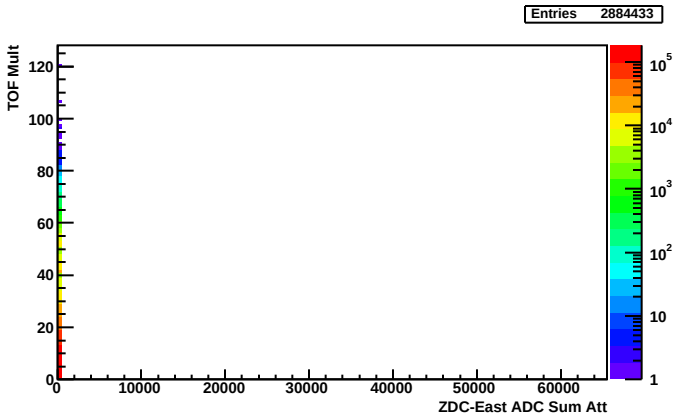

Entries 2884433
TOF Mult
BBC-L-West ADC Sum

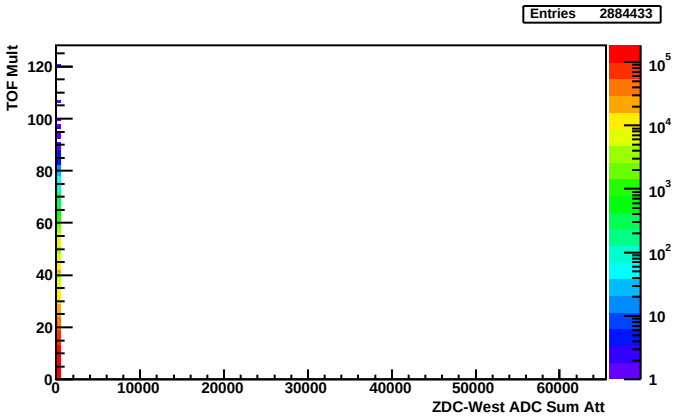

Entries 2884433
TOF Mult
BBC-S-East ADC Sum

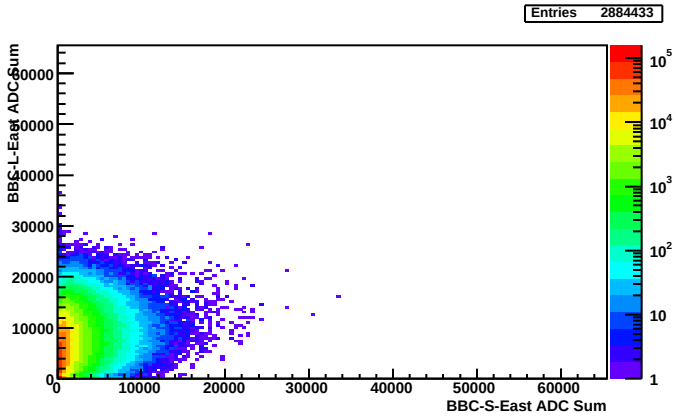

Entries 2884433
TOF Mult
BBC-S-West ADC Sum

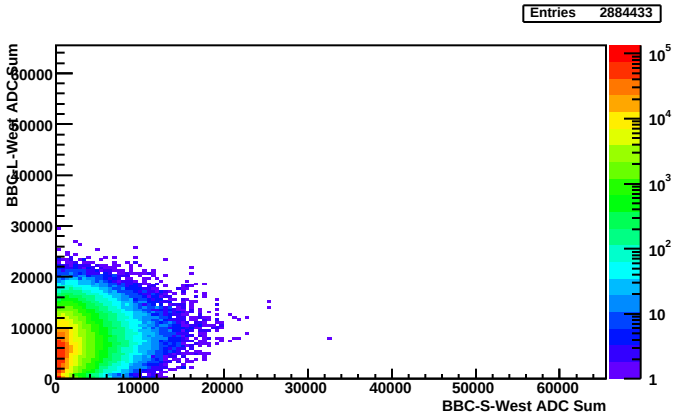

Entries 2884433
TOF Mult
VPD-East ADC Sum

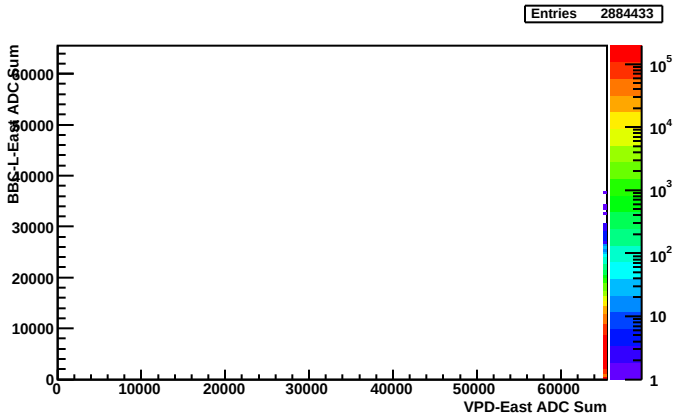

Entries 2884433
TOF Mult
VPD-West ADC Sum

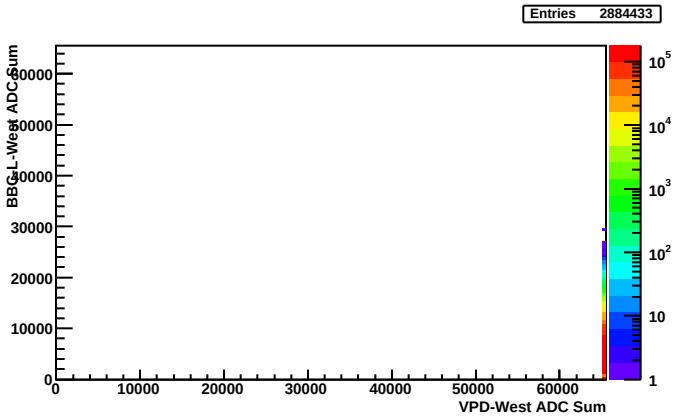

Entries 2884433
TOF Mult
ZDC-East ADC Sum Att

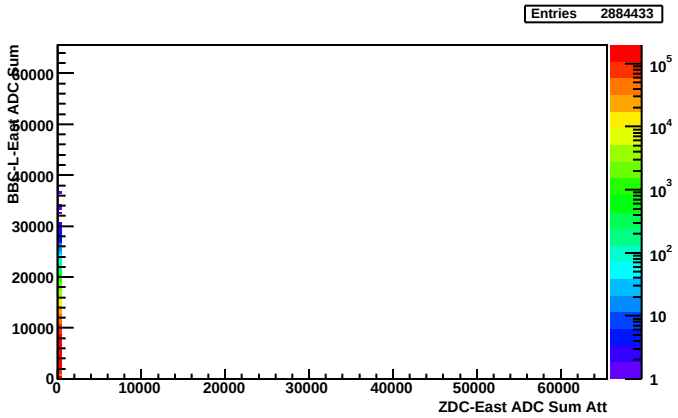

Entries 2884433
TOF Mult
ZDC-West ADC Sum Att

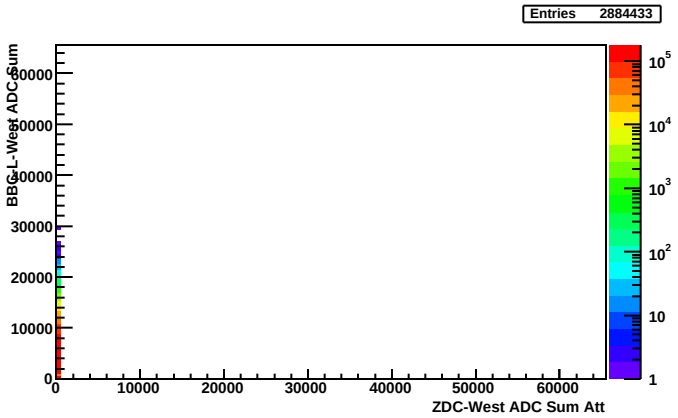

Entries 2884433
BBC-L-East ADC Sum
BBC-S-East ADC Sum

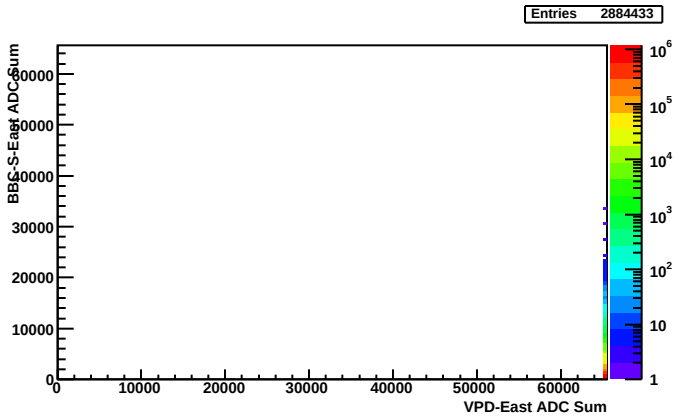

Entries 2884433
BBC-L-West ADC Sum
BBC-S-West ADC Sum

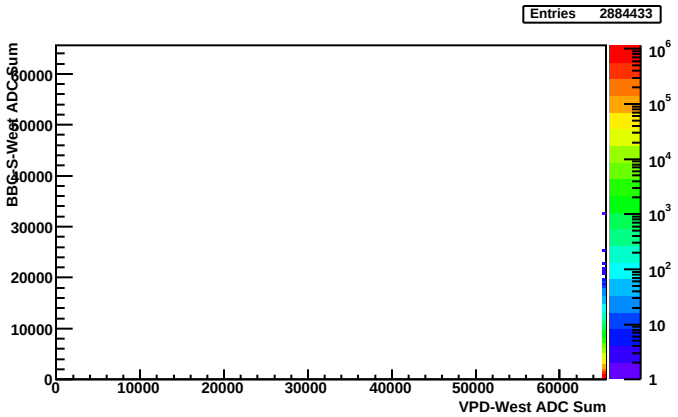

Entries 2884433
BBC-L-East ADC Sum
VPD-East ADC Sum

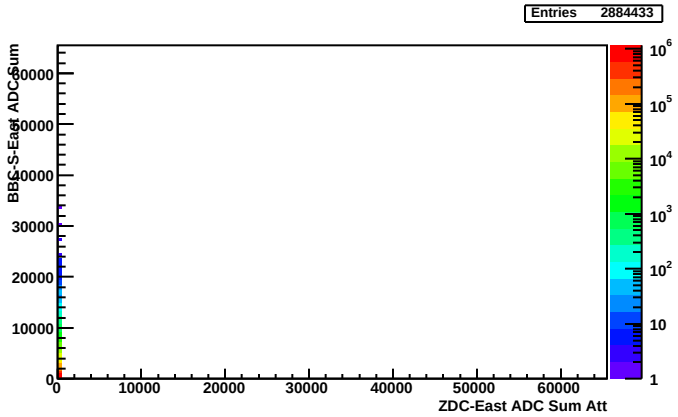

Entries 2884433
BBC-L-West ADC Sum
VPD-West ADC Sum

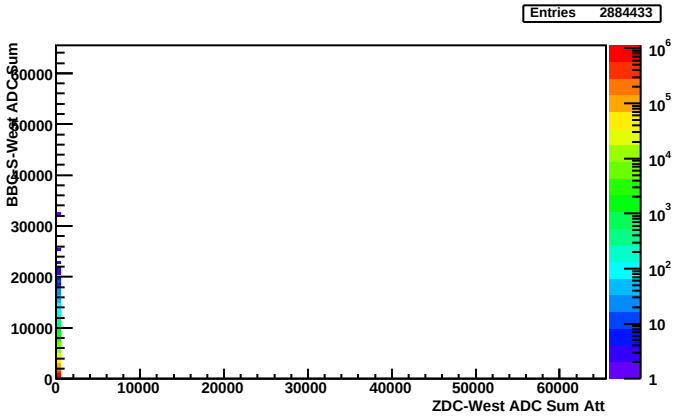

Entries 2884433
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

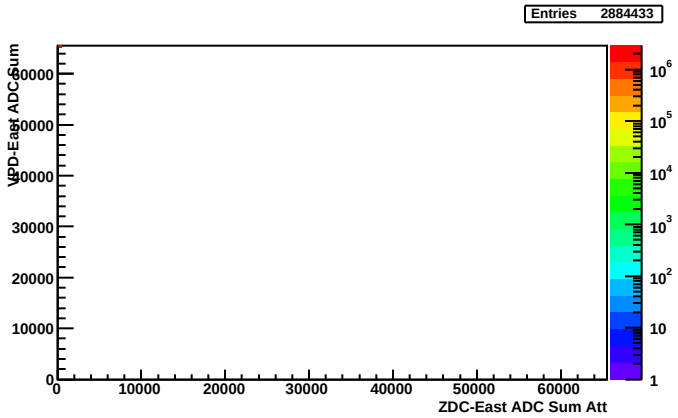

Entries 2884433
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

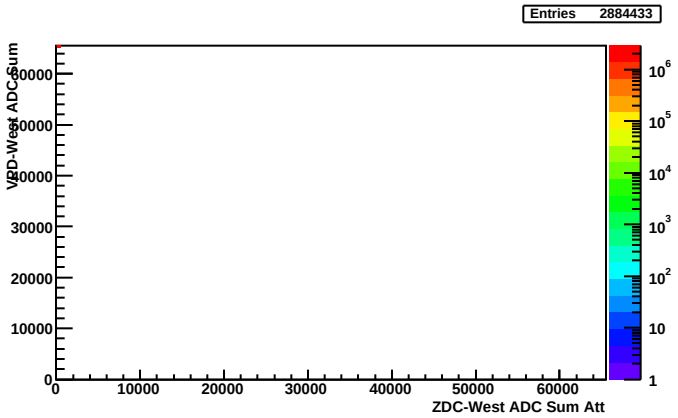

Entries 2884433
BBC-S-East ADC Sum
VPD-East ADC Sum

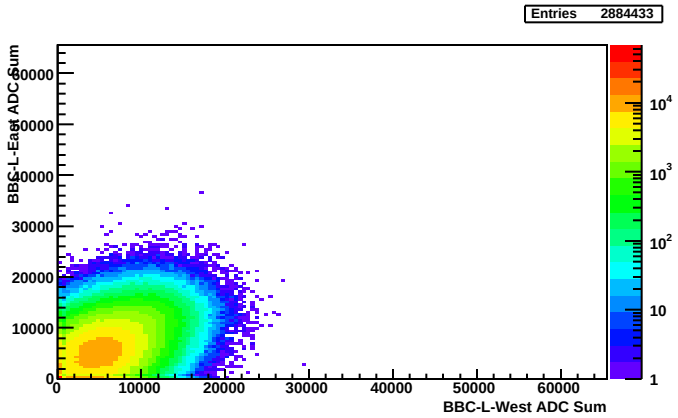

Entries 2884433
BBC-S-West ADC Sum
VPD-West ADC Sum

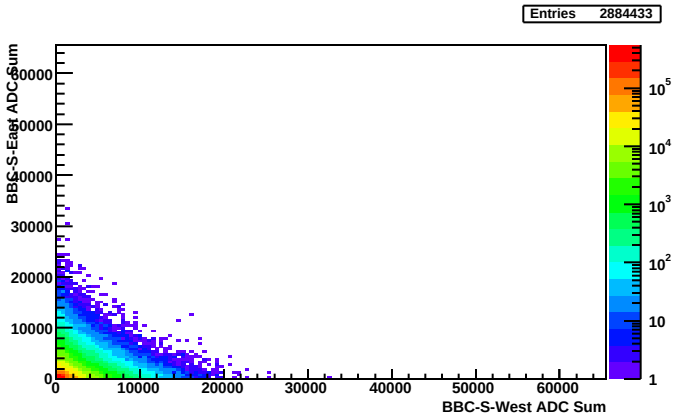

Entries 2884433
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

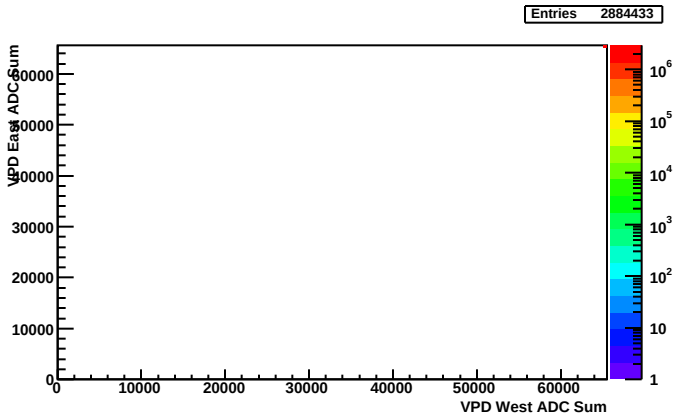

Entries 2884433
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

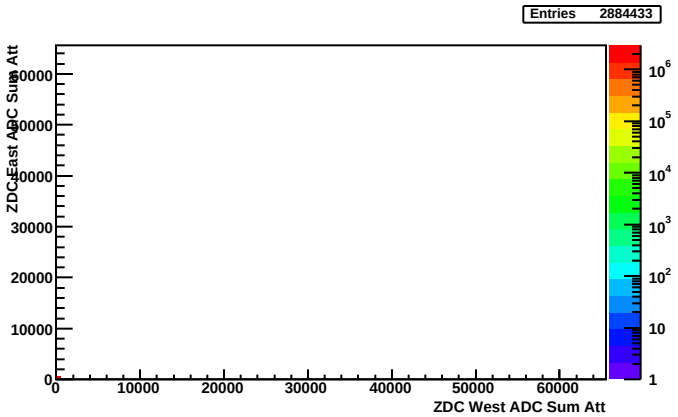

Entries 2884433
VPD-East ADC Sum
ZDC-East ADC Sum Att

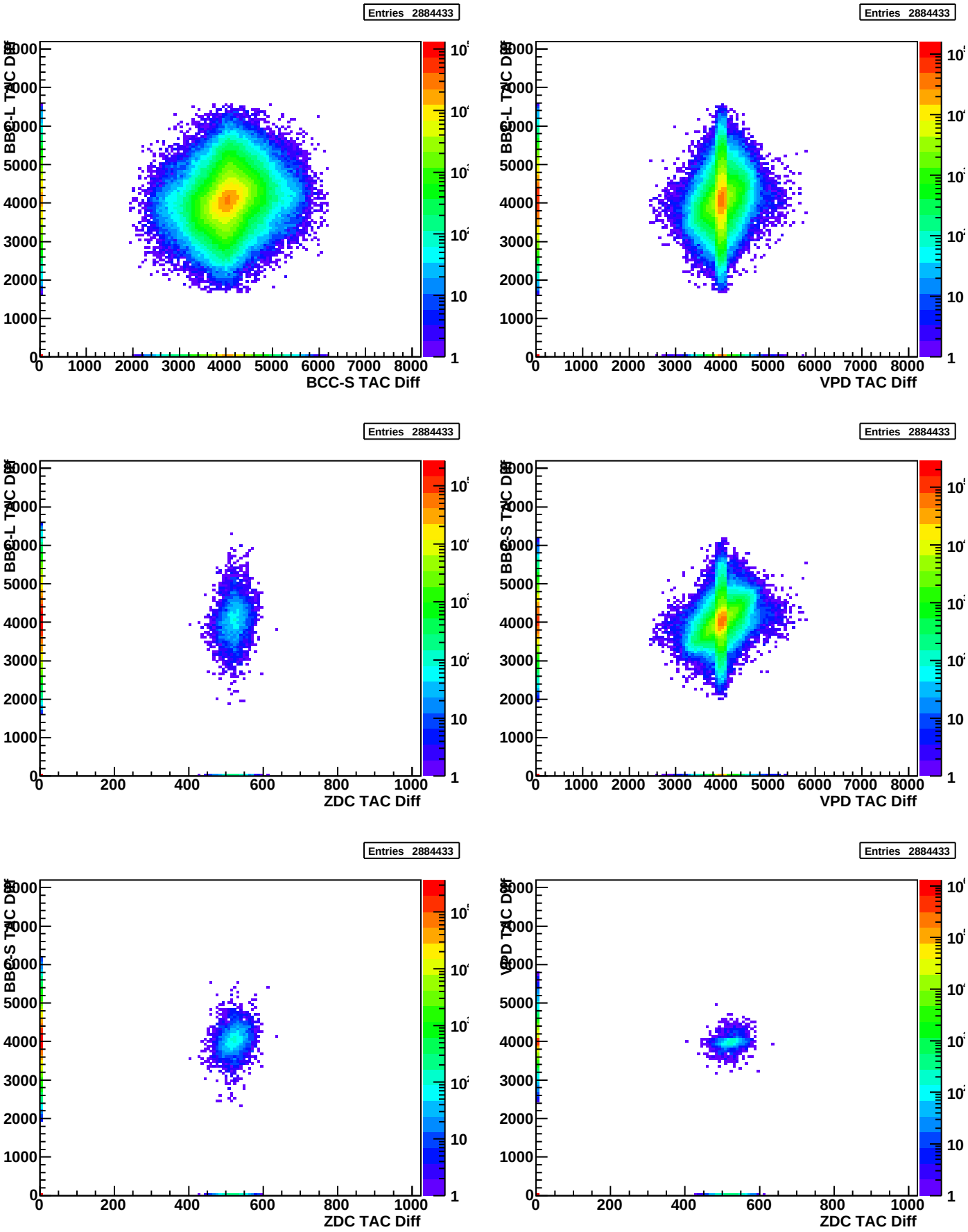

Entries 2884433
VPD-West ADC Sum
ZDC-West ADC Sum Att

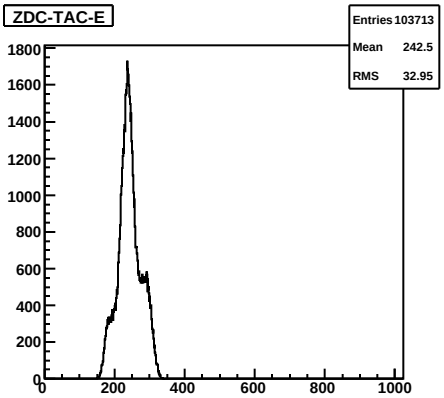

Entries 2884433
BBC-L-East ADC Sum
BBC-L-West ADC Sum

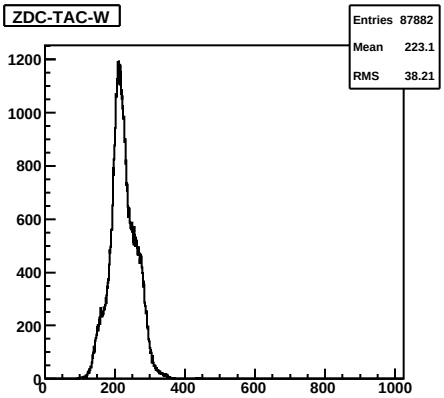

Entries 2884433
BBC-S-East ADC Sum
BBC-S-West ADC Sum

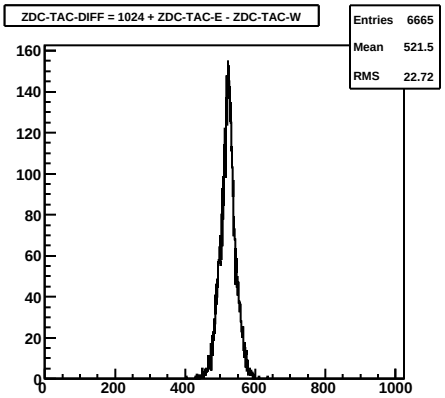

Entries 2884433
VPD East ADC Sum
VPD West ADC Sum

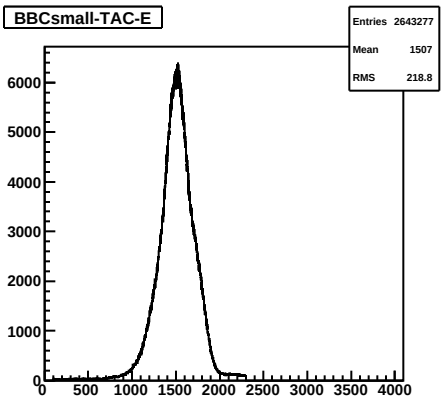

Entries 2884433
ZDC East ADC Sum Att
ZDC West ADC Sum Att

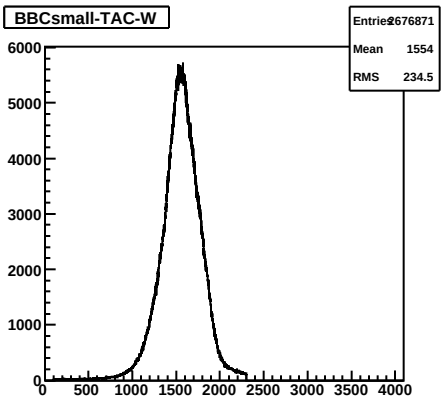

Entries 2884433
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2884433
BBC-L TAC Diff
VPD TAC Diff
Entries 2884433
BBC-L TAC Diff
ZDC TAC Diff
Entries 2884433
BBC-S TAC Diff
VPD TAC Diff
Entries 2884433
BBC-S TAC Diff
ZDC TAC Diff
Entries 2884433
VPD TAC Diff
ZDC TAC Diff

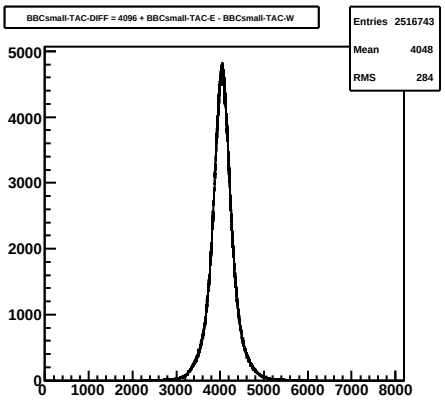

ZDC-TAC-E
Entries 103713
Mean 242.5
RMS 32.95

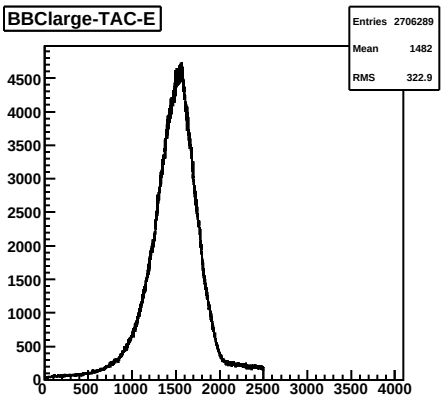

ZDC-TAC-W
Entries 87882
Mean 223.1
RMS 38.21

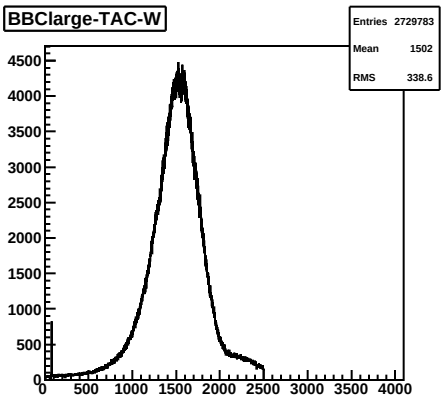

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 6665
Mean 521.5
RMS 22.72

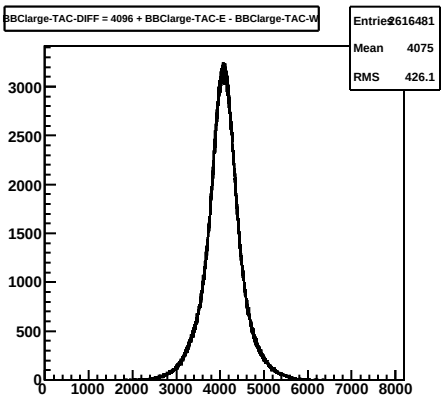

BBCsmall-TAC-E
Entries 2643277
Mean 1507
RMS 218.8

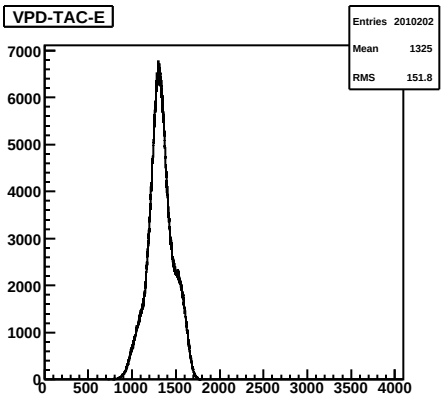

BBCsmall-TAC-W
Entries 2676871
Mean 1554
RMS 234.5

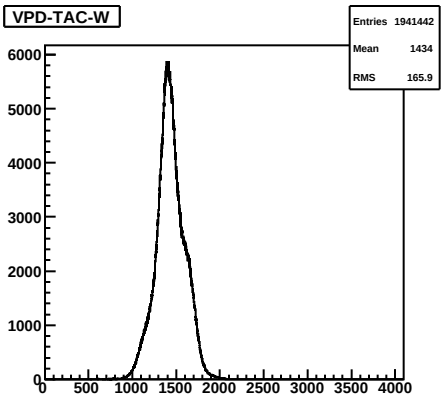

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2516743
Mean 4048
RMS 284

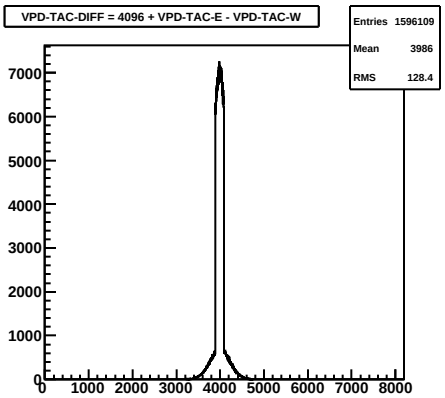

BBClarge-TAC-E
Entries 2706289
Mean 1482
RMS 322.9

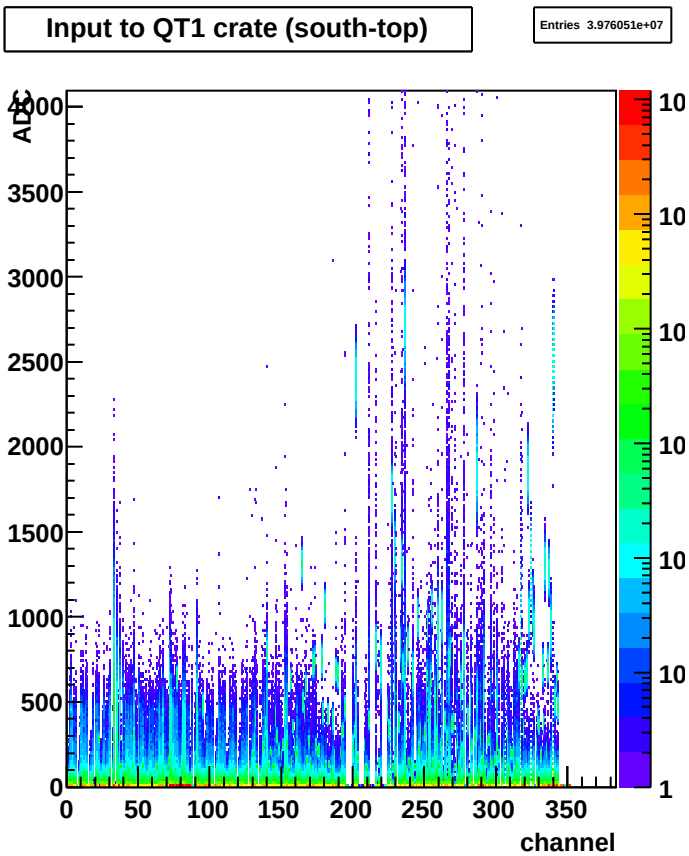

BBClarge-TAC-W
Entries 2729783
Mean 1502
RMS 338.6

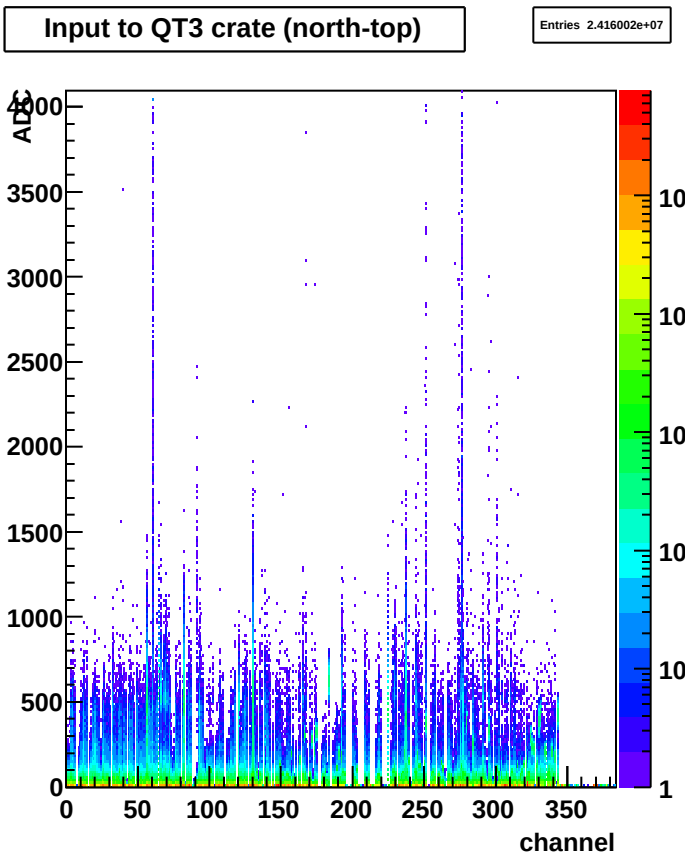

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2616481
Mean 4075
RMS 426.1

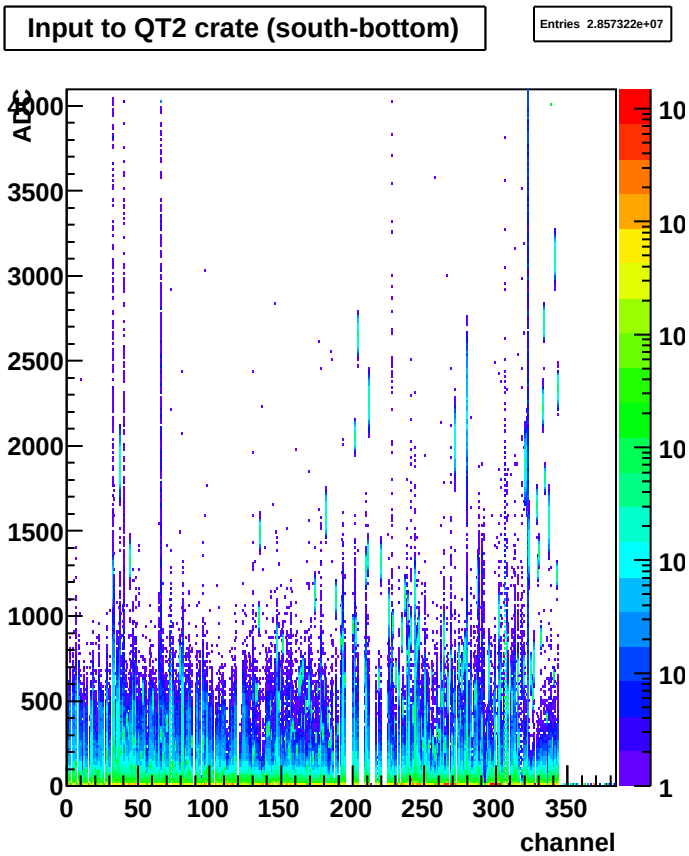

VPD-TAC-E
Entries 2010202
Mean 1325
RMS 151.8

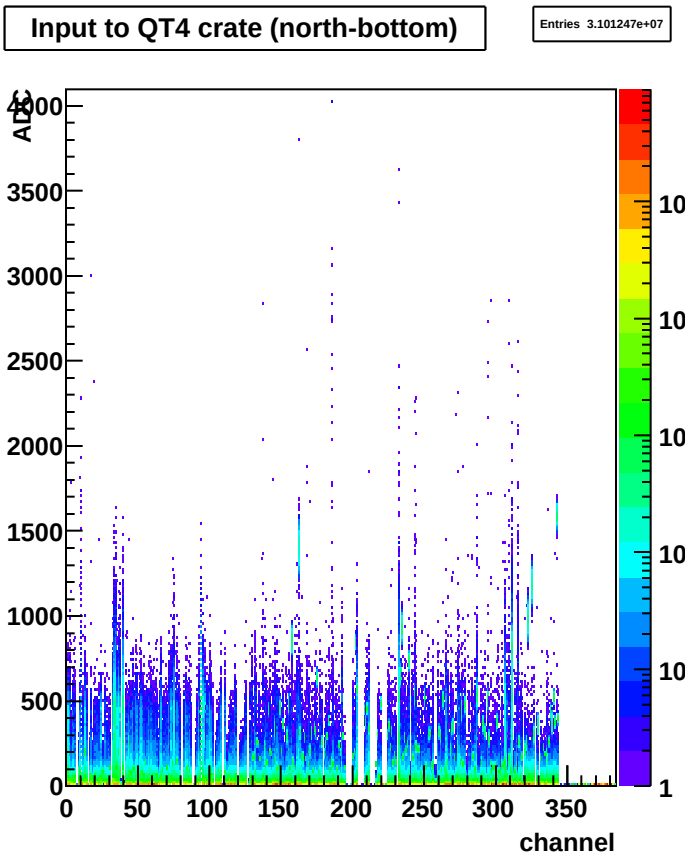

VPD-TAC-W
Entries 1941442
Mean 1434
RMS 165.9

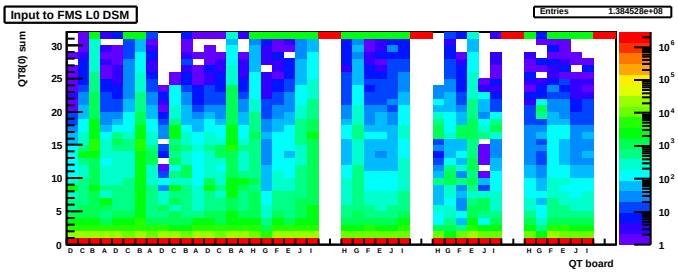

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1596109
Mean 3986
RMS 128.4

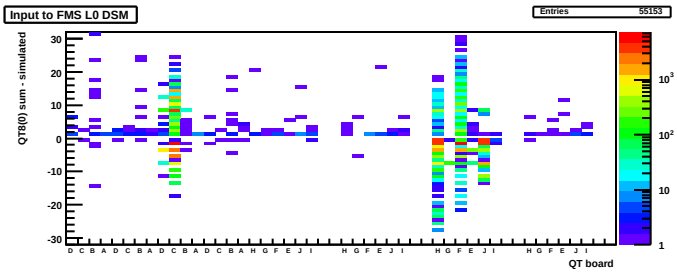

Input to QT1 crate (south-top)
Entries 3.976051e+07
ADC
channel

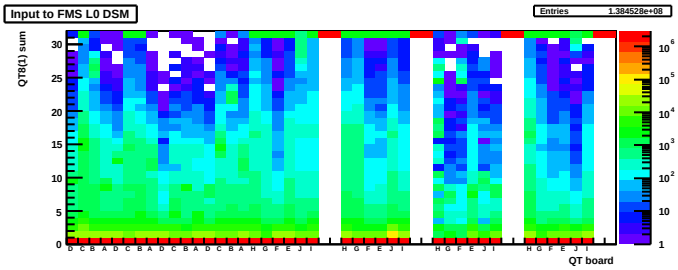

Input to QT3 crate (north-top)
Entries 2.416002e+07
ADC
channel

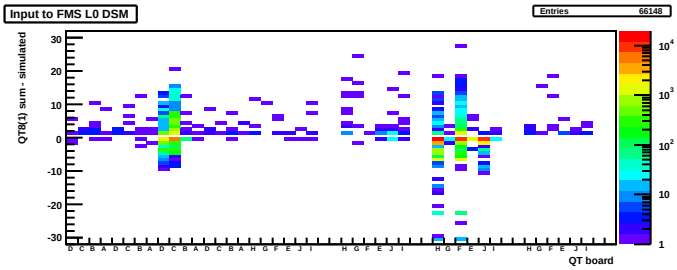

Input to QT2 crate (south-bottom)
Entries 2.857322e+07
ADC
channel

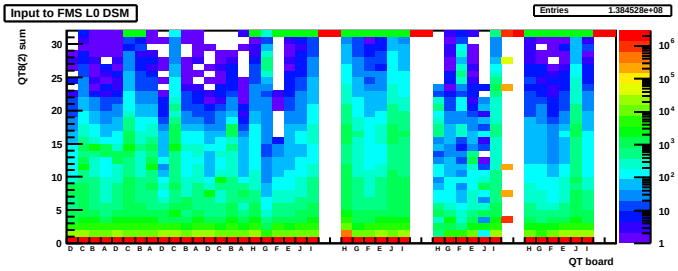

Input to QT4 crate (north-bottom)
Entries 3.101247e+07
ADC
channel

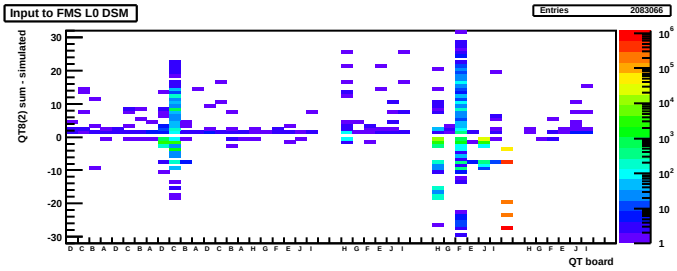

Input to FMS L0 DSM
Entries 1.384528e+08
QT8(0) sum
QT board

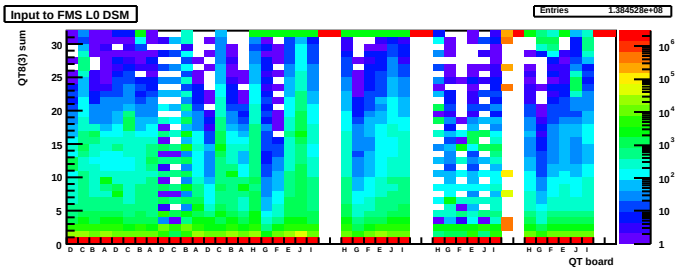

Input to FMS L0 DSM
Entries 55153
QT8(0) sum - simulated
QT board

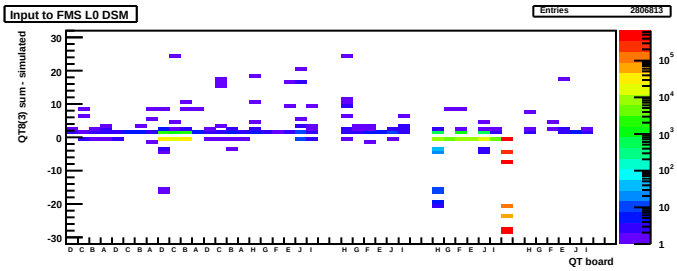

Input to FMS L0 DSM
Entries 1.384528e+08
QT8(1) sum
QT board

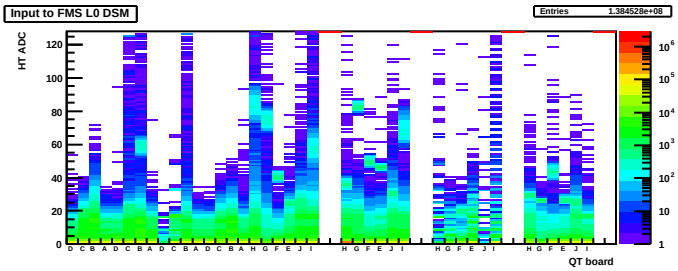

Input to FMS L0 DSM
Entries 66148
QT8(1) sum - simulated
QT board

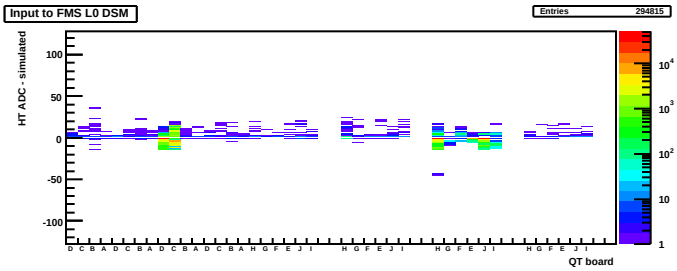

Input to FMS L0 DSM
Entries 1.384528e+08
QT8(2) sum
QT board

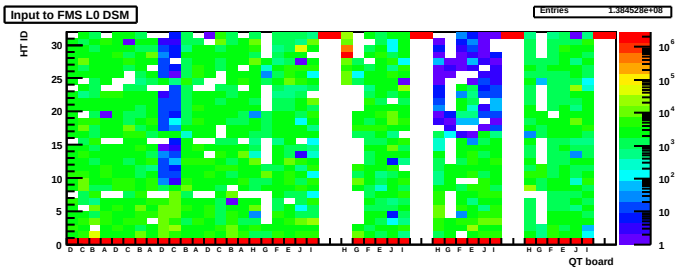

Input to FMS L0 DSM
Entries 2083066
QT8(2) sum - simulated
QT board

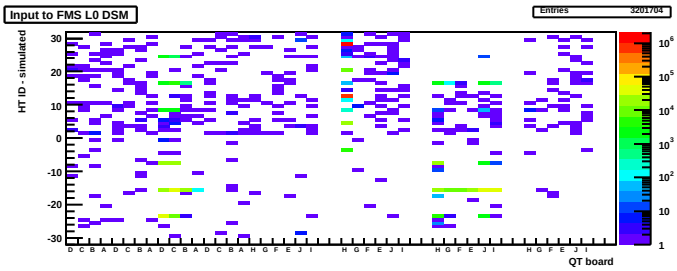

Input to FMS L0 DSM
Entries 1.384528e+08
QT8(3) sum
QT board

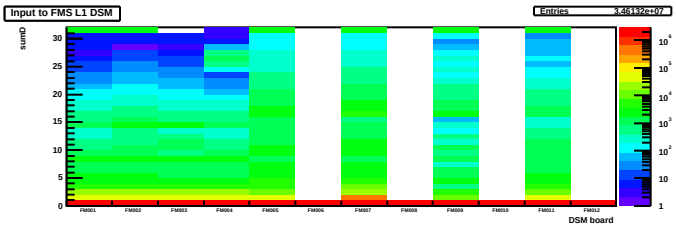

Input to FMS L0 DSM
Entries 2806813
QT8(3) sum - simulated
QT board

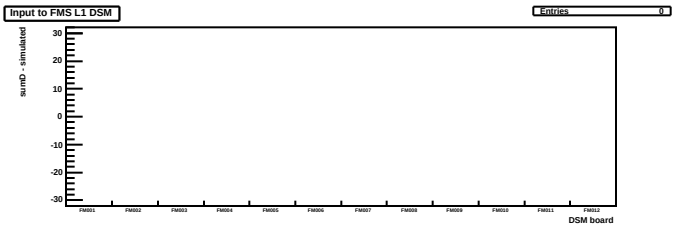

Input to FMS L0 DSM
Entries 1.384528e+08
HT ADC
QT board

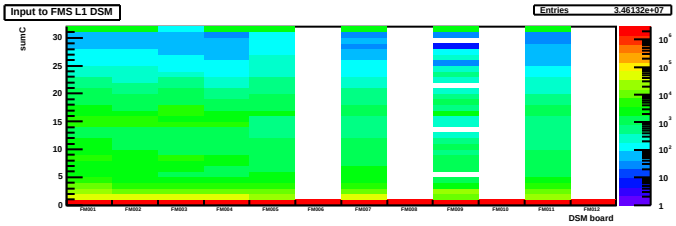

Input to FMS L0 DSM
Entries 294815
HT ADC - simulated
QT board

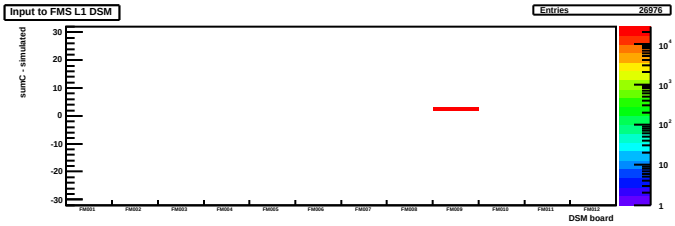

Input to FMS L0 DSM
Entries 1.384528e+08
HT ID
QT board

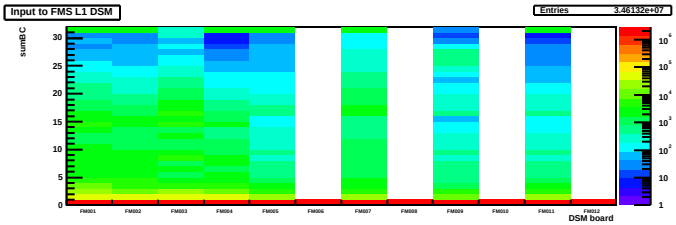

Input to FMS L0 DSM
Entries 3201704
HT ID - simulated
QT board

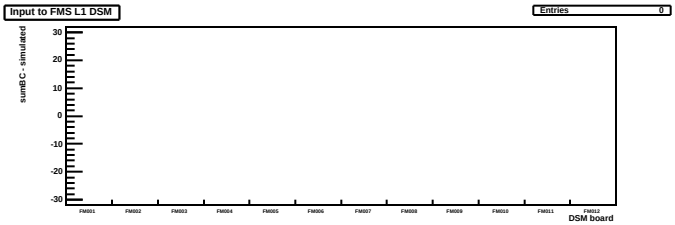

Input to FMS L1 DSM
Entries 3.46132e+07
sumD
DSM board

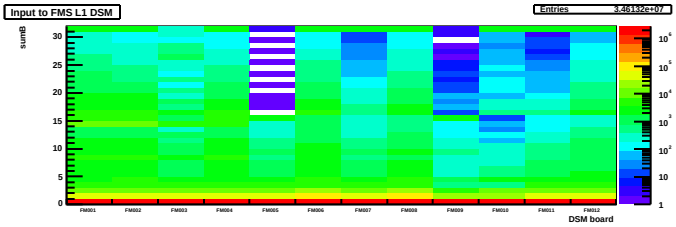

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

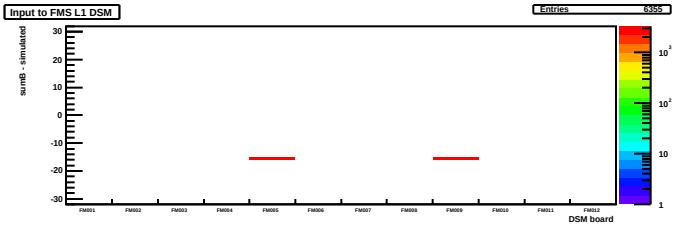

Input to FMS L1 DSM
Entries 3.46132e+07
sumC
DSM board

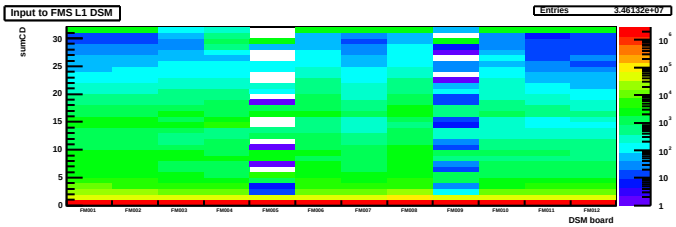

Input to FMS L1 DSM
Entries 26976
sumC - simulated
DSM board

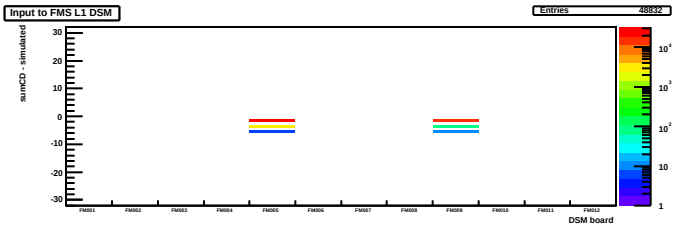

Input to FMS L1 DSM
Entries 3.46132e+07
sumBC
DSM board

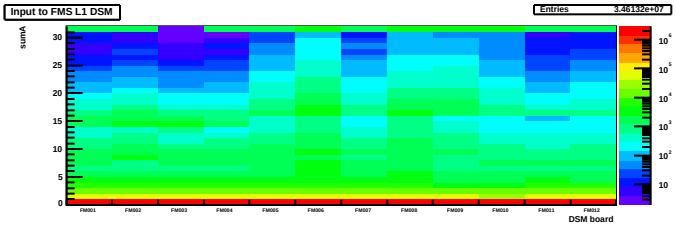

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

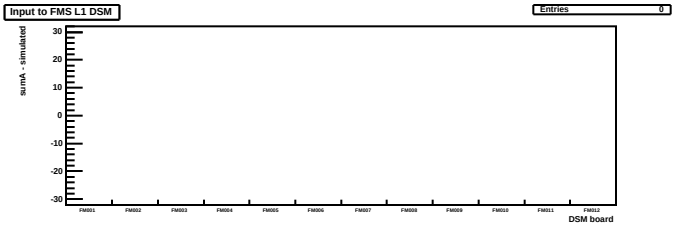

Input to FMS L1 DSM
Entries 3.46132e+07
sumB
DSM board

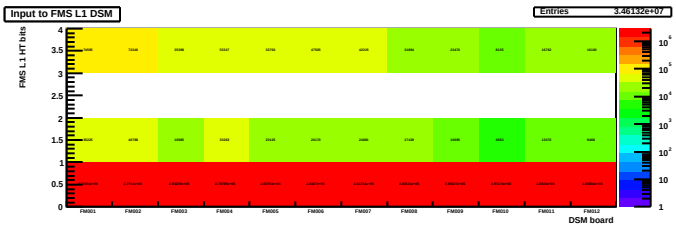

Input to FMS L1 DSM
Entries 6355
sumB - simulated
DSM board

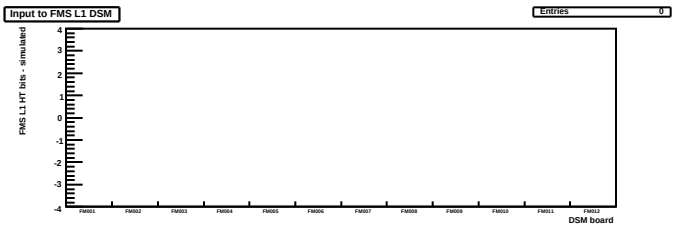

Input to FMS L1 DSM
Entries 3.46132e+07
sumCD
DSM board

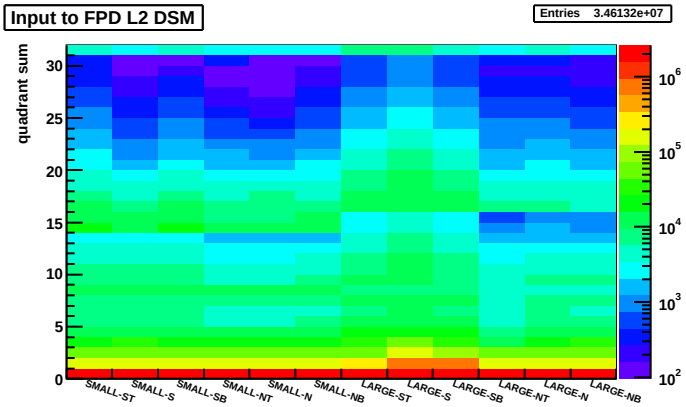

Input to FMS L1 DSM
Entries 48832
sumCD - simulated
DSM board

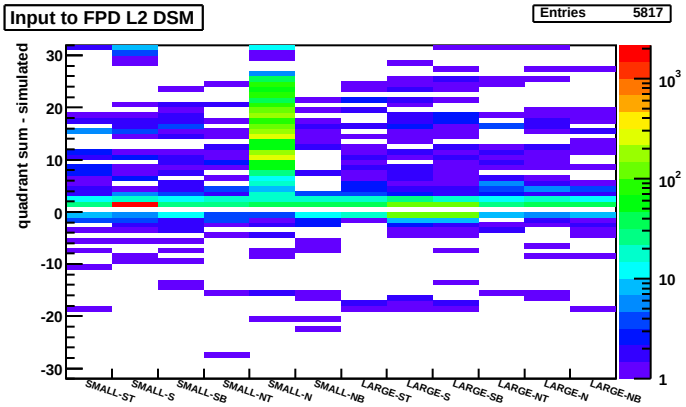

Input to FMS L1 DSM
Entries 3.46132e+07
sumA
DSM board

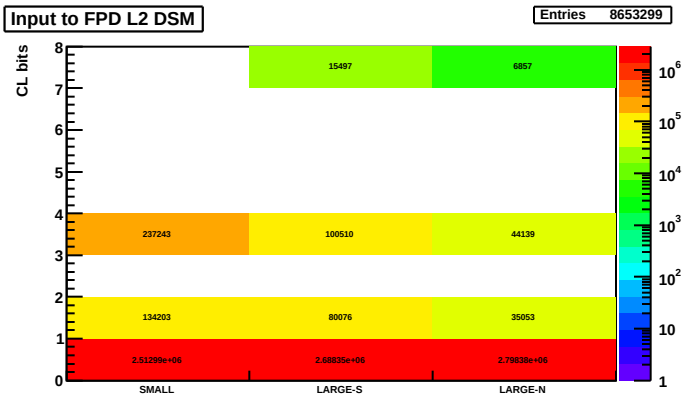

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

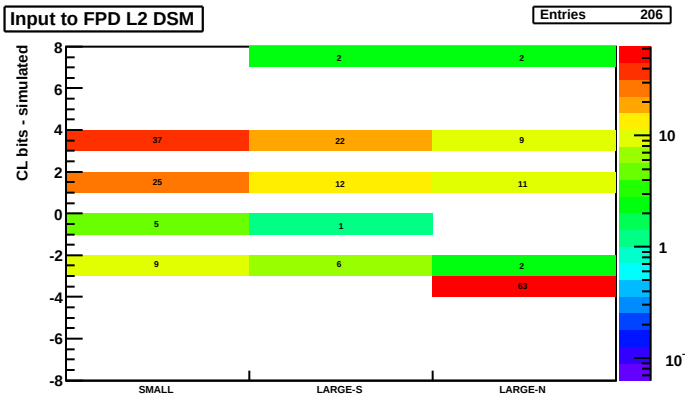

Input to FMS L1 DSM
Entries 3.46132e+07
FMS L1 HT bits
DSM board

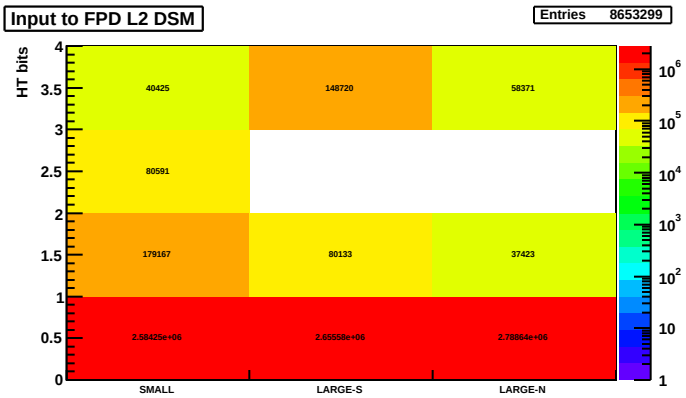

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

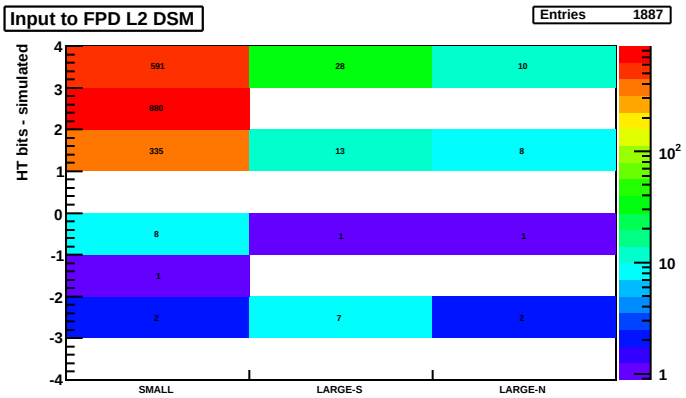

Input to FPD L2 DSM
Entries 3.46132e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

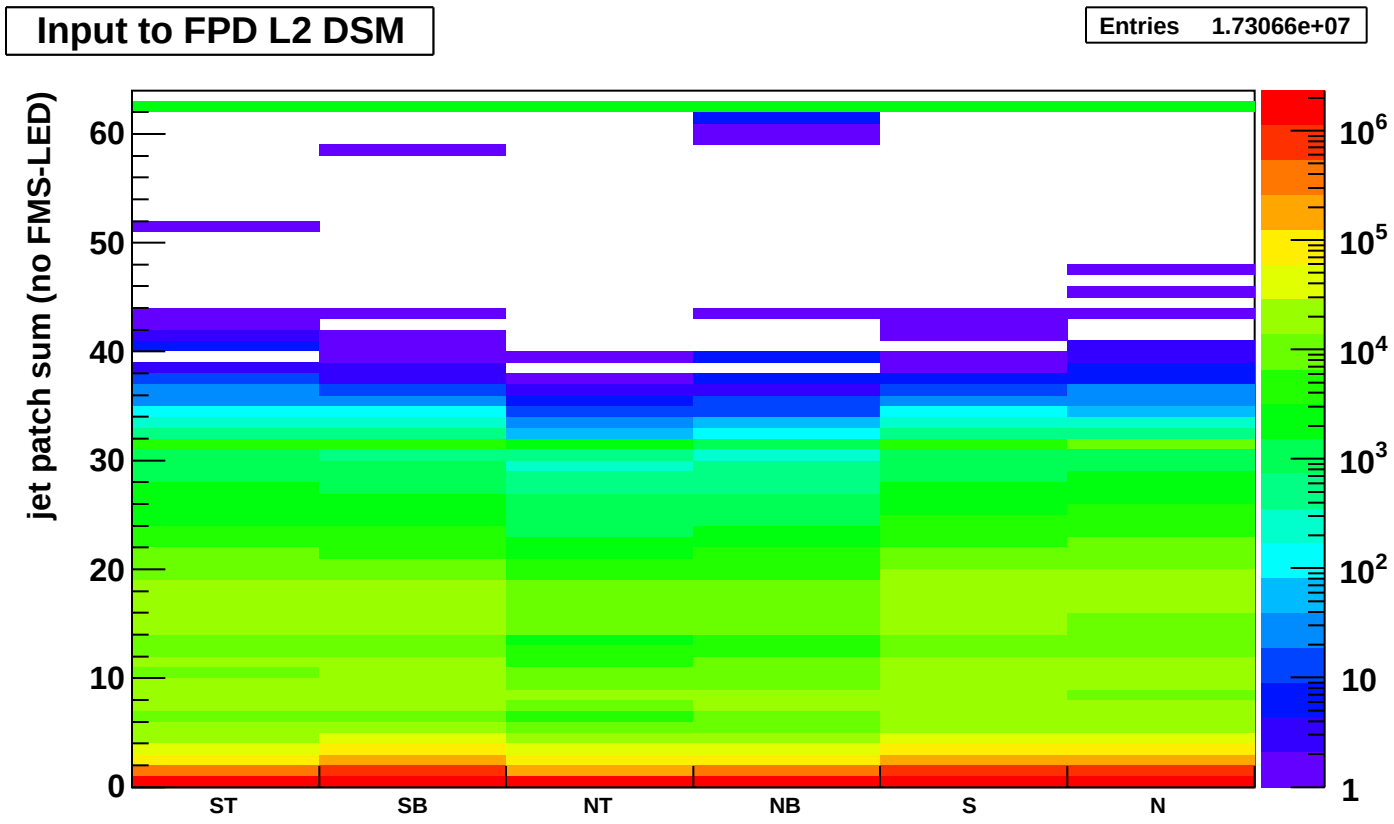

Input to FPD L2 DSM
Entries 5817
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

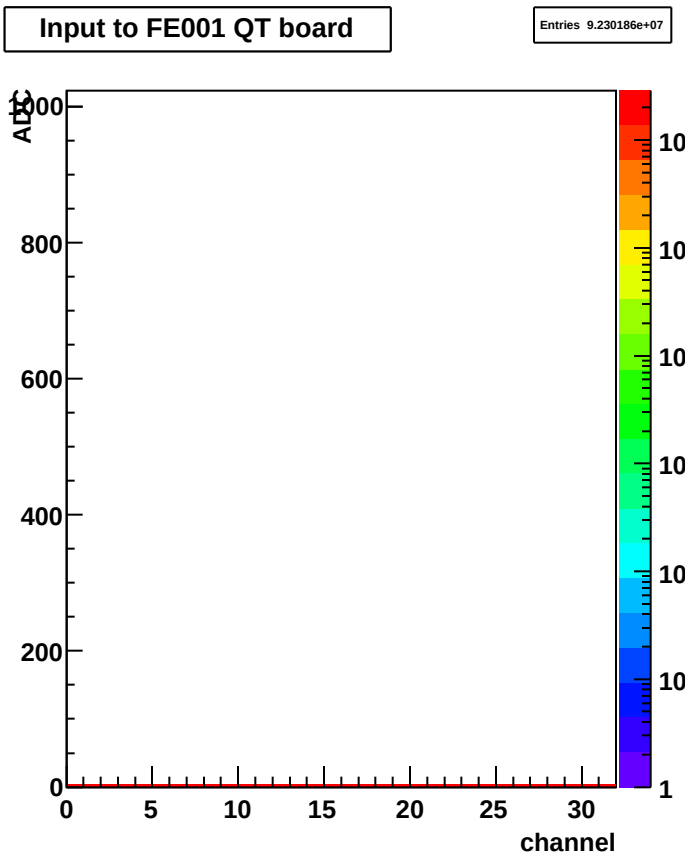

Input to FPD L2 DSM
Entries 8653299
CL bits
15497
6857
237243
100510
44139
134203
80076
35053
2.51299e+06
2.68835e+06
2.79838e+06
SMALL
LARGE-S
LARGE-N

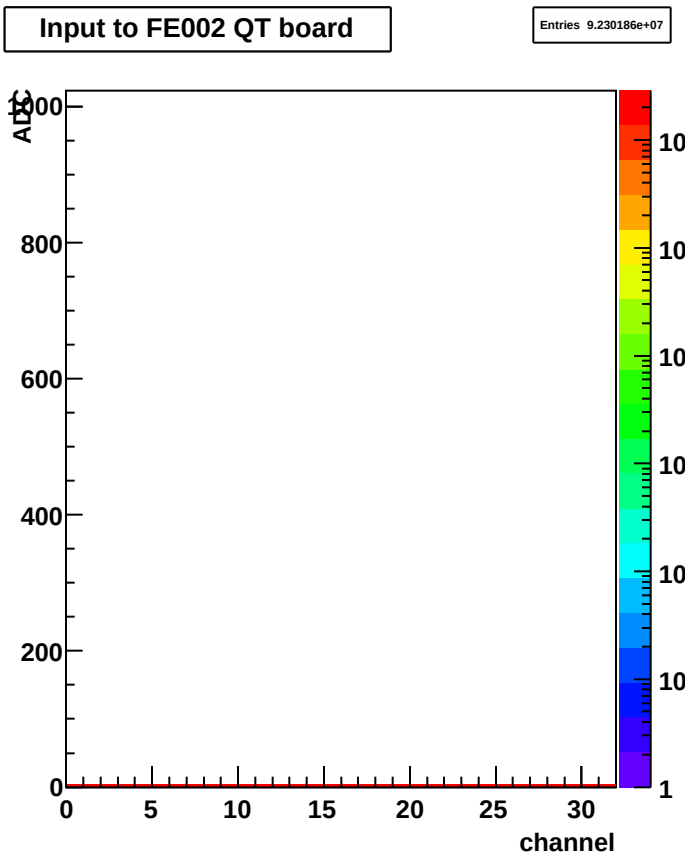

Input to FPD L2 DSM
Entries 206
CL bits - simulated
2
2
37
22
9
25
12
11
5
1
9
6
2
63
SMALL
LARGE-S
LARGE-N

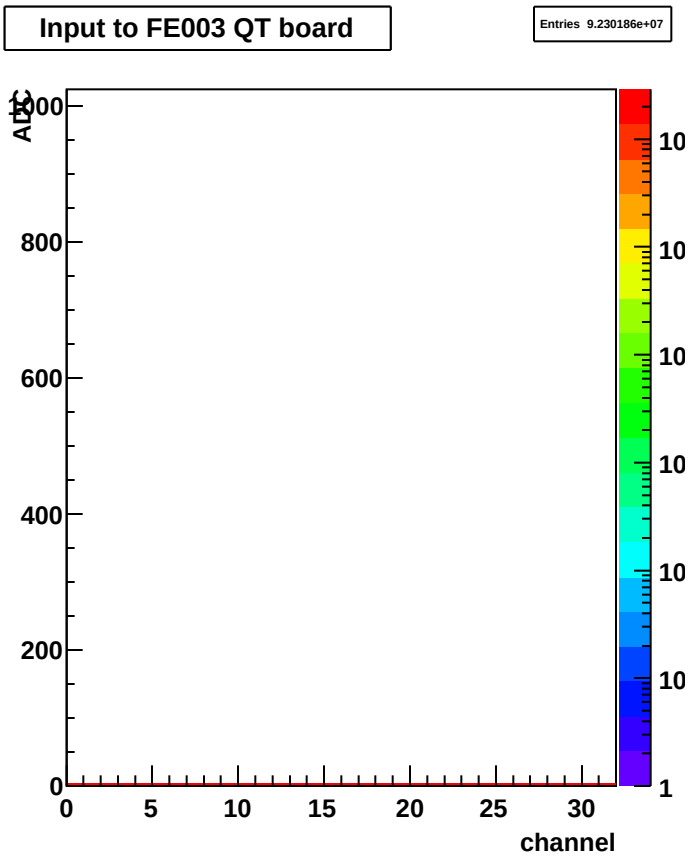

Input to FPD L2 DSM
Entries 8653299
HT bits
40425
148720
58371
80591
179167
80133
37423
2.58425e+06
2.65558e+06
2.78864e+06
SMALL
LARGE-S
LARGE-N

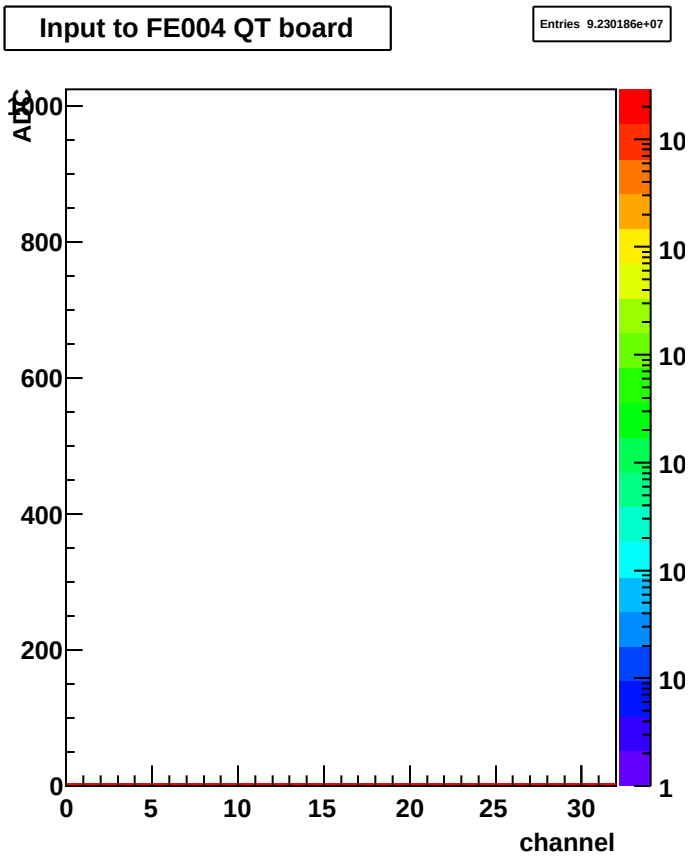

Input to FPD L2 DSM
Entries 1887
HT bits - simulated
591
28
10
880
335
13
8
8
1
1
1
2
7
2
SMALL
LARGE-S
LARGE-N

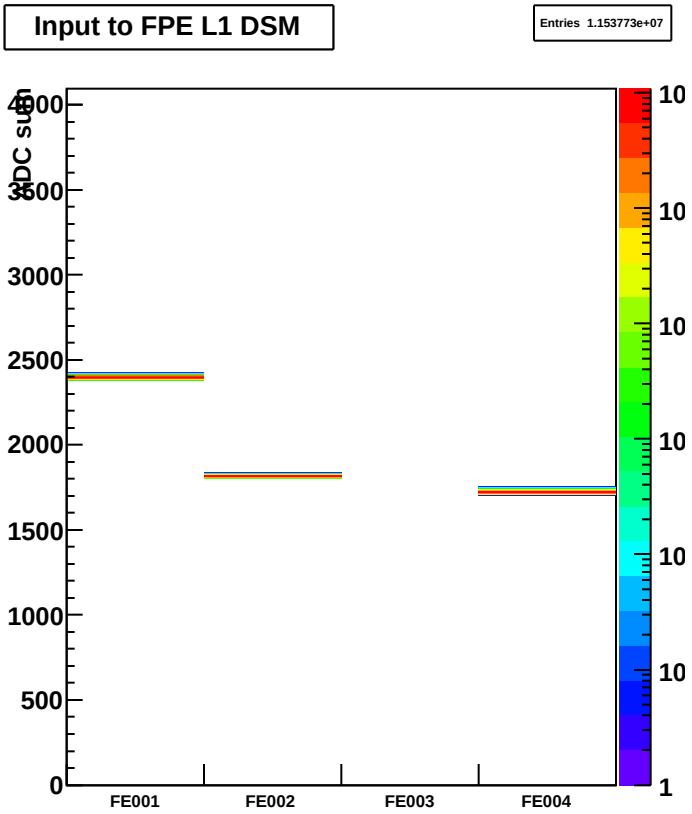

Input to FPD L2 DSM
Entries 1.73066e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 9.230186e+07
ADC
channel

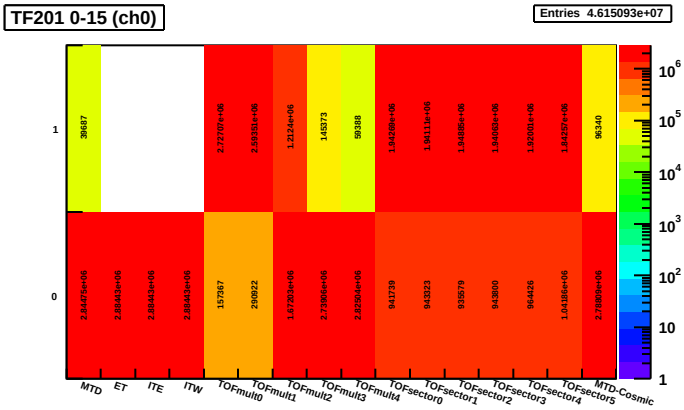

Input to FE002 QT board
Entries 9.230186e+07
ADC
channel


Input to FE003 QT board
Entries 9.230186e+07
ADC
channel

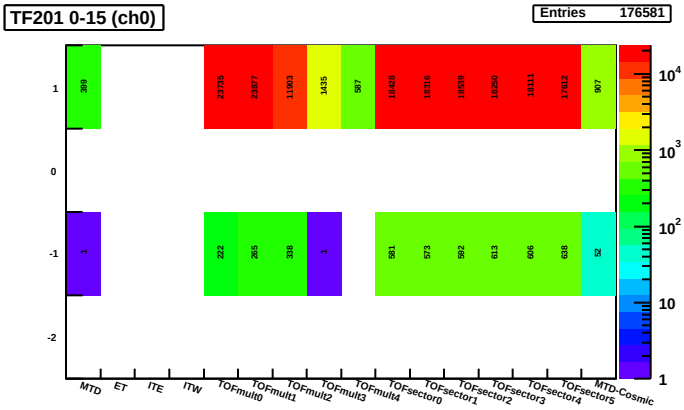

Input to FE004 QT board
Entries 9.230186e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.153773e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

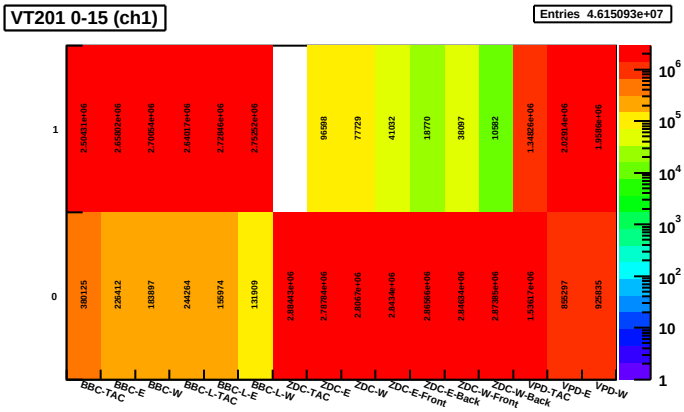
TF201 0-15 (ch0)



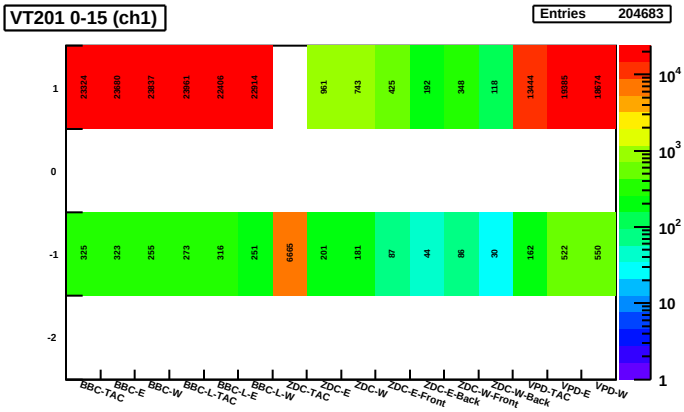
TF201 0-15 (ch0)



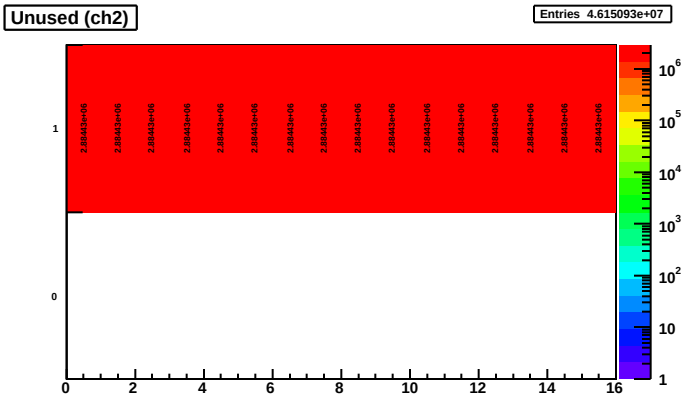
VT201 0-15 (ch1)

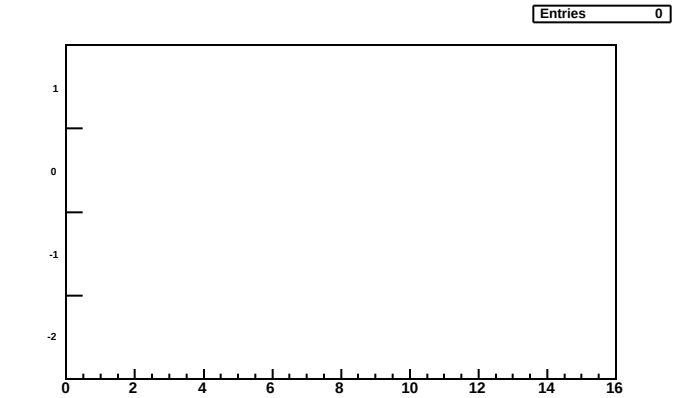


VT201 0-15 (ch1)

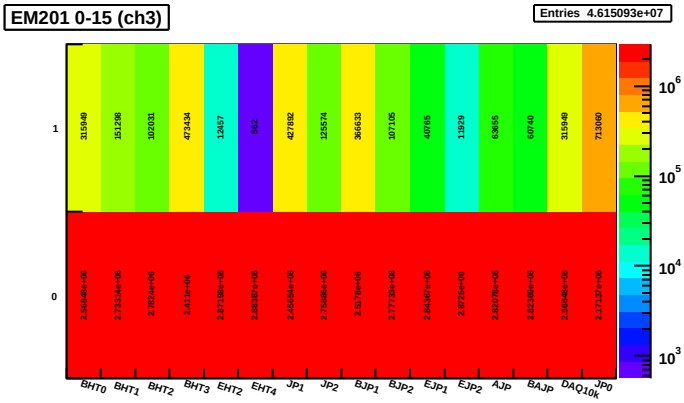


Unused (ch2)

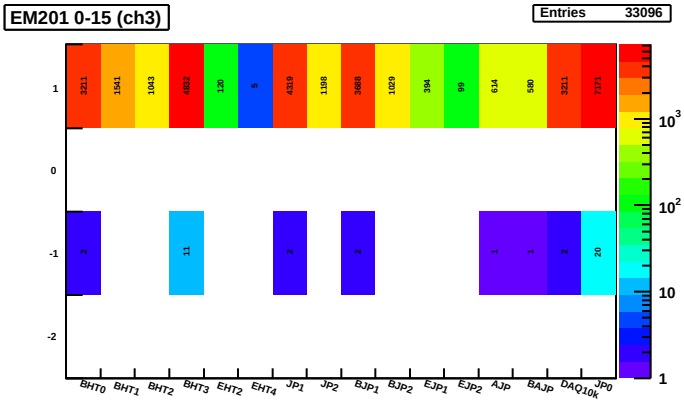




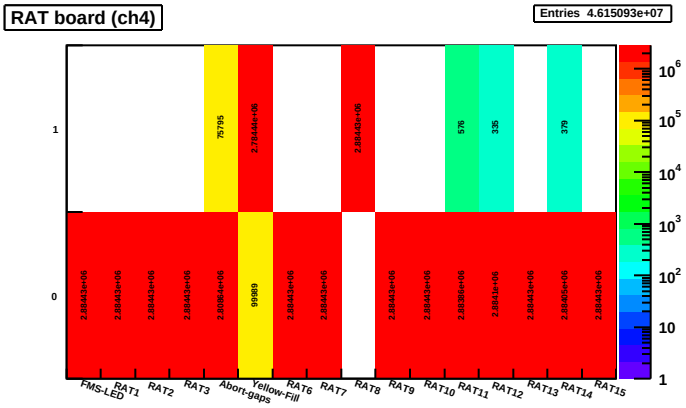
EM201 0-15 (ch3)

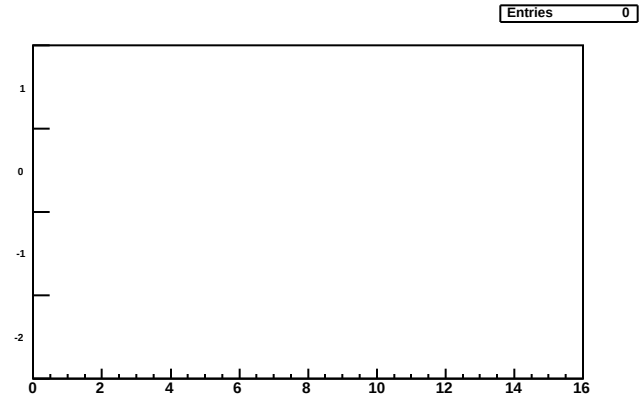


EM201 0-15 (ch3)

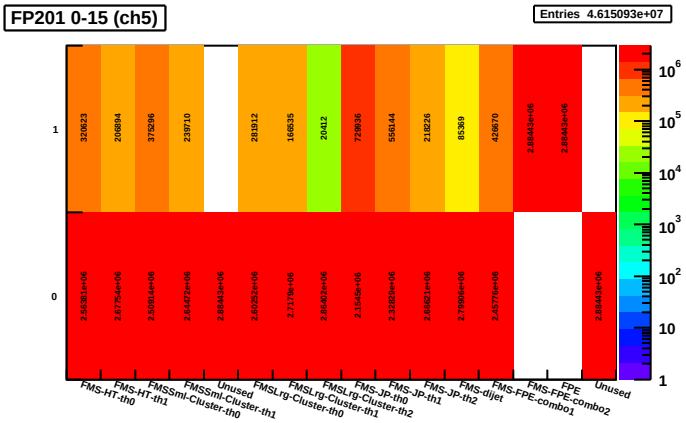


RAT board (ch4)

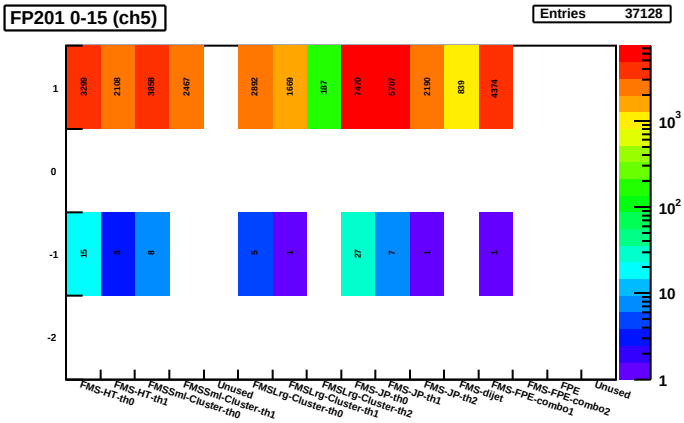




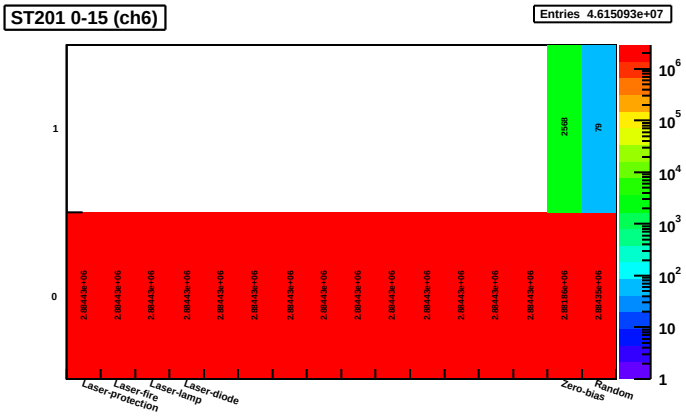
FP201 0-15 (ch5)

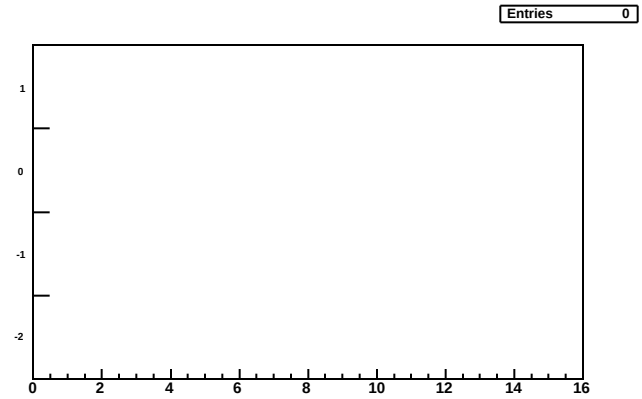
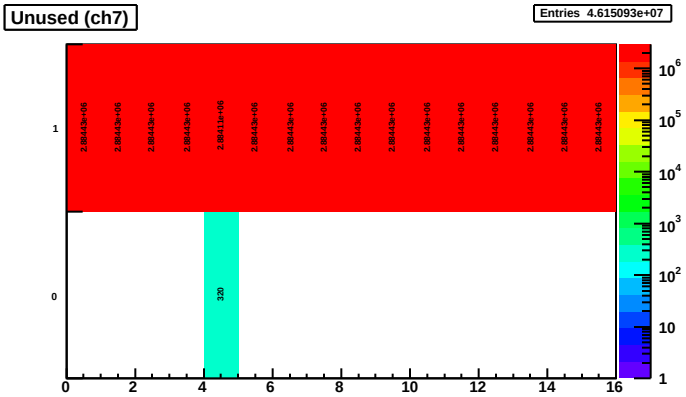
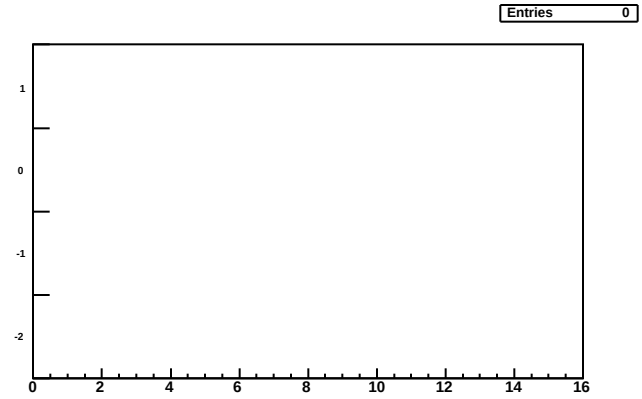


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)