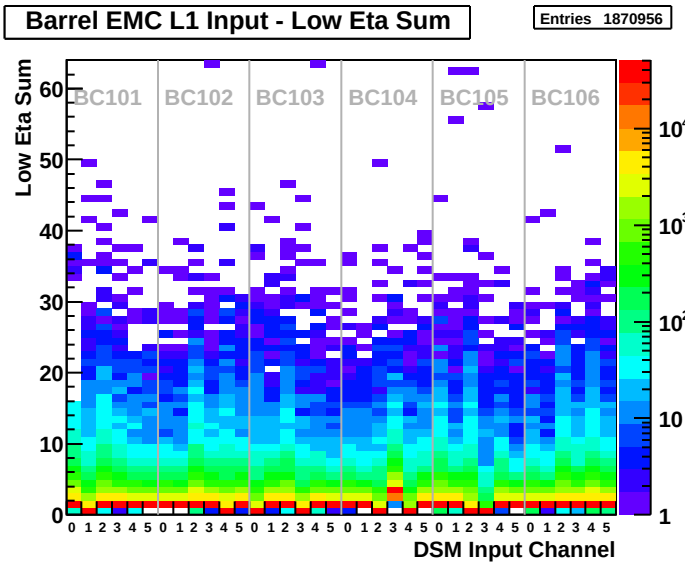
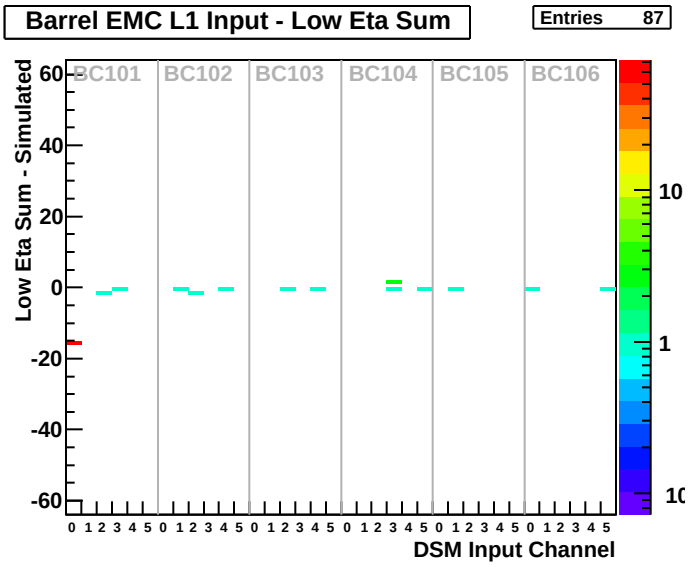
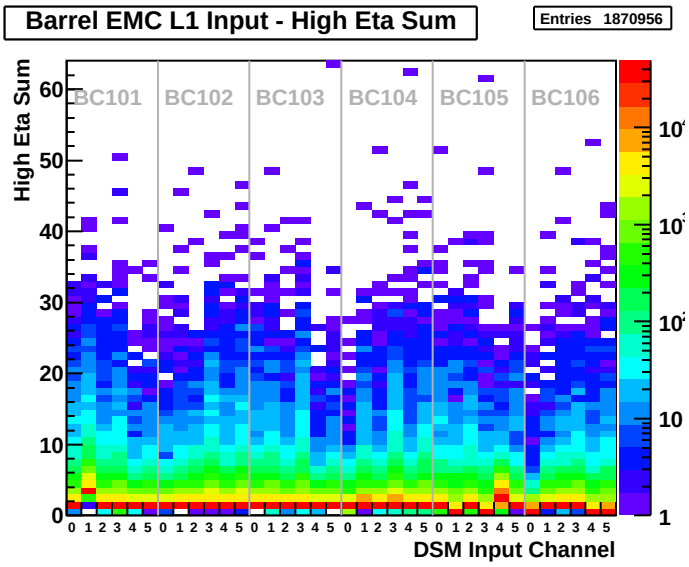
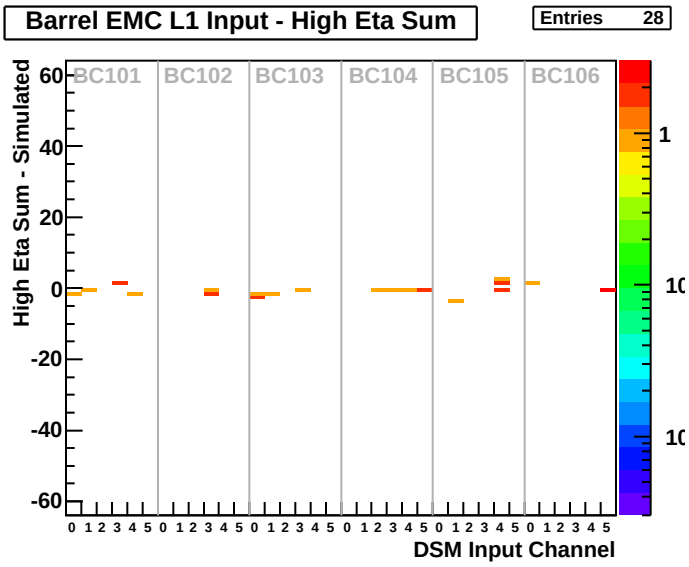
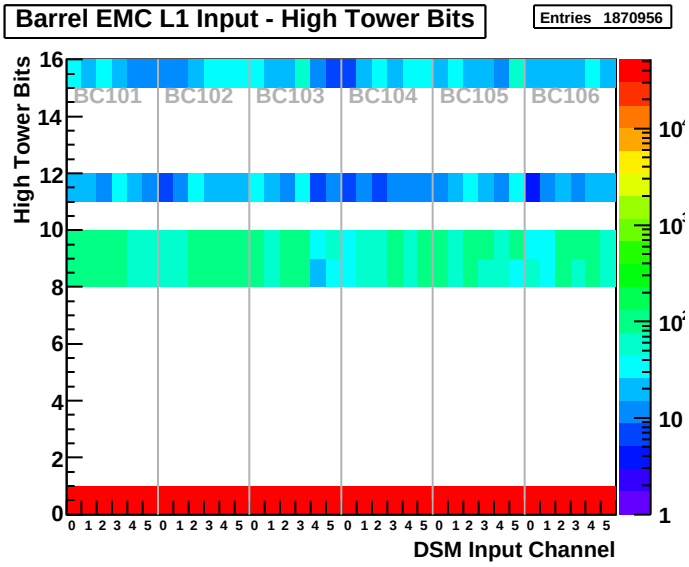
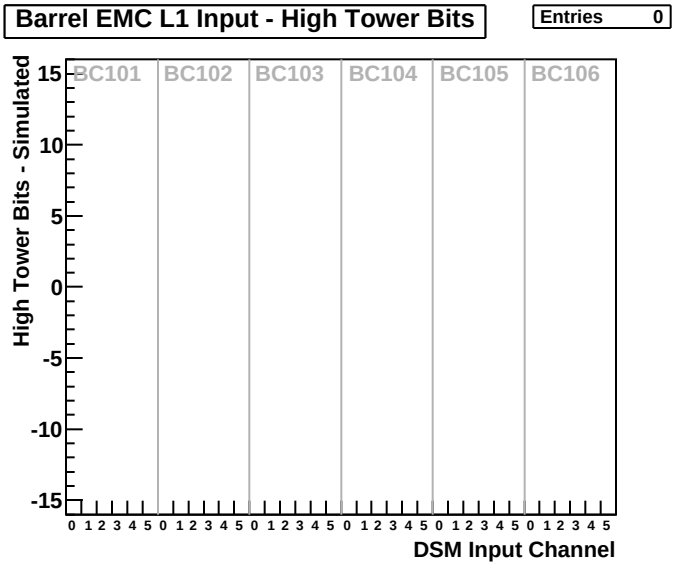

Barrel EMC L1 Input - Low Eta Sum
Entries 1870956
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

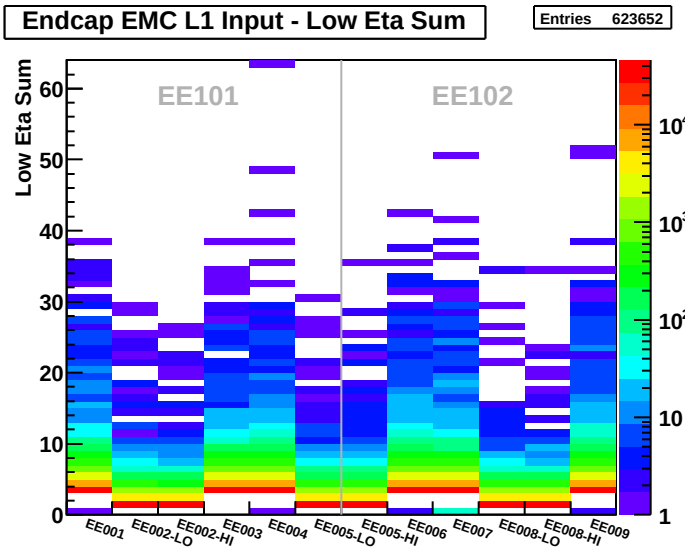

Barrel EMC L1 Input - Low Eta Sum
Entries 87
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

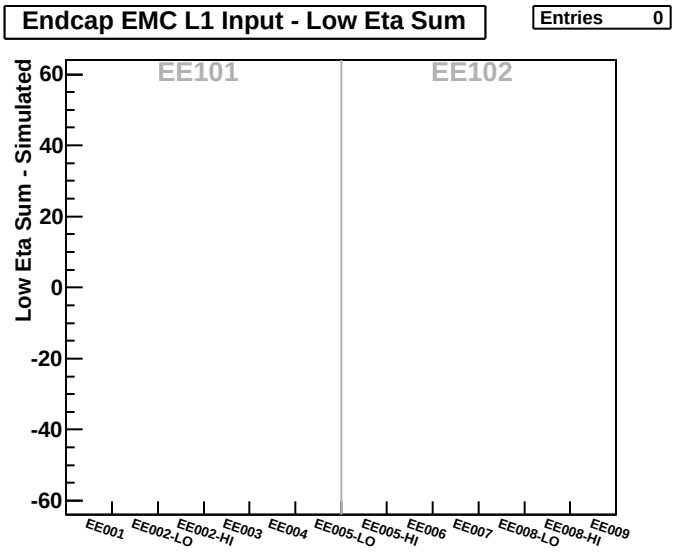

Barrel EMC L1 Input - High Eta Sum
Entries 1870956
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

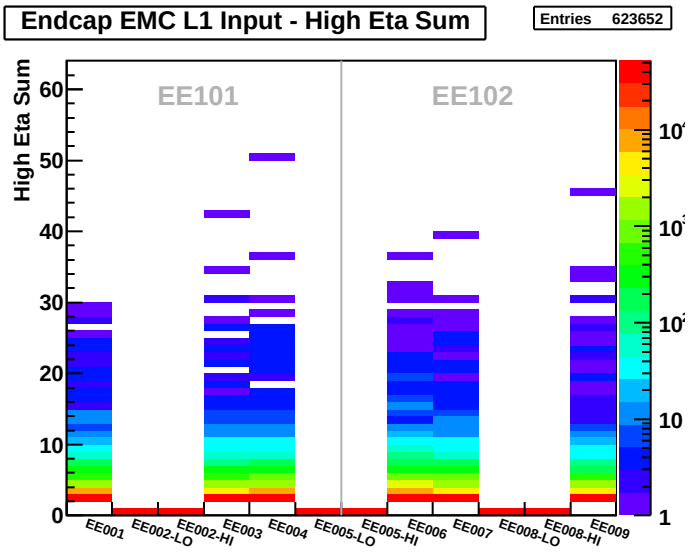

Barrel EMC L1 Input - High Eta Sum
Entries 28
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

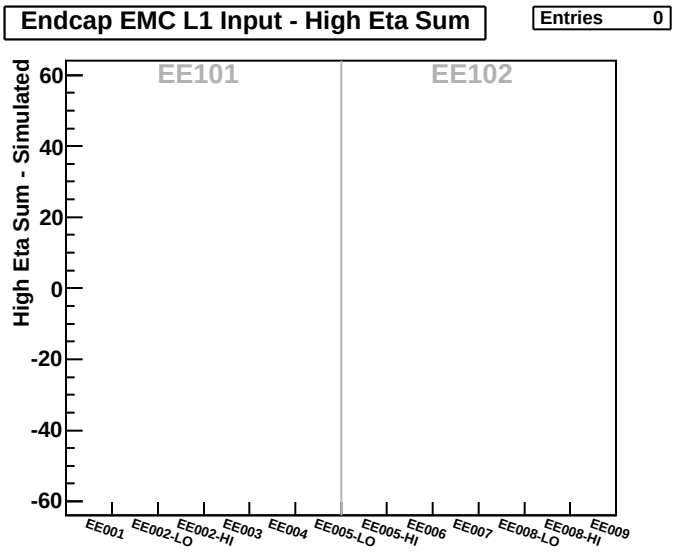

Barrel EMC L1 Input - High Tower Bits
Entries 1870956
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

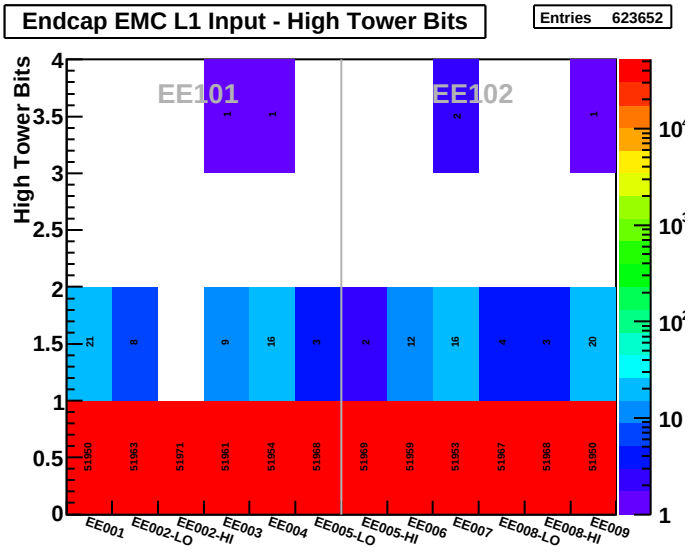

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

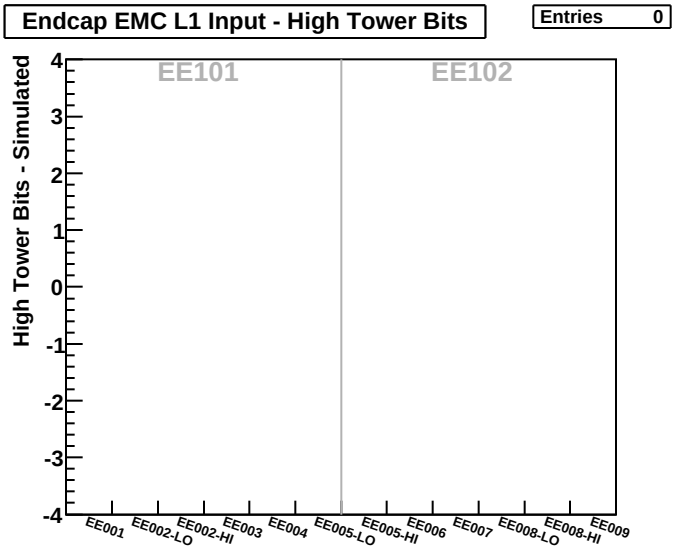

Endcap EMC L1 Input - Low Eta Sum
Entries 623652
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

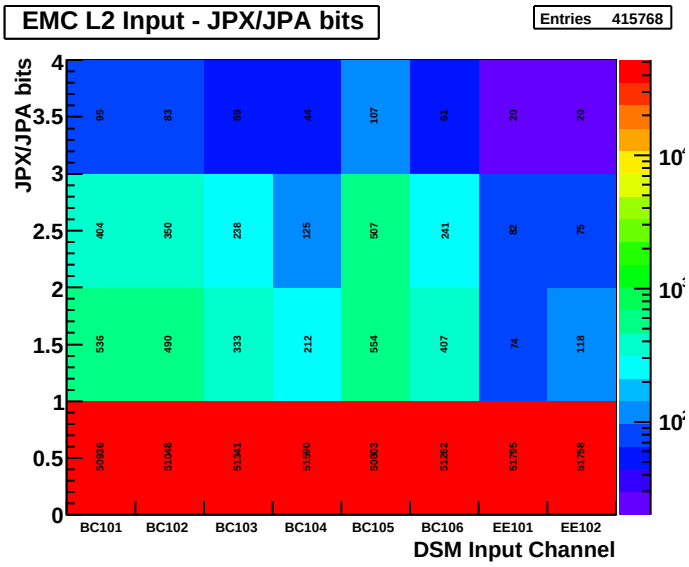

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

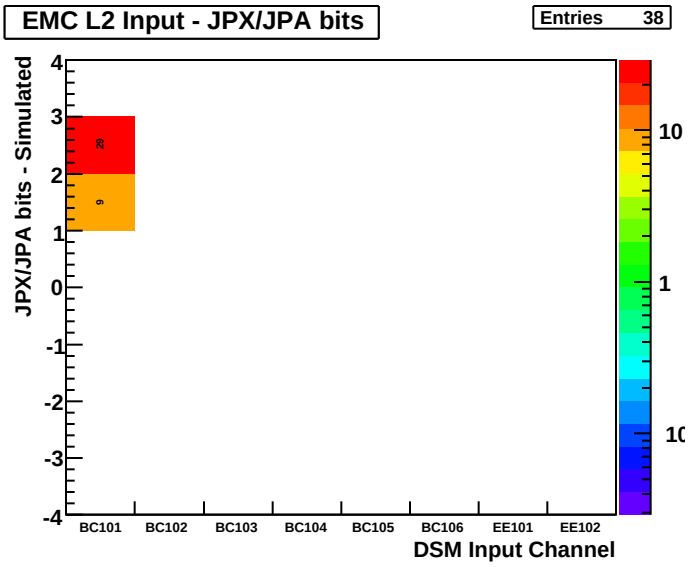

Endcap EMC L1 Input - High Eta Sum
Entries 623652
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

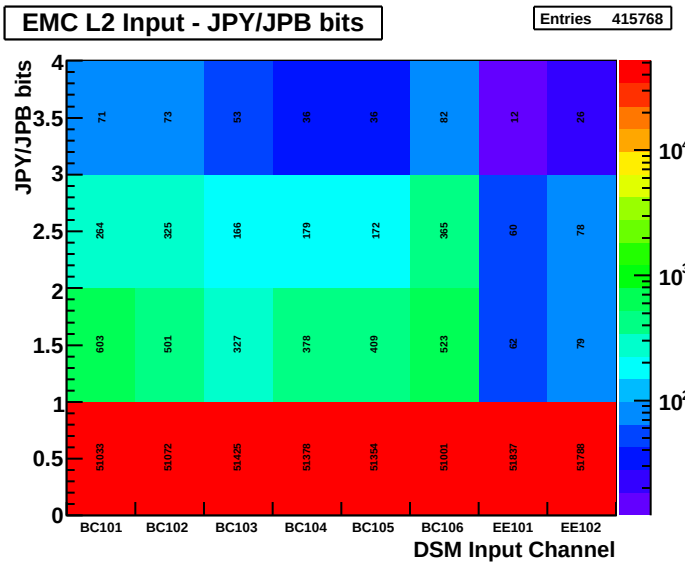

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

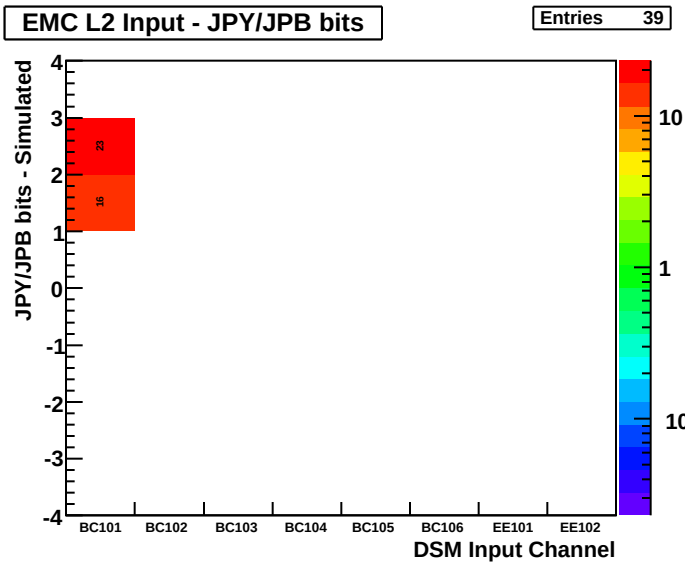

Endcap EMC L1 Input - High Tower Bits
Entries 623652
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

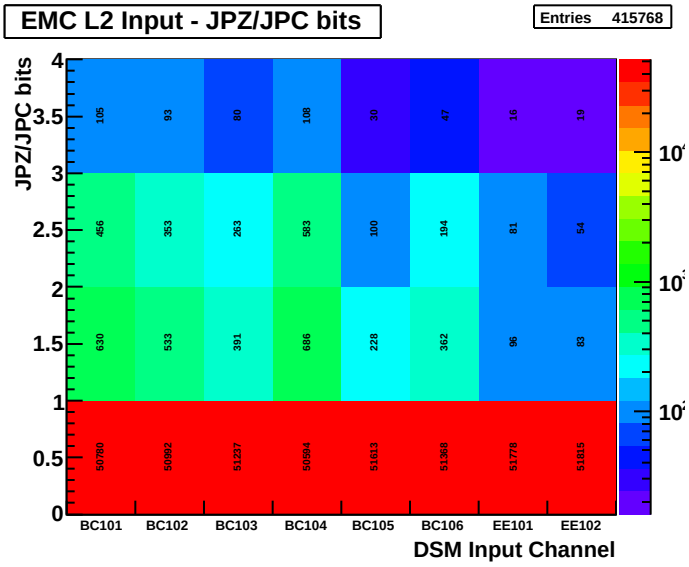

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

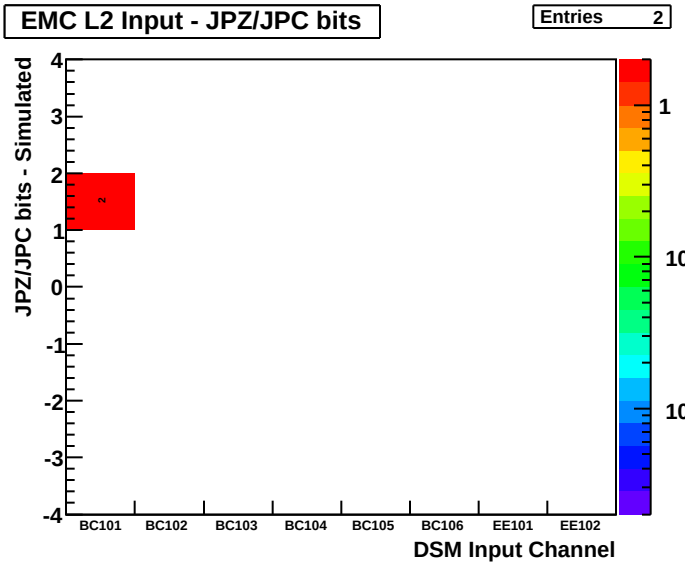

EMC L2 Input - JPX/JPA bits
Entries 415768
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
95 83 59 44 107 61 20 20
404 350 238 125 507 241 82 75
536 490 333 212 554 407 74 118
50936 51048 51341 51590 50803 51262 51795 51758

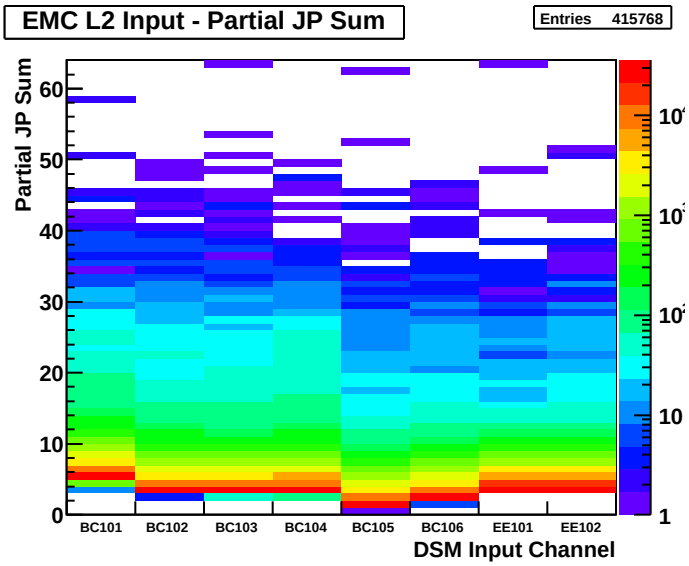

EMC L2 Input - JPX/JPA bits
Entries 38
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
29
9

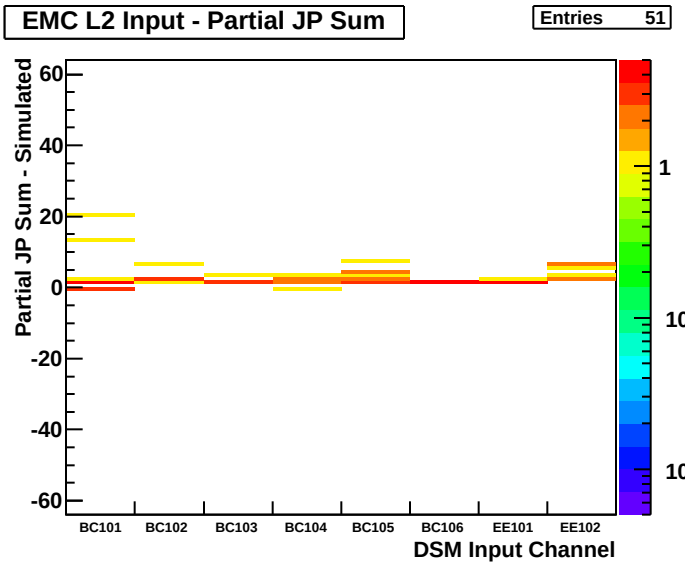

EMC L2 Input - JPY/JPB bits
Entries 415768
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
71 73 53 36 36 82 12 26
264 325 166 179 172 365 60 78
603 501 327 378 409 523 62 79
51033 51072 51425 51378 51354 51001 51837 51788

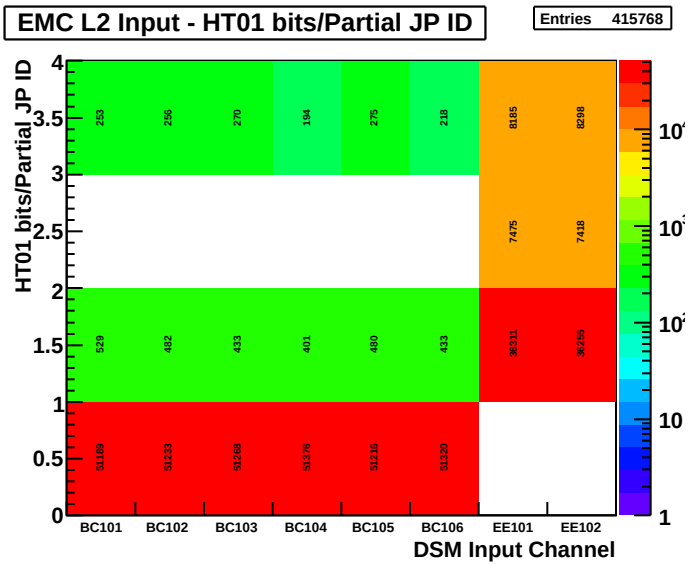

EMC L2 Input - JPY/JPB bits
Entries 39
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
23
16

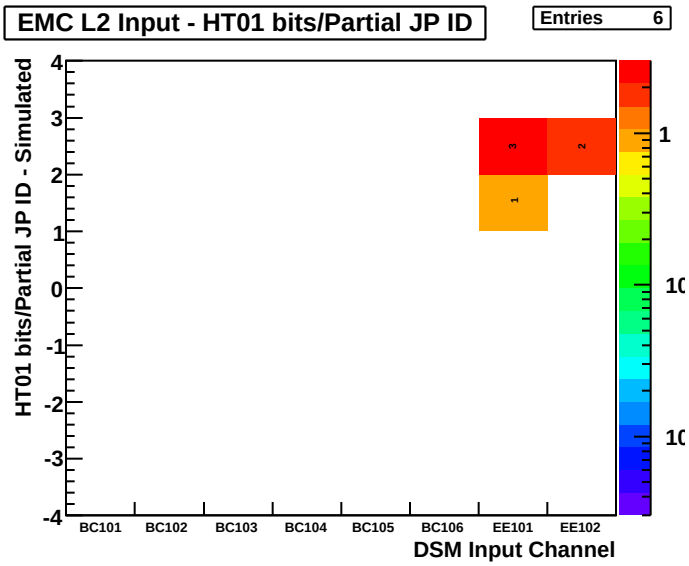

EMC L2 Input - JPZ/JPC bits
Entries 415768
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
105 93 80 108 30 47 16 19
456 353 263 583 100 194 81 54
630 533 391 686 228 362 96 83
50760 50892 51237 50594 51613 51368 51778 51815

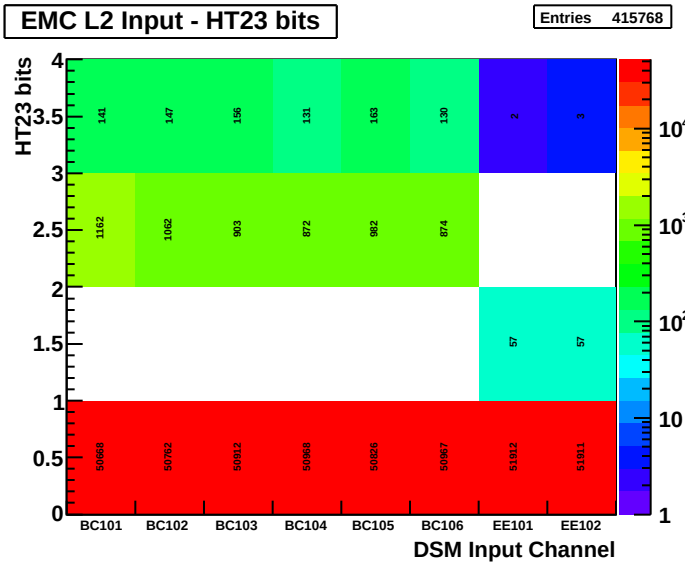

EMC L2 Input - JPZ/JPC bits
Entries 2
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2

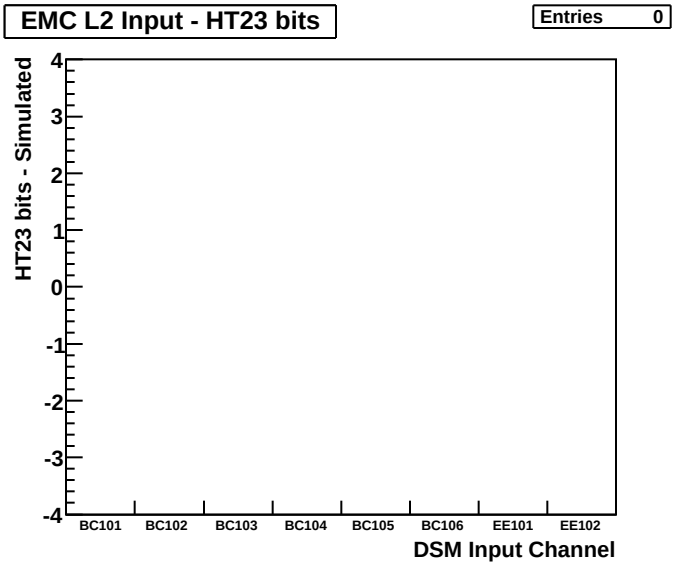

EMC L2 Input - Partial JP Sum
Entries 415768
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

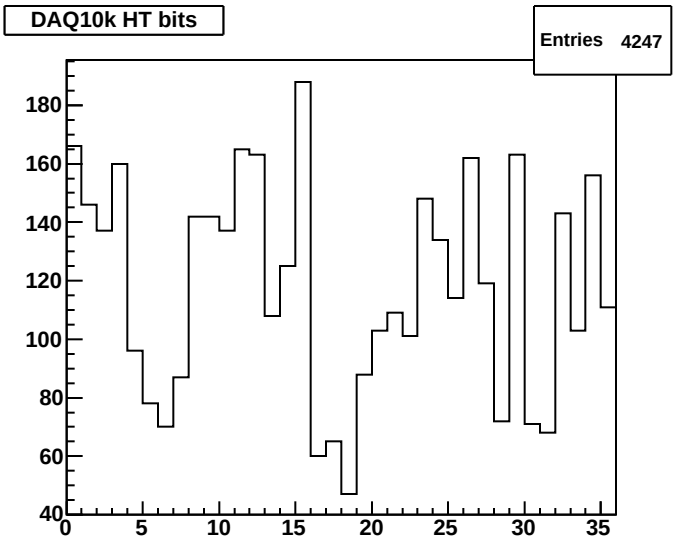

EMC L2 Input - Partial JP Sum
Entries 51
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

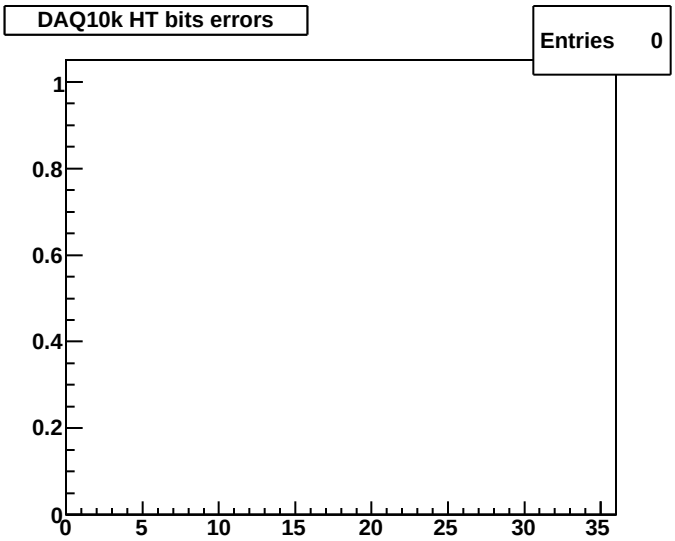

EMC L2 Input - HT01 bits/Partial JP ID
Entries 415768
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

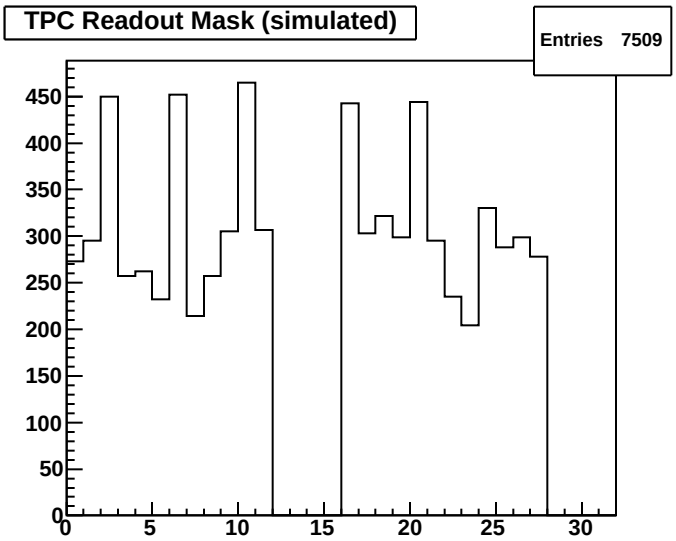

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

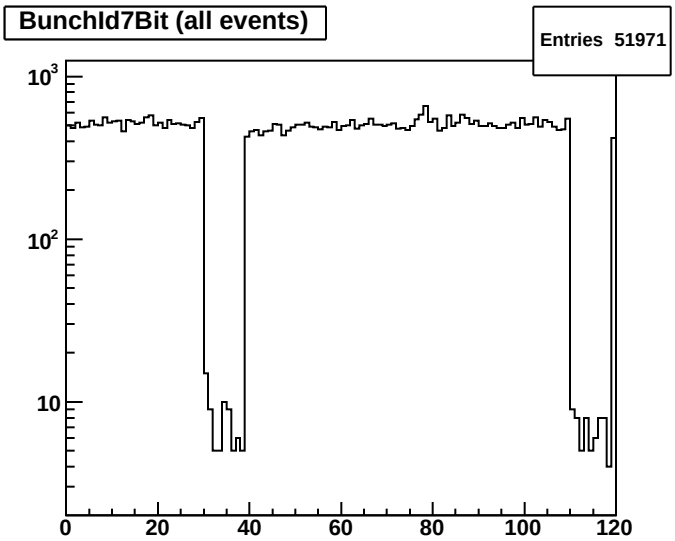

EMC L2 Input - HT23 bits
Entries 415768
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

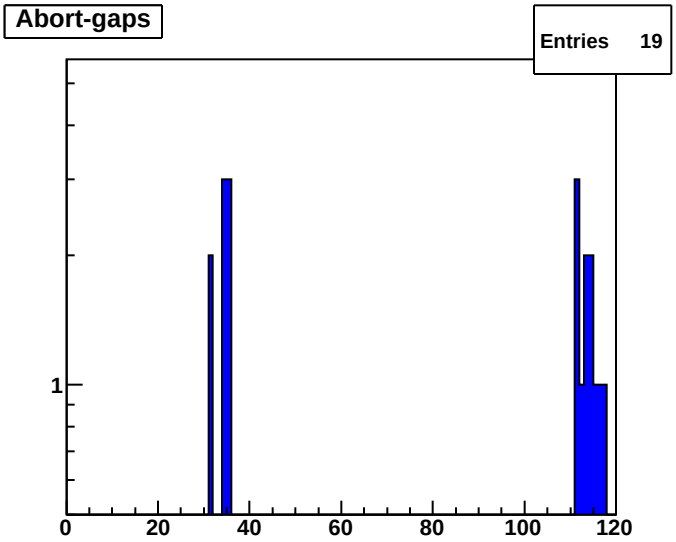

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

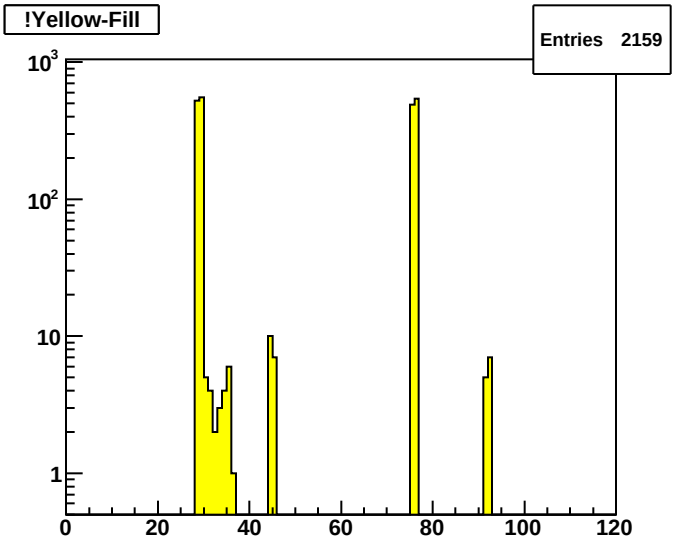

DAQ10k HT bits
Entries 4247

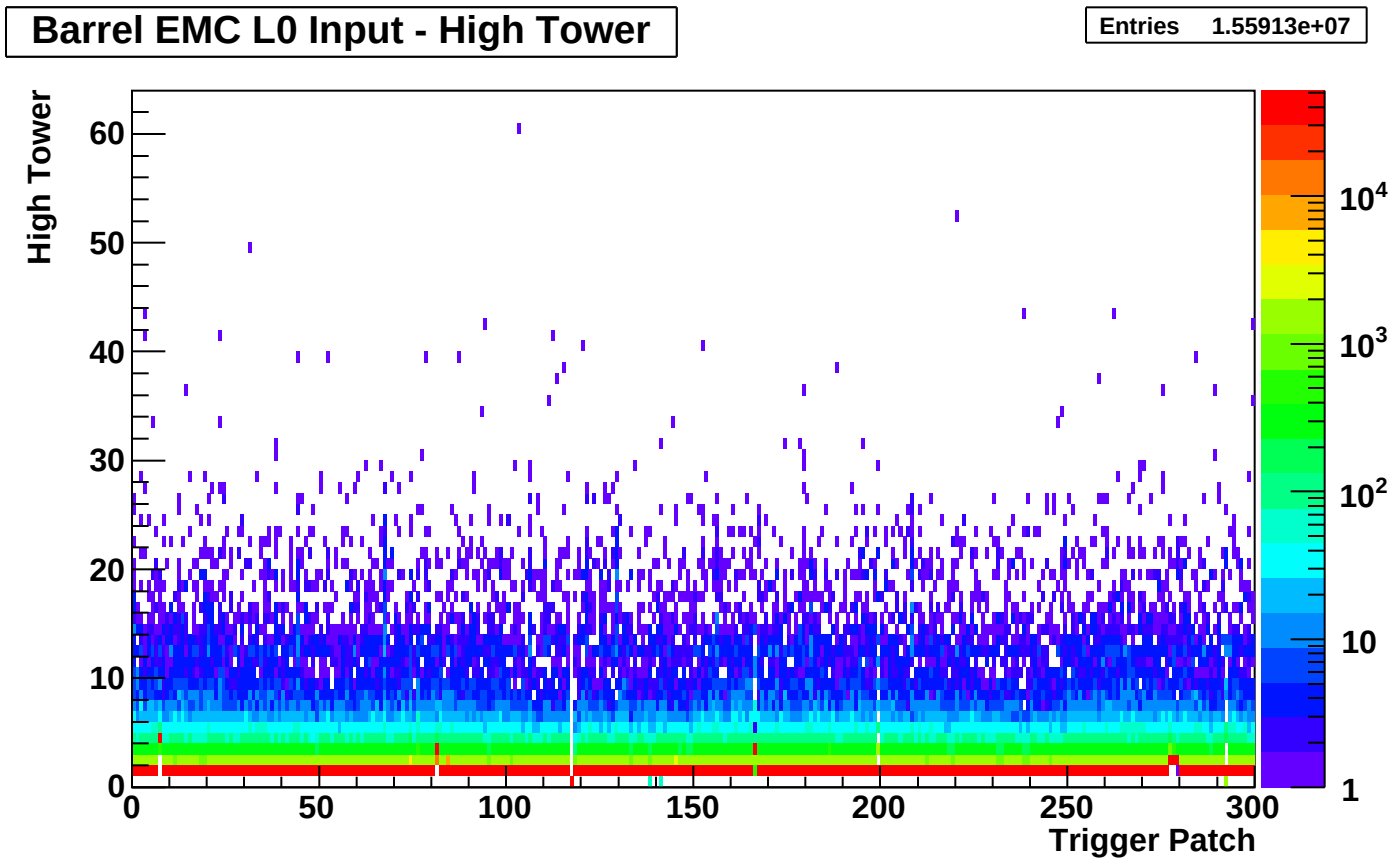

DAQ10k HT bits errors
Entries 0

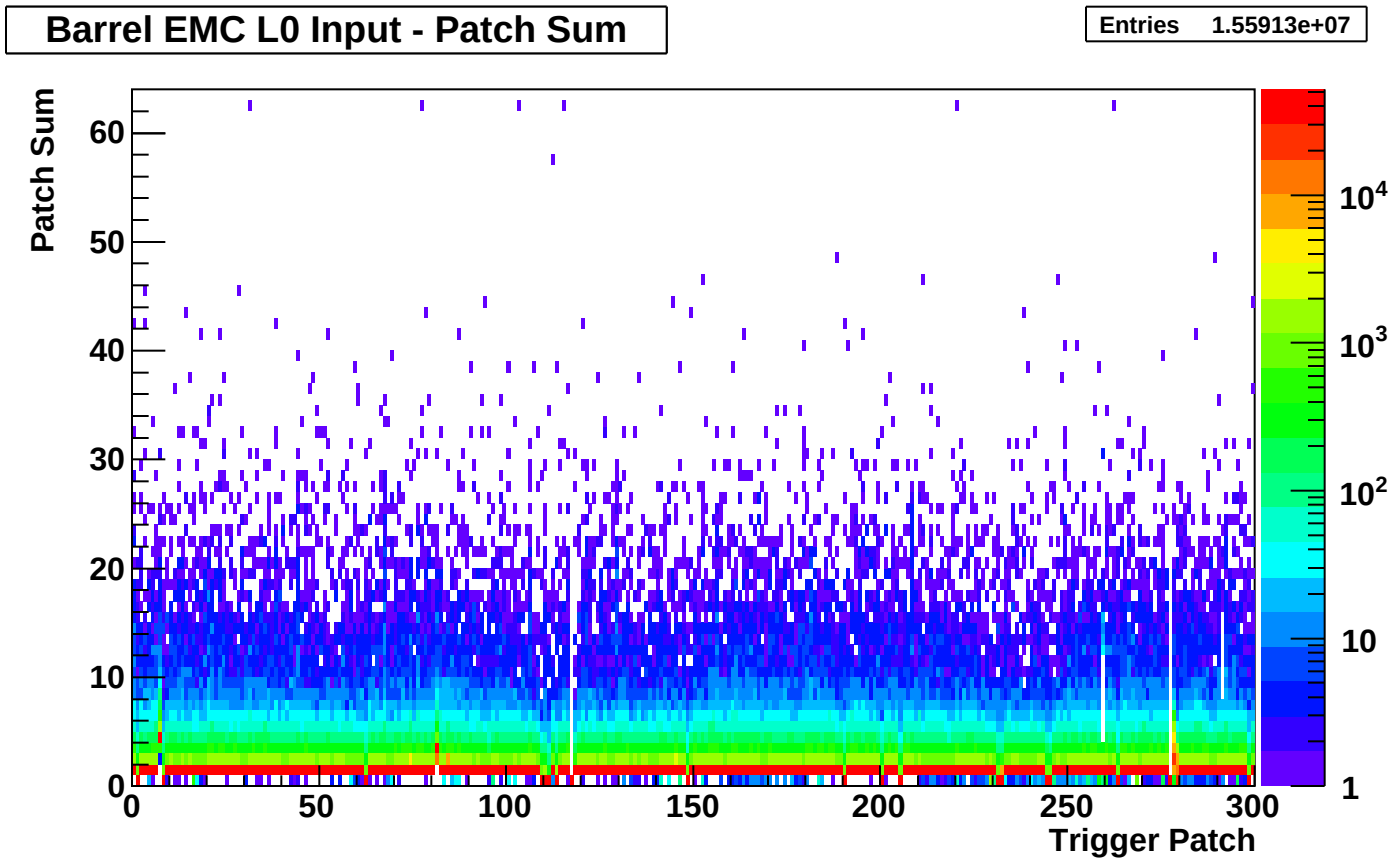

TPC Readout Mask (simulated)
Entries 7509

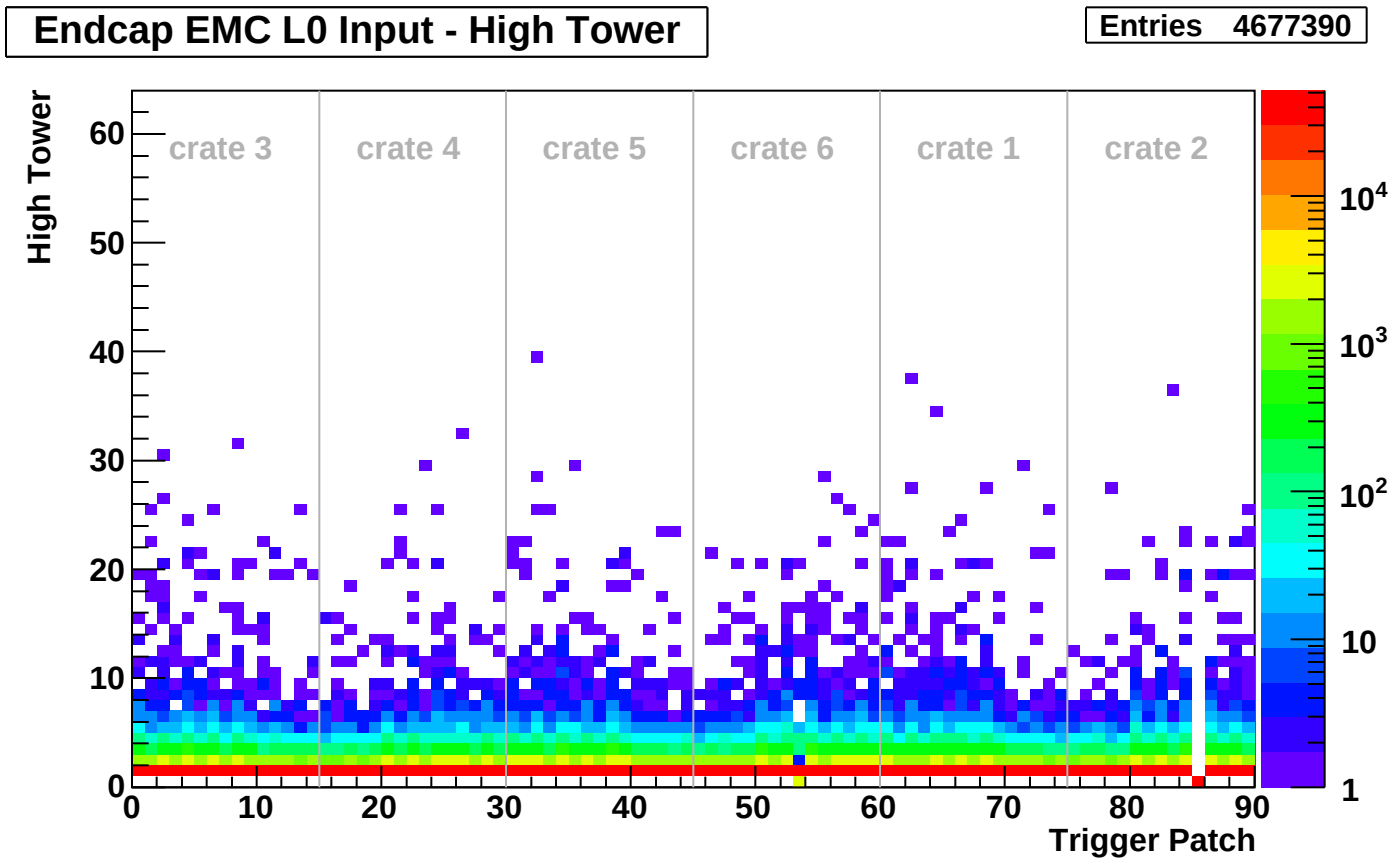

BunchId7Bit (all events)
Entries 51971

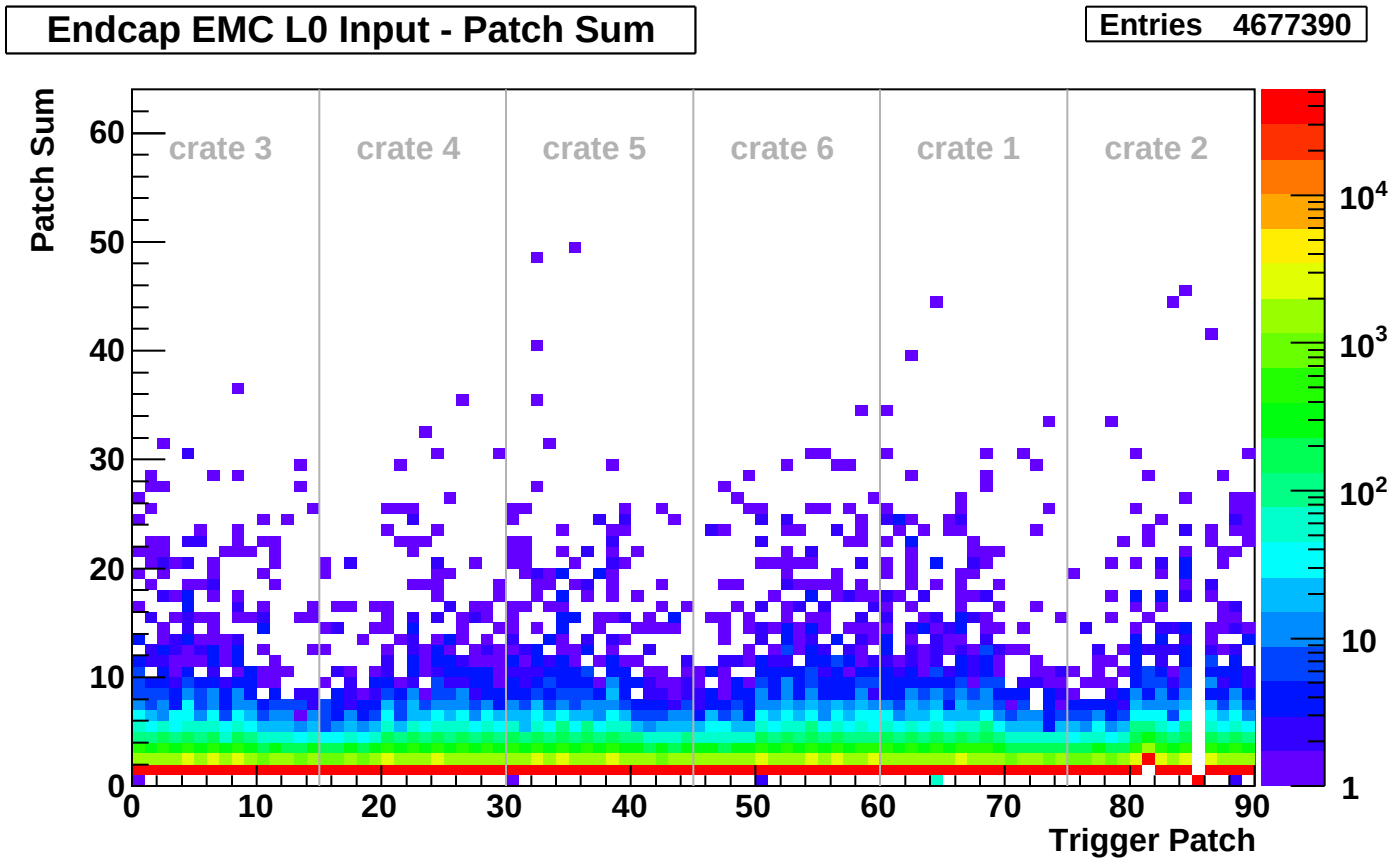

Abort-gaps
Entries 19

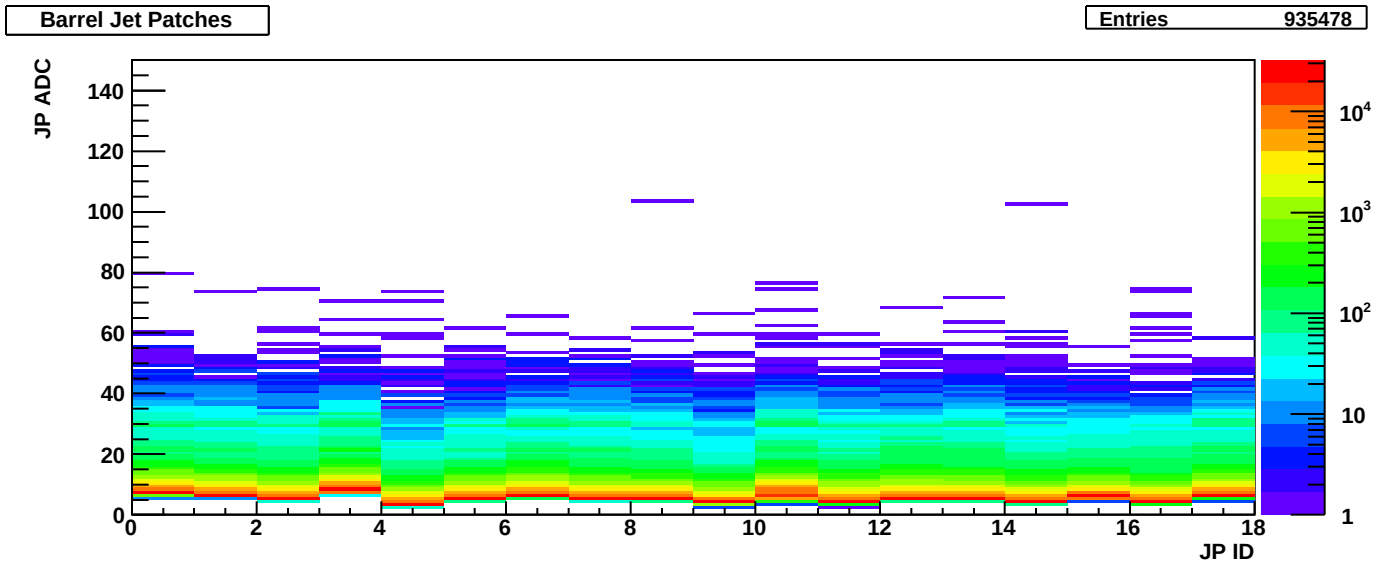

!Yellow-Fill
Entries 2159

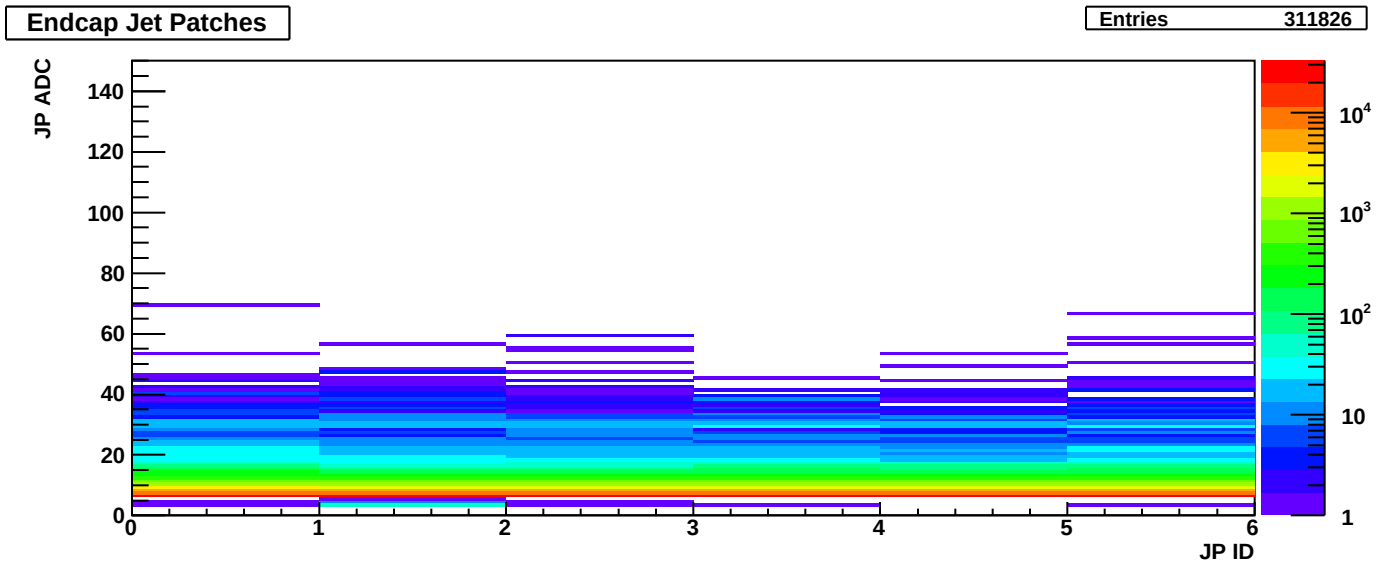

Barrel EMC L0 Input - High Tower
Entries 1.55913e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

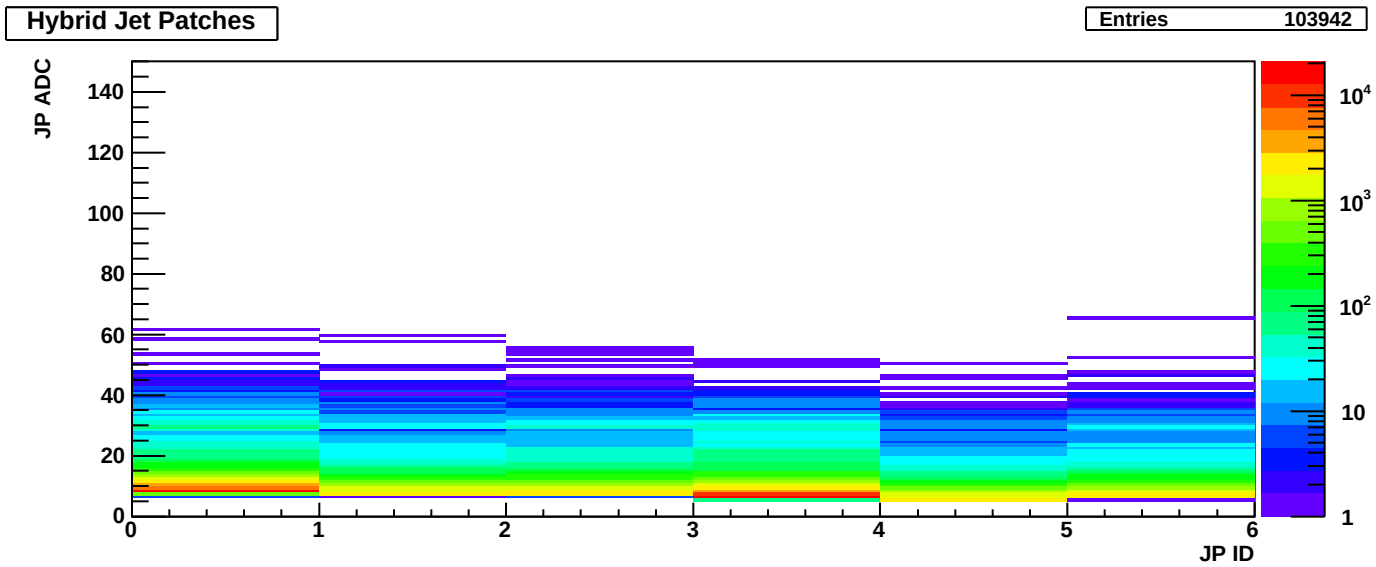

Barrel EMC L0 Input - Patch Sum
Entries 1.55913e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

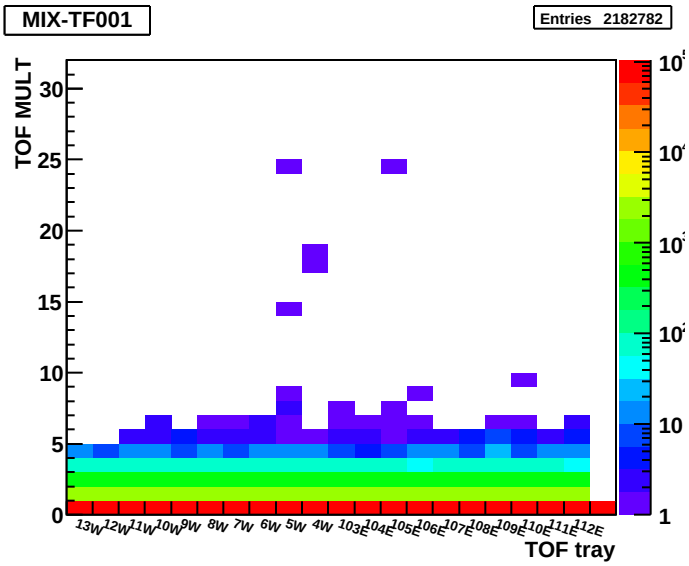

Endcap EMC L0 Input - High Tower
Entries 4677390
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

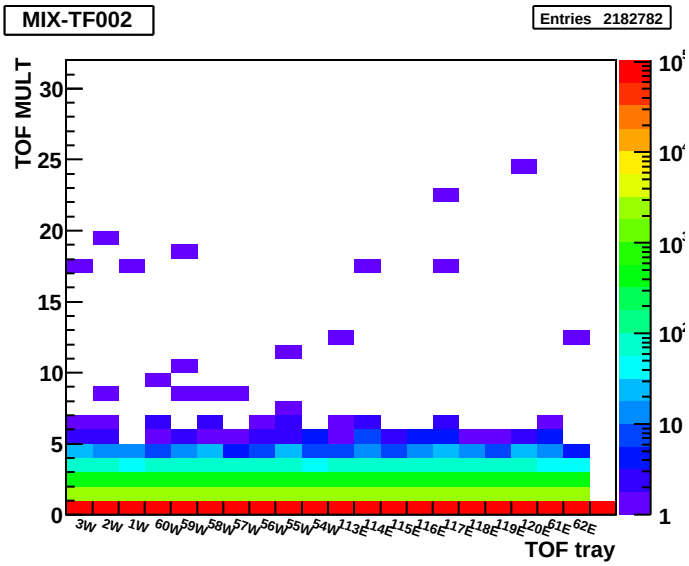

Endcap EMC L0 Input - Patch Sum
Entries 4677390
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

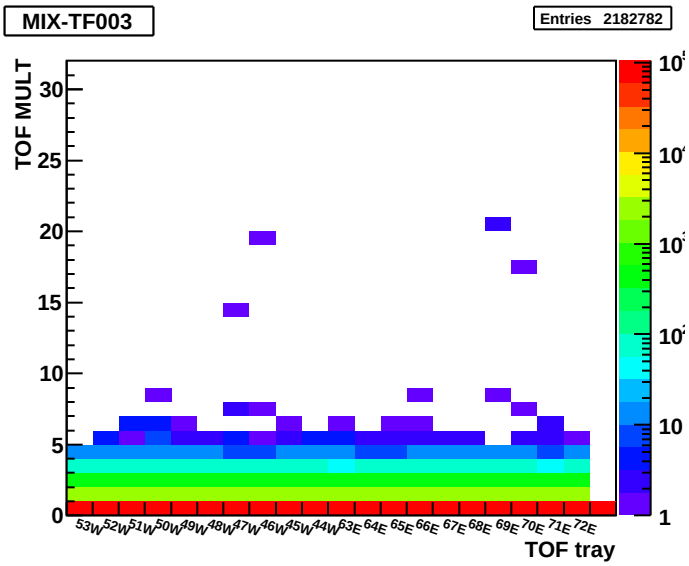

Barrel Jet Patches
Entries 935478
JP ADC
JP ID

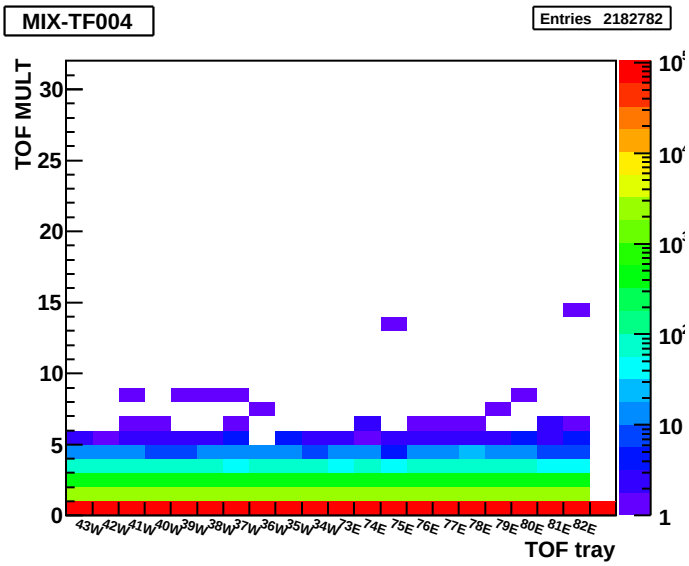

Endcap Jet Patches
Entries 311826
JP ADC
JP ID

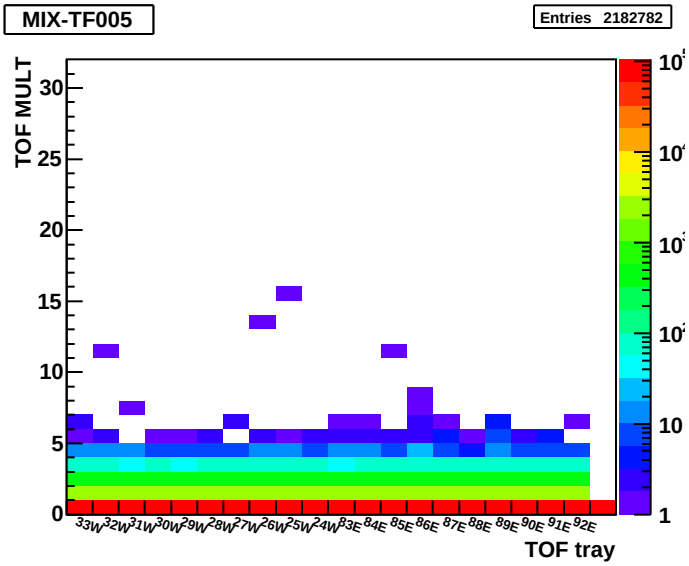

Hybrid Jet Patches
Entries 103942
JP ADC
JP ID

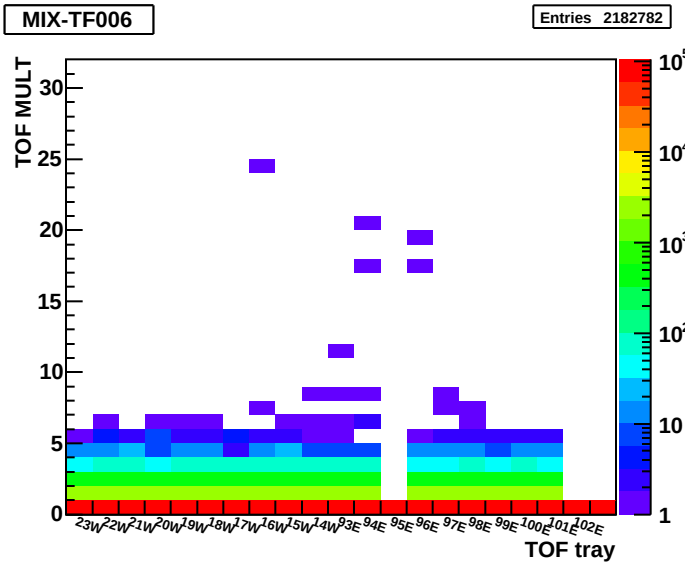

MIX-TF001
Entries 2182782
TOF MULT
TOF tray

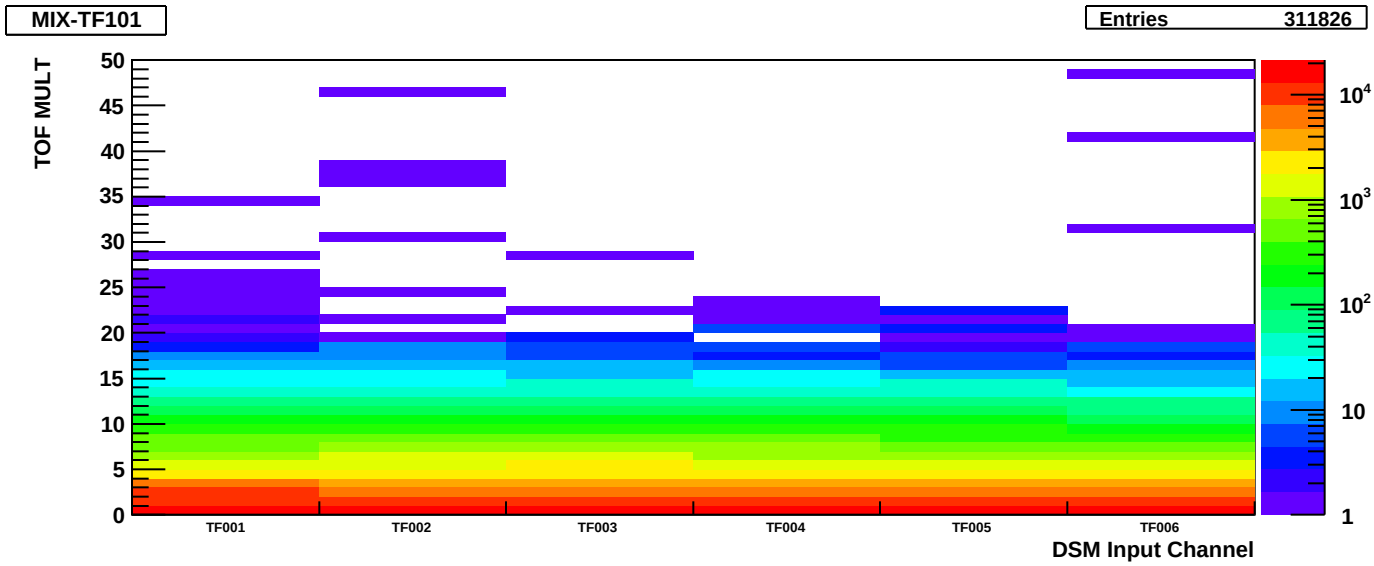

MIX-TF002
Entries 2182782
TOF MULT
TOF tray

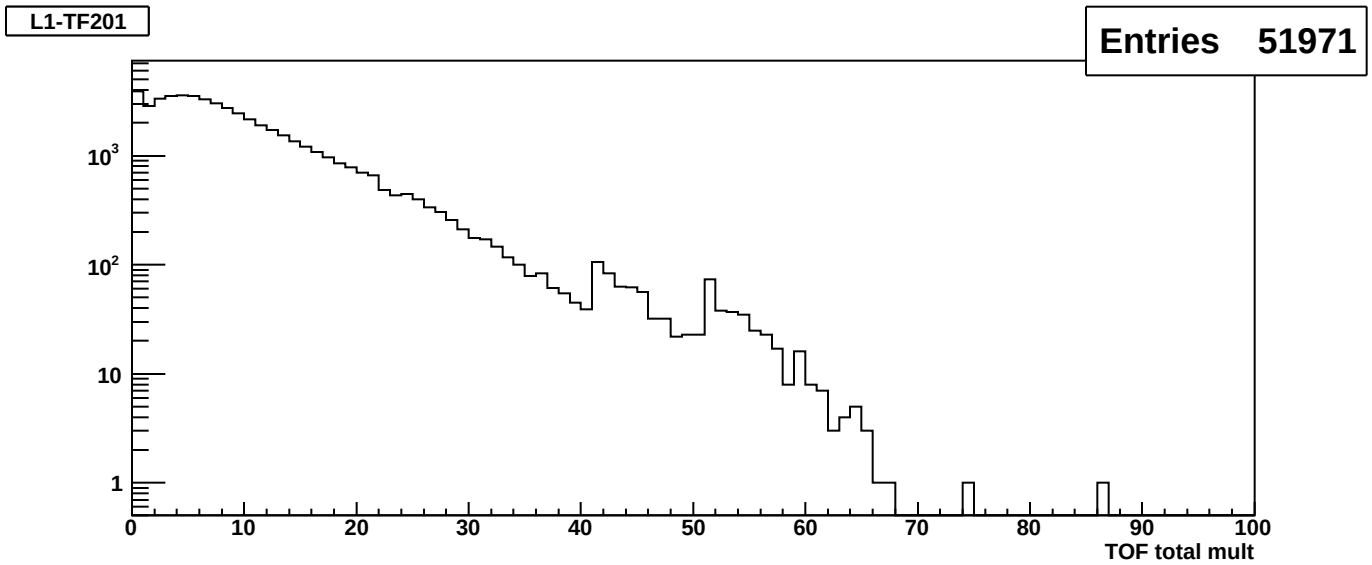

MIX-TF003
Entries 2182782
TOF MULT
TOF tray

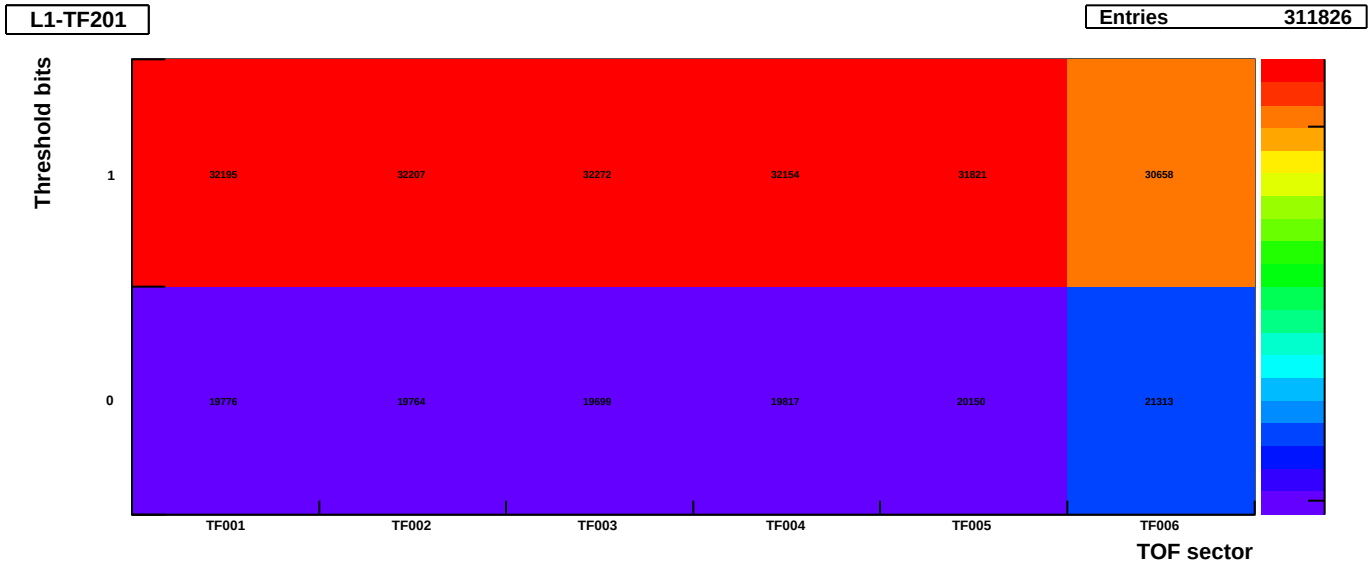

MIX-TF004
Entries 2182782
TOF MULT
TOF tray

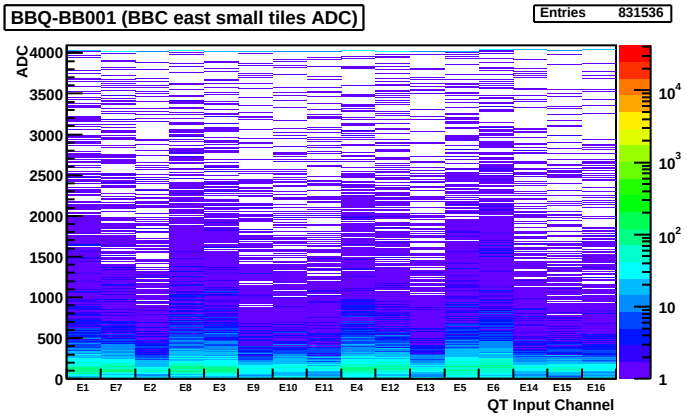

MIX-TF005
Entries 2182782
TOF MULT
TOF tray

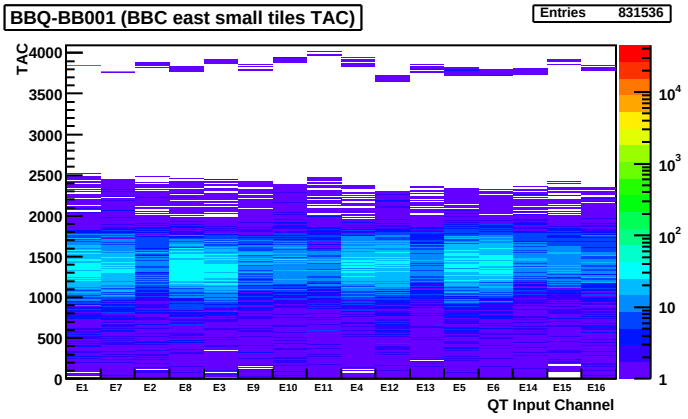

MIX-TF006
Entries 2182782
TOF MULT
TOF tray

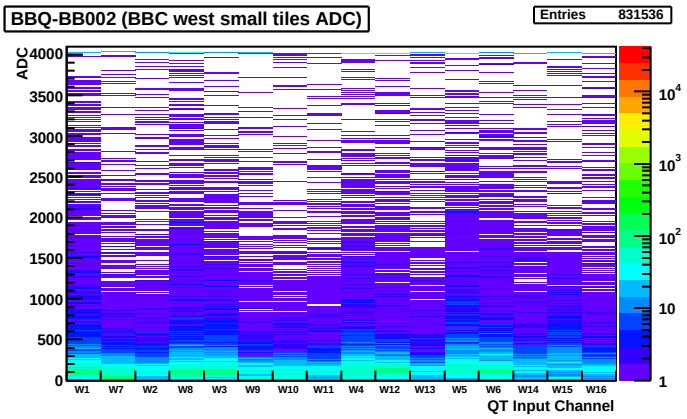

MIX-TF101
Entries 311826
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

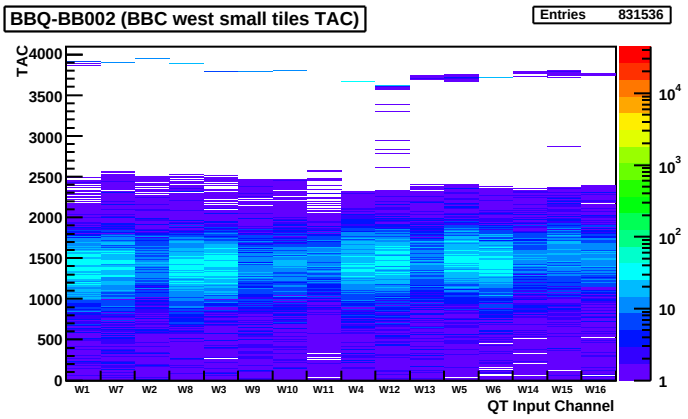

L1-TF201
Entries 51971
TOF total mult

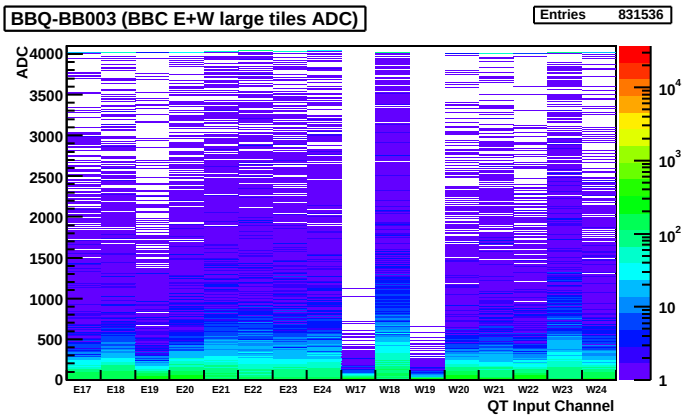

L1-TF201
Entries 311826
Threshold bits
32195
32207
32272
32154
31821
30658
19776
19764
19699
19817
20150
21313
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

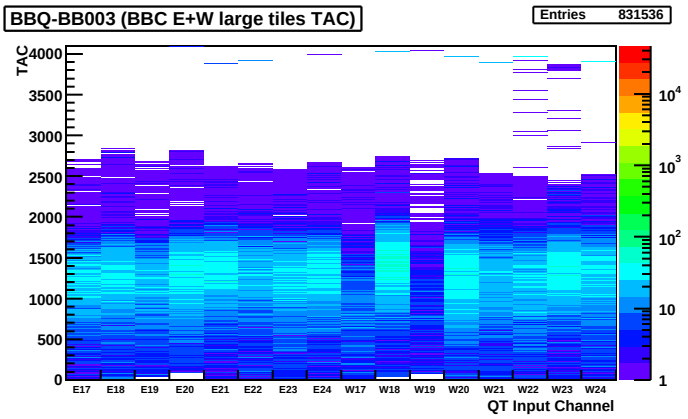

BBQ-BB001 (BBC east small tiles ADC)
Entries 831536
ADC
QT Input Channel

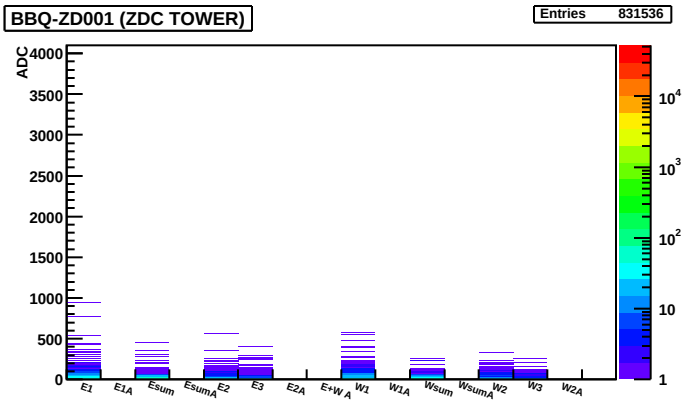

BBQ-BB001 (BBC east small tiles TAC)
Entries 831536
TAC
QT Input Channel

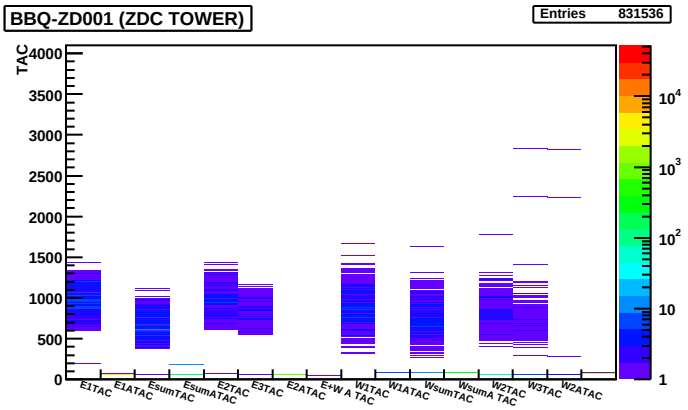

BBQ-BB002 (BBC west small tiles ADC)
Entries 831536
ADC
QT Input Channel

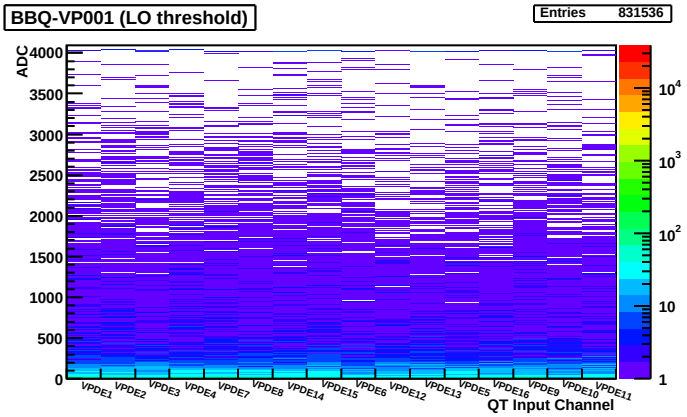

BBQ-BB002 (BBC west small tiles TAC)
Entries 831536
TAC
QT Input Channel

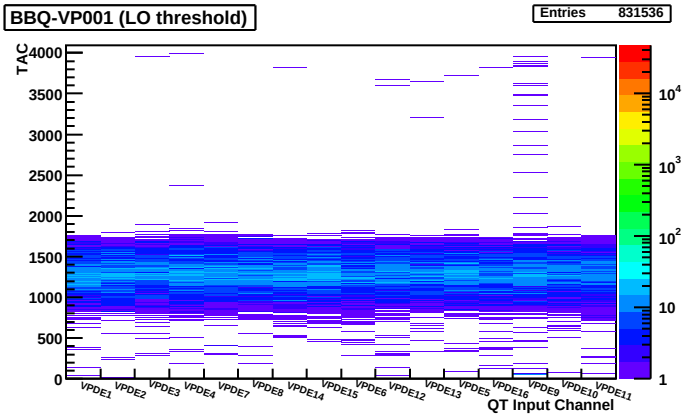

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 831536
ADC
QT Input Channel

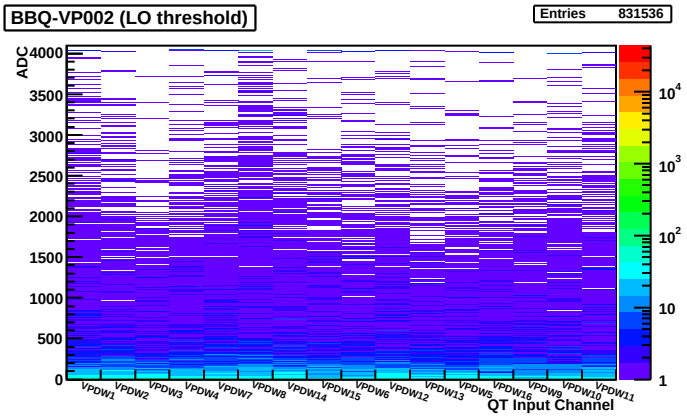

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 831536
TAC
QT Input Channel

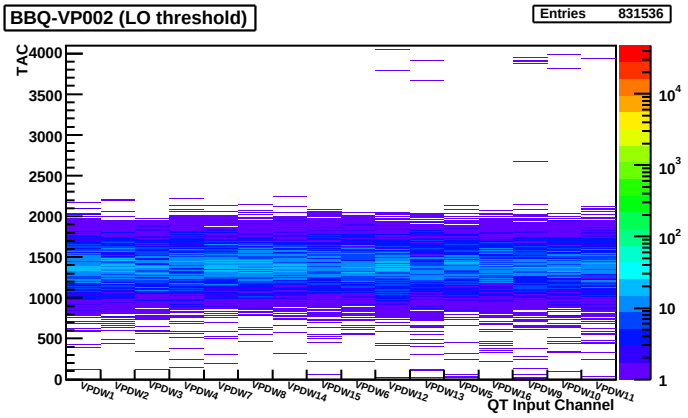

BBQ-ZD001 (ZDC TOWER)
Entries 831536
ADC

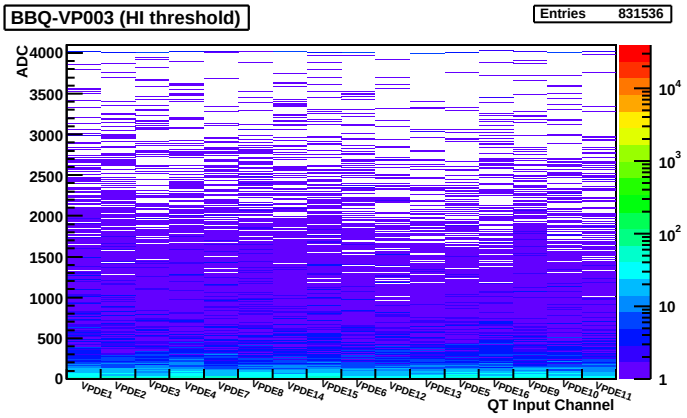

BBQ-ZD001 (ZDC TOWER)
Entries 831536
TAC

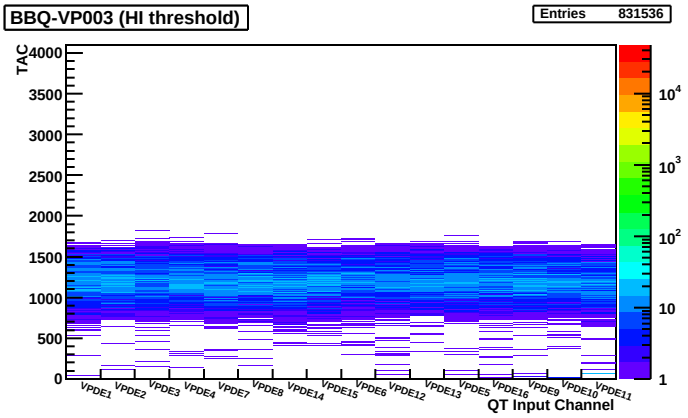

BBQ-VP001 (LO threshold)
Entries 831536
ADC
QT Input Channel

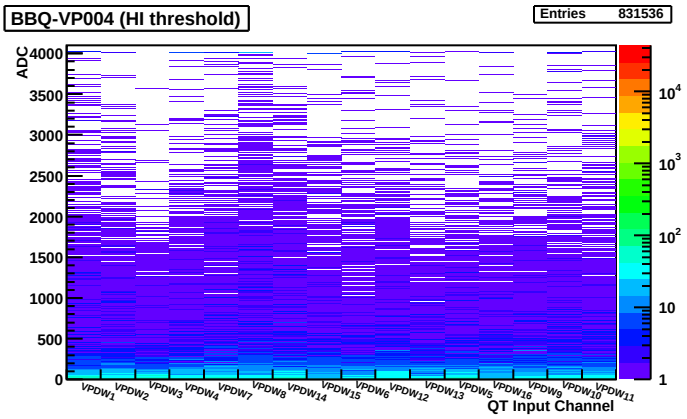

BBQ-VP001 (LO threshold)
Entries 831536
TAC
QT Input Channel

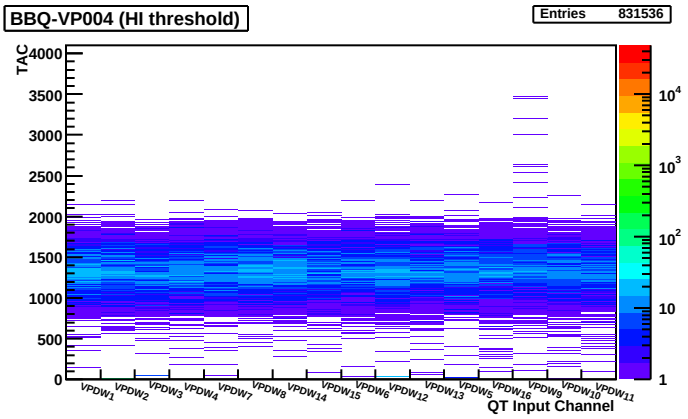

BBQ-VP002 (LO threshold)
Entries 831536
ADC
QT Input Channel

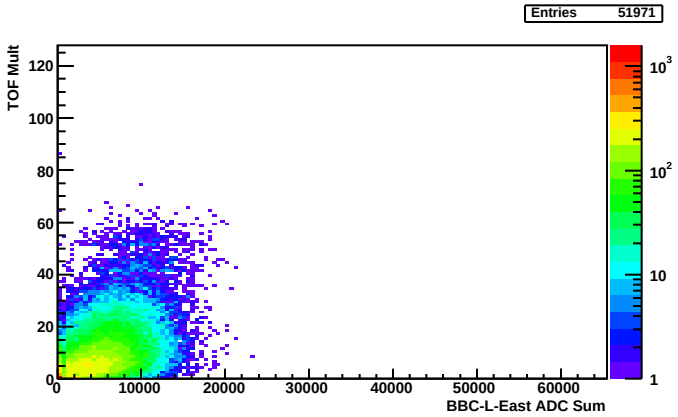

BBQ-VP002 (LO threshold)
Entries 831536
TAC
QT Input Channel

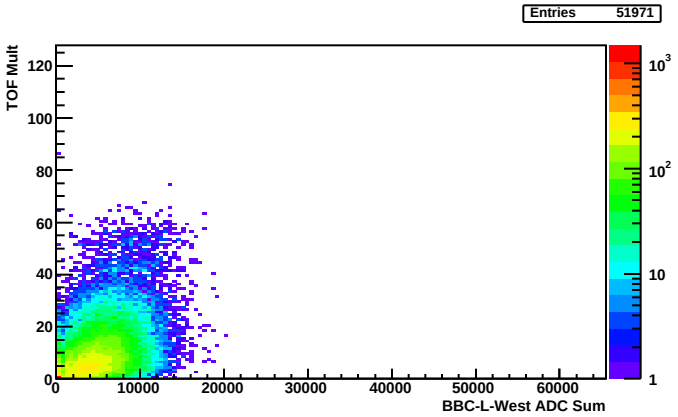

BBQ-VP003 (HI threshold)
Entries 831536
ADC
QT Input Channel

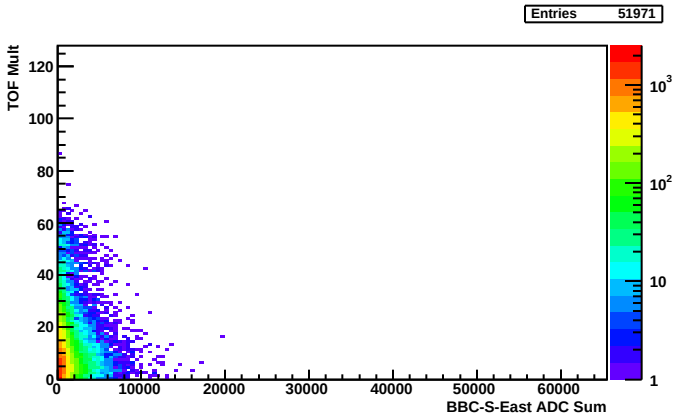

BBQ-VP003 (HI threshold)
Entries 831536
TAC
QT Input Channel

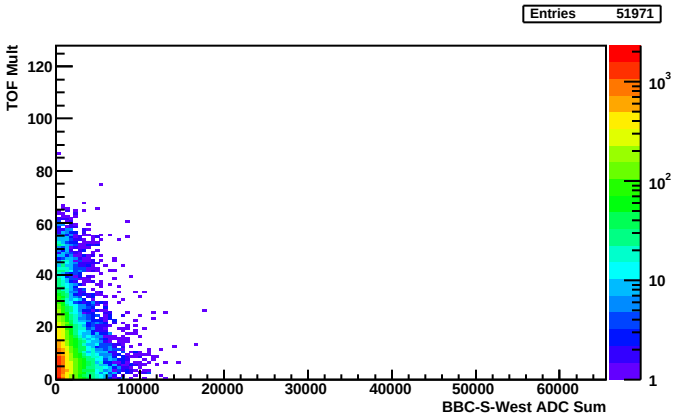

BBQ-VP004 (HI threshold)
Entries 831536
ADC
QT Input Channel

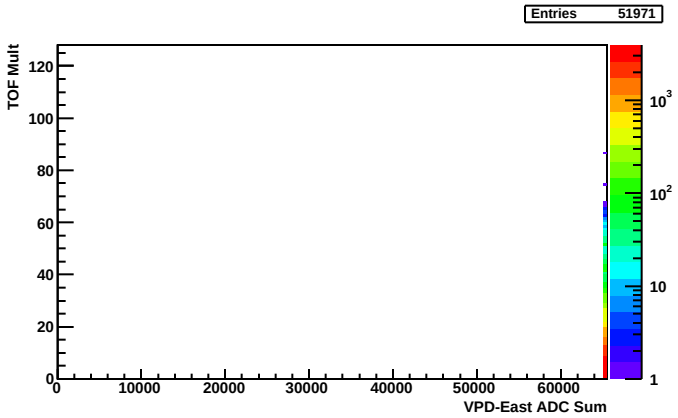

BBQ-VP004 (HI threshold)
Entries 831536
TAC
QT Input Channel

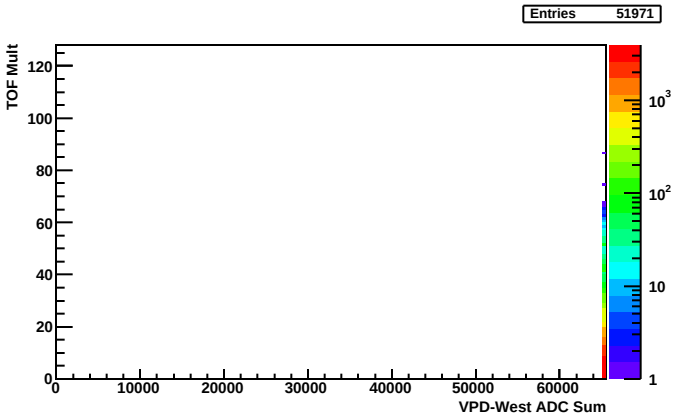

Entries 51971
TOF Mult
BBC-L-East ADC Sum

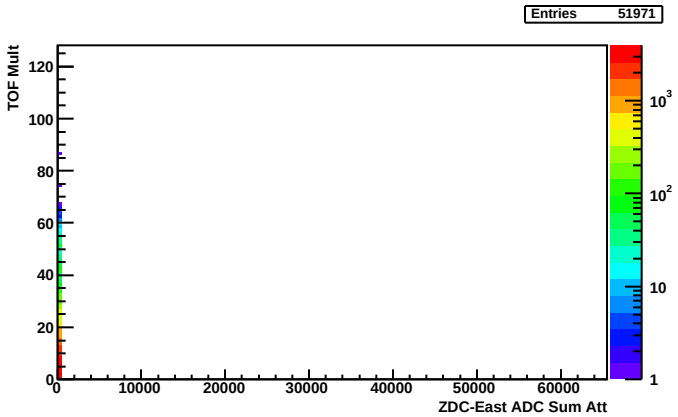

Entries 51971
TOF Mult
BBC-L-West ADC Sum

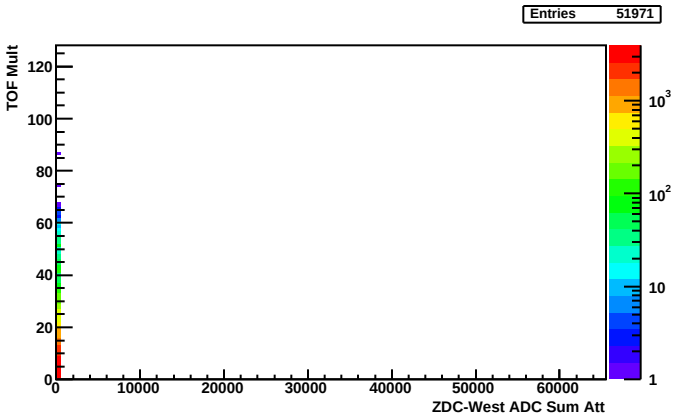

Entries 51971
TOF Mult
BBC-S-East ADC Sum

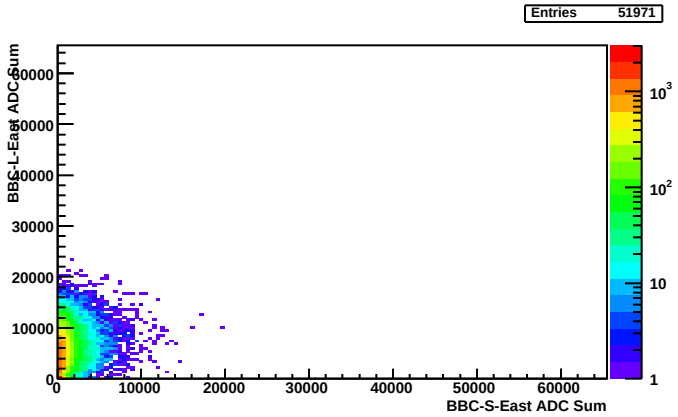

Entries 51971
TOF Mult
BBC-S-West ADC Sum

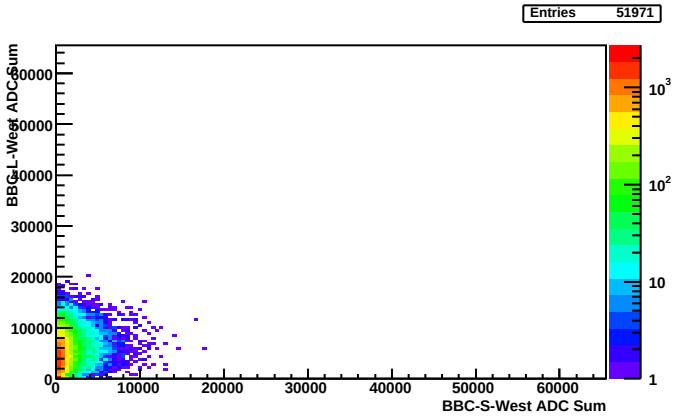

Entries 51971
TOF Mult
VPD-East ADC Sum

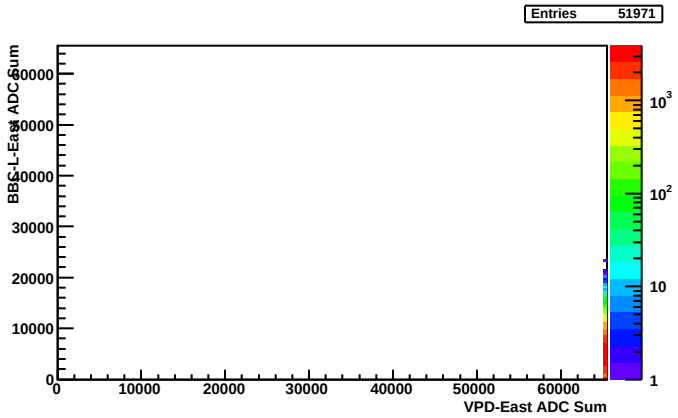

Entries 51971
TOF Mult
VPD-West ADC Sum


Entries 51971
TOF Mult
ZDC-East ADC Sum Att


Entries 51971
TOF Mult
ZDC-West ADC Sum Att


Entries 51971
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 51971
BBC-L-West ADC Sum
BBC-S-West ADC Sum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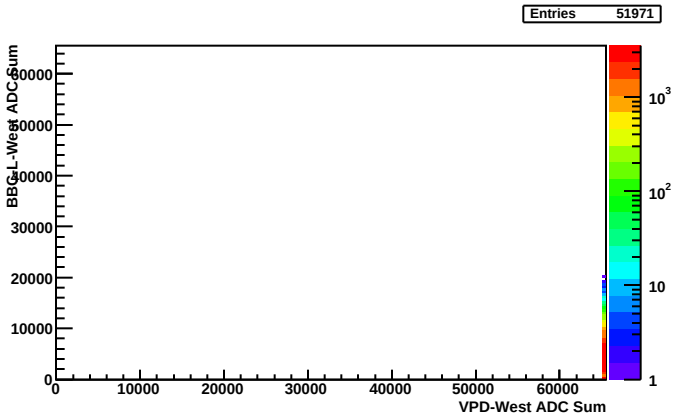
BBC-L-East ADC Sum
VPD-East ADC Sum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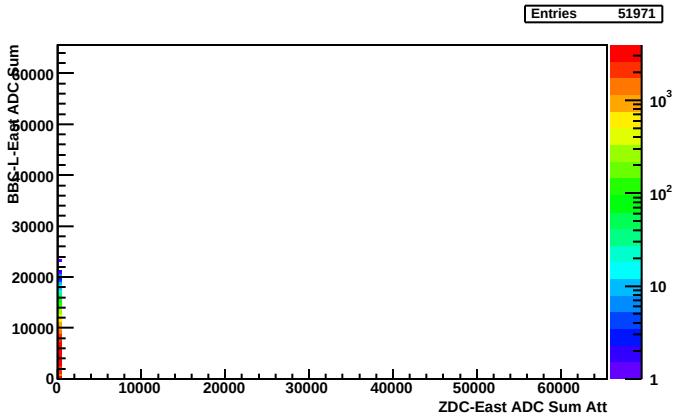
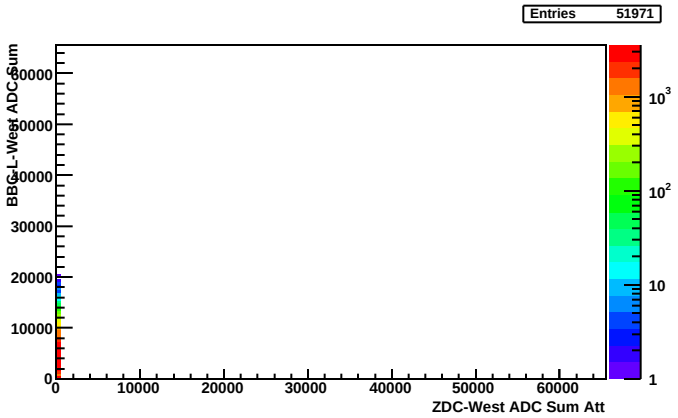
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 51971
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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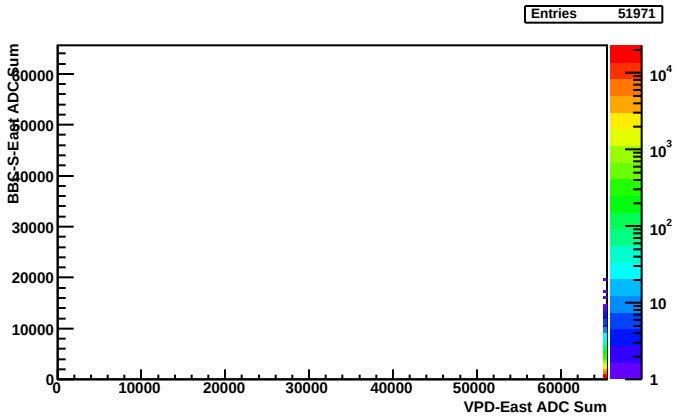
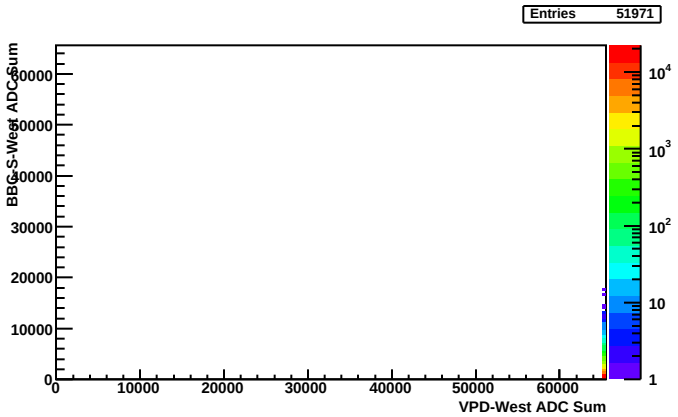
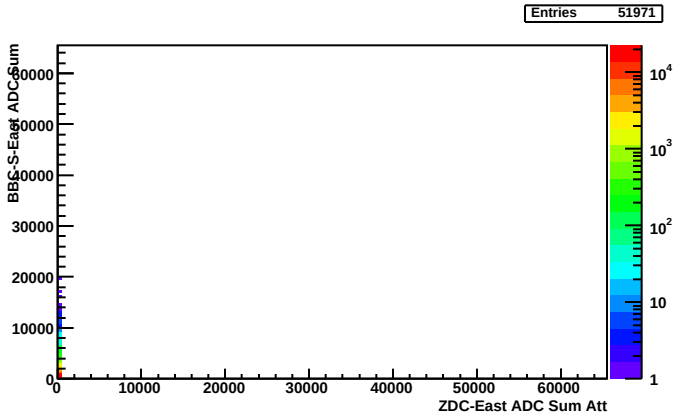
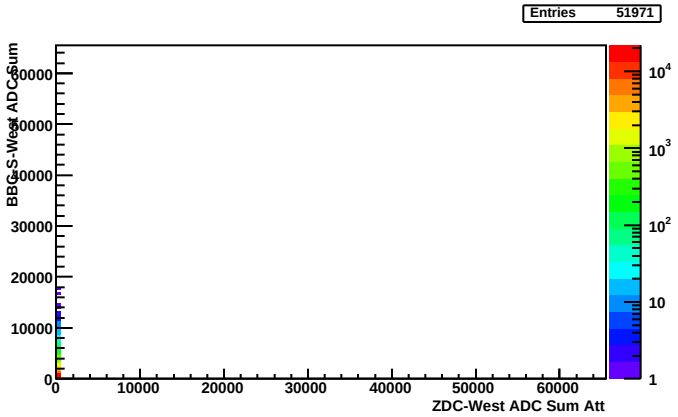
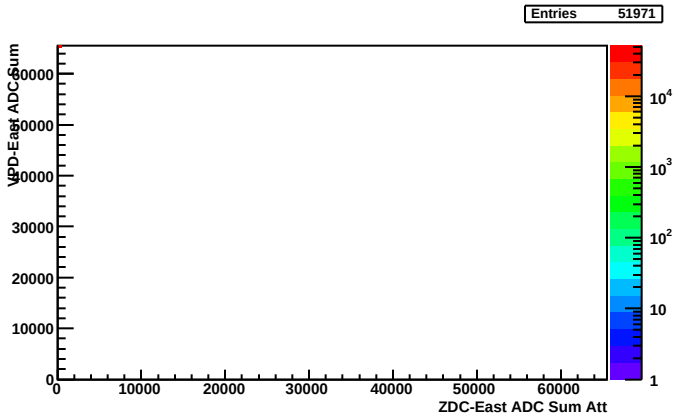
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

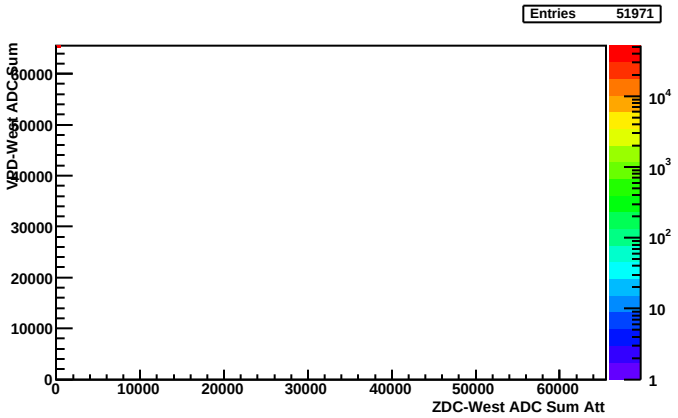

Entries 51971
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 51971
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 51971
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 51971
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 51971
VPD-East ADC Sum
ZDC-East ADC Sum Att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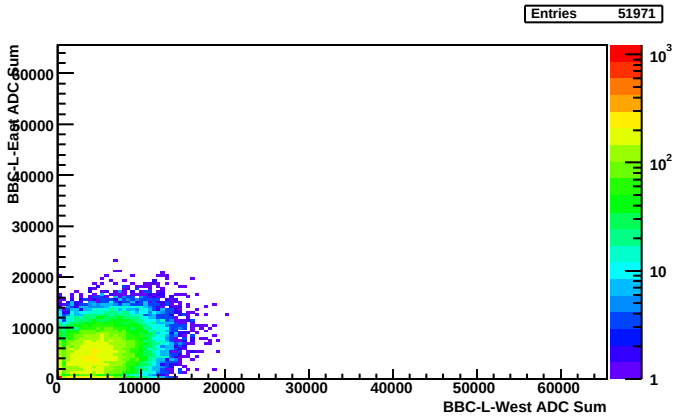
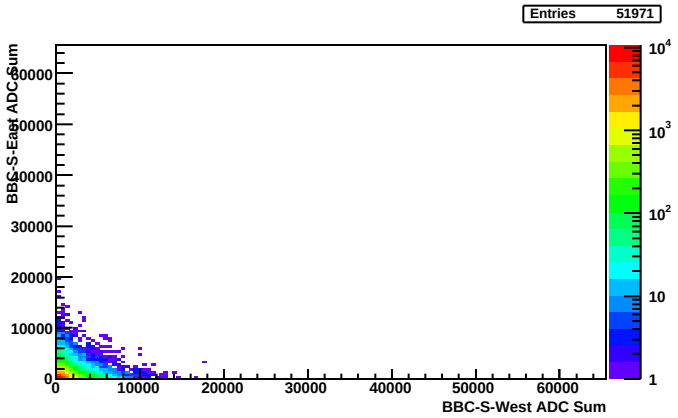
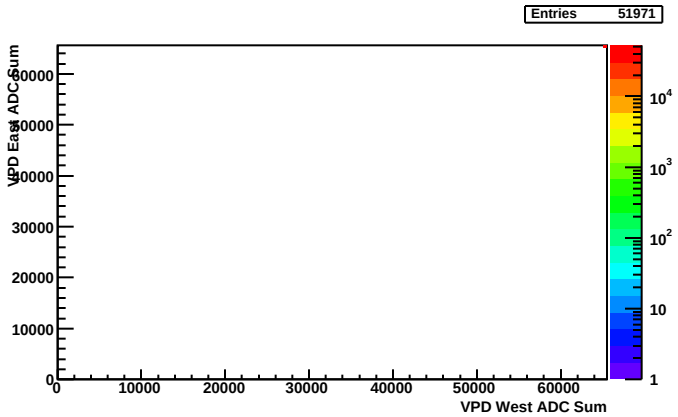
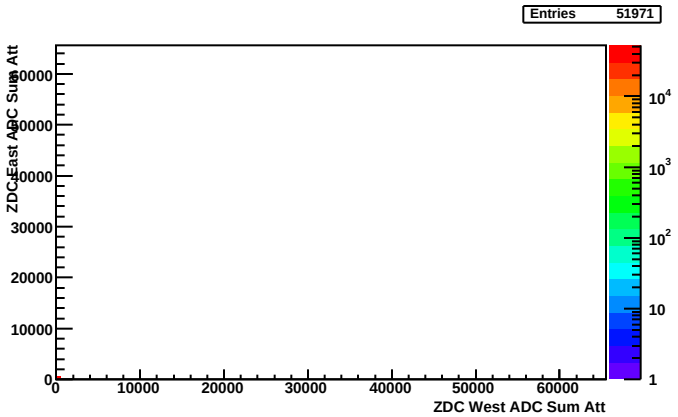
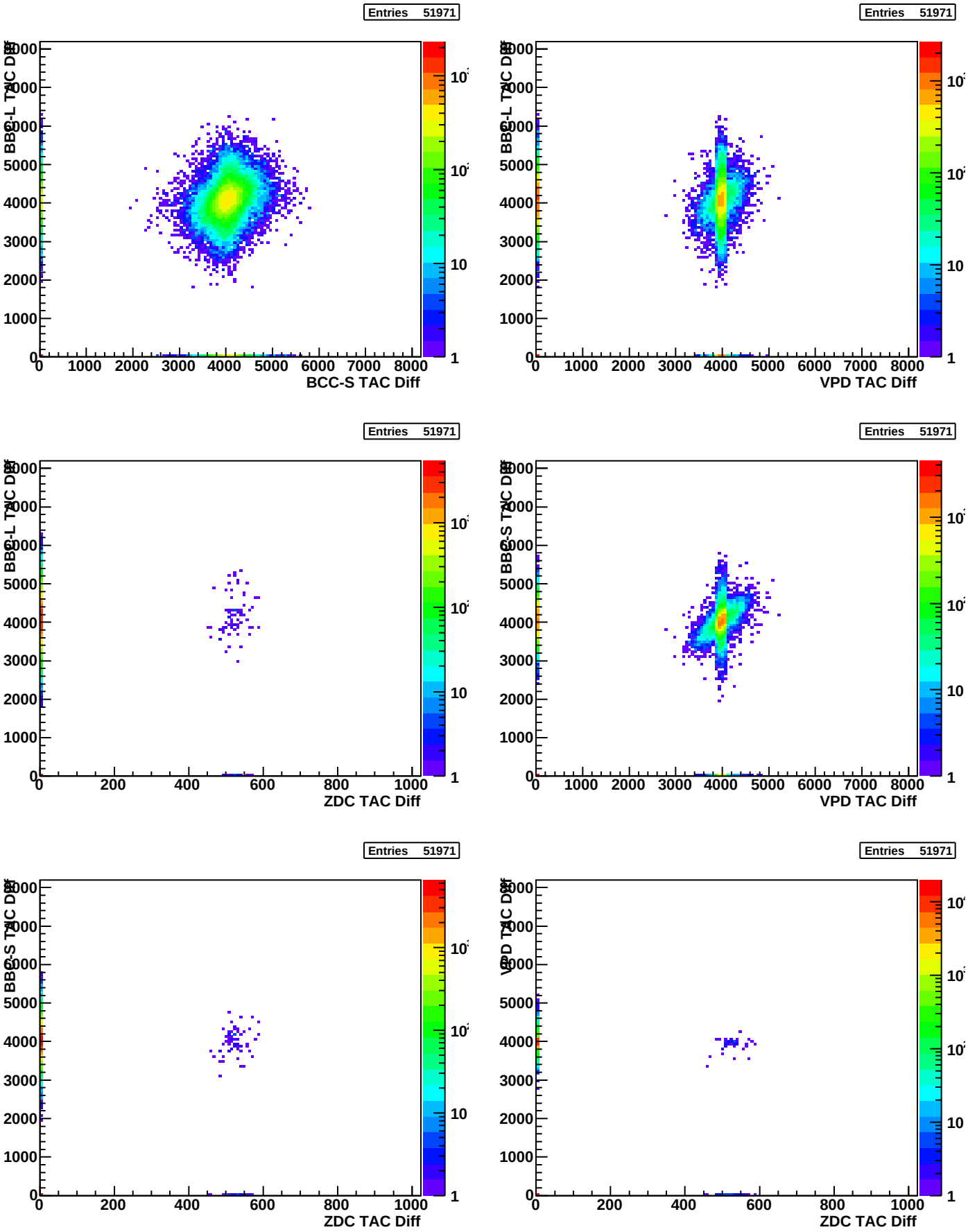
VPD-West ADC Sum
ZDC-West ADC Sum Att

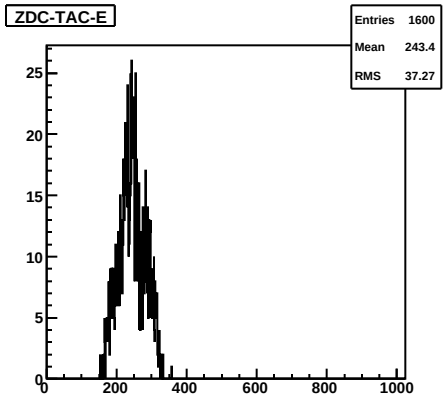

Entries 51971
BBC-L-East ADC Sum
BBC-L-West ADC Sum

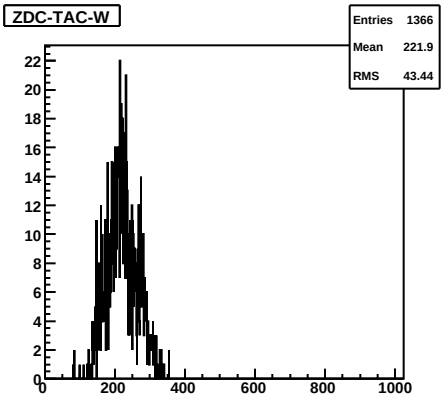

Entries 51971
BBC-S-East ADC Sum
BBC-S-West ADC Sum

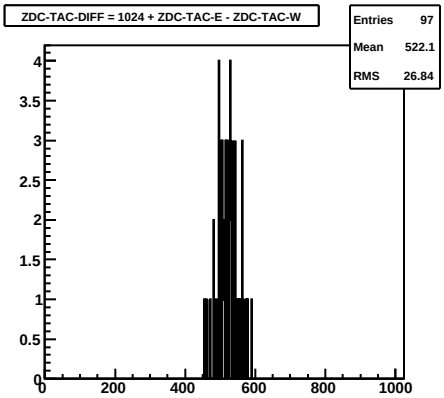

Entries 51971
VPD East ADC Sum
VPD West ADC Sum

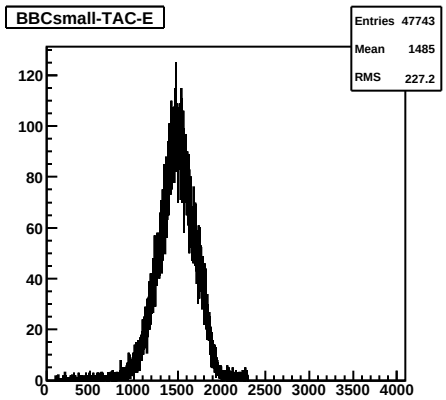

Entries 51971
ZDC East ADC Sum Att
ZDC West ADC Sum Att

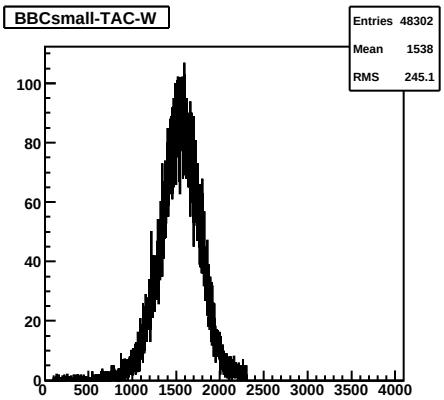

Entries 51971
BBC-L TAC Diff
BCC-S TAC Diff
Entries 51971
BBC-L TAC Diff
VPD TAC Diff
Entries 51971
BBC-L TAC Diff
ZDC TAC Diff
Entries 51971
BBC-S TAC Diff
VPD TAC Diff
Entries 51971
BBC-S TAC Diff
ZDC TAC Diff
Entries 51971
VPD TAC Diff
ZDC TAC Diff

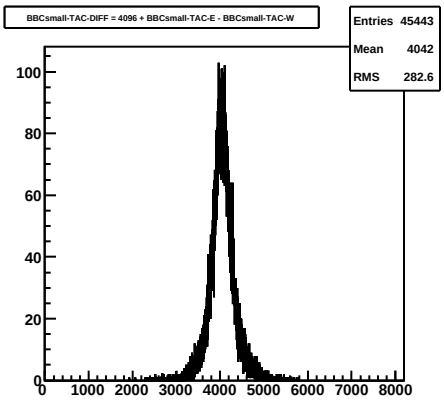

ZDC-TAC-E
Entries 1600
Mean 243.4
RMS 37.27

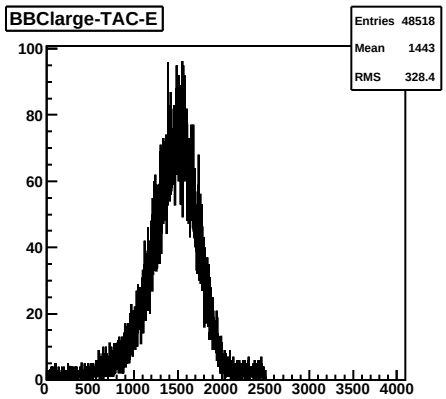

ZDC-TAC-W
Entries 1366
Mean 221.9
RMS 43.44

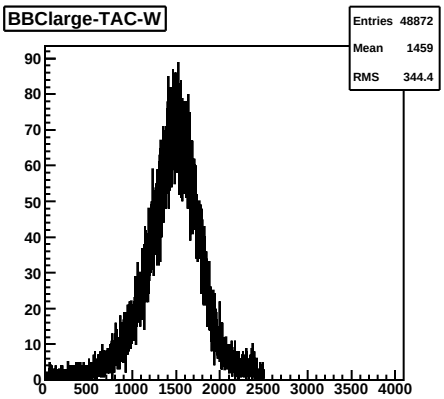

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 97
Mean 522.1
RMS 26.84

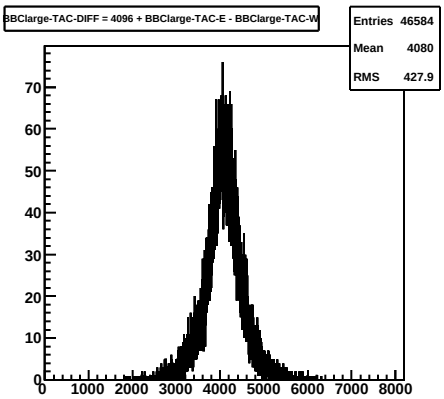

BBCsmall-TAC-E
Entries 47743
Mean 1485
RMS 227.2

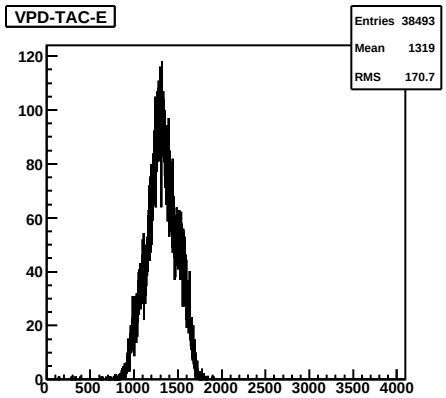

BBCsmall-TAC-W
Entries 48302
Mean 1538
RMS 245.1

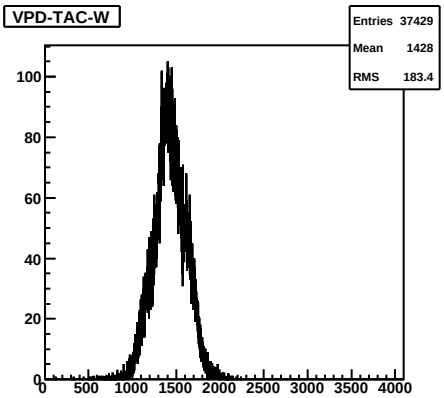

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 45443
Mean 4042
RMS 282.6

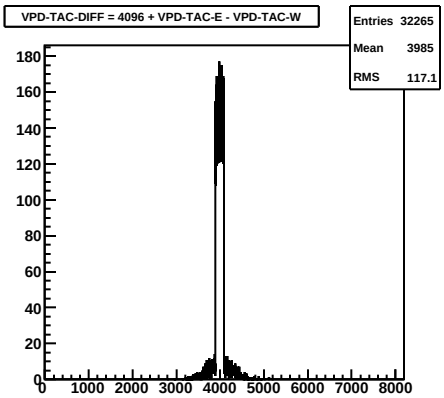

BBClarge-TAC-E
Entries 48518
Mean 1443
RMS 328.4

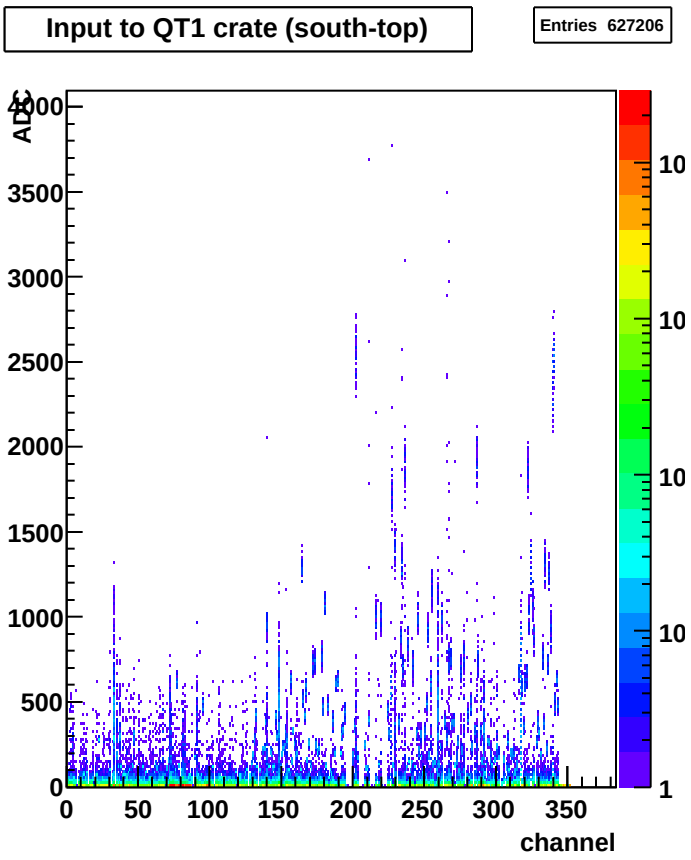

BBClarge-TAC-W
Entries 48872
Mean 1459
RMS 344.4

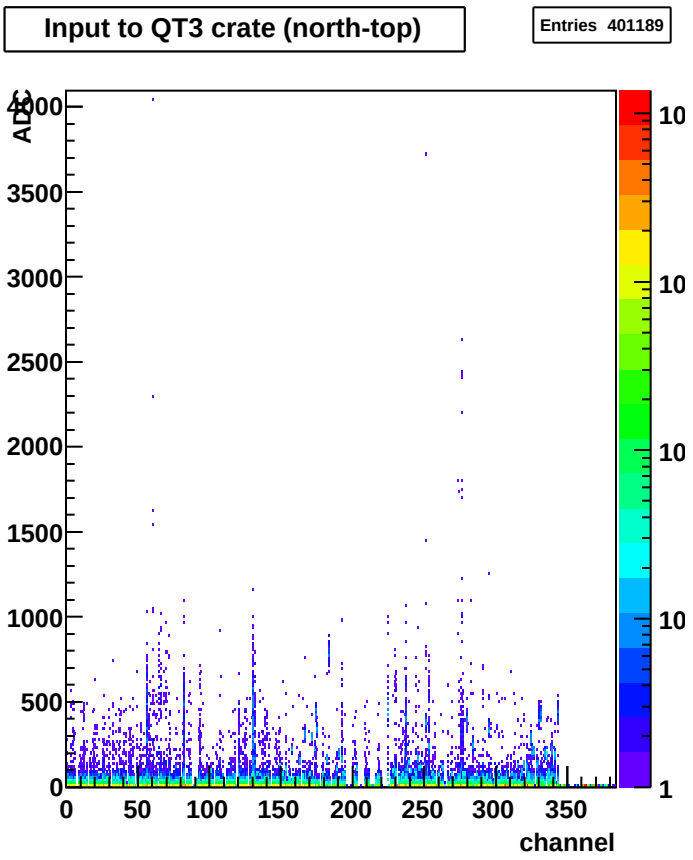

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 46584
Mean 4080
RMS 427.9

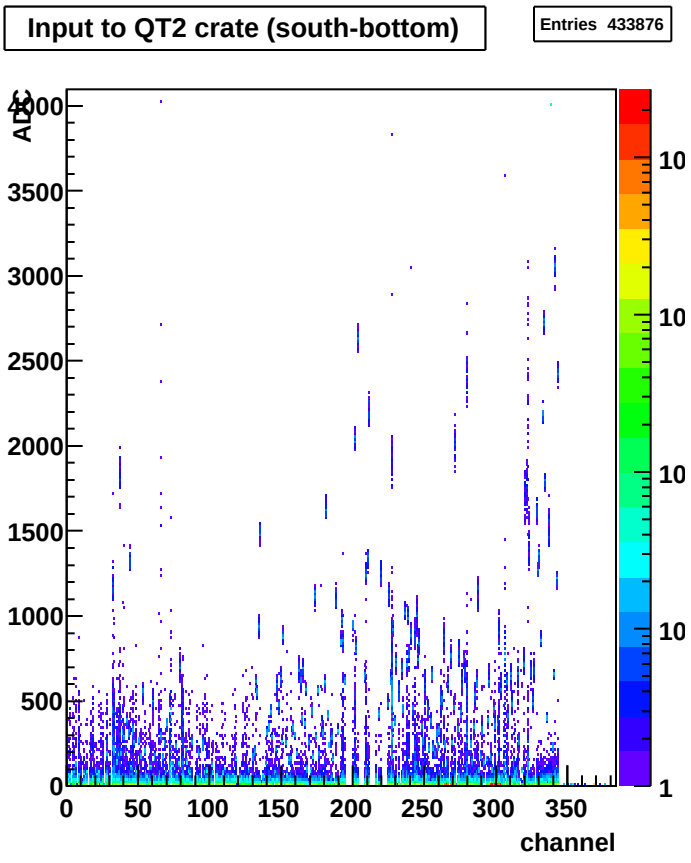

VPD-TAC-E
Entries 38493
Mean 1319
RMS 170.7

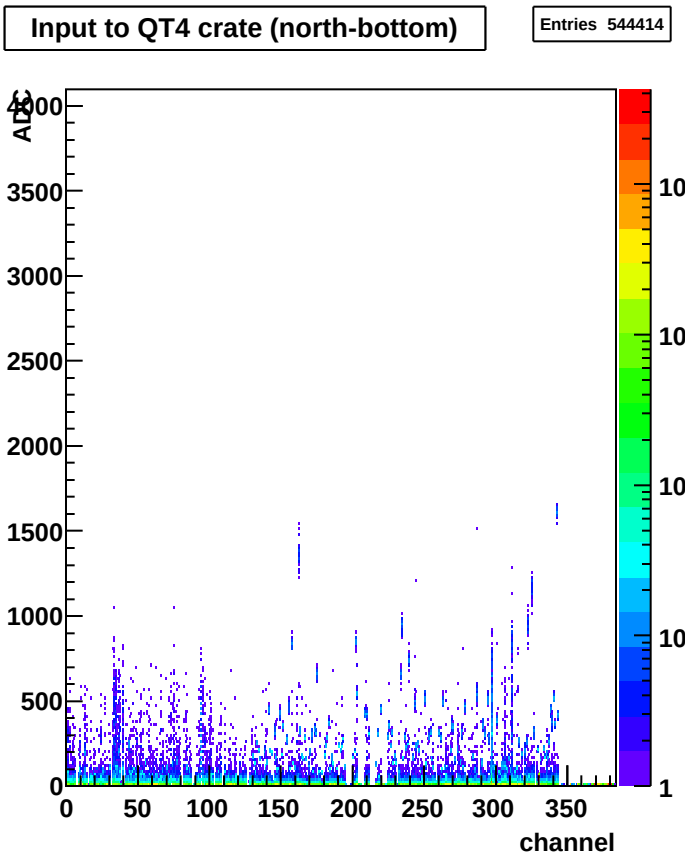

VPD-TAC-W
Entries 37429
Mean 1428
RMS 183.4

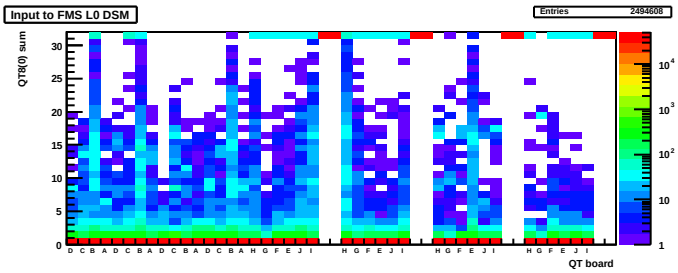

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 32265
Mean 3985
RMS 117.1

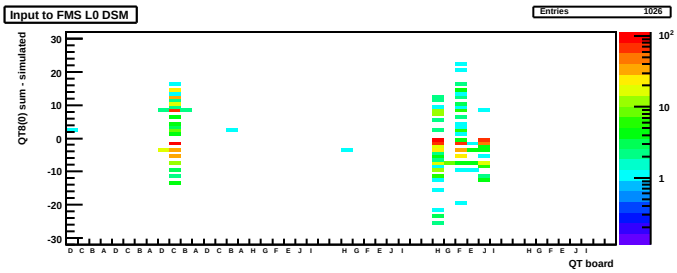

Input to QT1 crate (south-top)
Entries 627206
ADC
channel

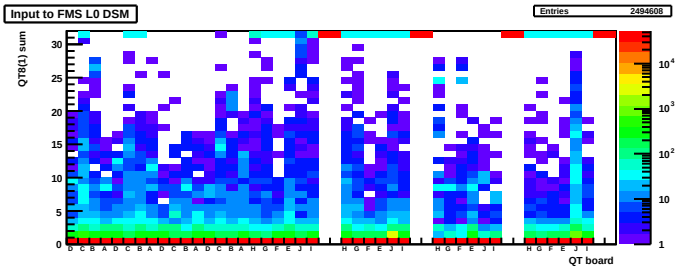

Input to QT3 crate (north-top)
Entries 401189
ADC
channel

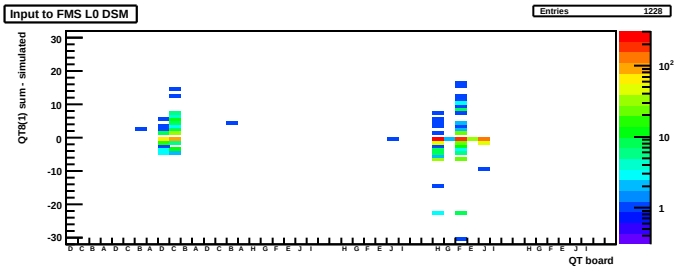

Input to QT2 crate (south-bottom)
Entries 433876
ADC
channel

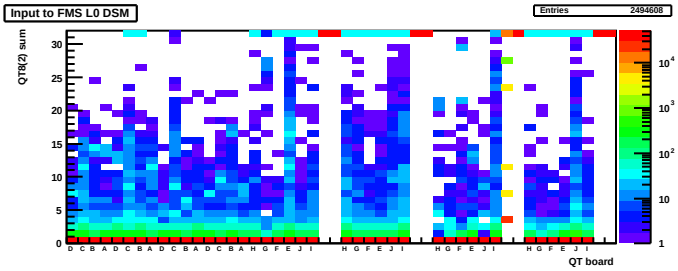

Input to QT4 crate (north-bottom)
Entries 544414
ADC
channel

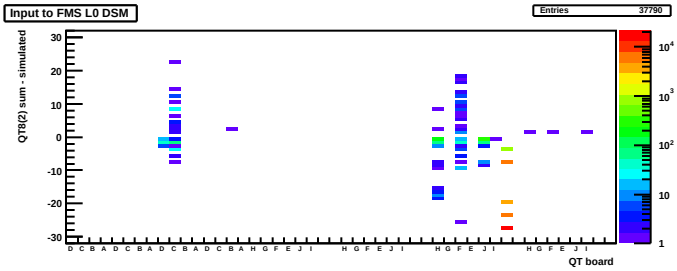

Input to FMS L0 DSM
Entries 2494608
QT8(0) sum
QT board

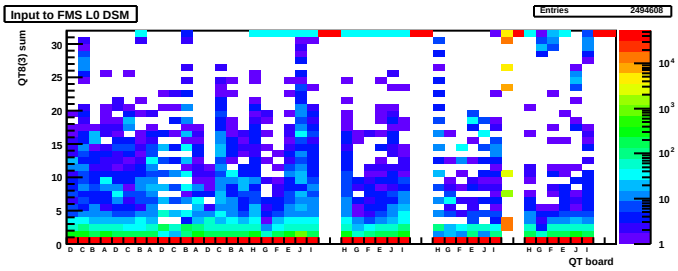

Input to FMS L0 DSM
Entries 1026
QT8(0) sum - simulated
QT board

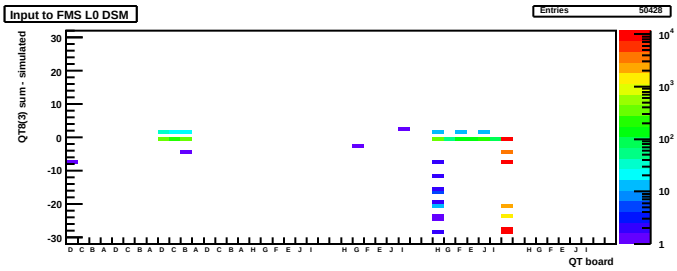

Input to FMS L0 DSM
Entries 2494608
QT8(1) sum
QT board

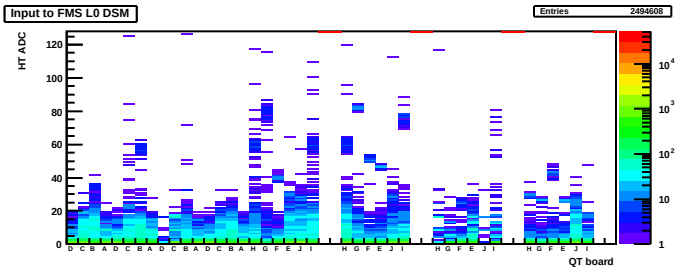

Input to FMS L0 DSM
Entries 1228
QT8(1) sum - simulated
QT board

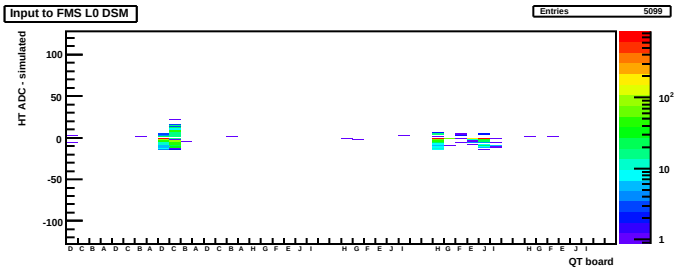

Input to FMS L0 DSM
Entries 2494608
QT8(2) sum
QT board

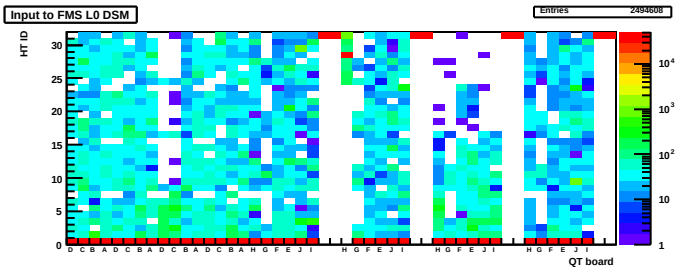

Input to FMS L0 DSM
Entries 37790
QT8(2) sum - simulated
QT board

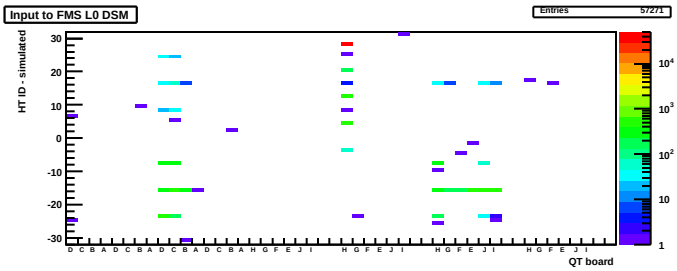

Input to FMS L0 DSM
Entries 2494608
QT8(3) sum
QT board

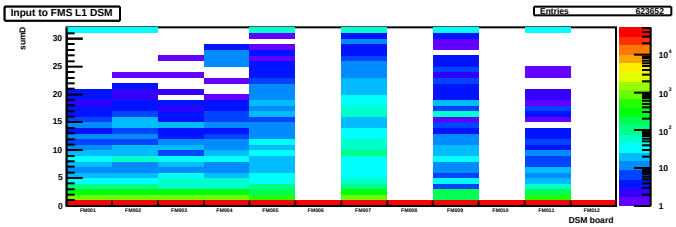

Input to FMS L0 DSM
Entries 50428
QT8(3) sum - simulated
QT board

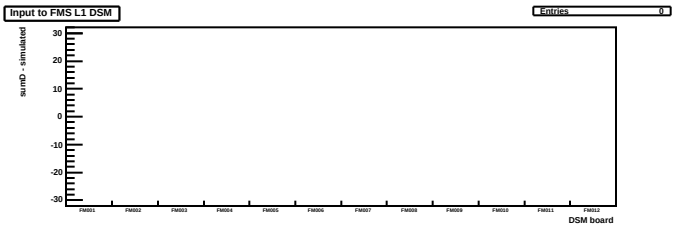

Input to FMS L0 DSM
Entries 2494608
HT ADC
QT board

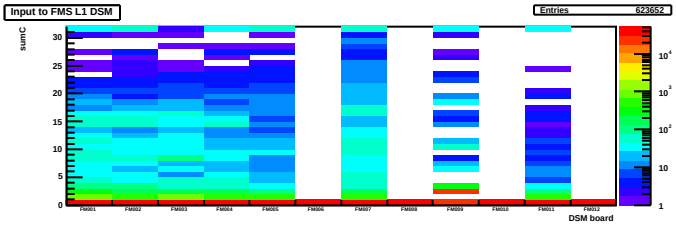

Input to FMS L0 DSM
Entries 5099
HT ADC - simulated
QT board

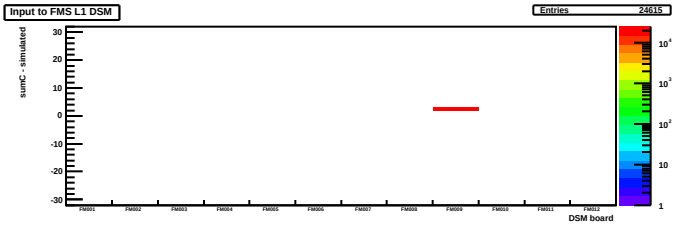

Input to FMS L0 DSM
Entries 2494608
HT ID
QT board

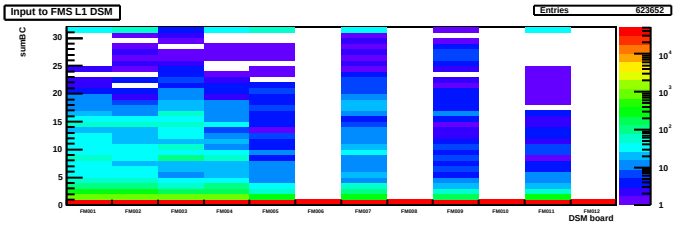

Input to FMS L0 DSM
Entries 57271
HT ID - simulated
QT board

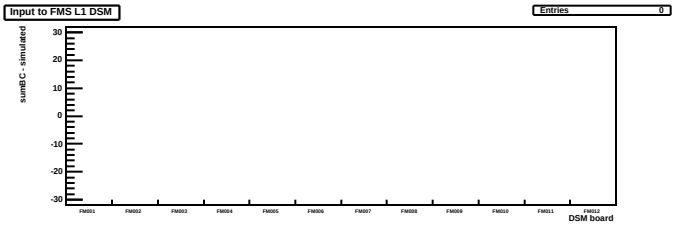

Input to FMS L1 DSM
Entries 623652
sumD
DSM board

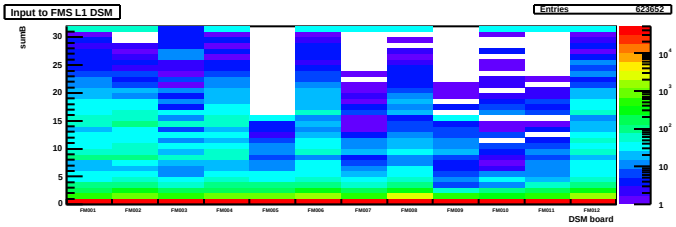

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

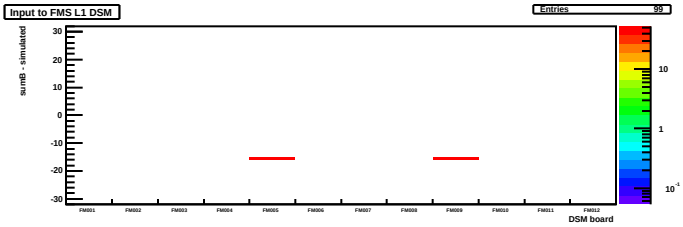

Input to FMS L1 DSM
Entries 623652
sumC
DSM board

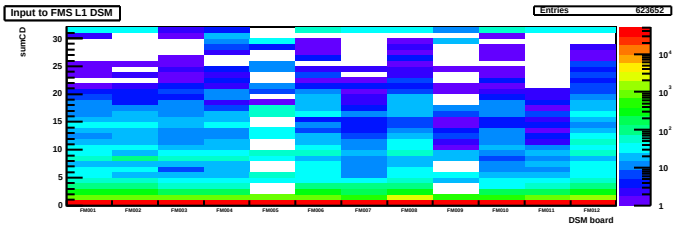

Input to FMS L1 DSM
Entries 24615
sumC - simulated
DSM board

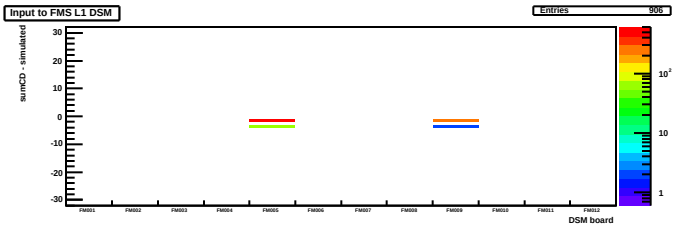

Input to FMS L1 DSM
Entries 623652
sumBC
DSM board

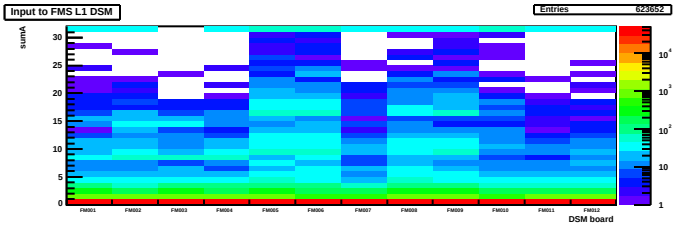

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

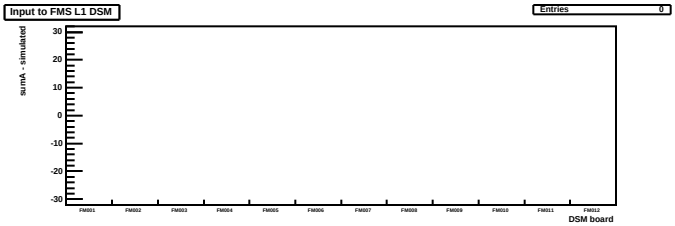

Input to FMS L1 DSM
Entries 623652
sumB
DSM board

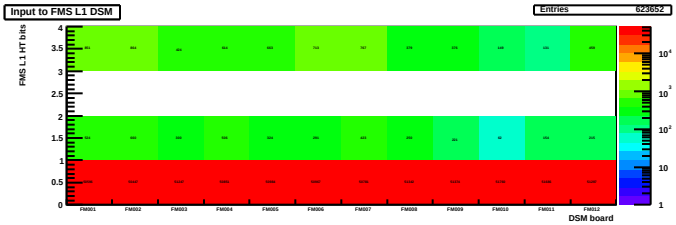

Input to FMS L1 DSM
Entries 99
sumB - simulated
DSM board

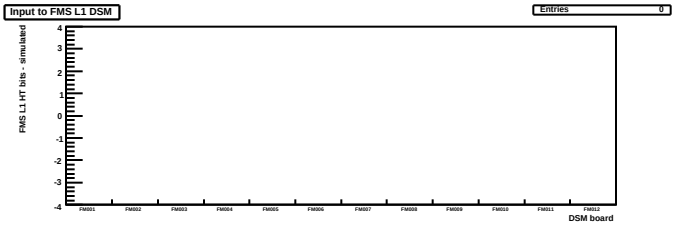

Input to FMS L1 DSM
Entries 623652
sumCD
DSM board

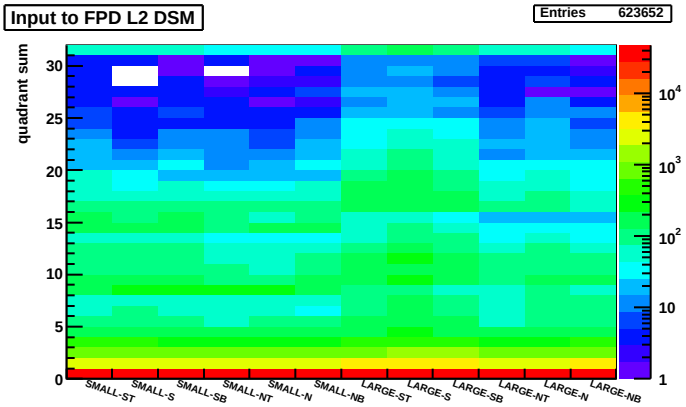

Input to FMS L1 DSM
Entries 906
sumCD - simulated
DSM board

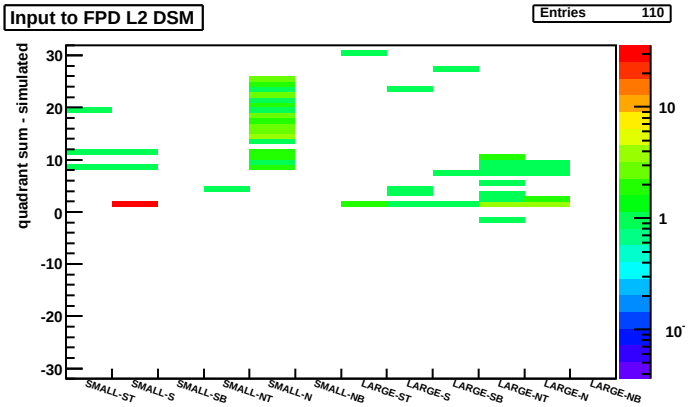

Input to FMS L1 DSM
Entries 623652
sumA
DSM board

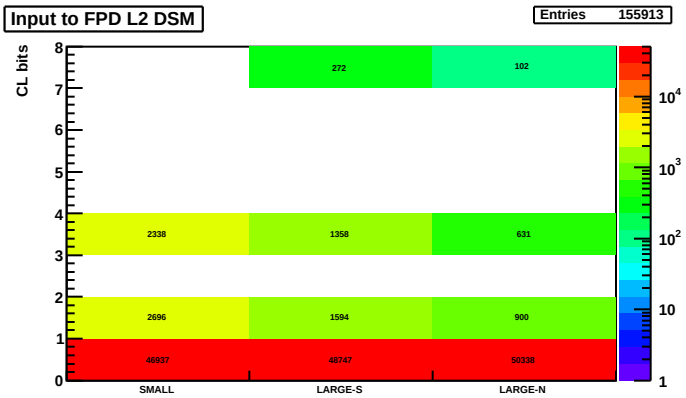

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

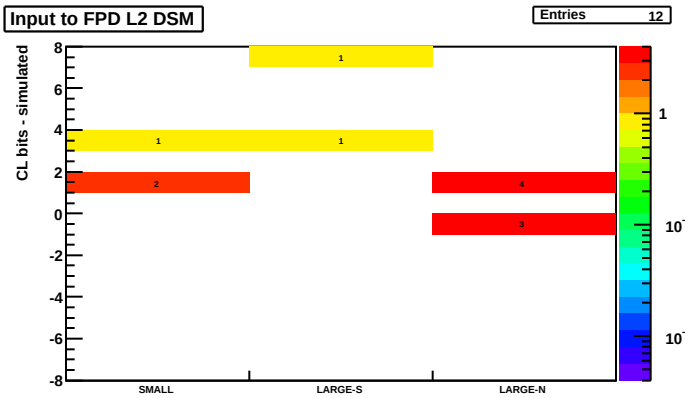

Input to FMS L1 DSM
Entries 623652
FMS L1 HT bits
DSM board

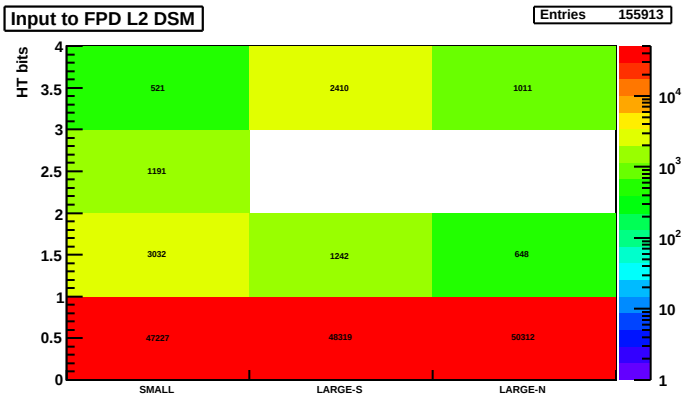

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

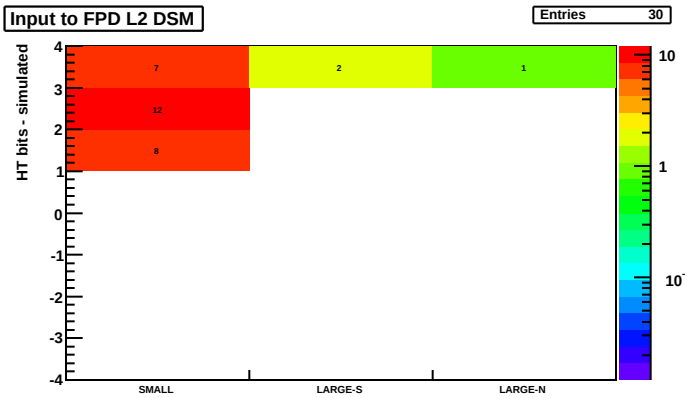

Input to FPD L2 DSM
Entries 623652
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

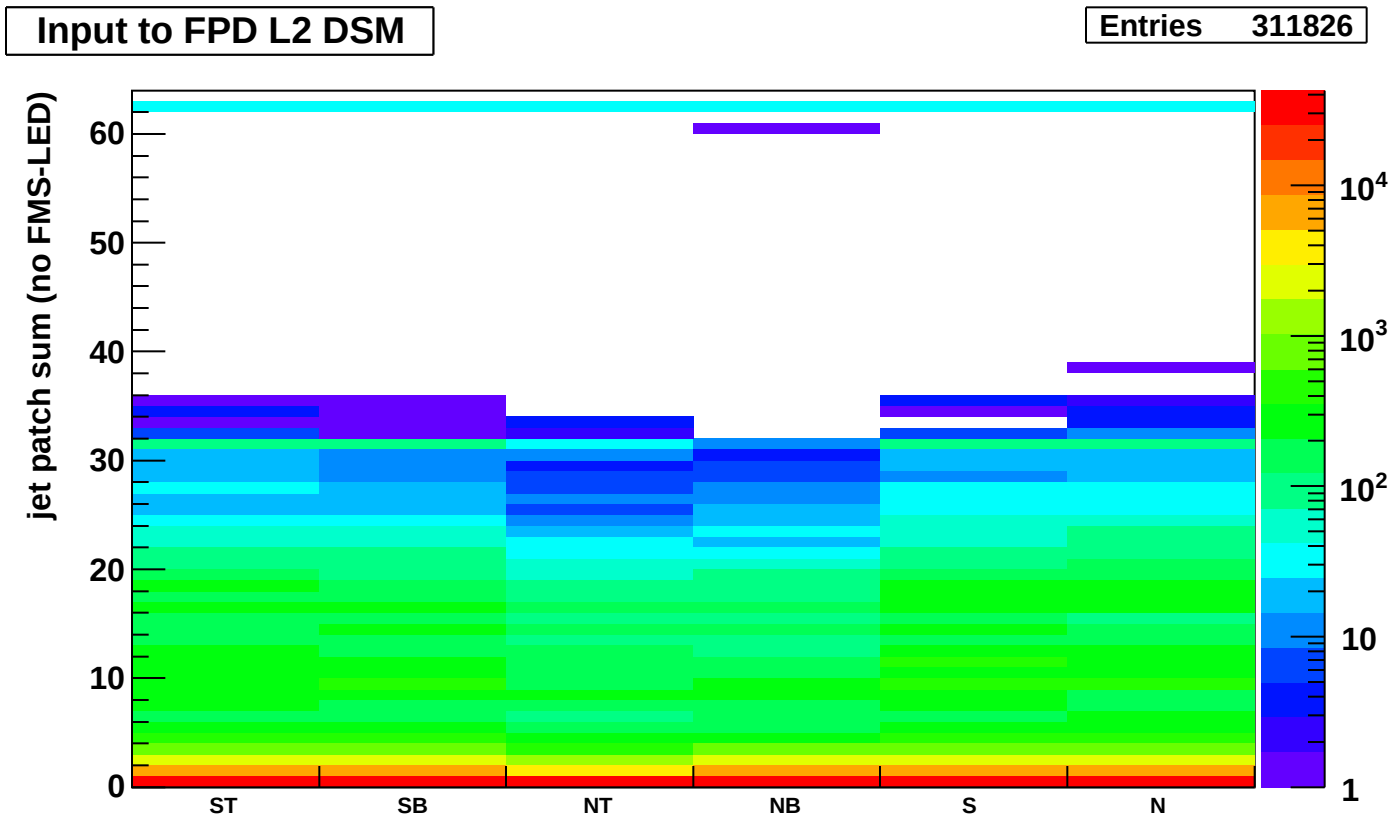

Input to FPD L2 DSM
Entries 110
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

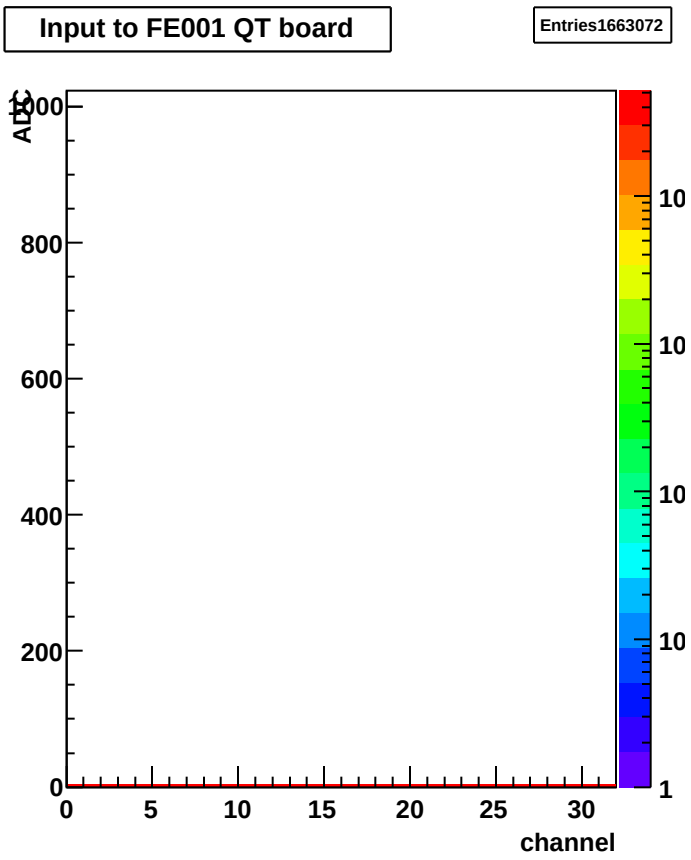

Input to FPD L2 DSM
Entries 155913
CL bits
272
102
2338
1358
631
2696
1594
900
46937
48747
50338
SMALL
LARGE-S
LARGE-N

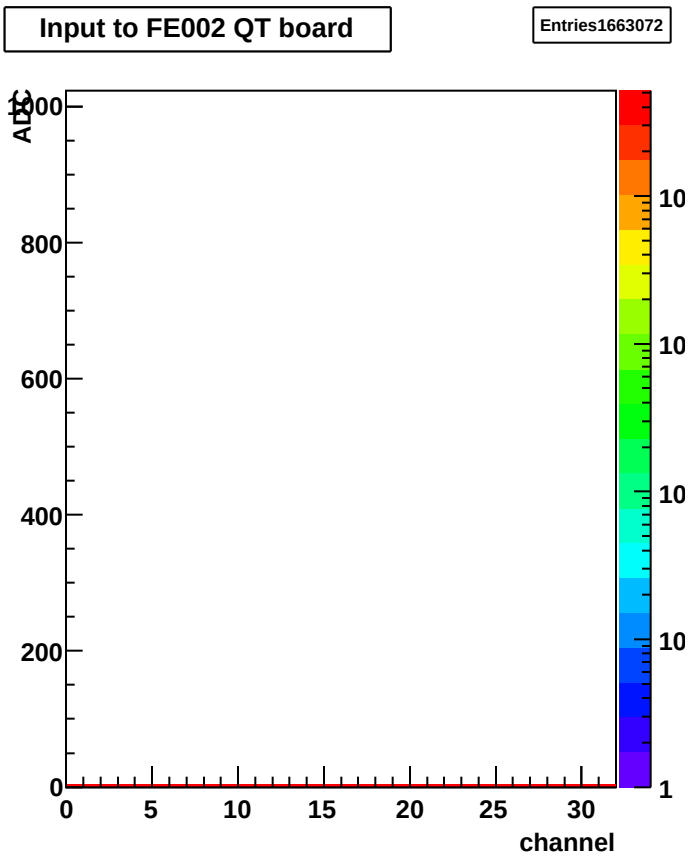

Input to FPD L2 DSM
Entries 12
CL bits - simulated
1
1
1
2
4
3
SMALL
LARGE-S
LARGE-N

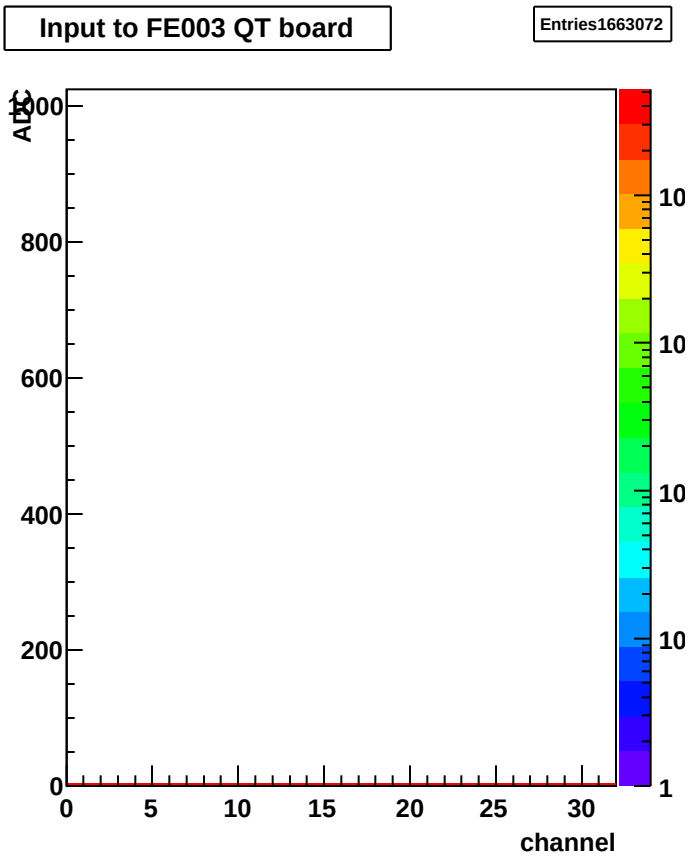

Input to FPD L2 DSM
Entries 155913
HT bits
521
2410
1011
1191
3032
1242
648
47227
48319
50312
SMALL
LARGE-S
LARGE-N

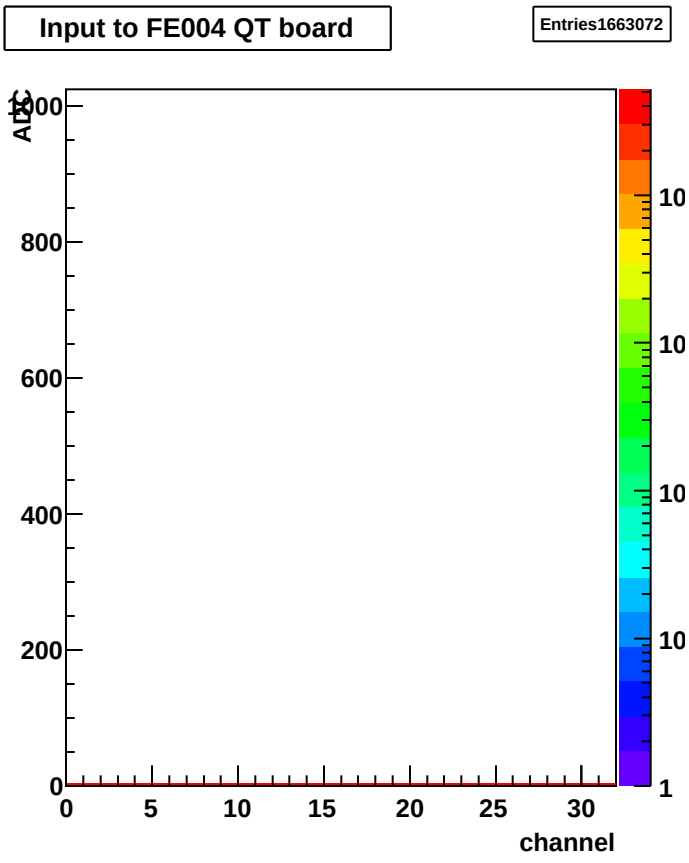

Input to FPD L2 DSM
Entries 30
HT bits - simulated
7
2
1
12
8
SMALL
LARGE-S
LARGE-N

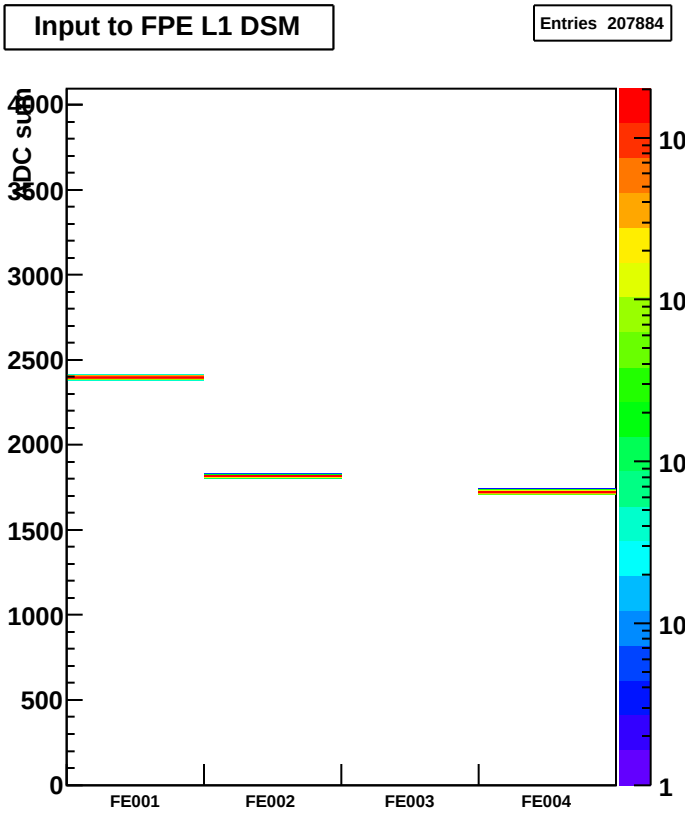

Input to FPD L2 DSM
Entries 311826
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

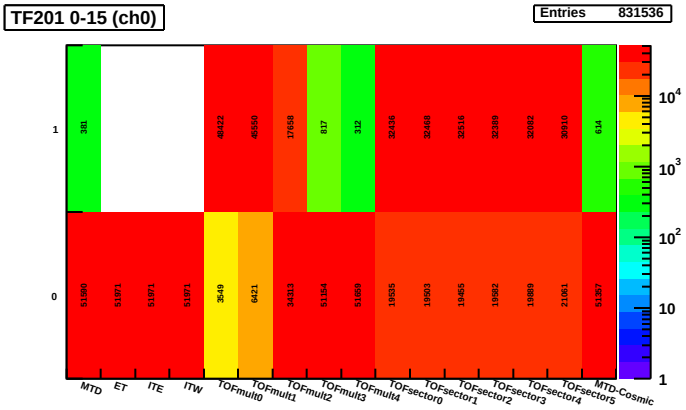

Input to FE001 QT board
Entries1663072
ADC
channel

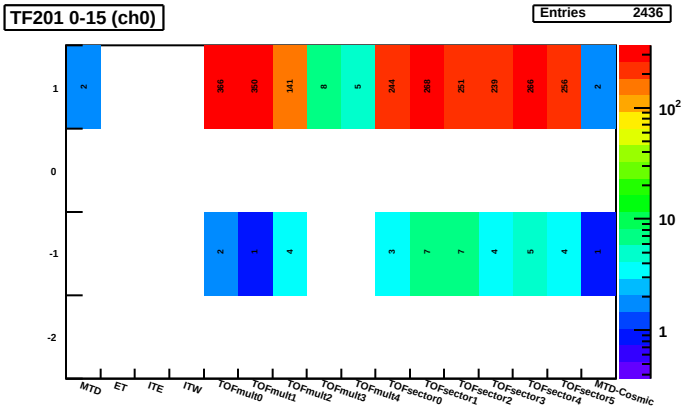

Input to FE002 QT board
Entries1663072
ADC
channel

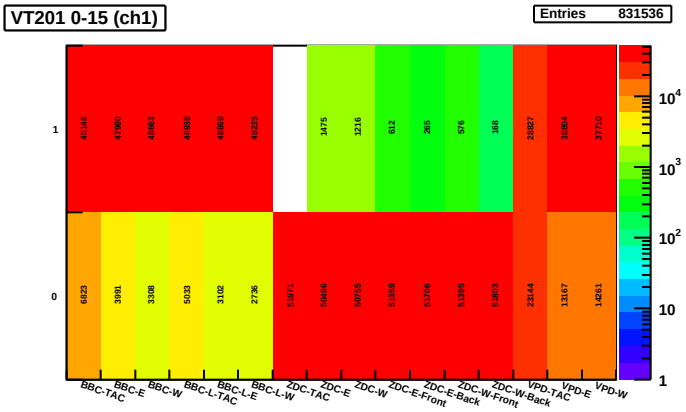

Input to FE003 QT board
Entries1663072
ADC
channel

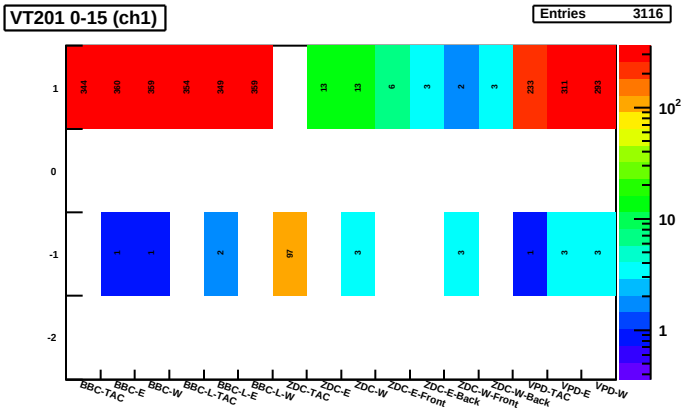

Input to FE004 QT board
Entries1663072
ADC
channel

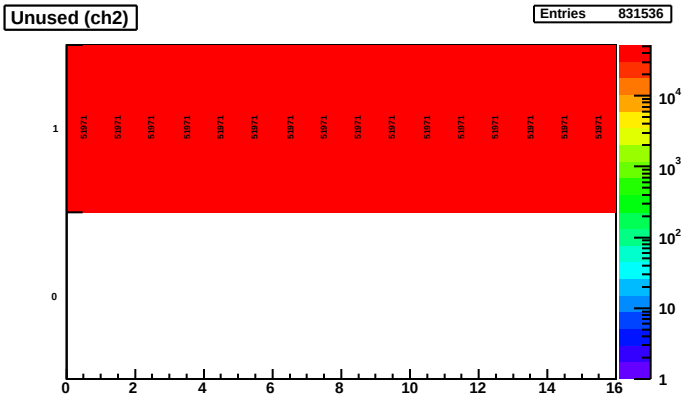

Input to FPE L1 DSM
Entries 207884
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

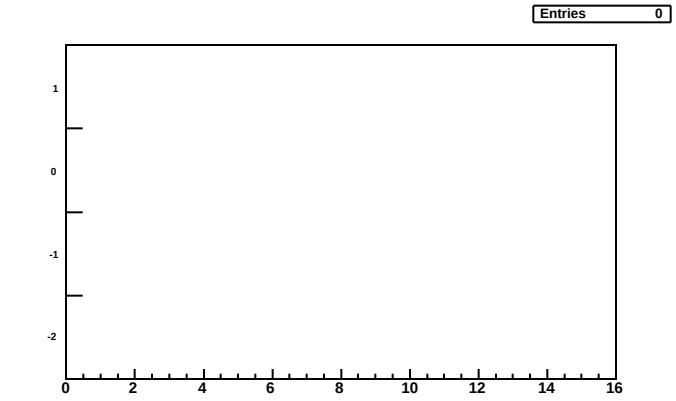

TF201 0-15 (ch0)
Entries 831536
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

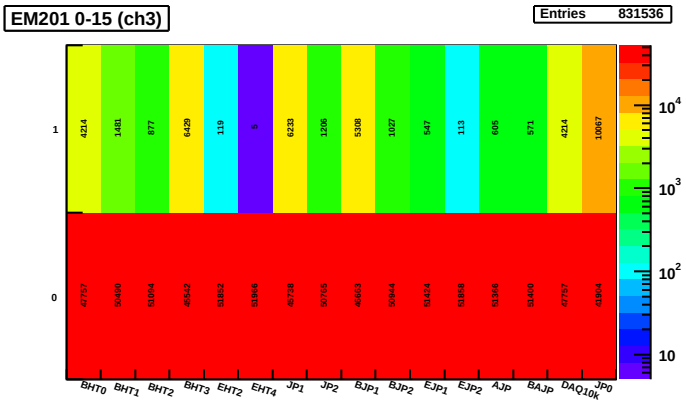

TF201 0-15 (ch0)
Entries 2436
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 831536
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 3116
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 831536


Entries 0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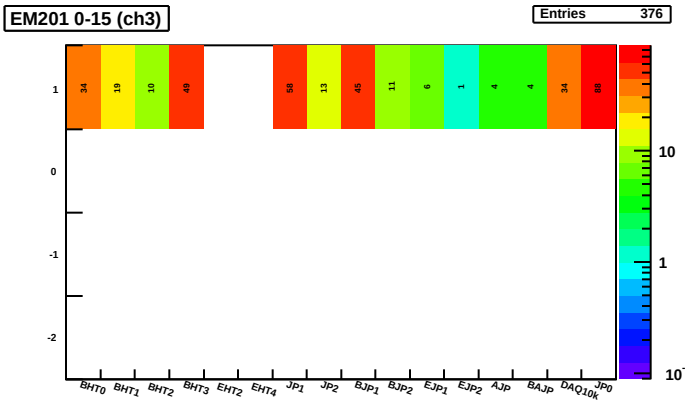
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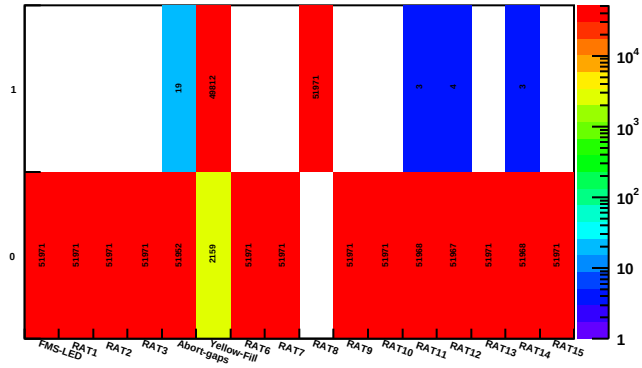
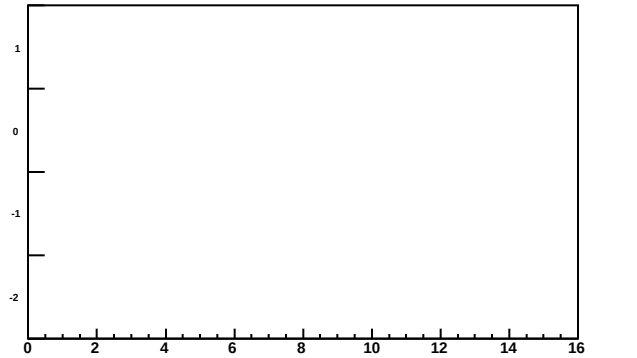
RAT board (ch4)





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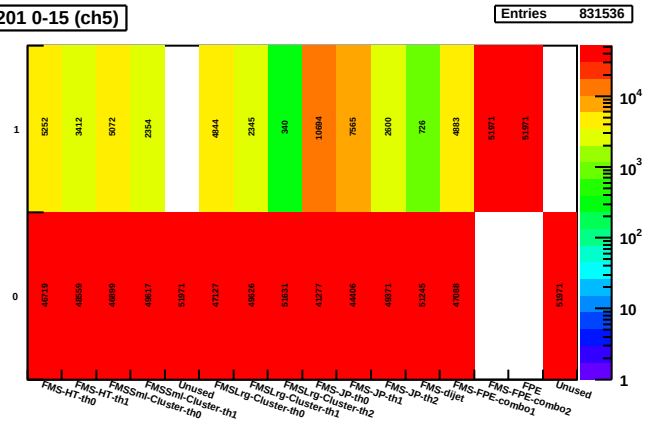

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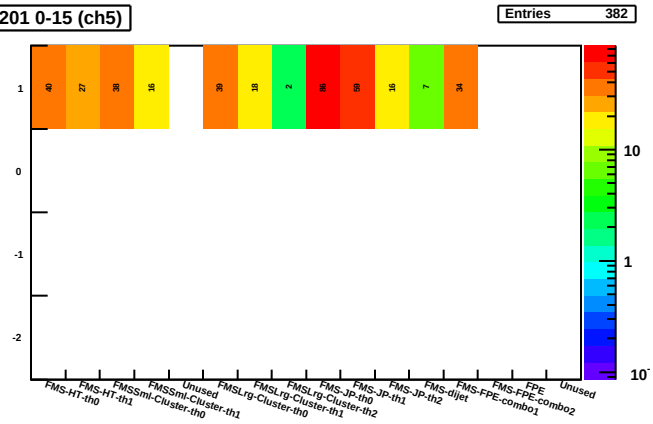

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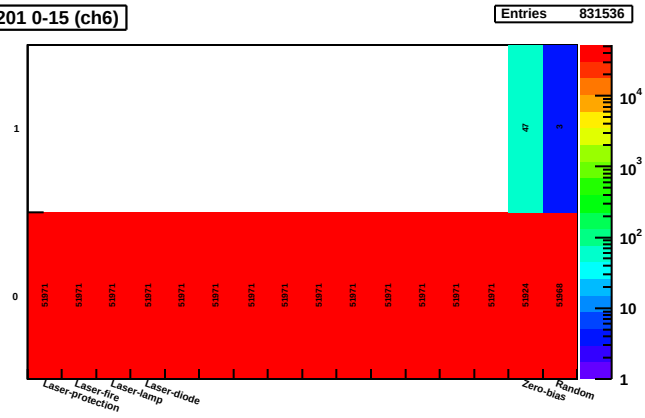

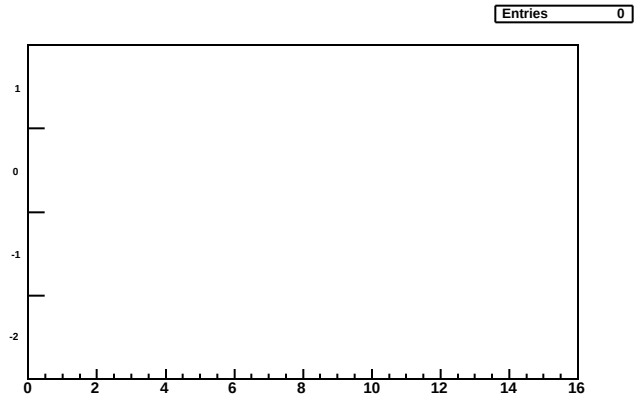
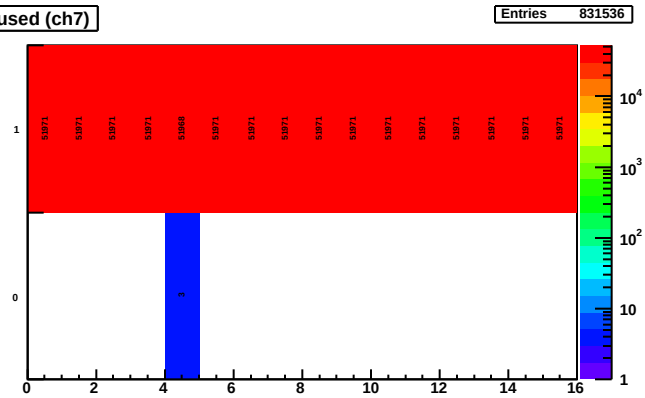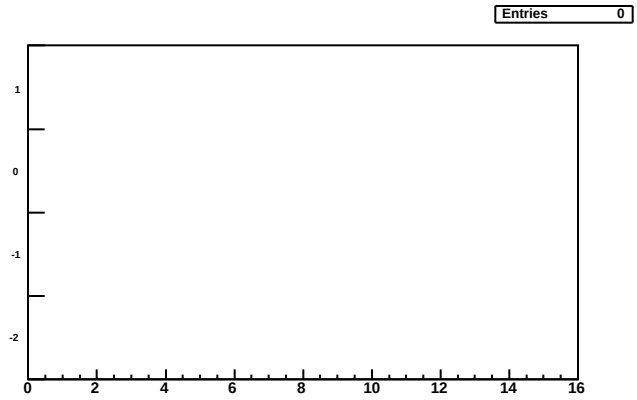
Unused (ch7)