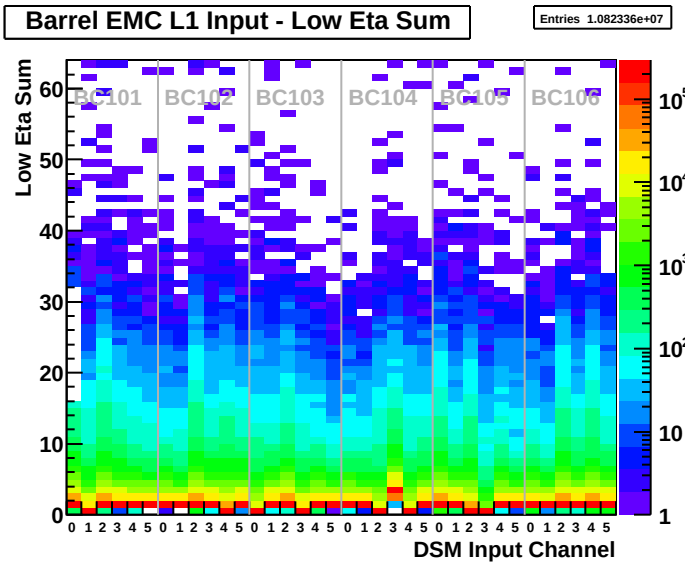
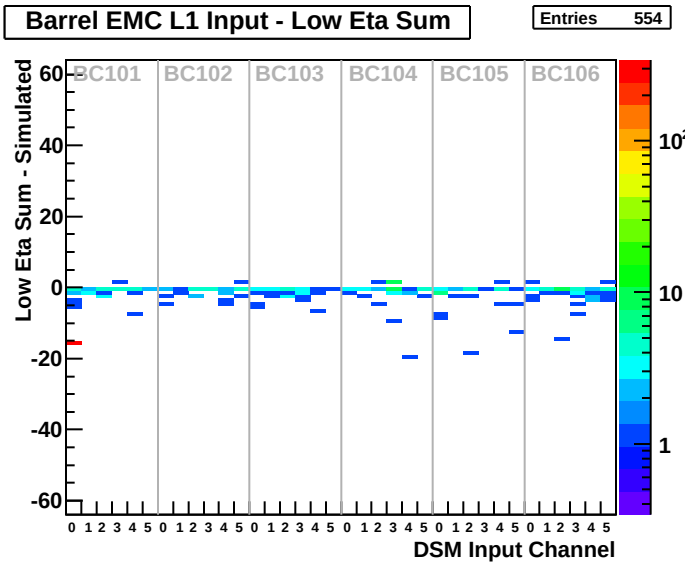
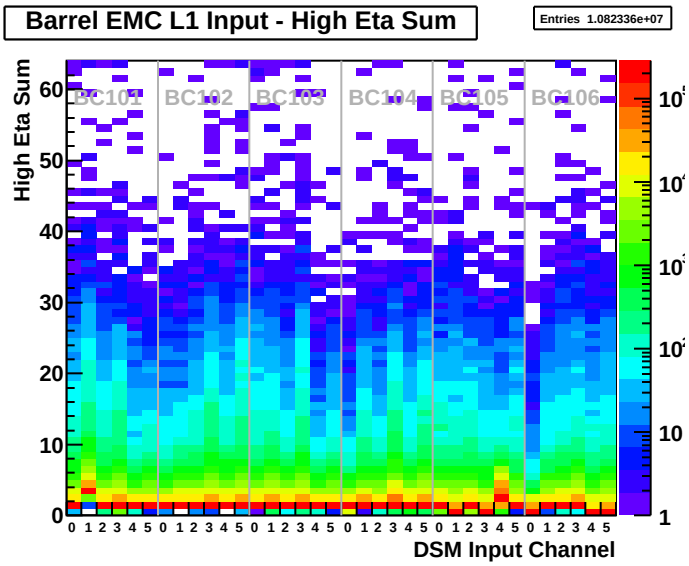
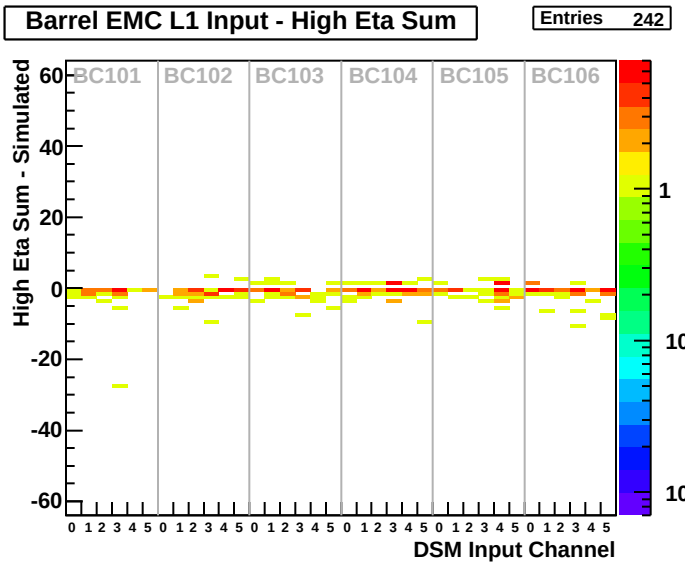
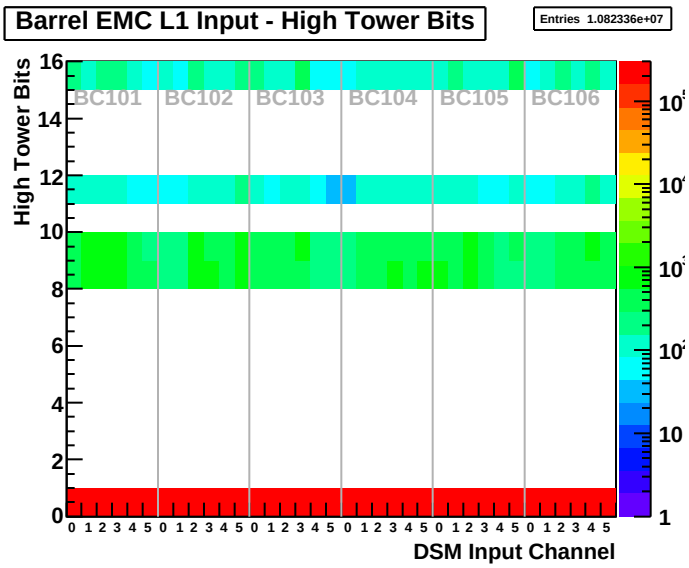
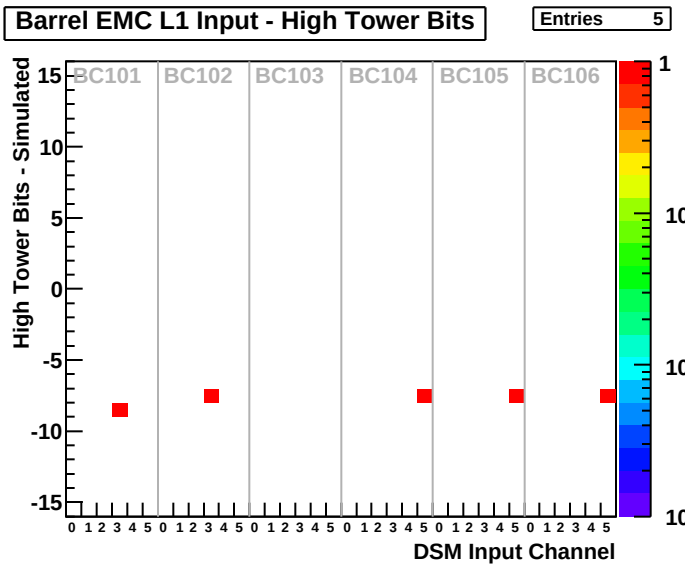

Barrel EMC L1 Input - Low Eta Sum
Entries 1.082336e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

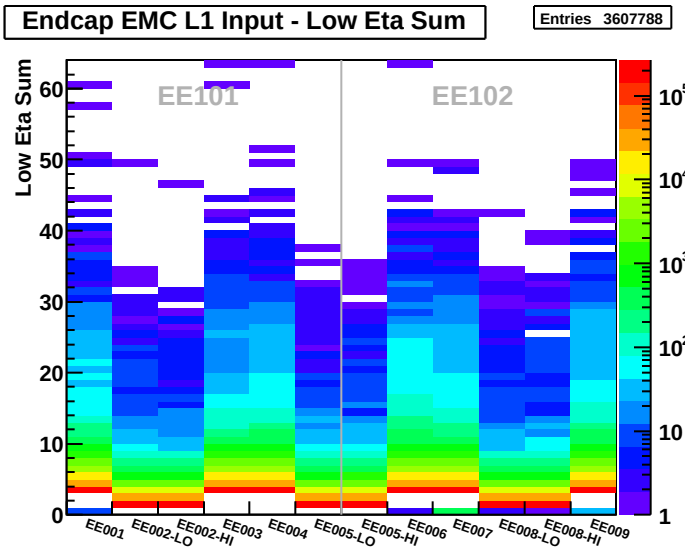

Barrel EMC L1 Input - Low Eta Sum
Entries 554
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

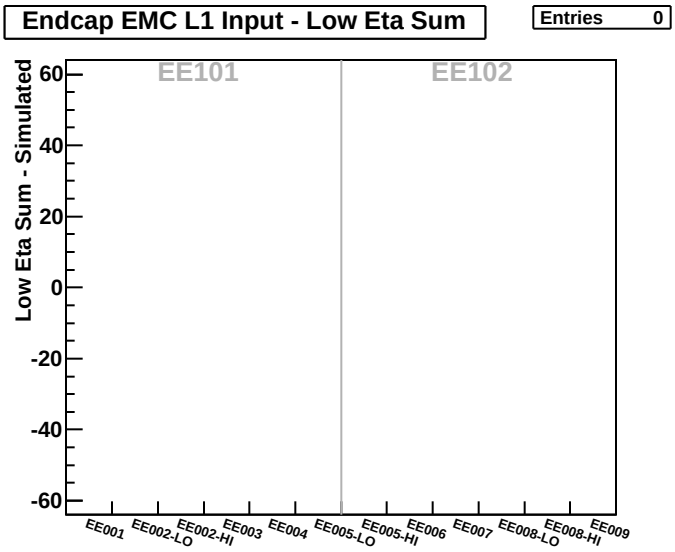

Barrel EMC L1 Input - High Eta Sum
Entries 1.082336e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

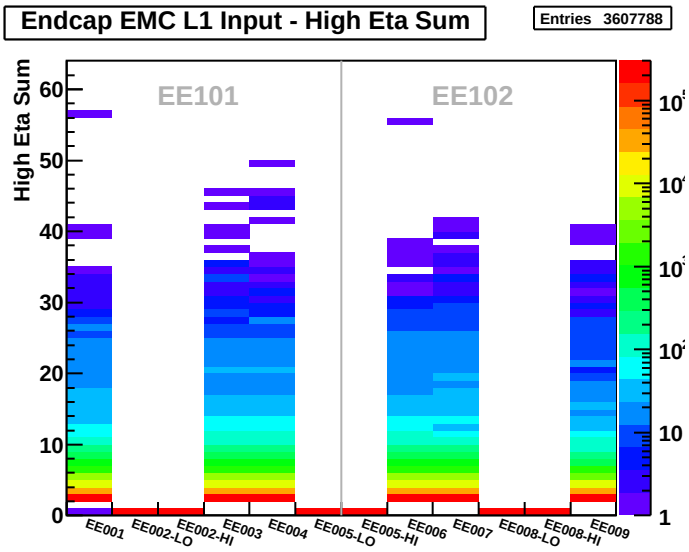

Barrel EMC L1 Input - High Eta Sum
Entries 242
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

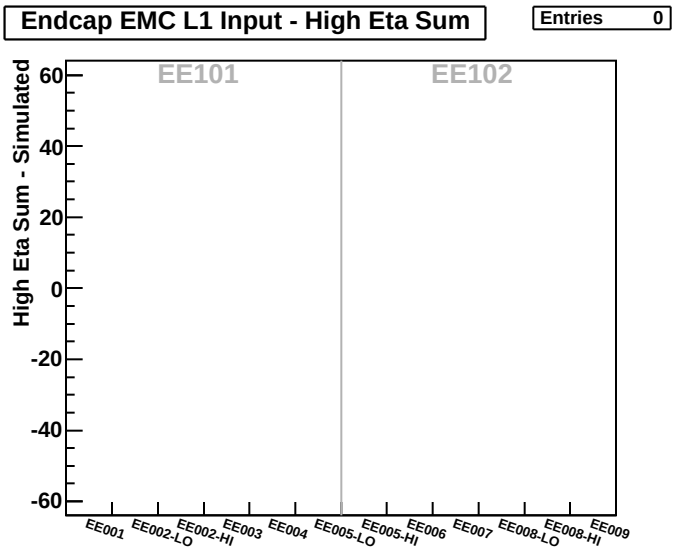

Barrel EMC L1 Input - High Tower Bits
Entries 1.082336e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

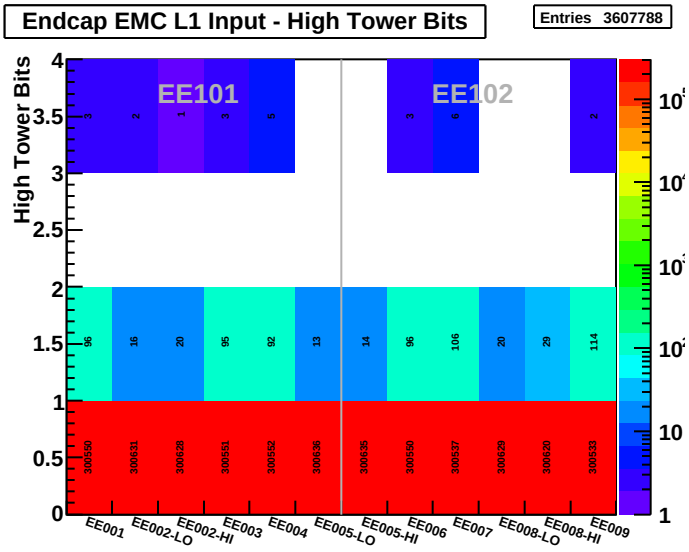

Barrel EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

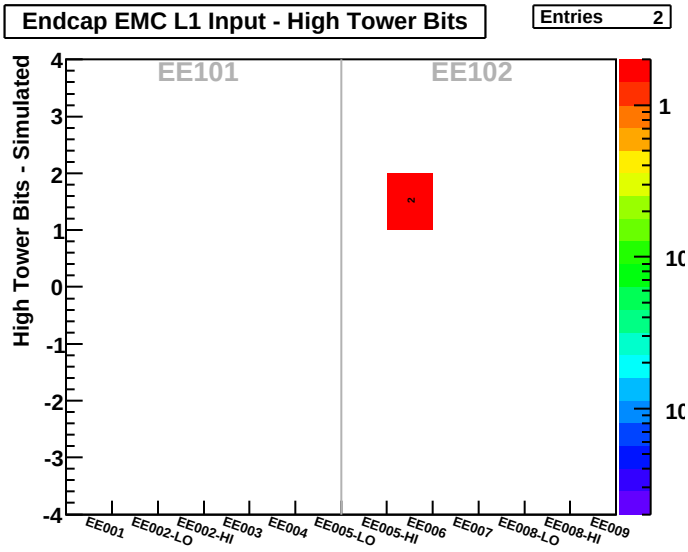

Endcap EMC L1 Input - Low Eta Sum
Entries 3607788
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

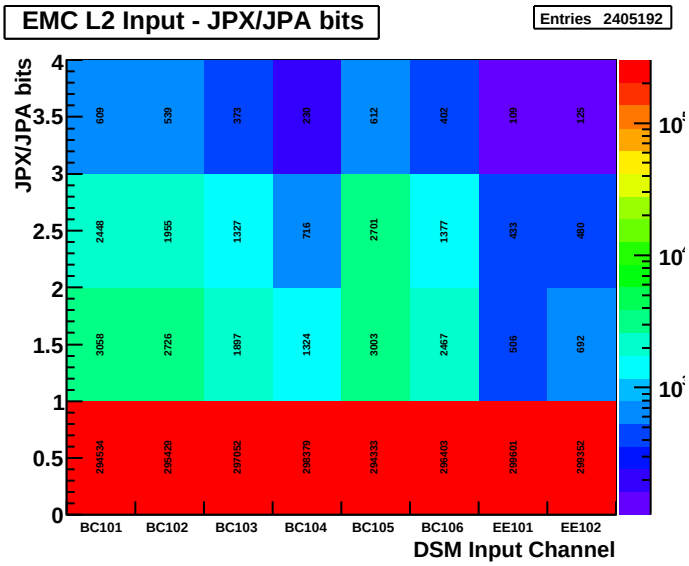

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

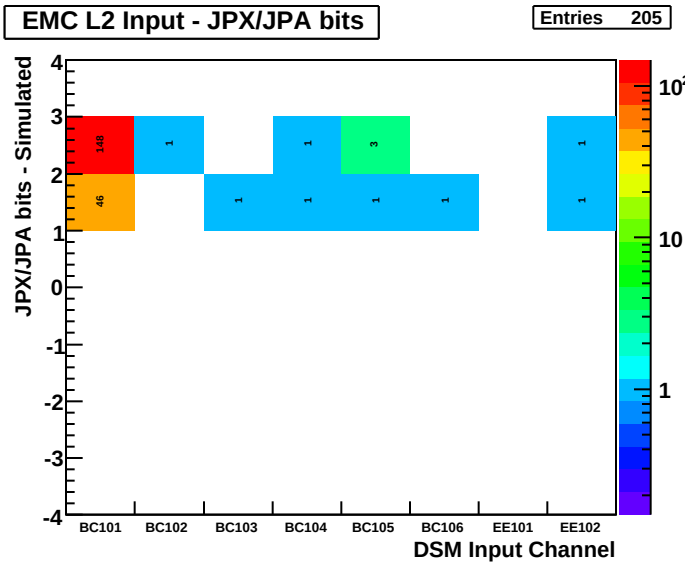

Endcap EMC L1 Input - High Eta Sum
Entries 3607788
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

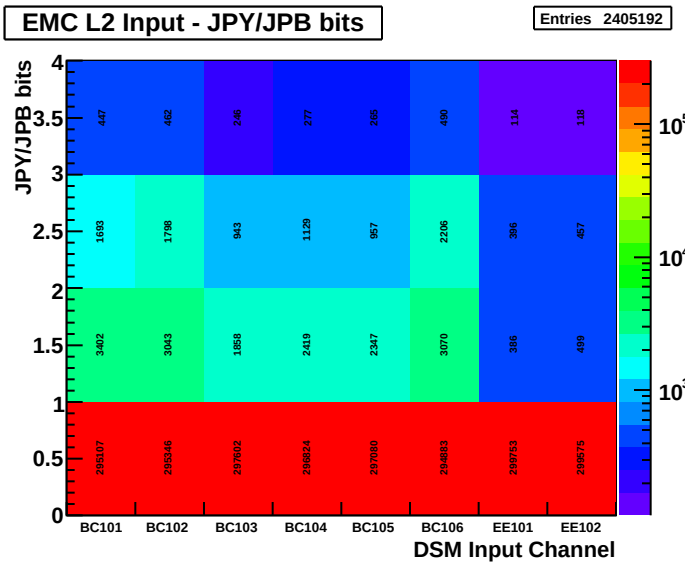

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

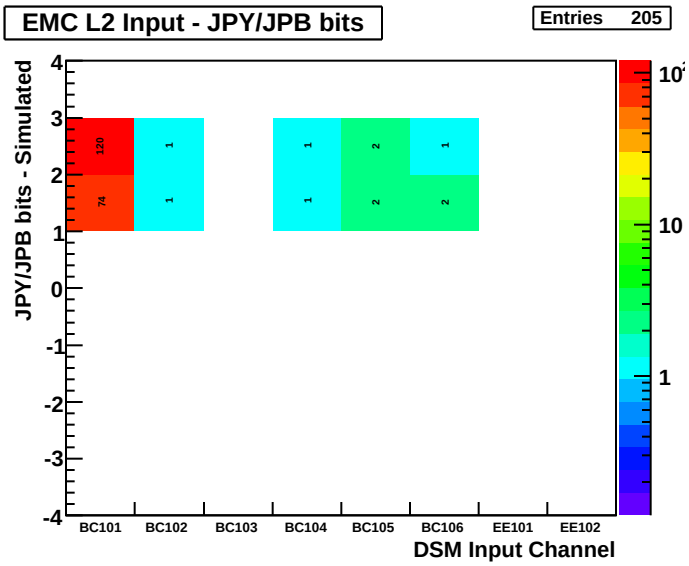

Endcap EMC L1 Input - High Tower Bits
Entries 3607788
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

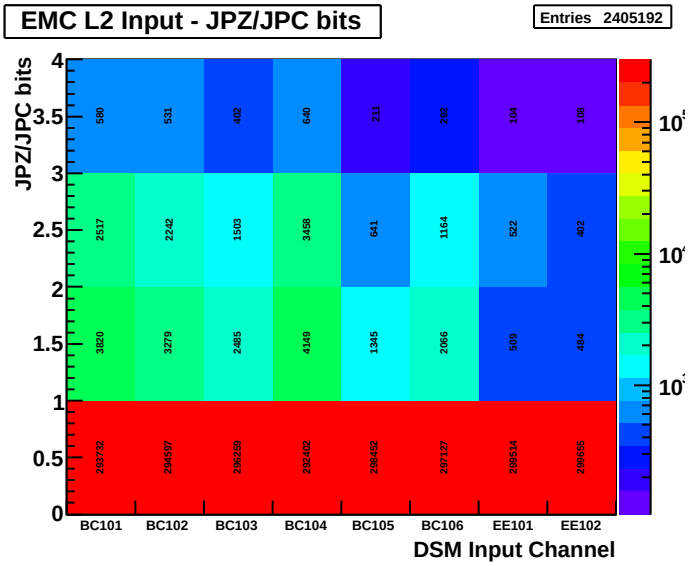

Endcap EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

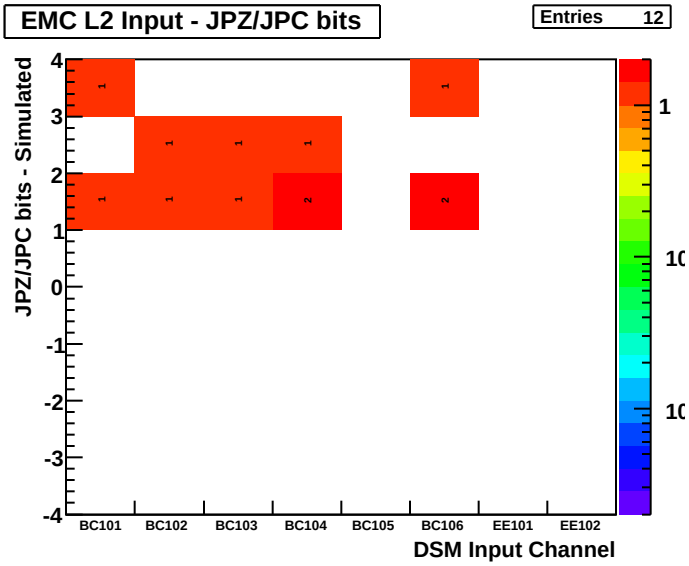

EMC L2 Input - JPX/JPA bits
Entries 2405192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

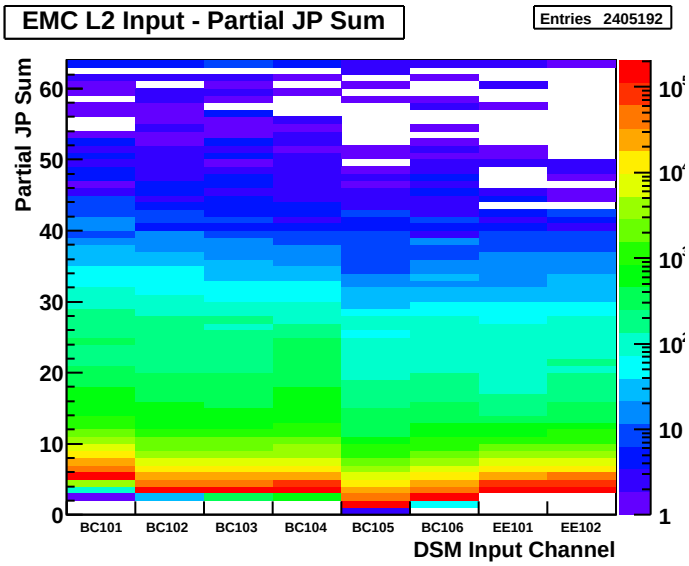

EMC L2 Input - JPX/JPA bits
Entries 205
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

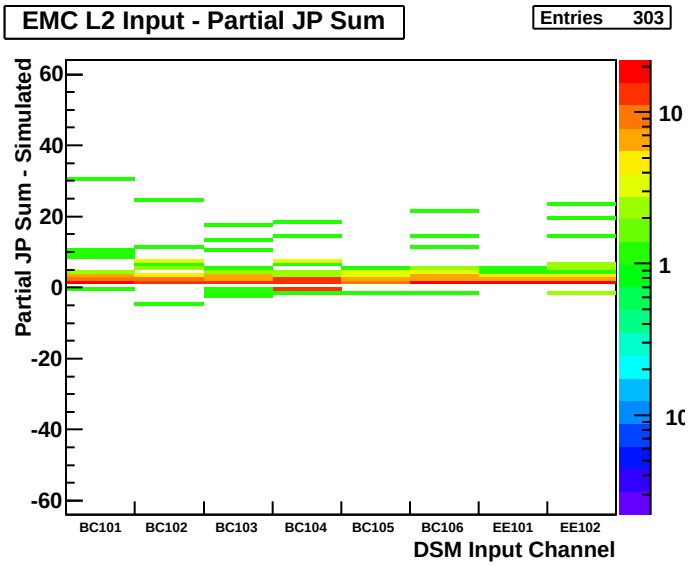

EMC L2 Input - JPY/JPB bits
Entries 2405192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

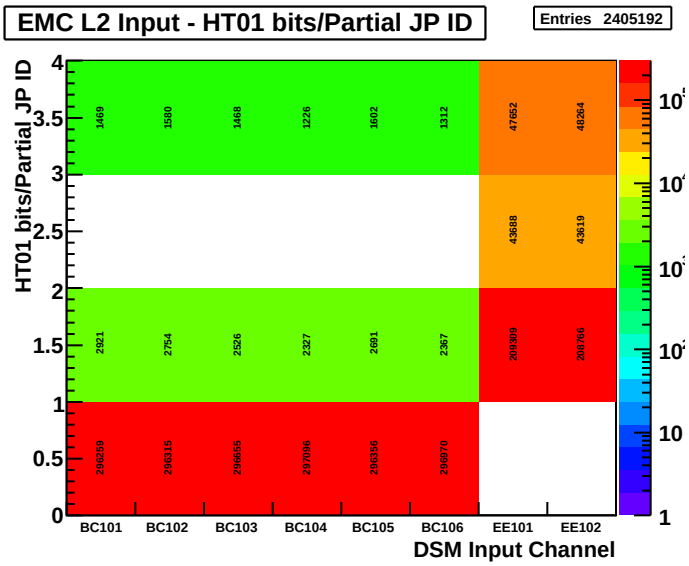

EMC L2 Input - JPY/JPB bits
Entries 205
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

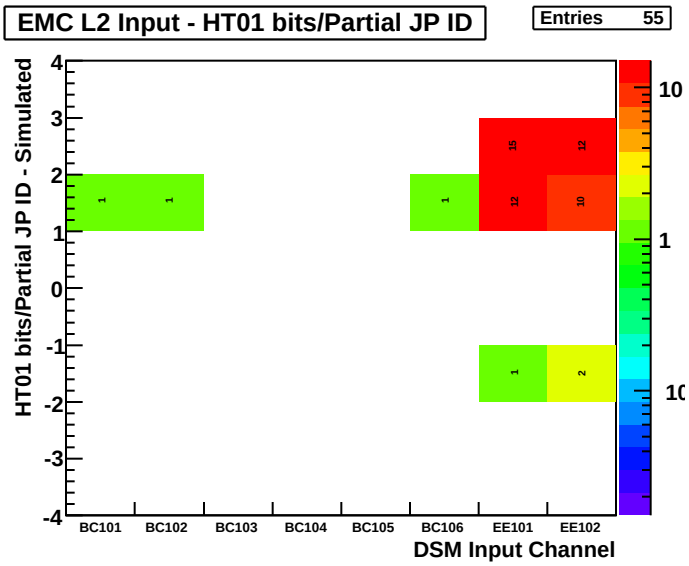

EMC L2 Input - JPZ/JPC bits
Entries 2405192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

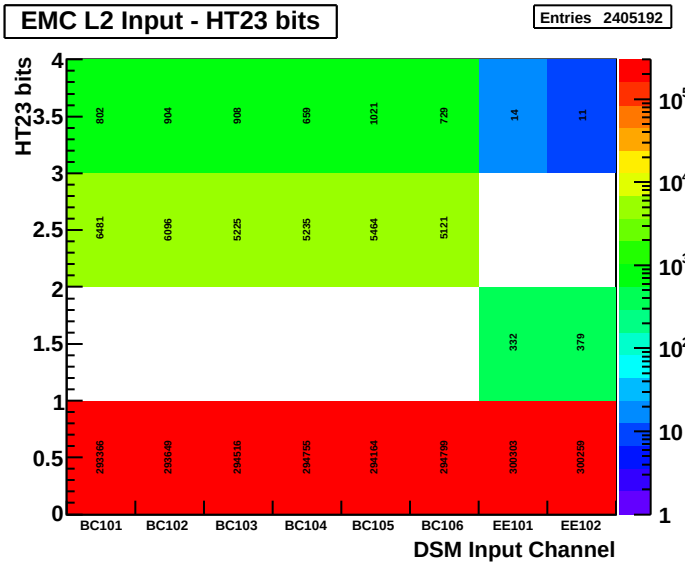

EMC L2 Input - JPZ/JPC bits
Entries 12
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

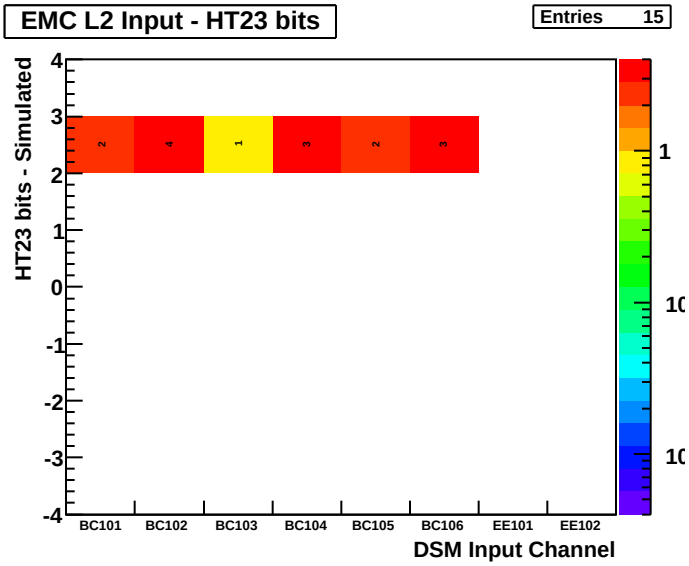

EMC L2 Input - Partial JP Sum
Entries 2405192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

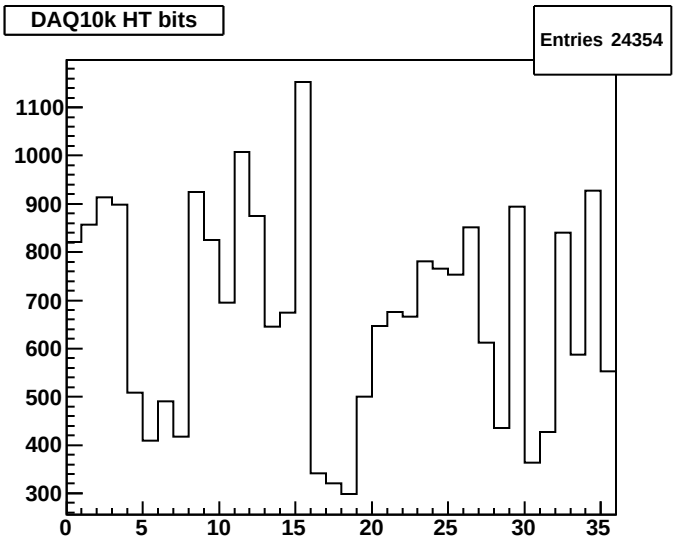

EMC L2 Input - Partial JP Sum
Entries 303
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

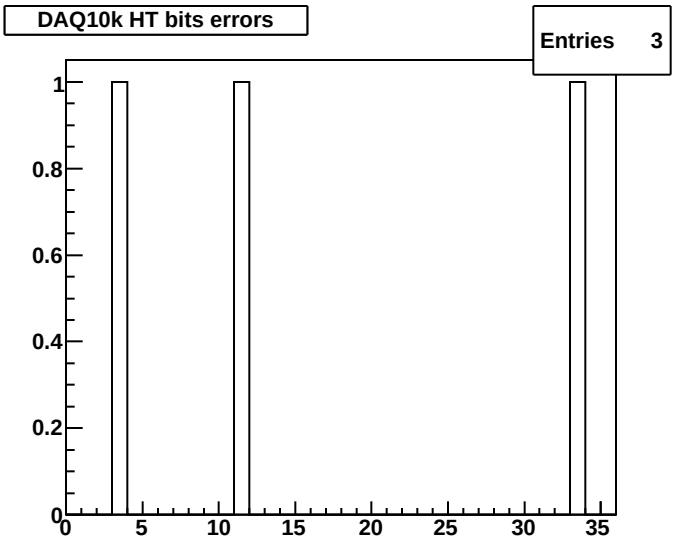

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2405192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

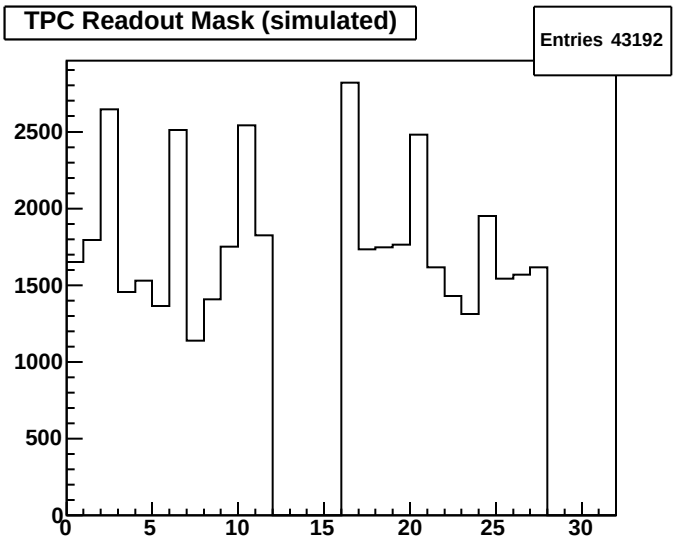

EMC L2 Input - HT01 bits/Partial JP ID
Entries 55
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

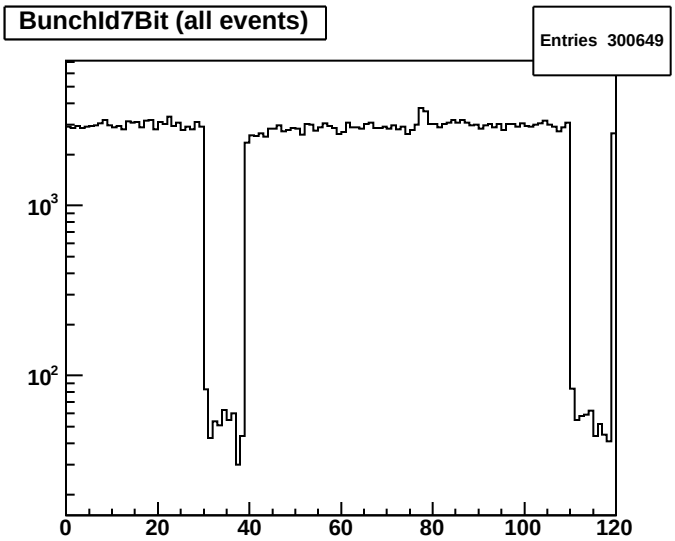

EMC L2 Input - HT23 bits
Entries 2405192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

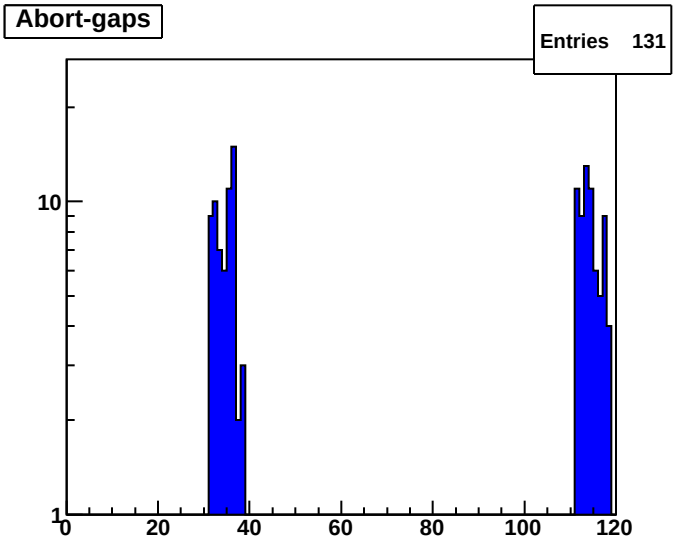

EMC L2 Input - HT23 bits
Entries 15
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

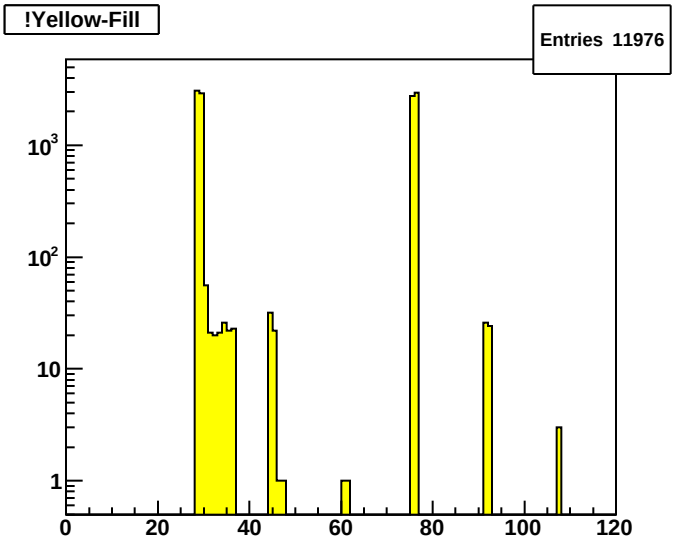

DAQ10k HT bits
Entries 24354

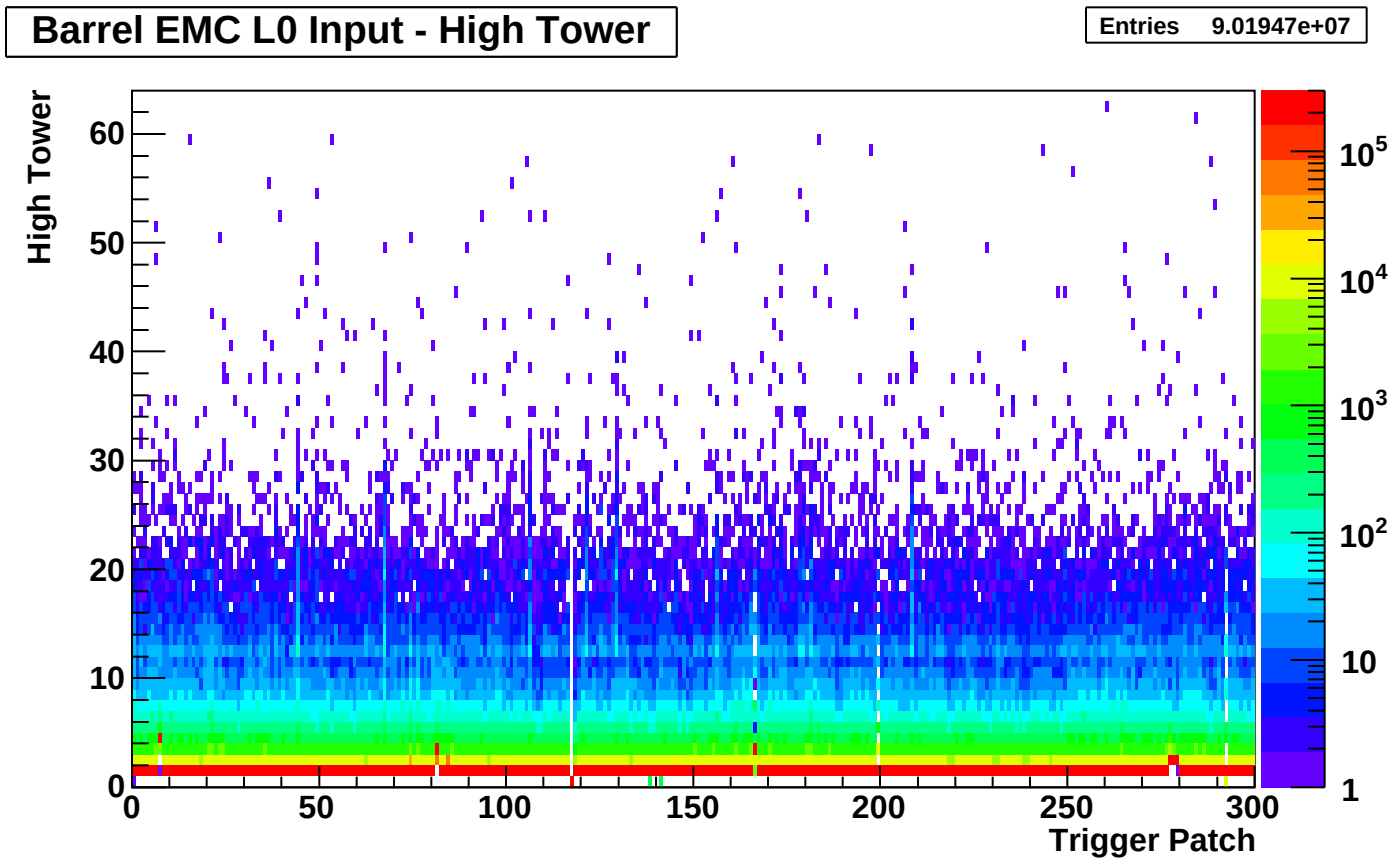

DAQ10k HT bits errors
Entries 3

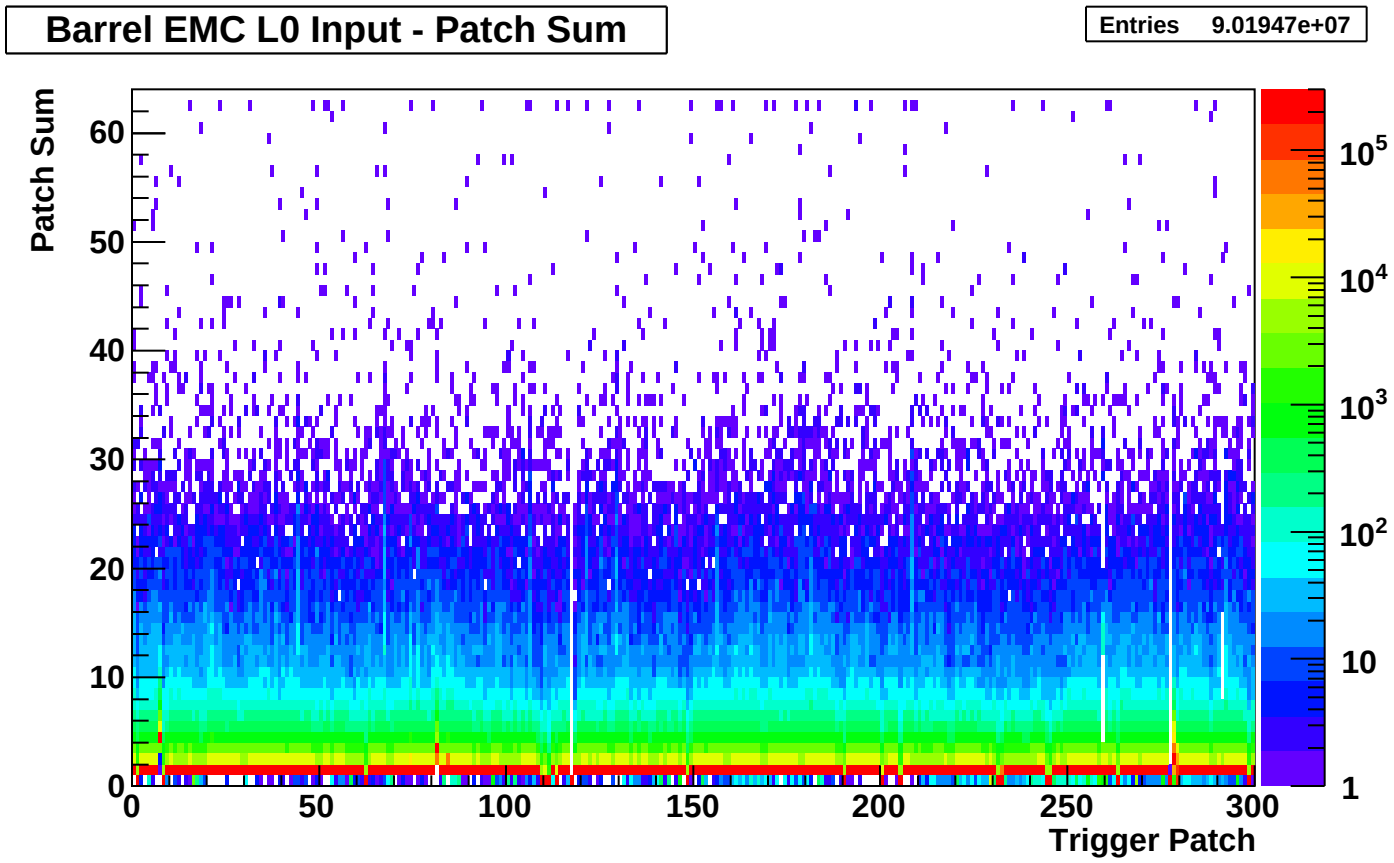

TPC Readout Mask (simulated)
Entries 43192

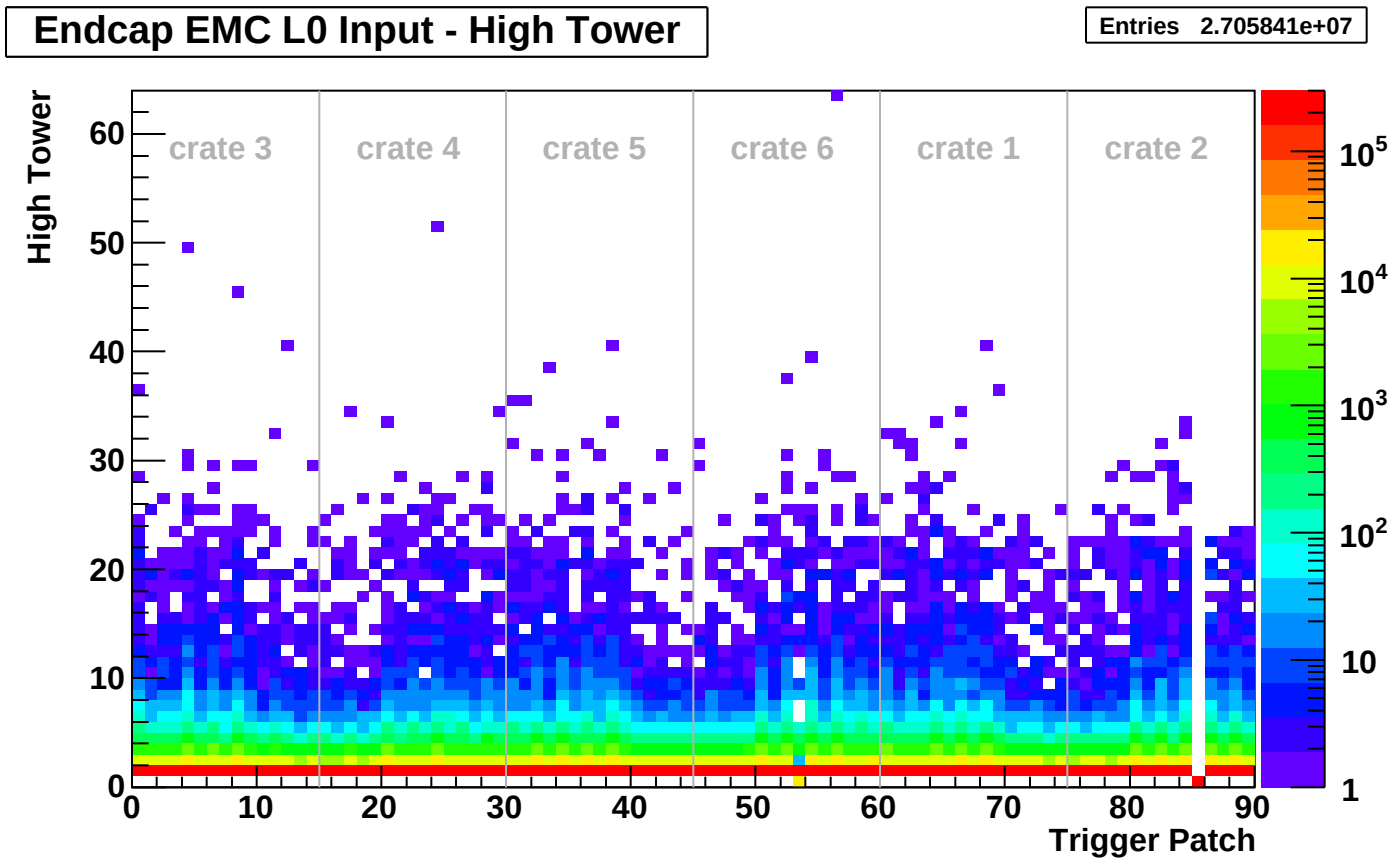

BunchId7Bit (all events)
Entries 300649

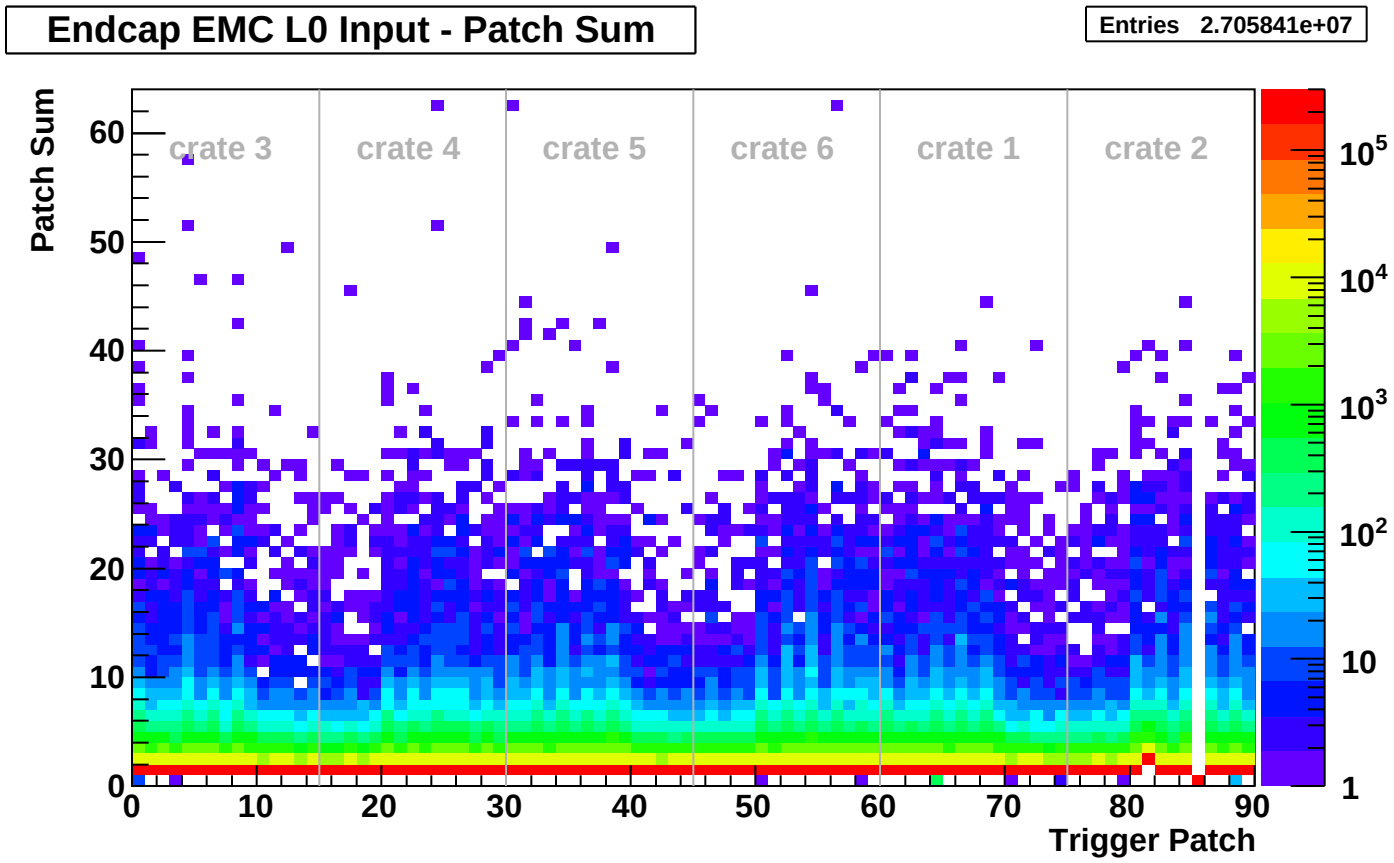

Abort-gaps
Entries 131

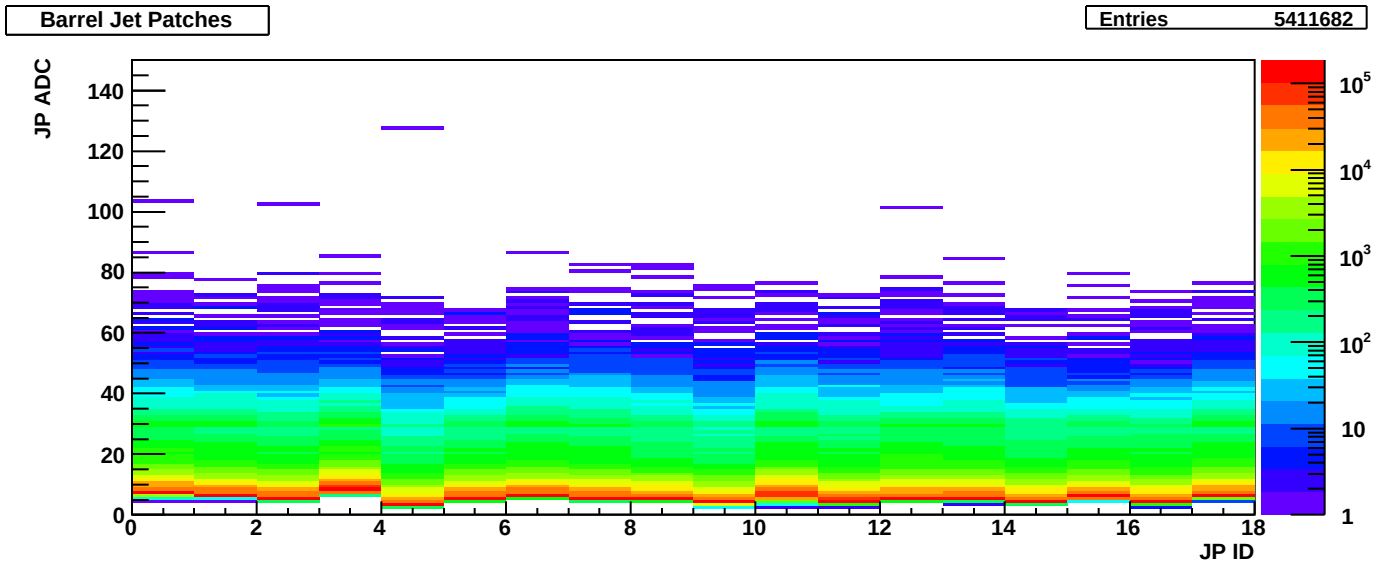

!Yellow-Fill
Entries 11976

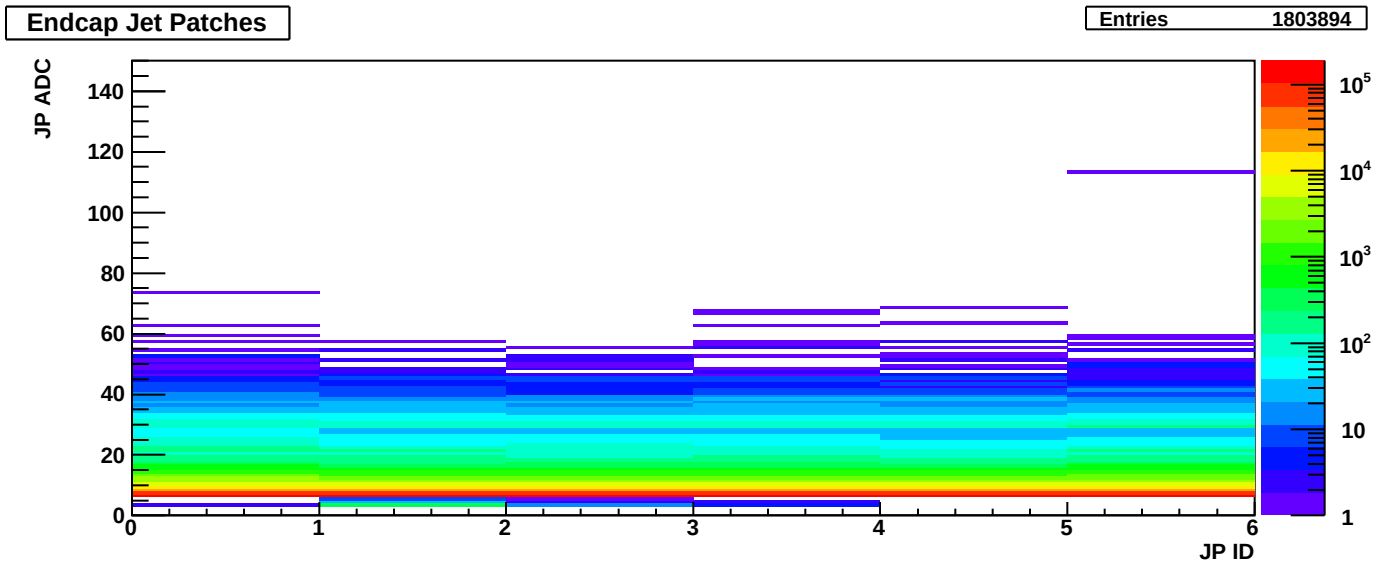

Barrel EMC L0 Input - High Tower
Entries 9.01947e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

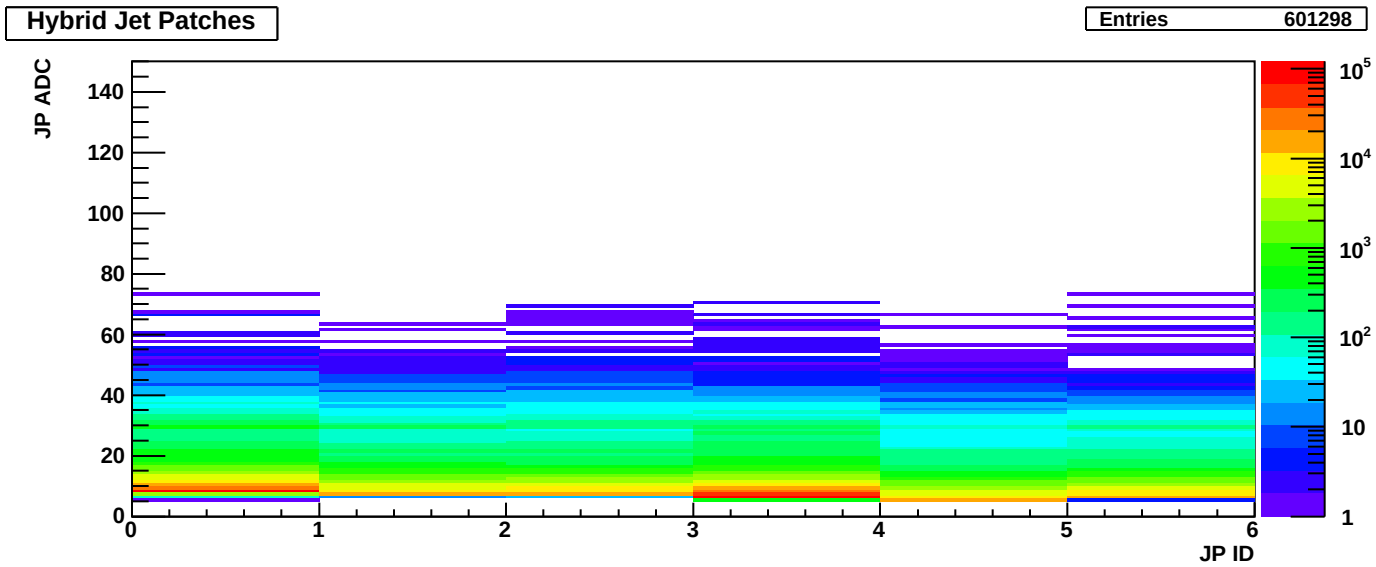

Barrel EMC L0 Input - Patch Sum
Entries 9.01947e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

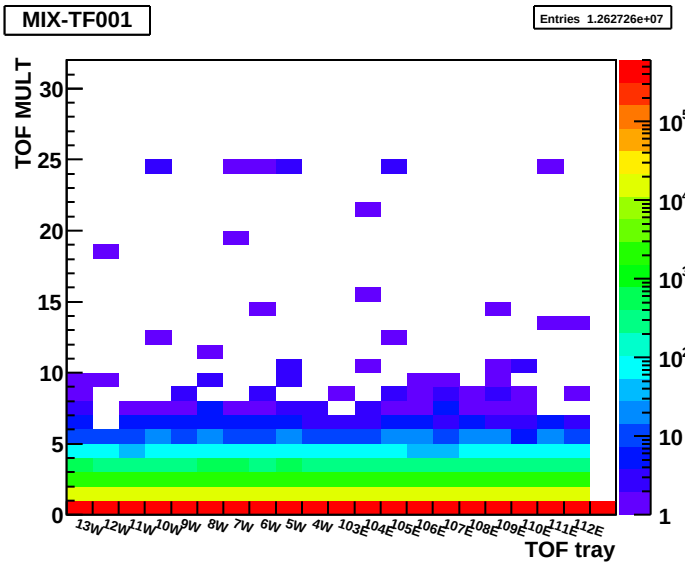

Endcap EMC L0 Input - High Tower
Entries 2.705841e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

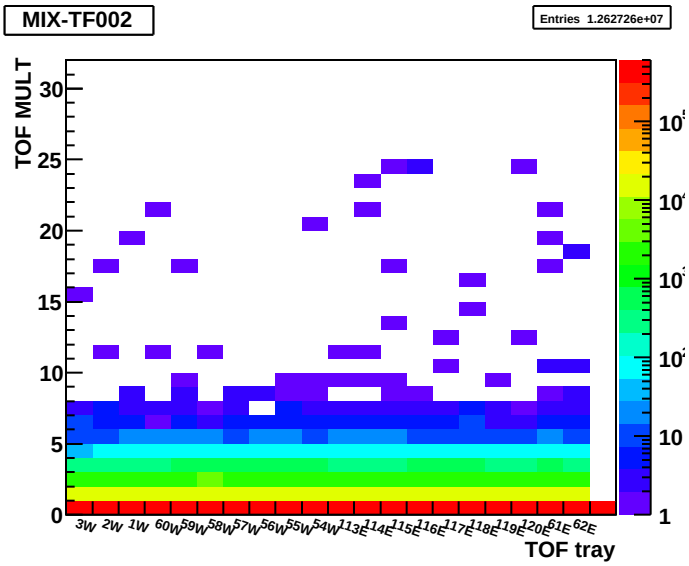

Endcap EMC L0 Input - Patch Sum
Entries 2.705841e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

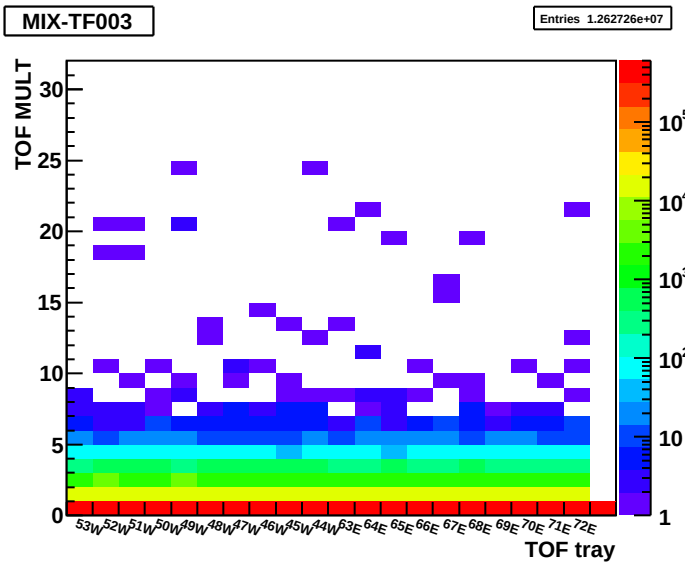

Barrel Jet Patches
Entries 5411682
JP ADC
JP ID

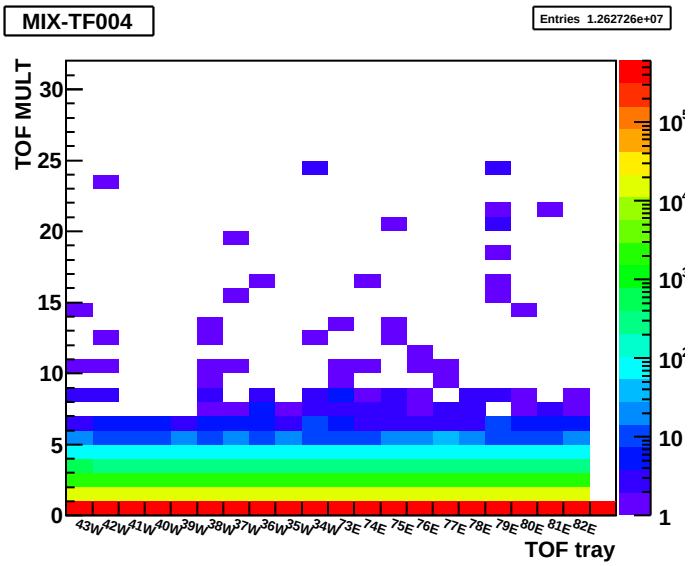

Endcap Jet Patches
Entries 1803894
JP ADC
JP ID

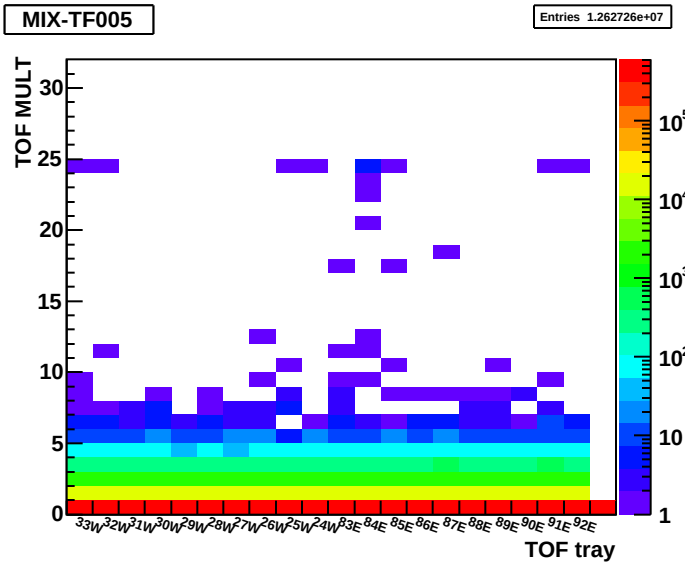

Hybrid Jet Patches
Entries 601298
JP ADC
JP ID

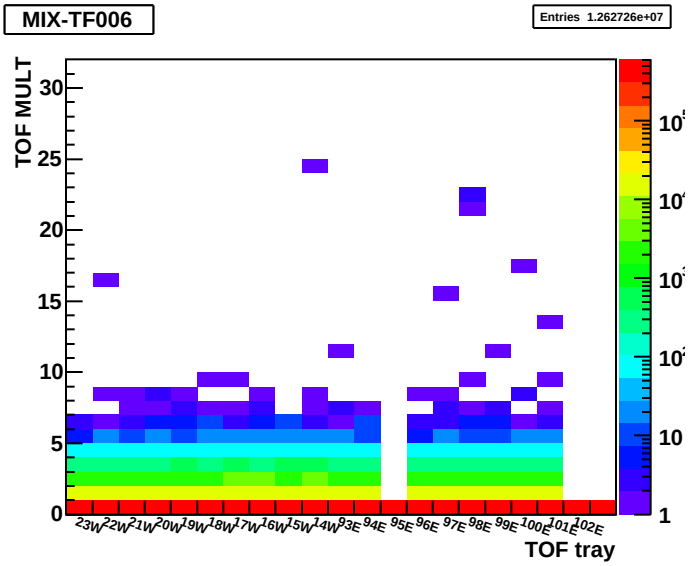

MIX-TF001
Entries 1.262726e+07
TOF MULT
TOF tray

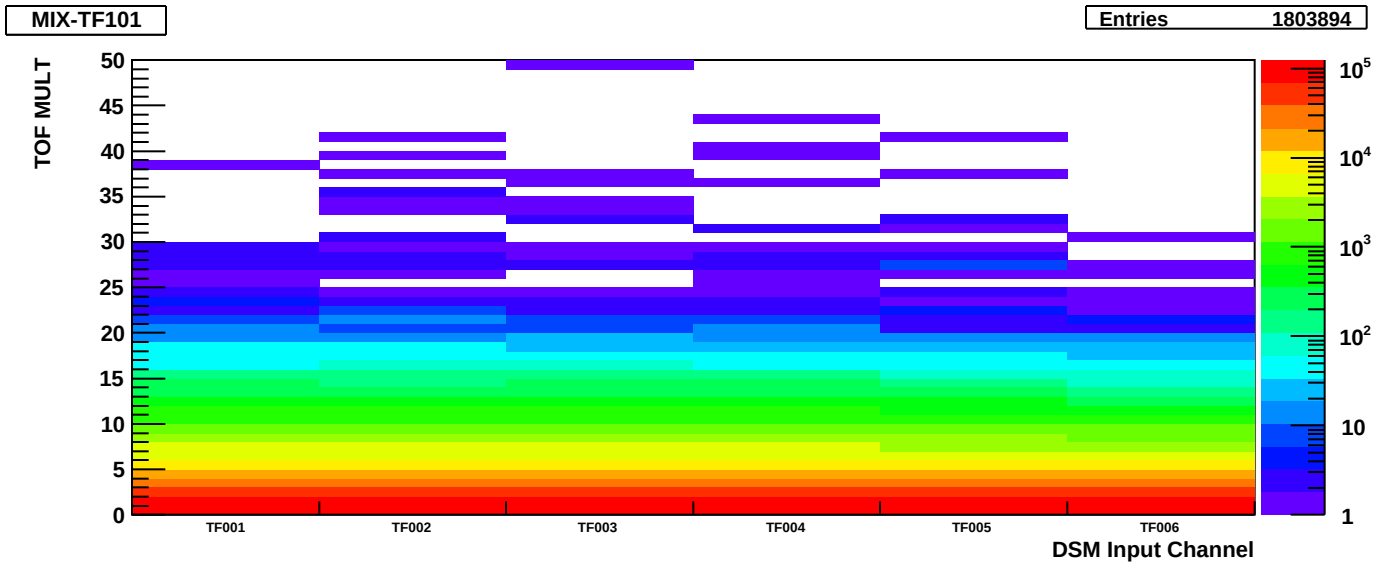

MIX-TF002
Entries 1.262726e+07
TOF MULT
TOF tray

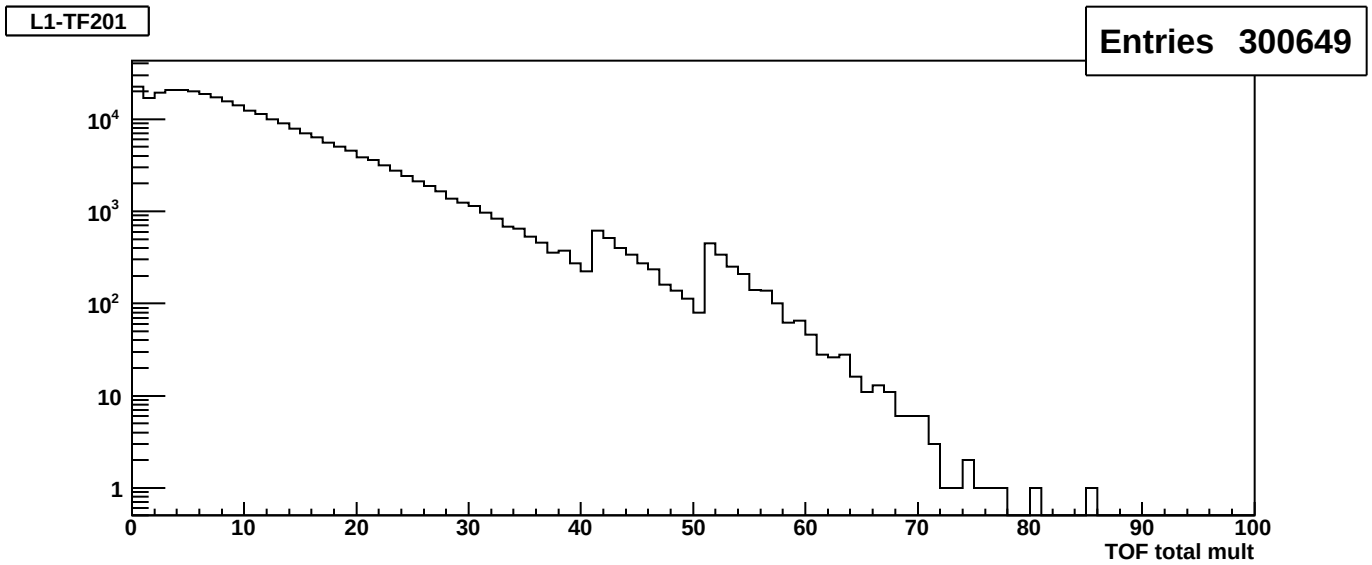

MIX-TF003
Entries 1.262726e+07
TOF MULT
TOF tray

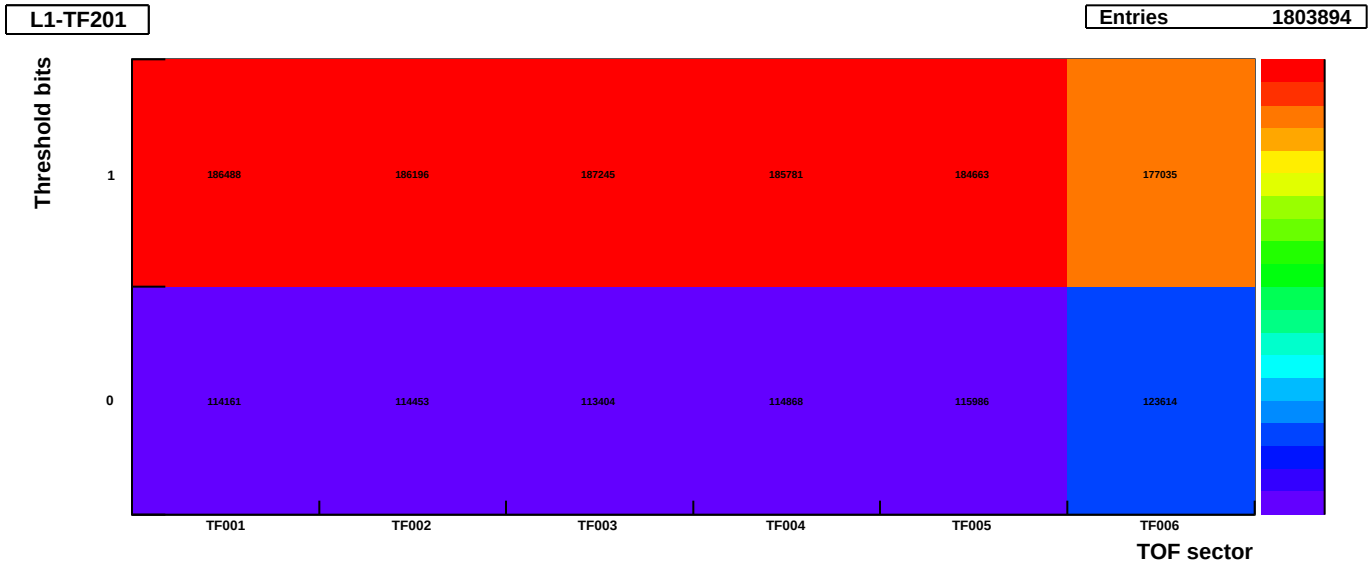

MIX-TF004
Entries 1.262726e+07
TOF MULT
TOF tray

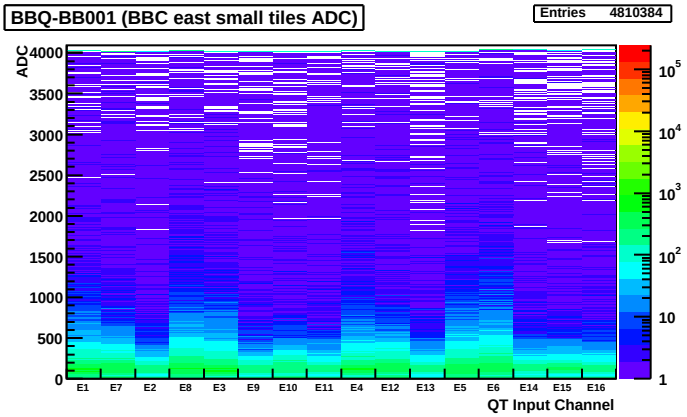

MIX-TF005
Entries 1.262726e+07
TOF MULT
TOF tray

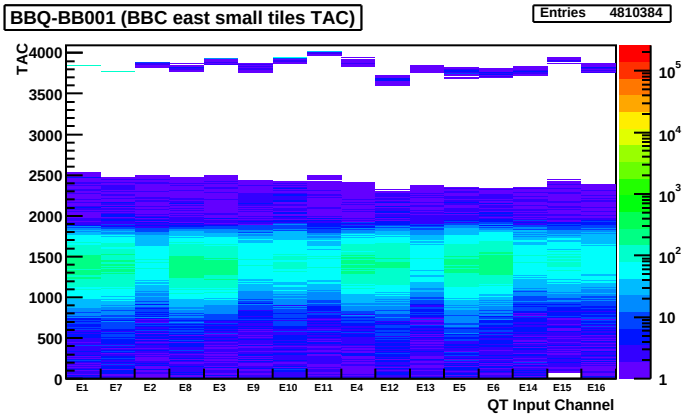

MIX-TF006
Entries 1.262726e+07
TOF MULT
TOF tray

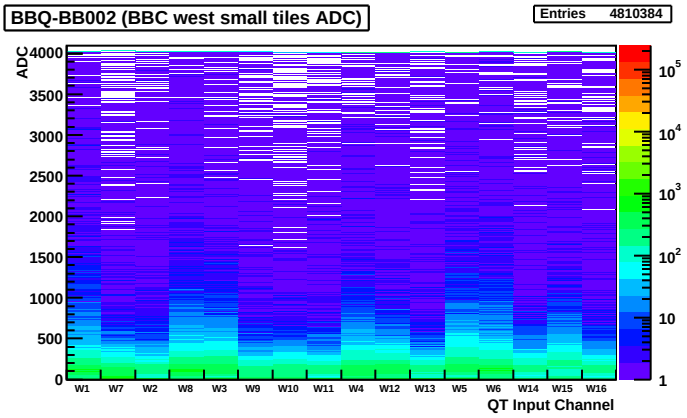

MIX-TF101
Entries 1803894
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

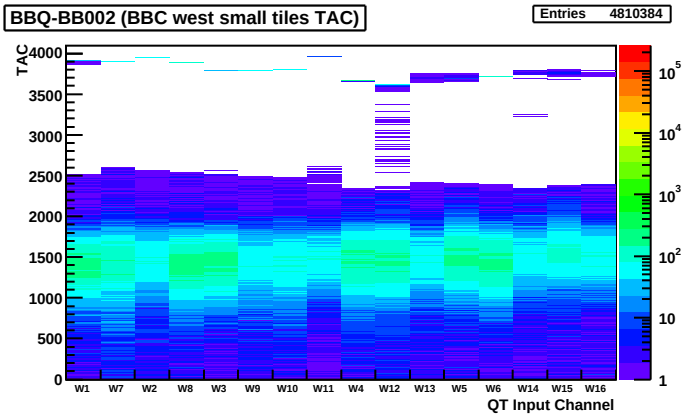

L1-TF201
Entries 300649
TOF total mult

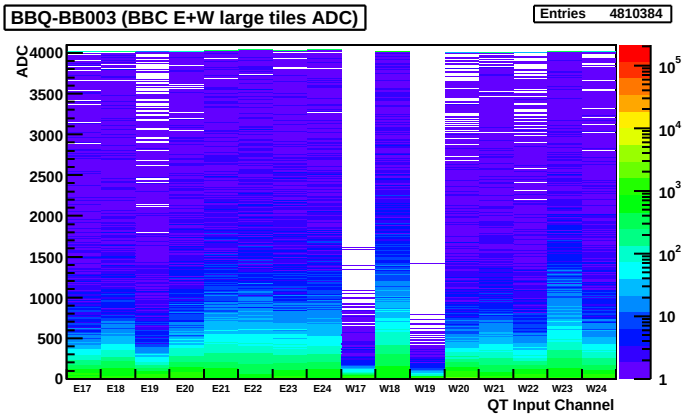

L1-TF201
Entries 1803894
Threshold bits
186488
186196
187245
185781
184663
177035
114161
114453
113404
114868
115986
123614
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

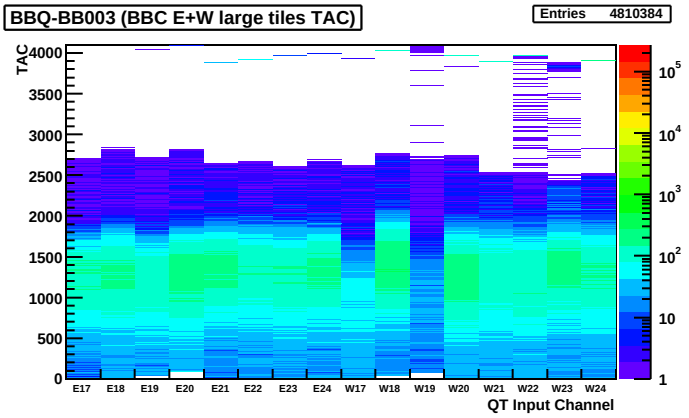

BBQ-BB001 (BBC east small tiles ADC)
Entries 4810384
ADC
QT Input Channel

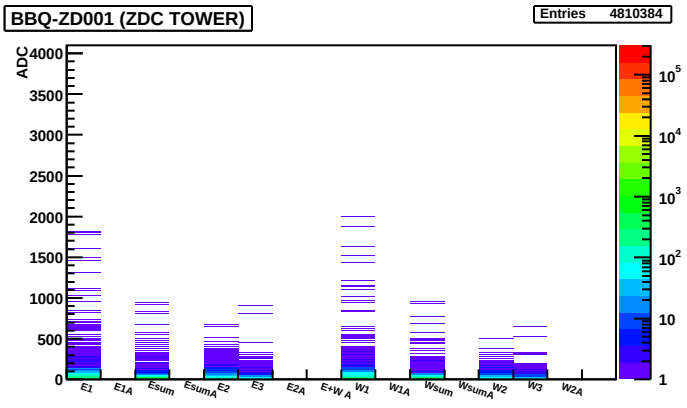

BBQ-BB001 (BBC east small tiles TAC)
Entries 4810384
TAC
QT Input Channel

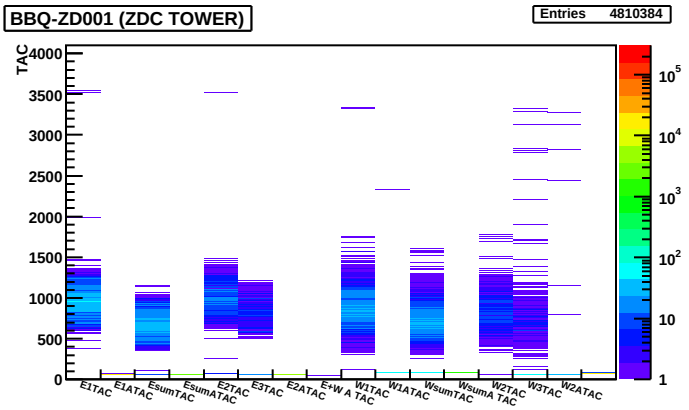

BBQ-BB002 (BBC west small tiles ADC)
Entries 4810384
ADC
QT Input Channel

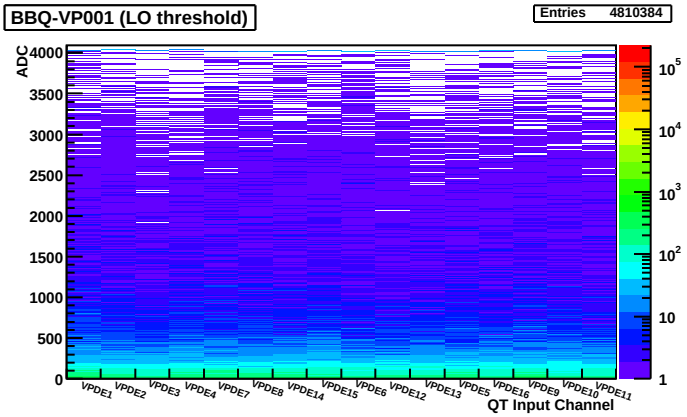

BBQ-BB002 (BBC west small tiles TAC)
Entries 4810384
TAC
QT Input Channel

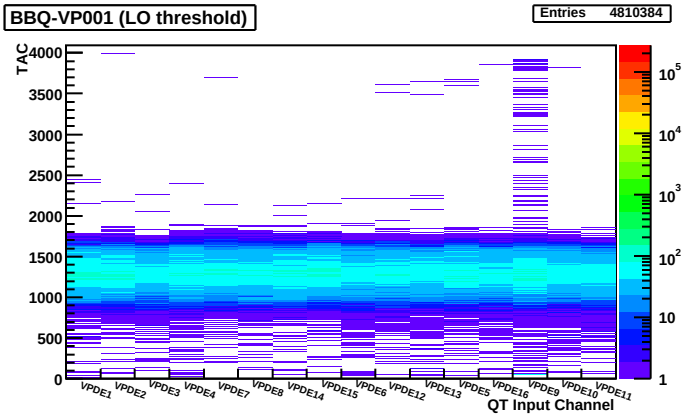

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4810384
ADC
QT Input Channel

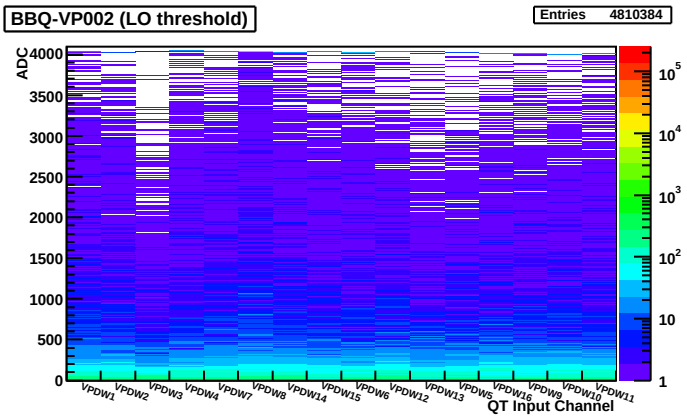

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4810384
TAC
QT Input Channel

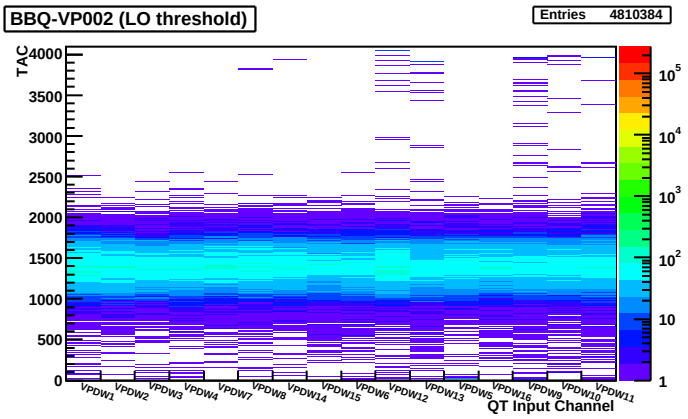

BBQ-ZD001 (ZDC TOWER)
Entries 4810384
ADC

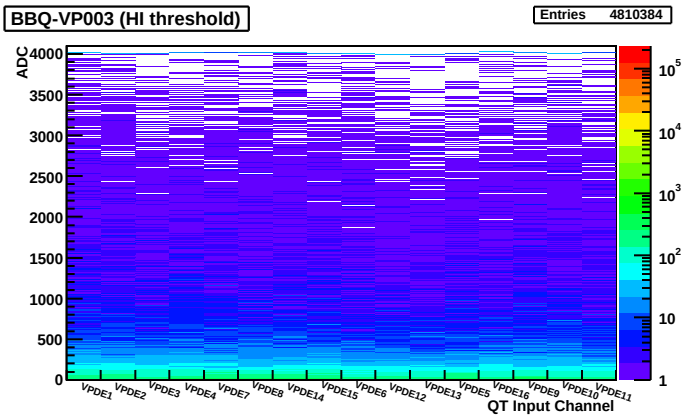

BBQ-ZD001 (ZDC TOWER)
Entries 4810384
TAC

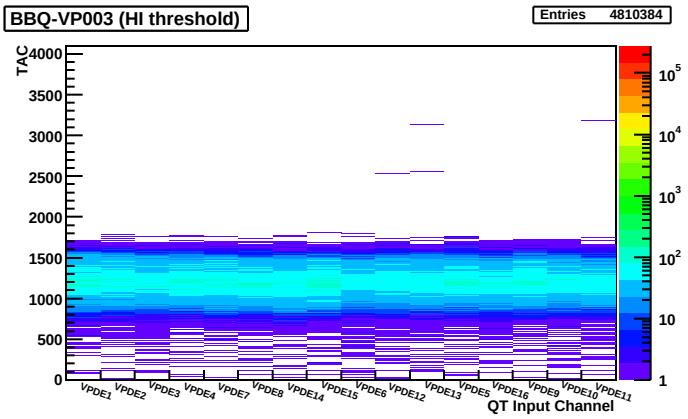

BBQ-VP001 (LO threshold)
Entries 4810384
ADC
QT Input Channel

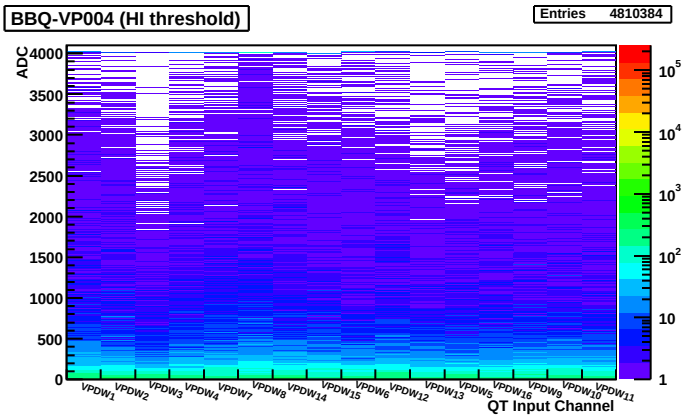

BBQ-VP001 (LO threshold)
Entries 4810384
TAC
QT Input Channel

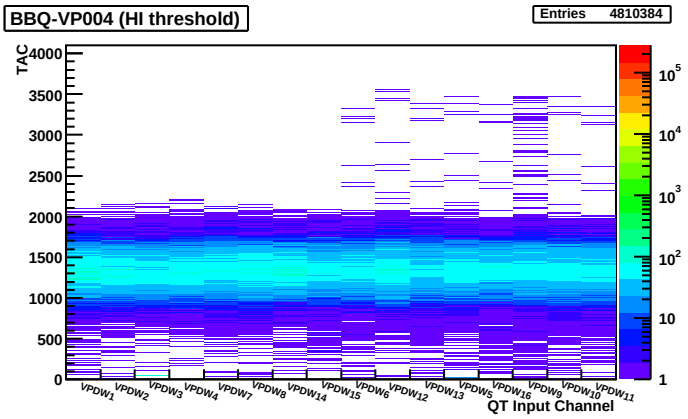

BBQ-VP002 (LO threshold)
Entries 4810384
ADC
QT Input Channel

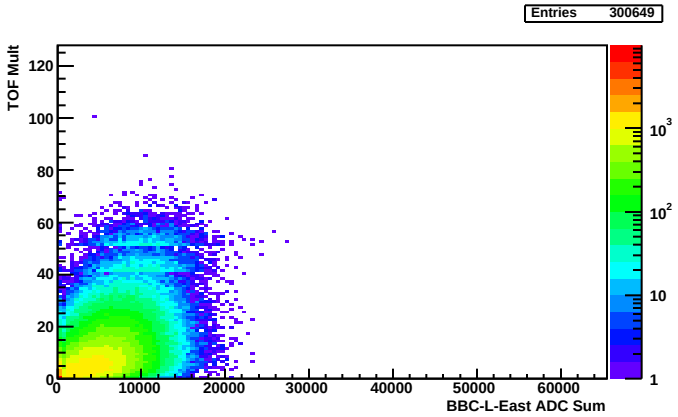

BBQ-VP002 (LO threshold)
Entries 4810384
TAC
QT Input Channel

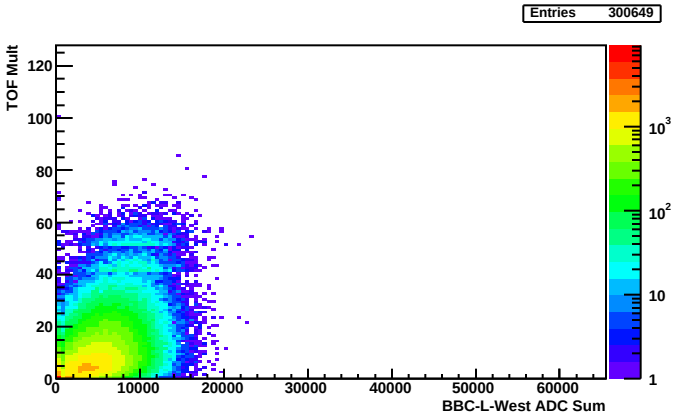

BBQ-VP003 (HI threshold)
Entries 4810384
ADC
QT Input Channel

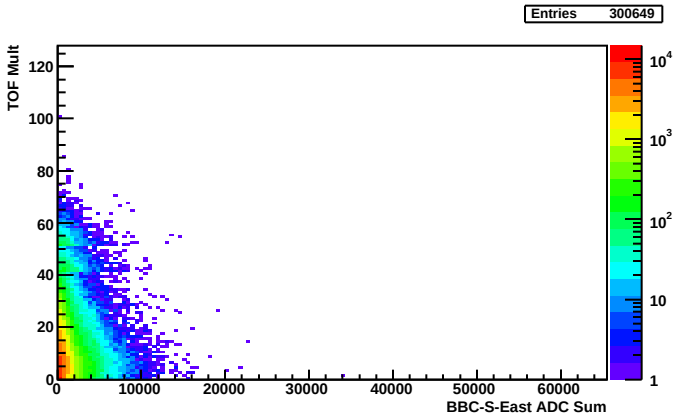

BBQ-VP003 (HI threshold)
Entries 4810384
TAC
QT Input Channel

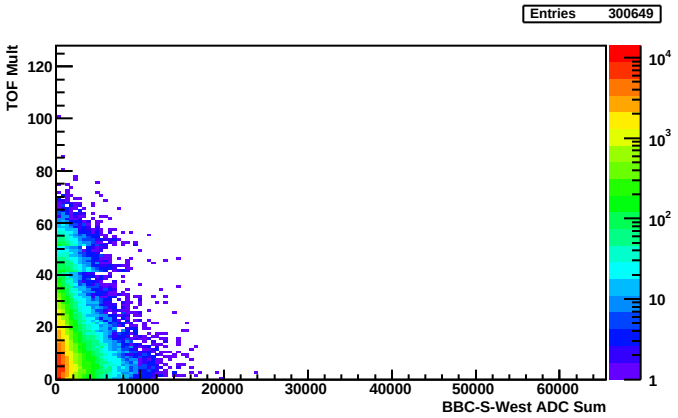

BBQ-VP004 (HI threshold)
Entries 4810384
ADC
QT Input Channel

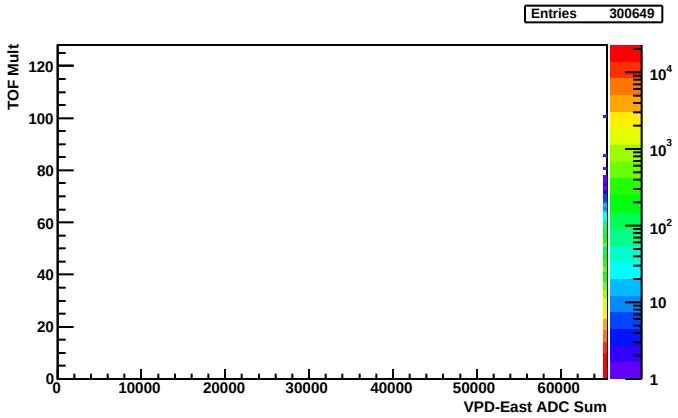

BBQ-VP004 (HI threshold)
Entries 4810384
TAC
QT Input Channel

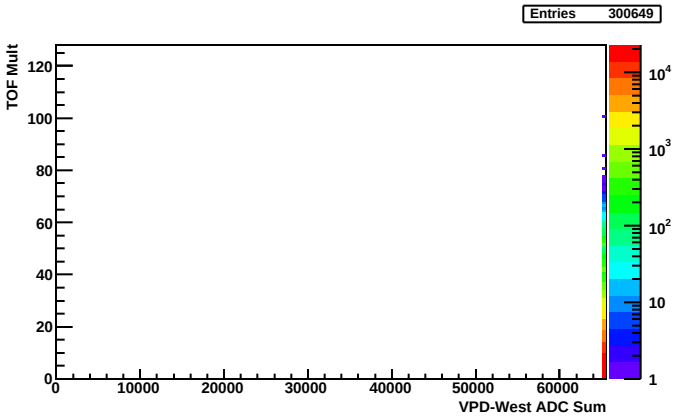

Entries 300649
TOF Mult
BBC-L-East ADC Sum

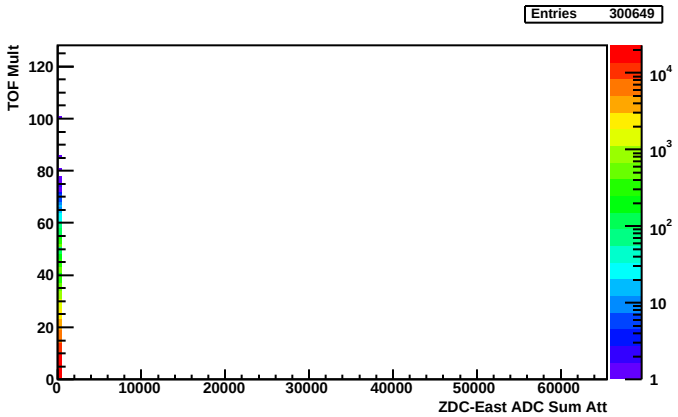

Entries 300649
TOF Mult
BBC-L-West ADC Sum

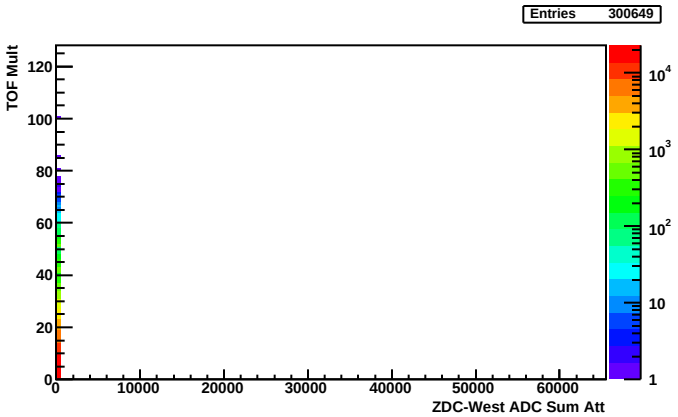

Entries 300649
TOF Mult
BBC-S-East ADC Sum

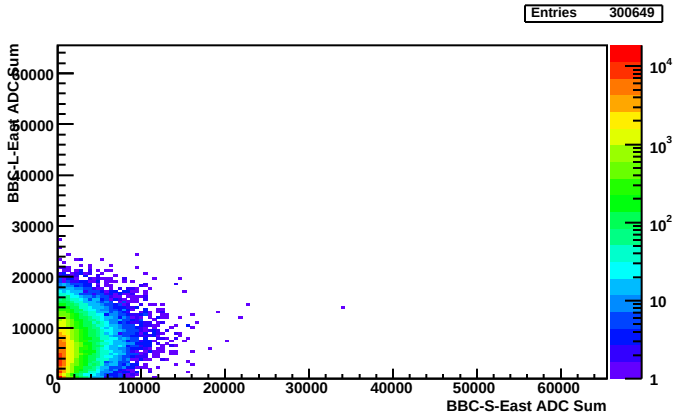

Entries 300649
TOF Mult
BBC-S-West ADC Sum

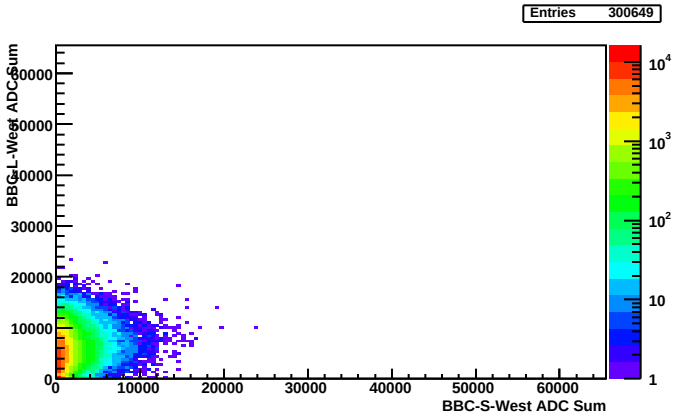

Entries 300649
TOF Mult
VPD-East ADC Sum

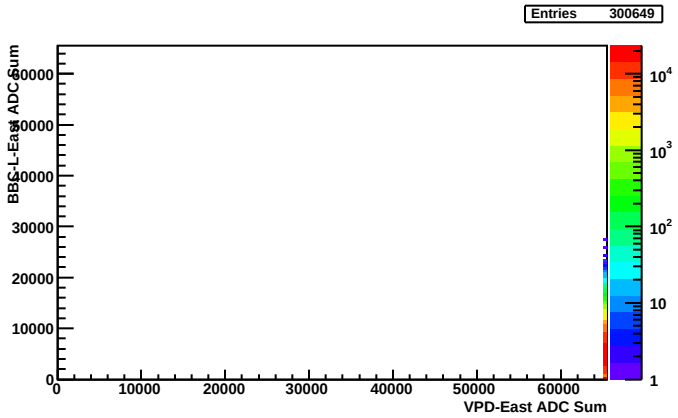

Entries 300649
TOF Mult
VPD-West ADC Sum

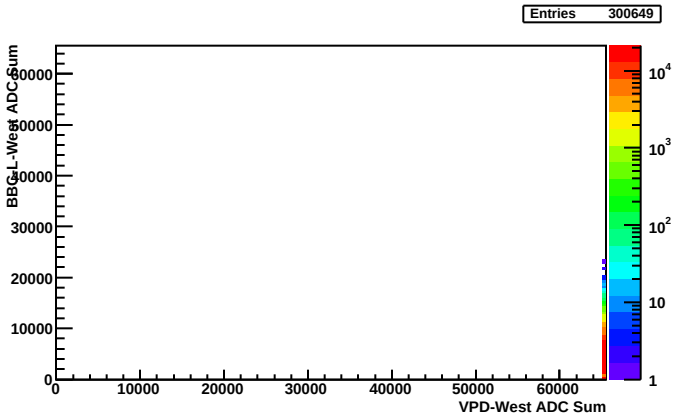

Entries 300649
TOF Mult
ZDC-East ADC Sum Att

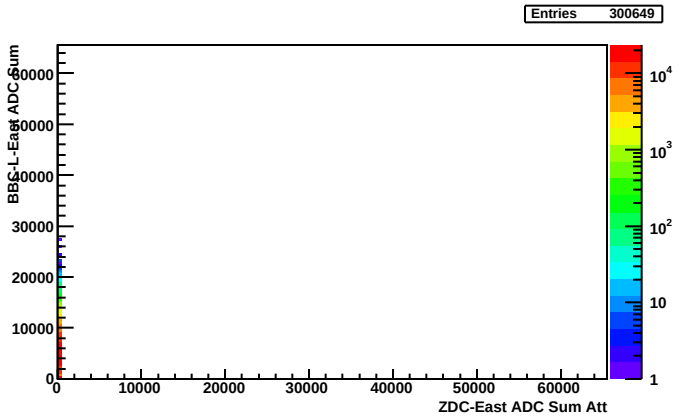

Entries 300649
TOF Mult
ZDC-West ADC Sum Att

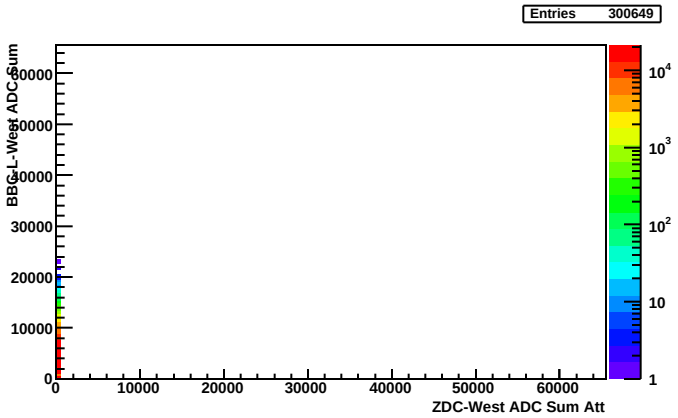

Entries 300649
BBC-L-East ADC Sum
BBC-S-East ADC Sum

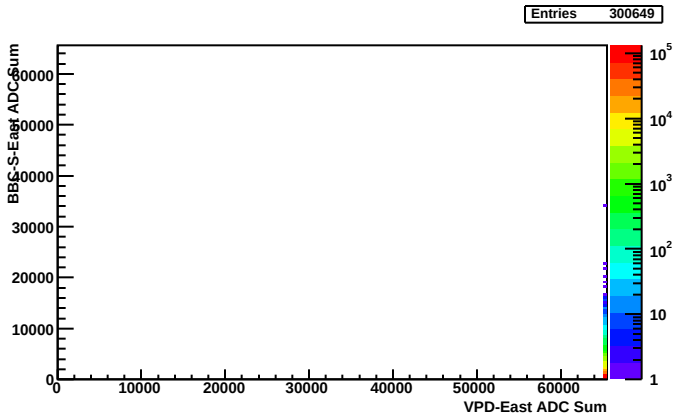

Entries 300649
BBC-L-West ADC Sum
BBC-S-West ADC Sum

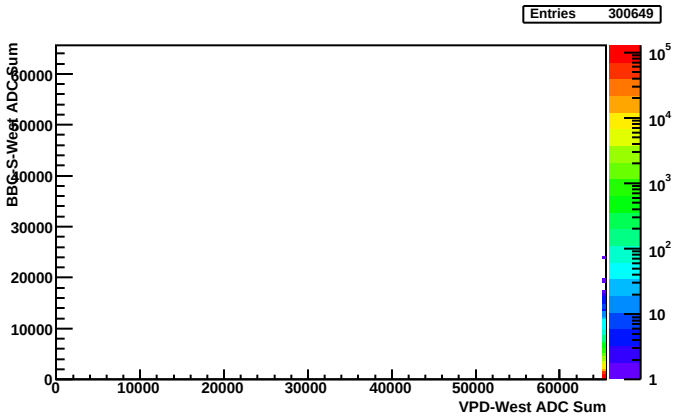

Entries 300649
BBC-L-East ADC Sum
VPD-East ADC Sum

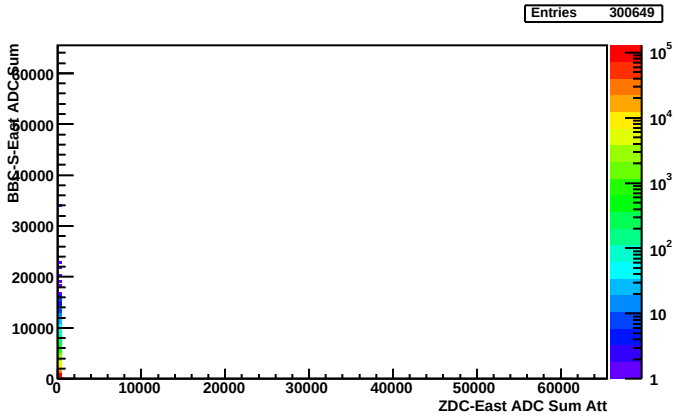

Entries 300649
BBC-L-West ADC Sum
VPD-West ADC Sum

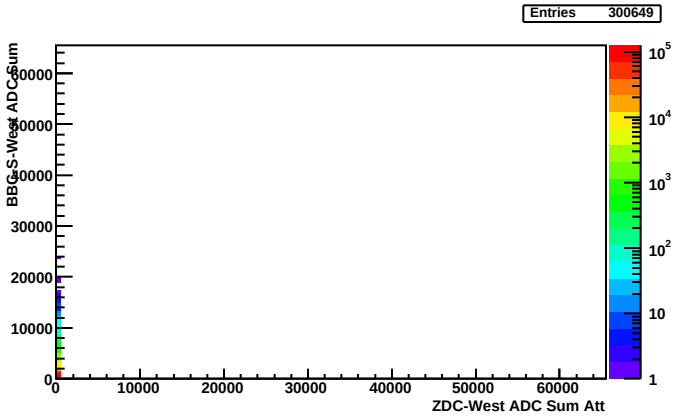

Entries 300649
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

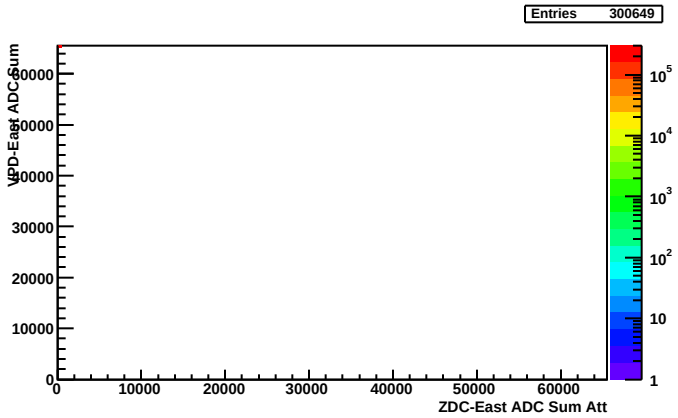

Entries 300649
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

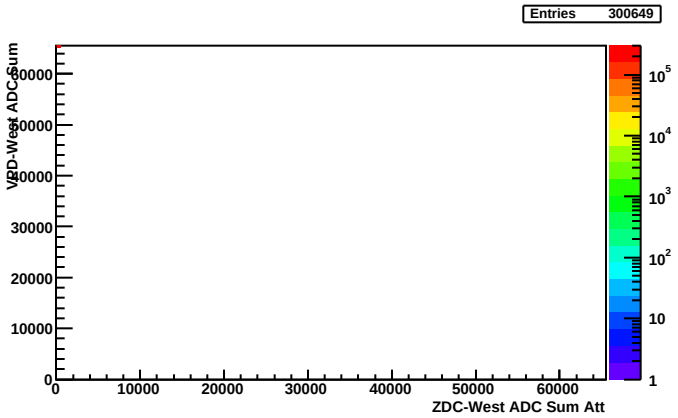

Entries 300649
BBC-S-East ADC Sum
VPD-East ADC Sum

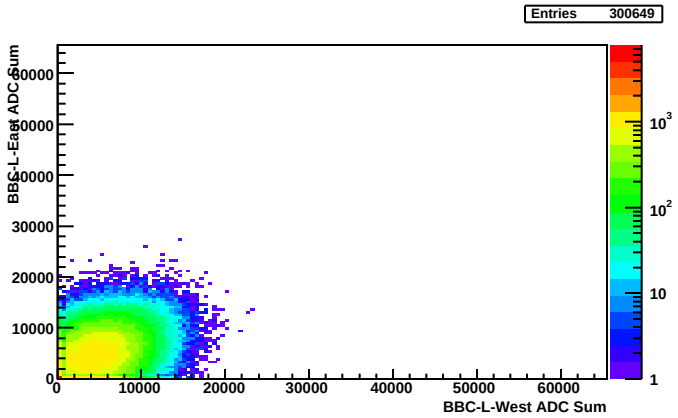

Entries 300649
BBC-S-West ADC Sum
VPD-West ADC Sum

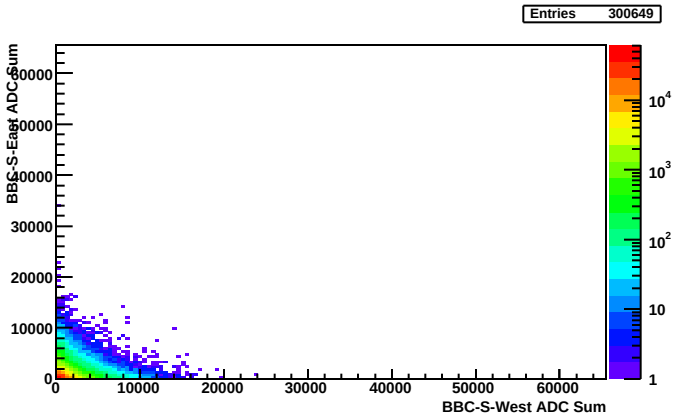

Entries 300649
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

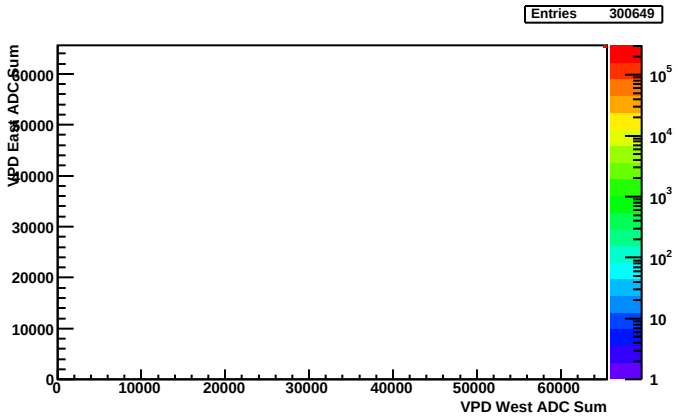

Entries 300649
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

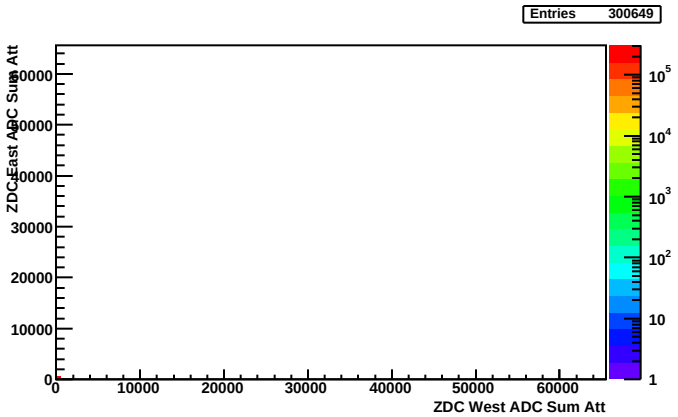

Entries 300649
VPD-East ADC Sum
ZDC-East ADC Sum Att

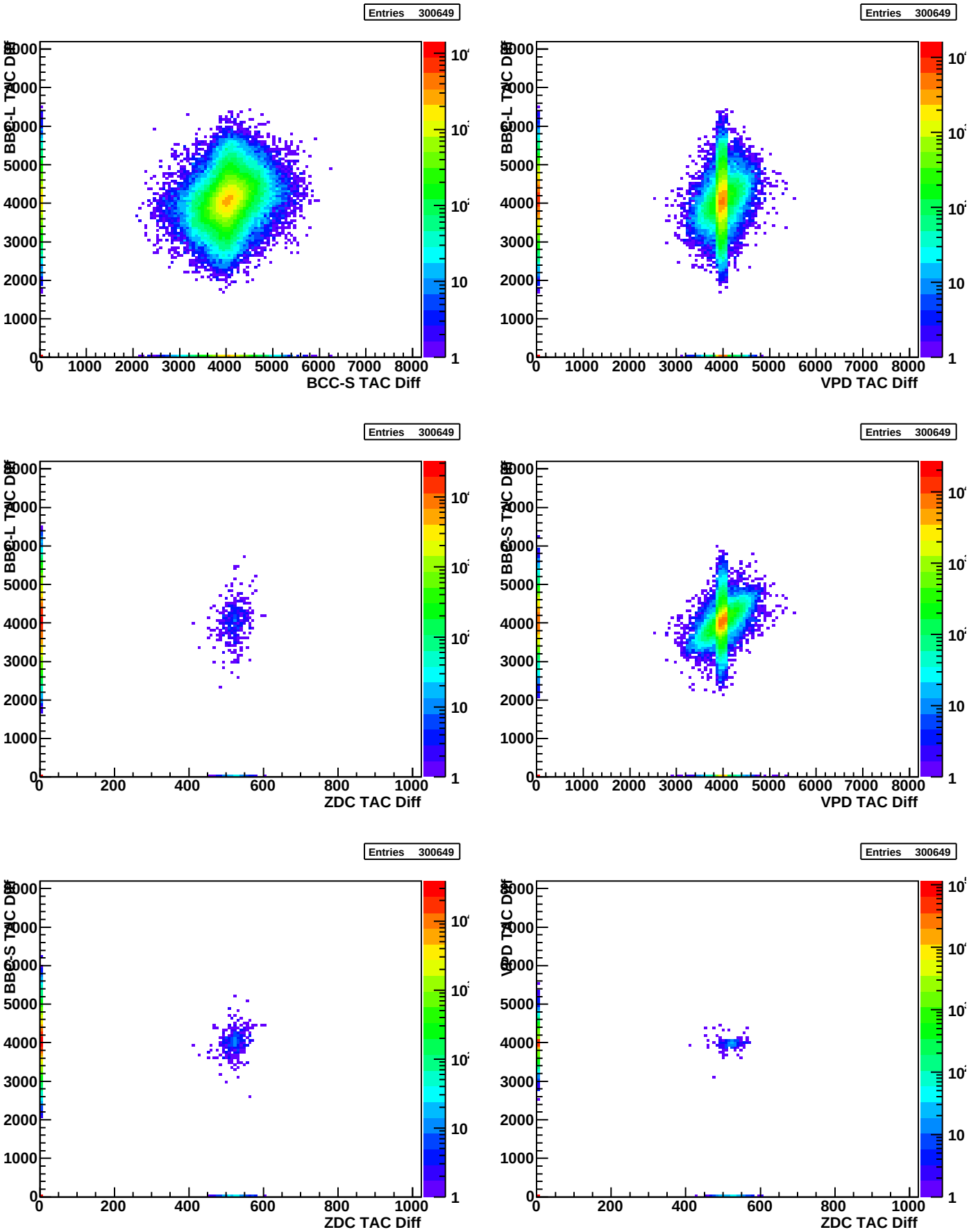

Entries 300649
VPD-West ADC Sum
ZDC-West ADC Sum Att

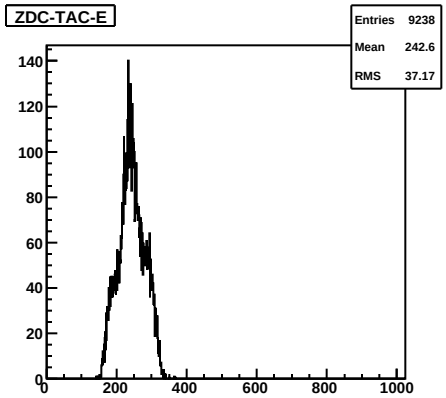

Entries 300649
BBC-L-East ADC Sum
BBC-L-West ADC Sum

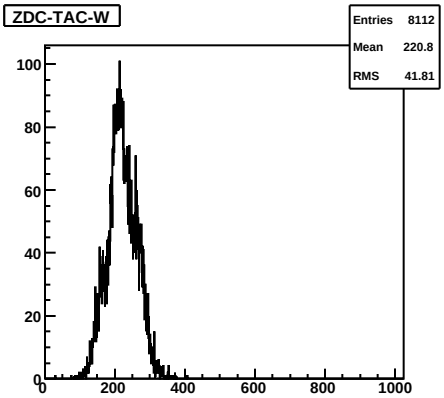

Entries 300649
BBC-S-East ADC Sum
BBC-S-West ADC Sum

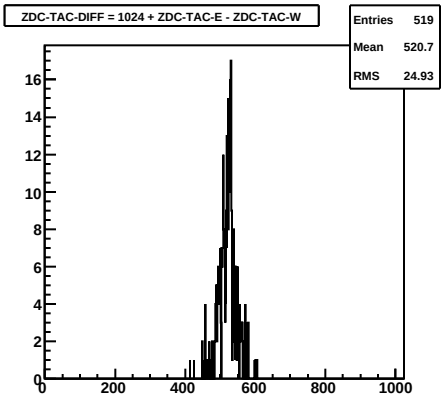

Entries 300649
VPD East ADC Sum
VPD West ADC Sum

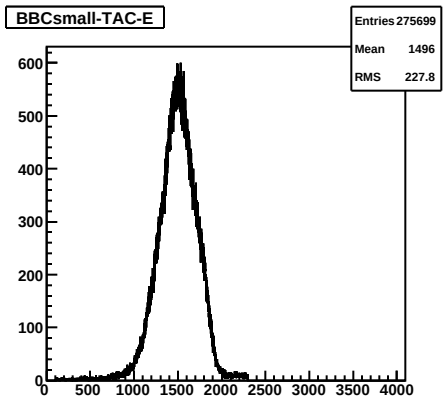

Entries 300649
ZDC East ADC Sum Att
ZDC West ADC Sum Att

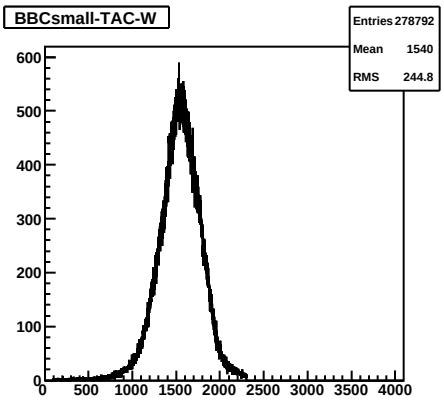

Entries 300649
BBC-L TAC Diff
BCC-S TAC Diff
Entries 300649
BBC-L TAC Diff
VPD TAC Diff
Entries 300649
BBC-L TAC Diff
ZDC TAC Diff
Entries 300649
BBC-S TAC Diff
VPD TAC Diff
Entries 300649
BBC-S TAC Diff
ZDC TAC Diff
Entries 300649
VPD TAC Diff
ZDC TAC Diff

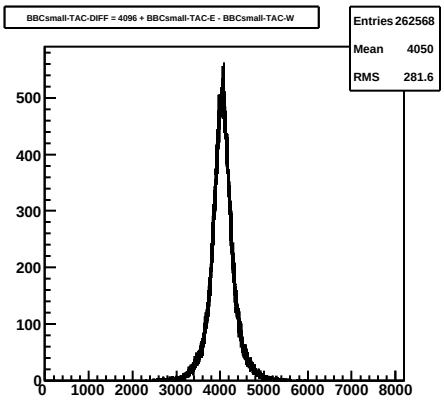

ZDC-TAC-E
Entries 9238
Mean 242.6
RMS 37.17

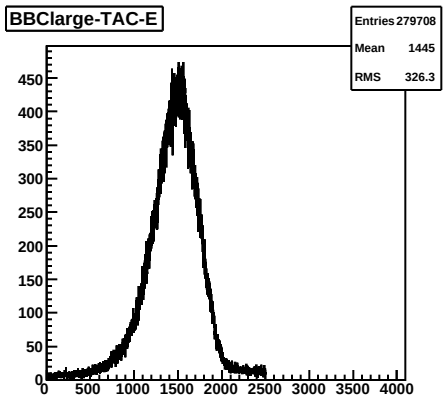

ZDC-TAC-W
Entries 8112
Mean 220.8
RMS 41.81

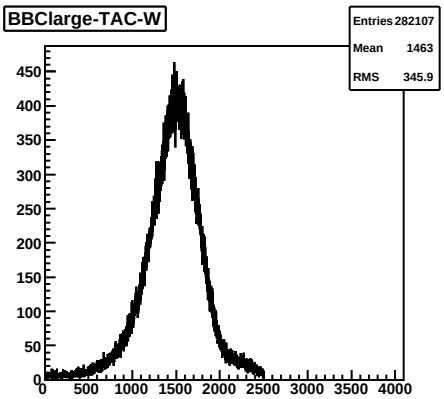

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 519
Mean 520.7
RMS 24.93

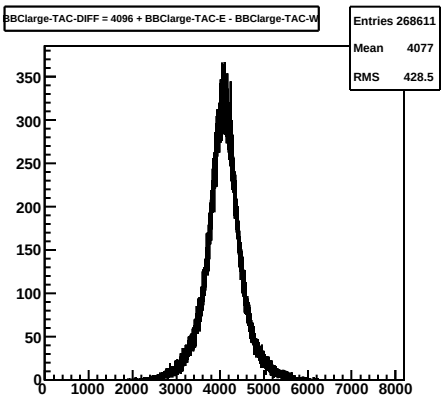

BBCsmall-TAC-E
Entries 275699
Mean 1496
RMS 227.8

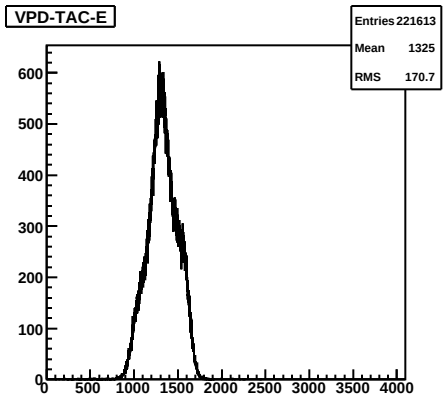

BBCsmall-TAC-W
Entries 278792
Mean 1540
RMS 244.8

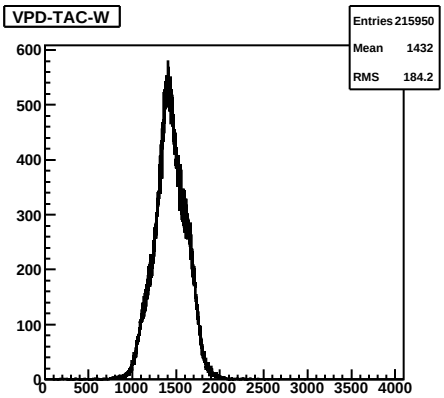

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 262568
Mean 4050
RMS 281.6

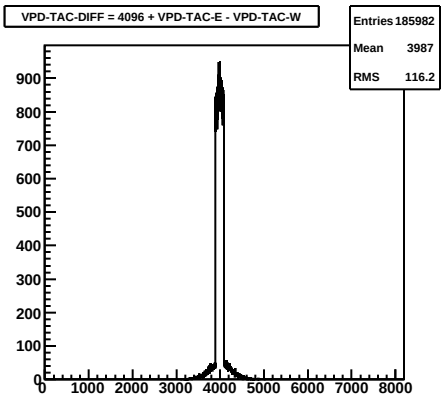

BBClarge-TAC-E
Entries 279708
Mean 1445
RMS 326.3

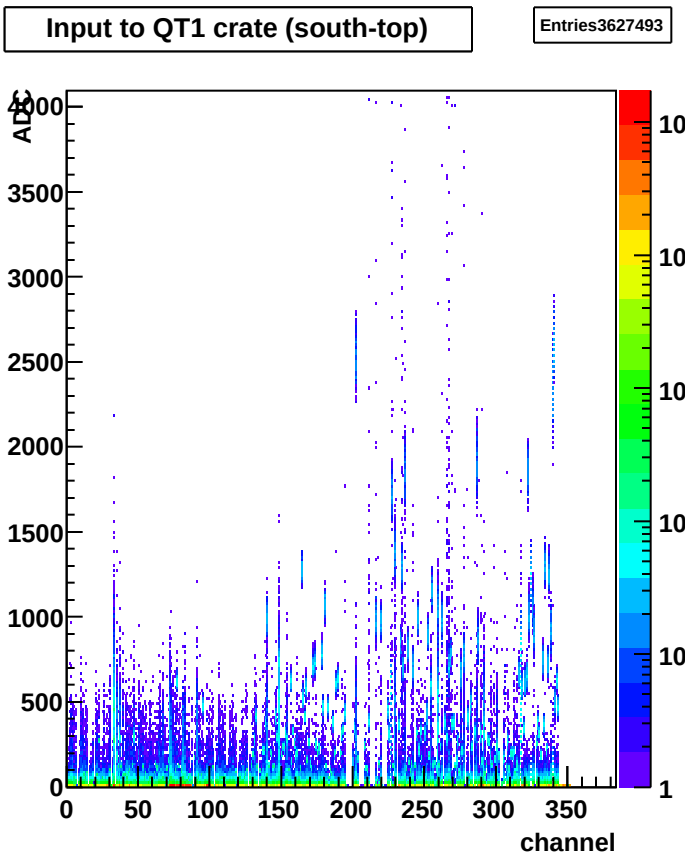

BBClarge-TAC-W
Entries 282107
Mean 1463
RMS 345.9

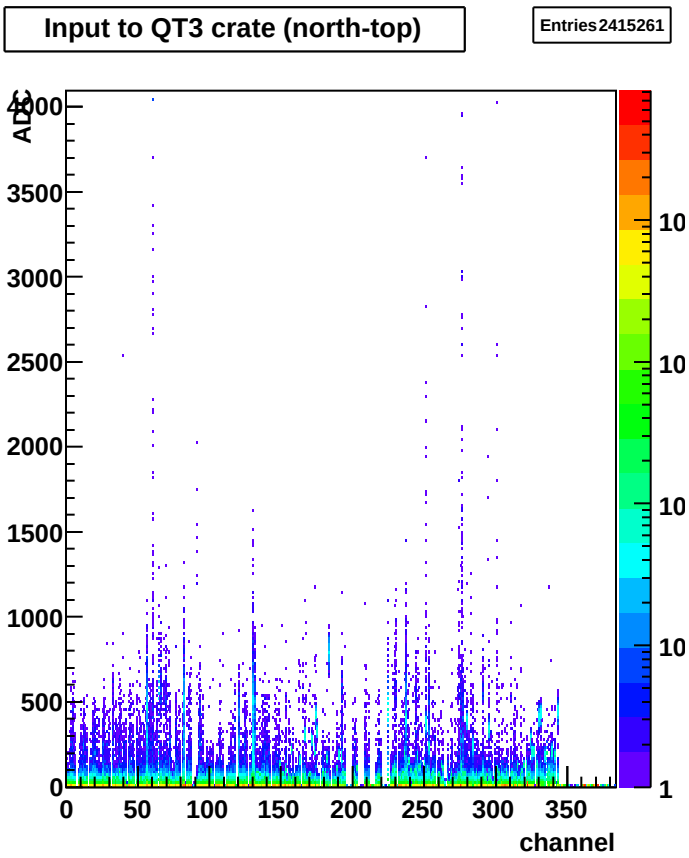

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 268611
Mean 4077
RMS 428.5

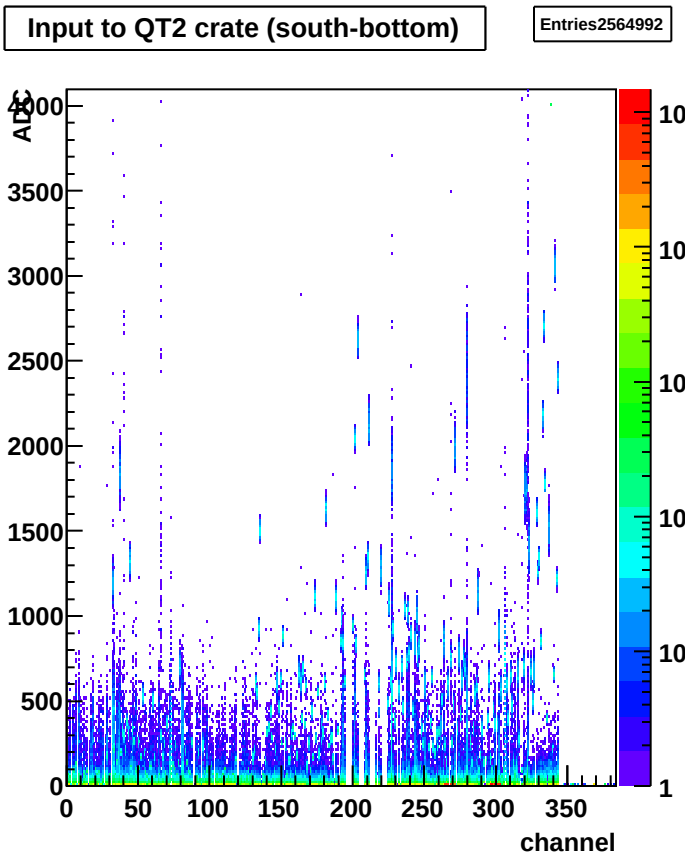

VPD-TAC-E
Entries 221613
Mean 1325
RMS 170.7

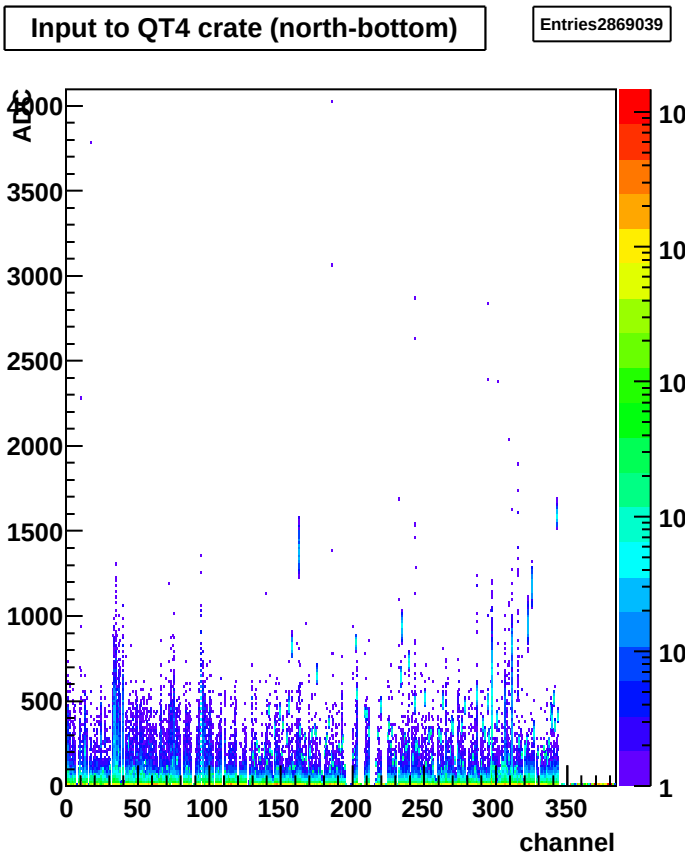

VPD-TAC-W
Entries 215950
Mean 1432
RMS 184.2

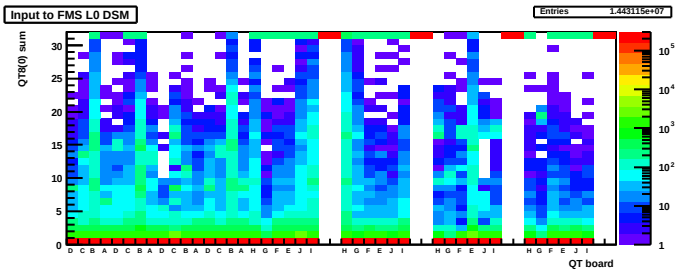

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 185982
Mean 3987
RMS 116.2

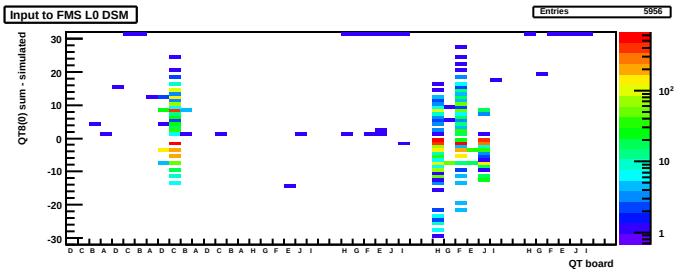

Input to QT1 crate (south-top)
Entries3627493
ADC
channel

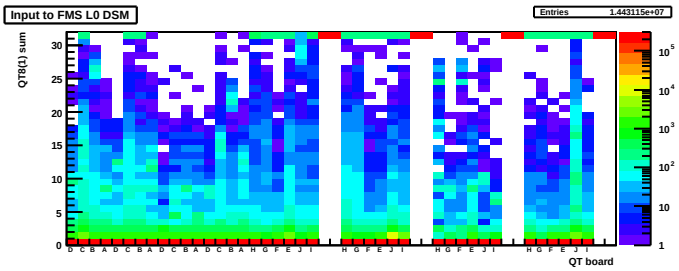

Input to QT3 crate (north-top)
Entries2415261
ADC
channel

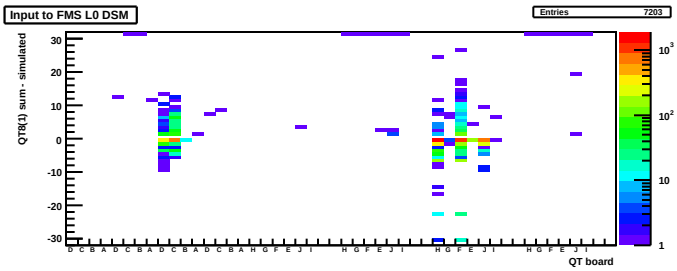

Input to QT2 crate (south-bottom)
Entries2564992
ADC
channel

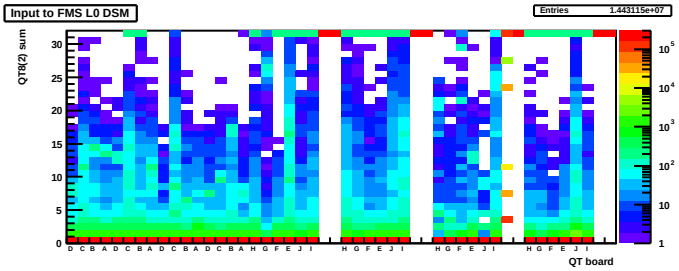

Input to QT4 crate (north-bottom)
Entries2869039
ADC
channel

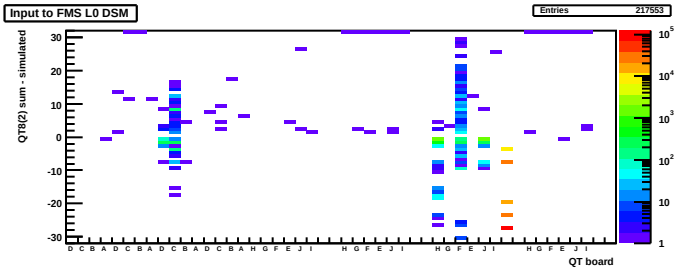

Input to FMS L0 DSM
Entries 1.443115e+07
QT8(0) sum
QT board

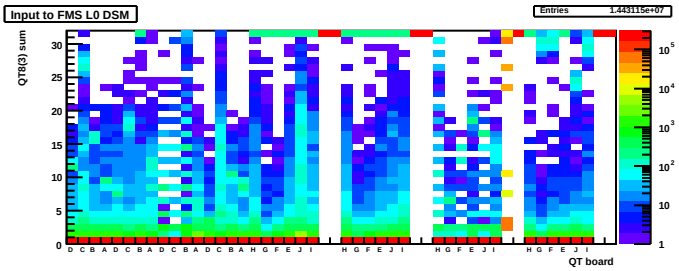

Input to FMS L0 DSM
Entries 5956
QT8(0) sum - simulated
QT board

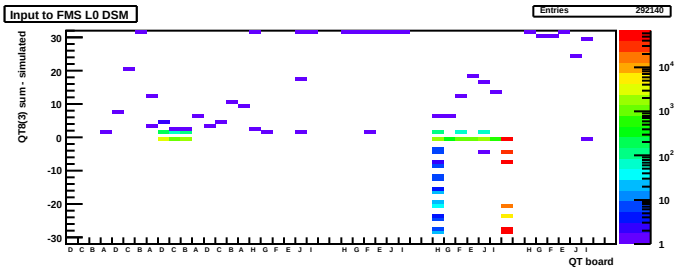

Input to FMS L0 DSM
Entries 1.443115e+07
QT8(1) sum
QT board

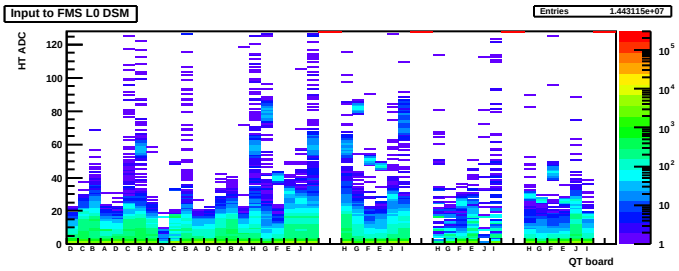

Input to FMS L0 DSM
Entries 7203
QT8(1) sum - simulated
QT board

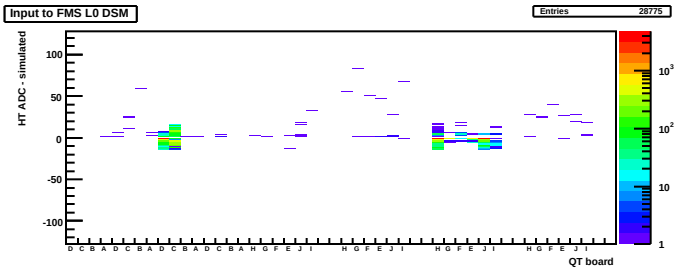

Input to FMS L0 DSM
Entries 1.443115e+07
QT8(2) sum
QT board

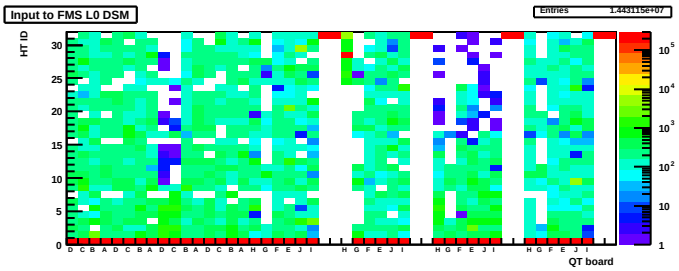

Input to FMS L0 DSM
Entries 217553
QT8(2) sum - simulated
QT board

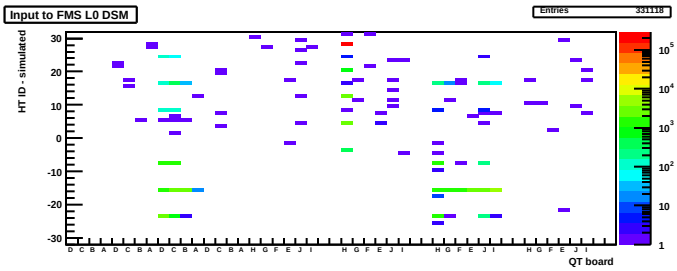

Input to FMS L0 DSM
Entries 1.443115e+07
QT8(3) sum
QT board

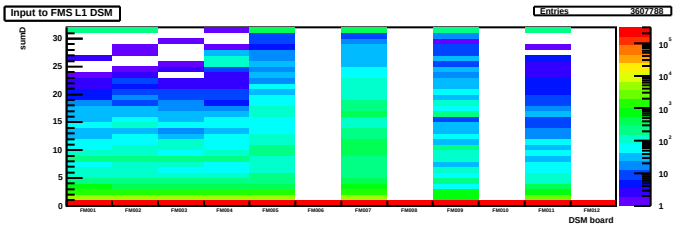

Input to FMS L0 DSM
Entries 292140
QT8(3) sum - simulated
QT board

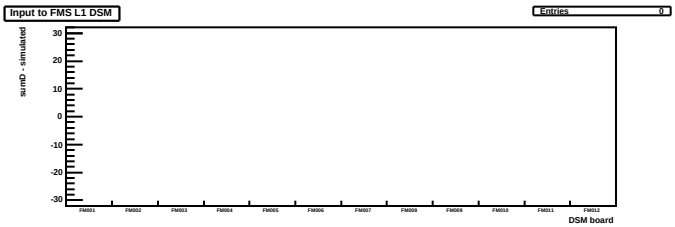

Input to FMS L0 DSM
Entries 1.443115e+07
HT ADC
QT board

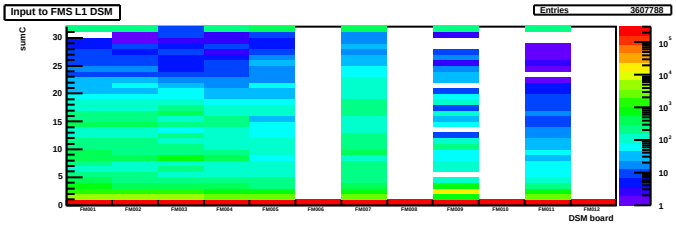

Input to FMS L0 DSM
Entries 28775
HT ADC - simulated
QT board

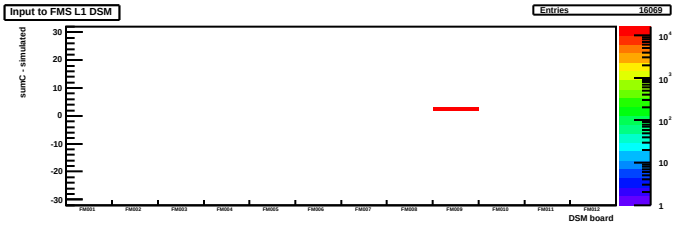

Input to FMS L0 DSM
Entries 1.443115e+07
HT ID
QT board

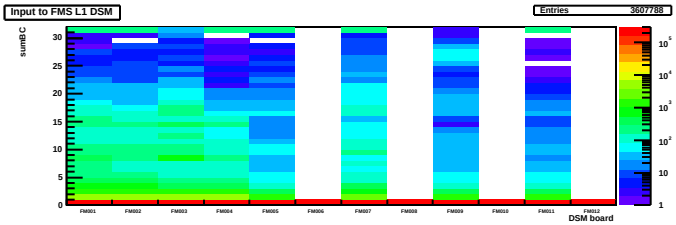

Input to FMS L0 DSM
Entries 331118
HT ID - simulated
QT board

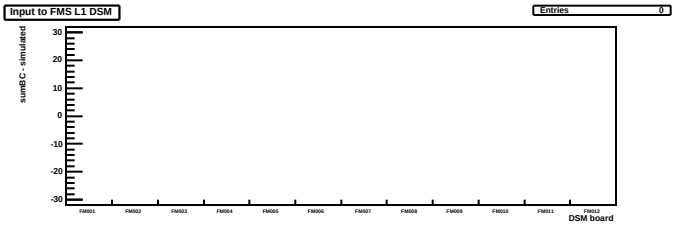

Input to FMS L1 DSM
Entries 3607788
sumD
DSM board

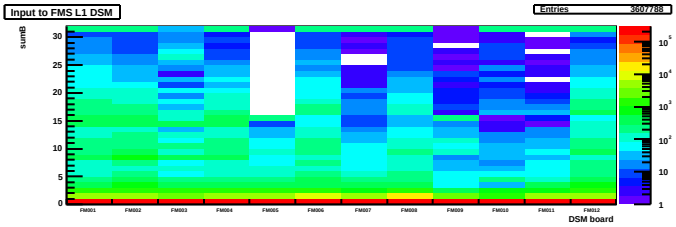

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

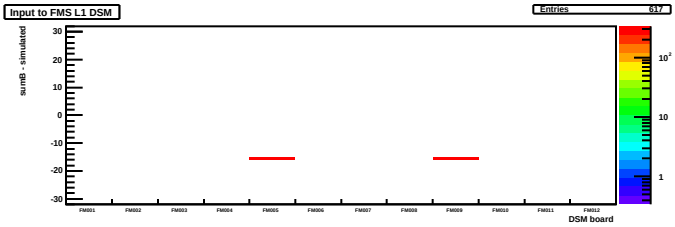

Input to FMS L1 DSM
Entries 3607788
sumC
DSM board

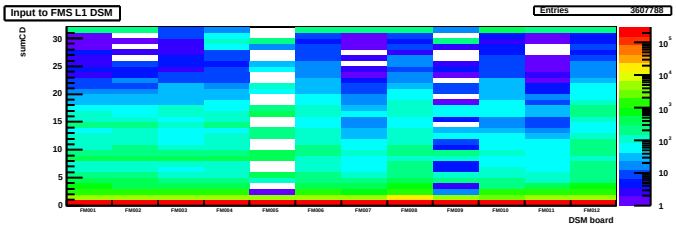

Input to FMS L1 DSM
Entries 16069
sumC - simulated
DSM board

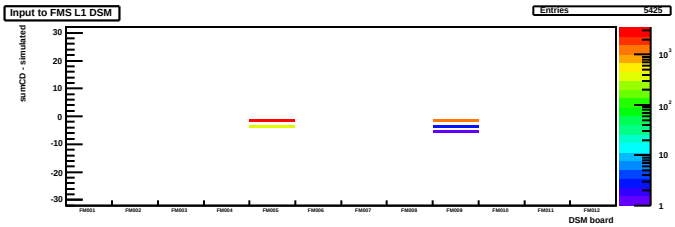

Input to FMS L1 DSM
Entries 3607788
sumBC
DSM board

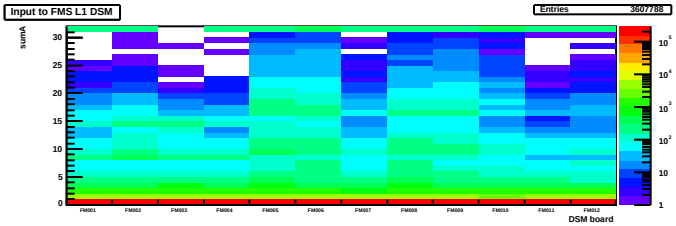

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

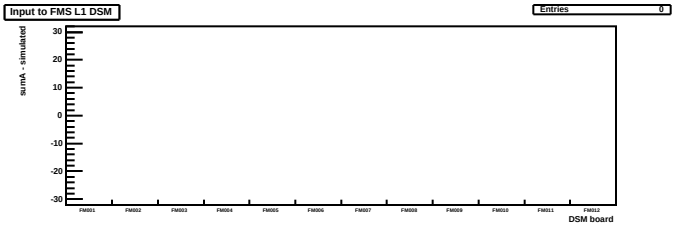

Input to FMS L1 DSM
Entries 3607788
sumB
DSM board

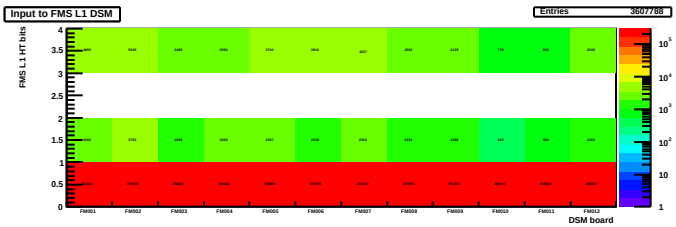

Input to FMS L1 DSM
Entries 617
sumB - simulated
DSM board

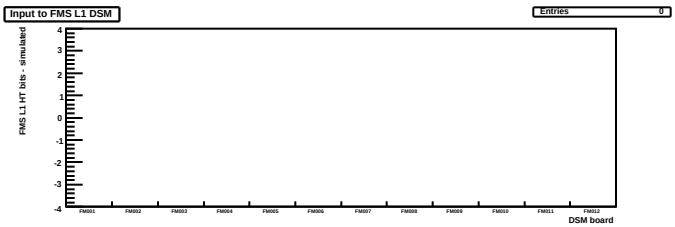

Input to FMS L1 DSM
Entries 3607788
sumCD
DSM board

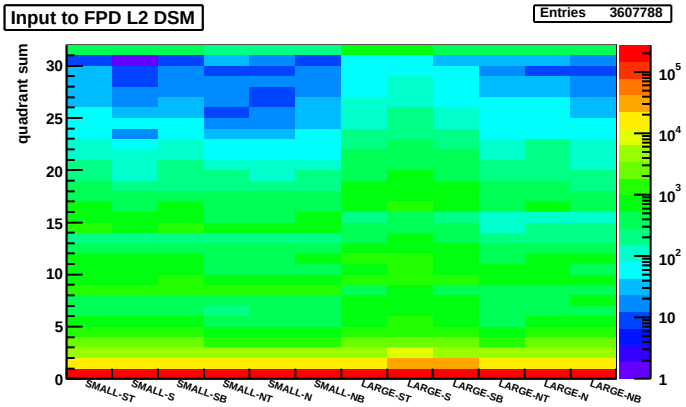

Input to FMS L1 DSM
Entries 5425
sumCD - simulated
DSM board

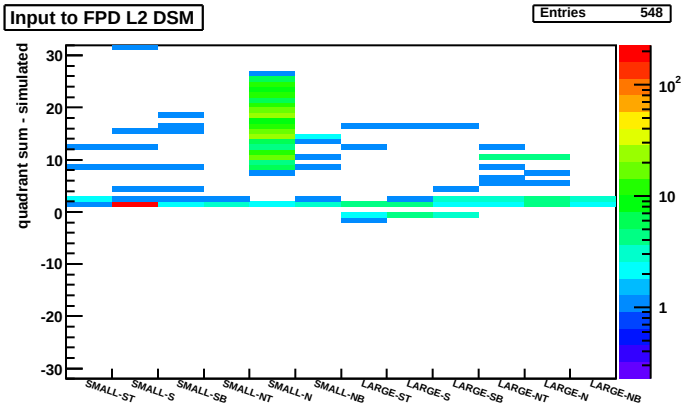

Input to FMS L1 DSM
Entries 3607788
sumA
DSM board

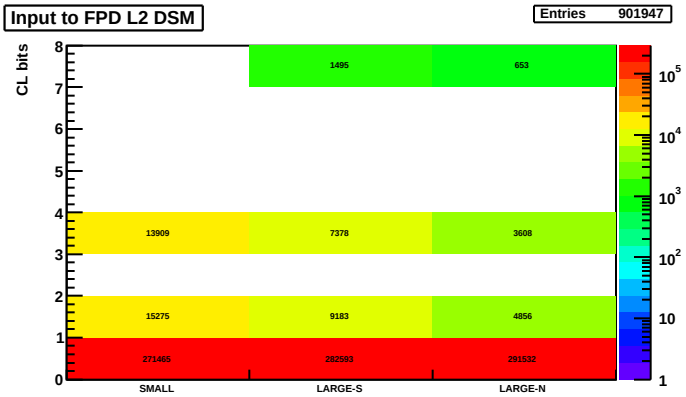

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

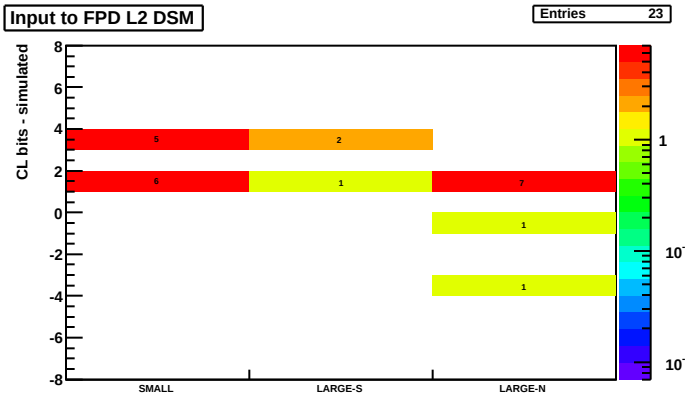

Input to FMS L1 DSM
Entries 3607788
FMS L1 HT bits
DSM board

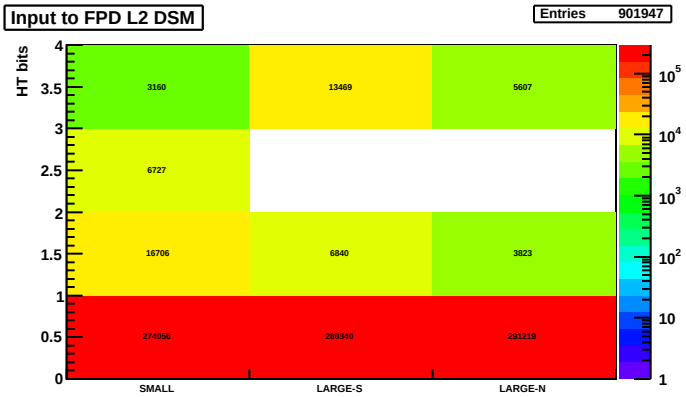

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

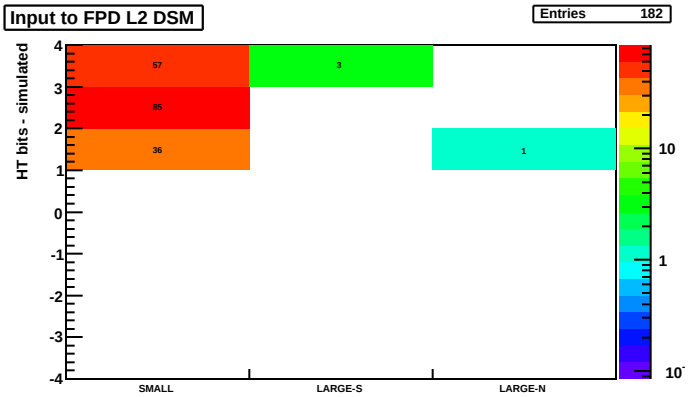

Input to FPD L2 DSM
Entries 3607788
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

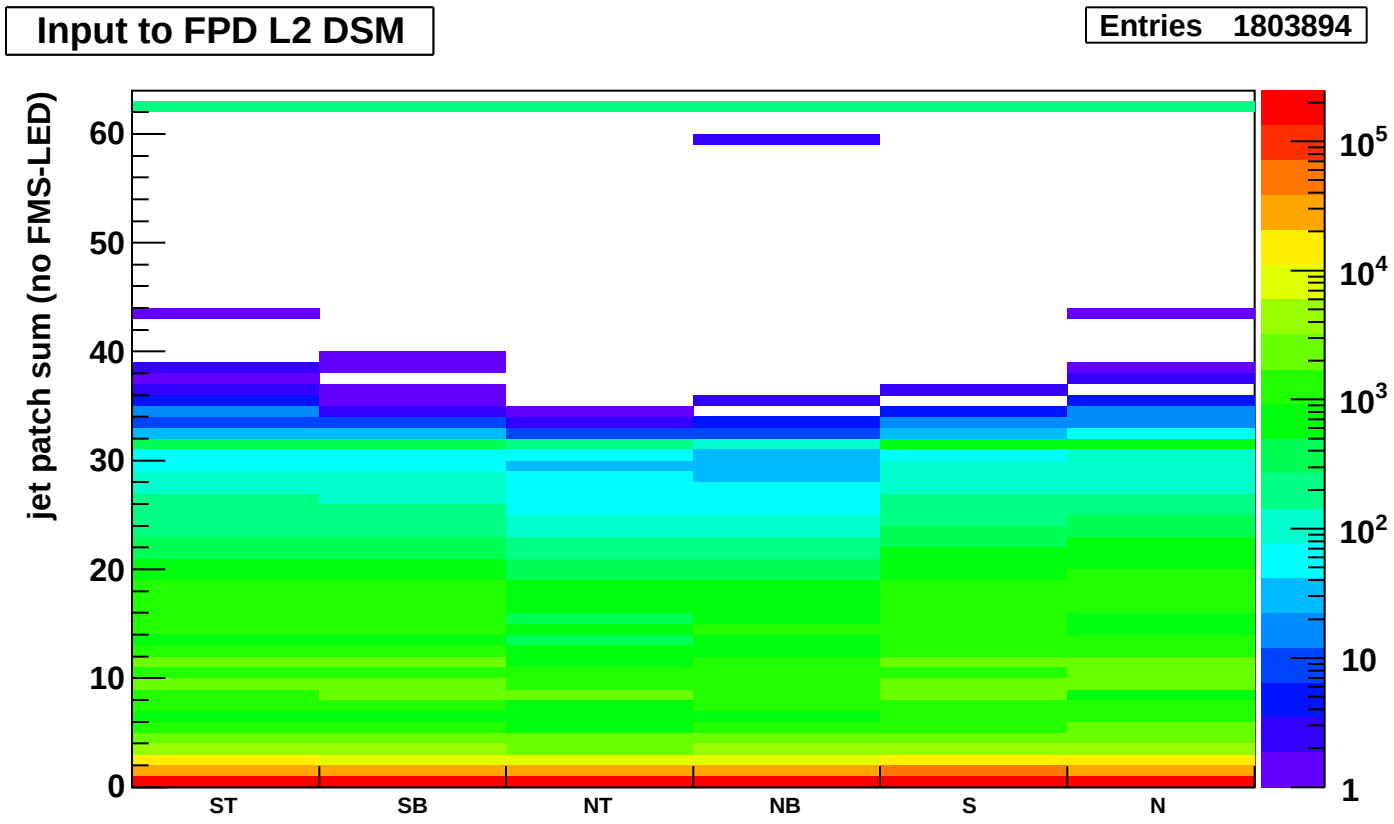

Input to FPD L2 DSM
Entries 548
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

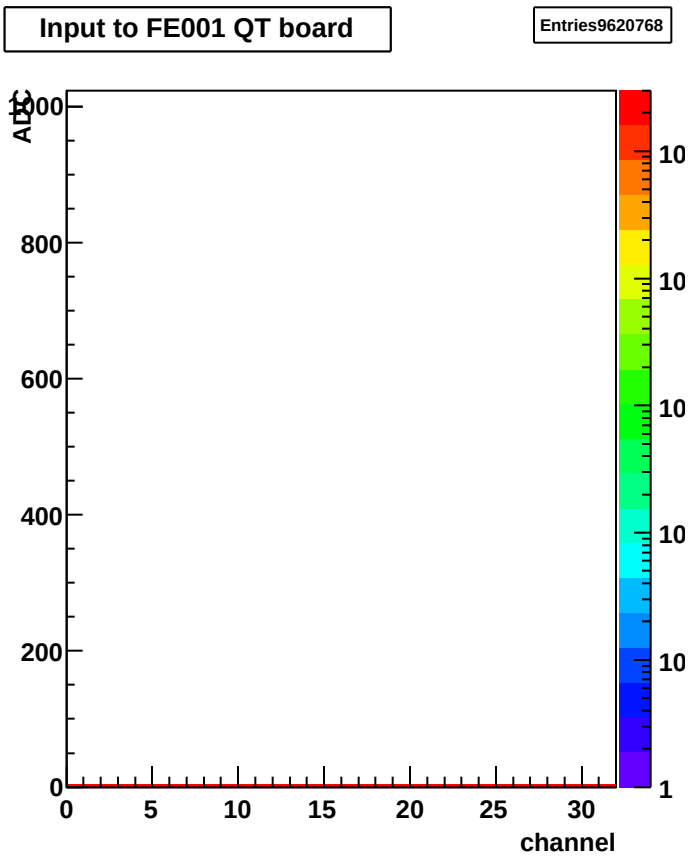

Input to FPD L2 DSM
Entries 901947
CL bits
1495
653
13909
7378
3608
15275
9183
4856
271465
282593
291532
SMALL
LARGE-S
LARGE-N

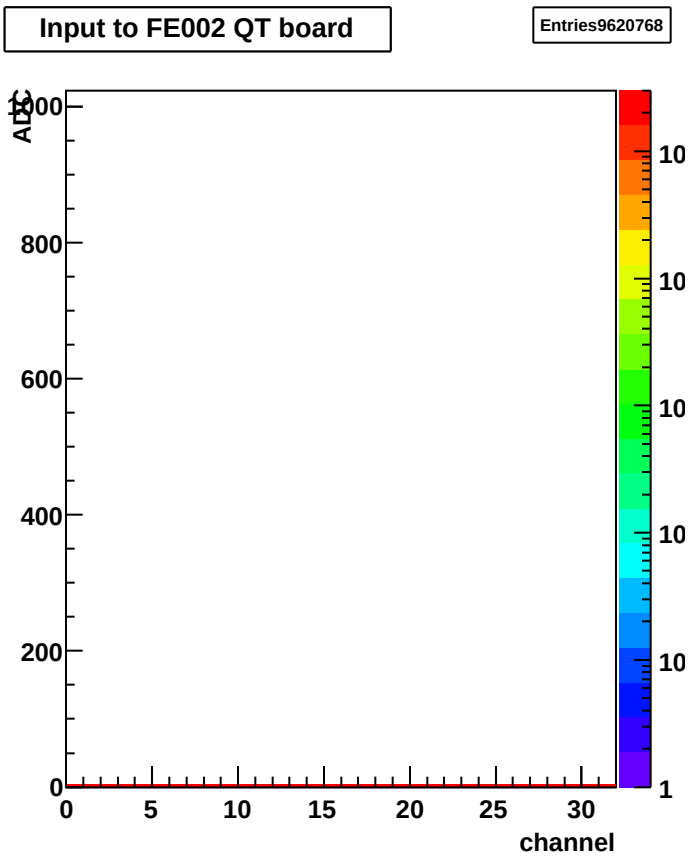

Input to FPD L2 DSM
Entries 23
CL bits - simulated
5
2
6
1
7
1
1
SMALL
LARGE-S
LARGE-N

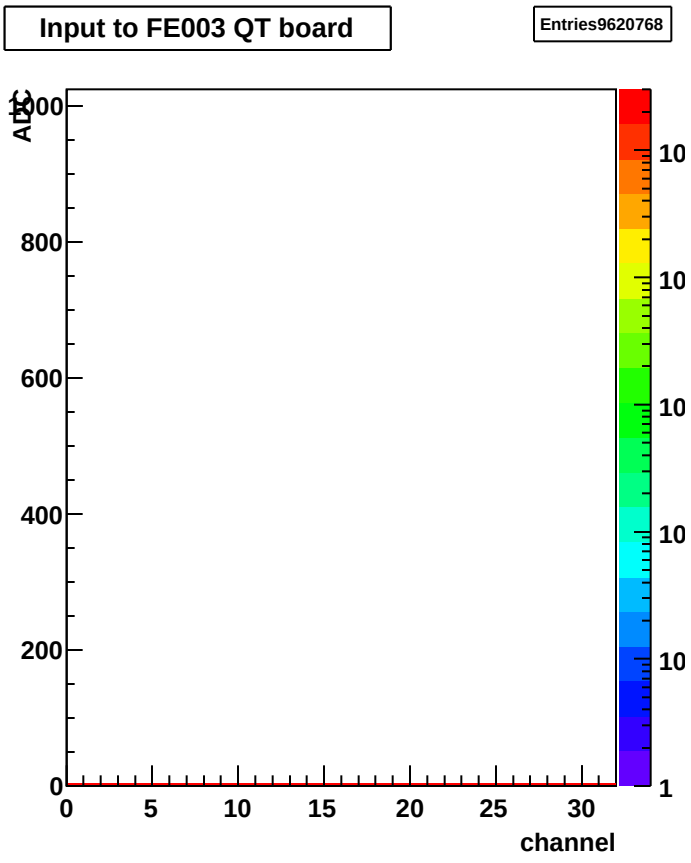

Input to FPD L2 DSM
Entries 901947
HT bits
3160
13469
5607
6727
16706
6840
3823
274056
280340
291219
SMALL
LARGE-S
LARGE-N

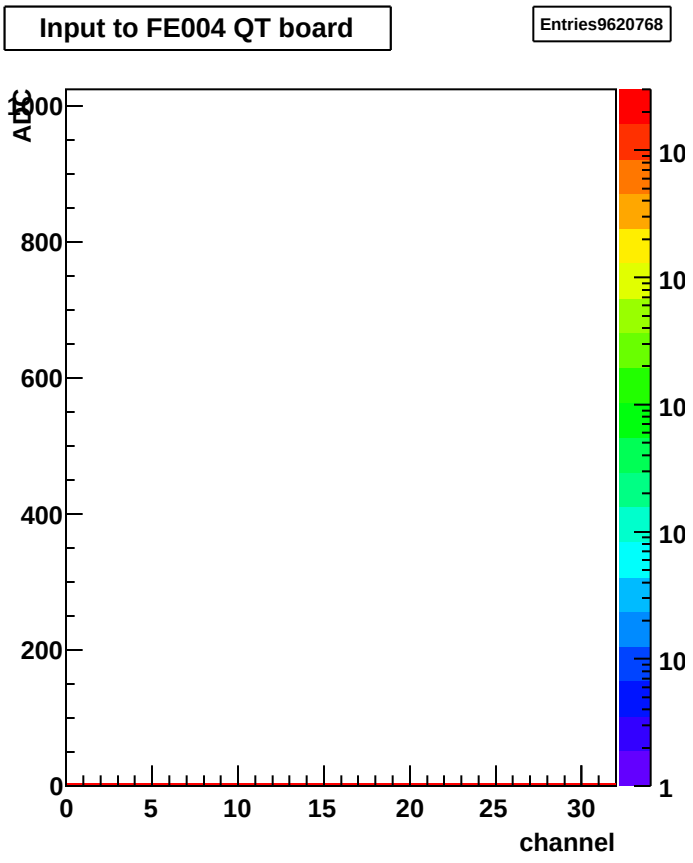

Input to FPD L2 DSM
Entries 182
HT bits - simulated
57
3
85
36
1
SMALL
LARGE-S
LARGE-N

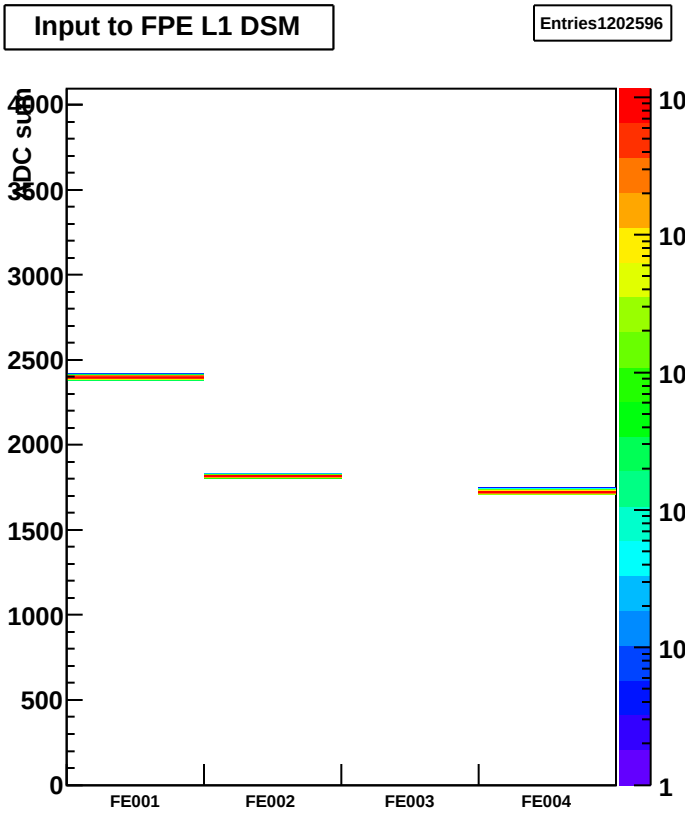

Input to FPD L2 DSM
Entries 1803894
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries9620768
ADC
channel

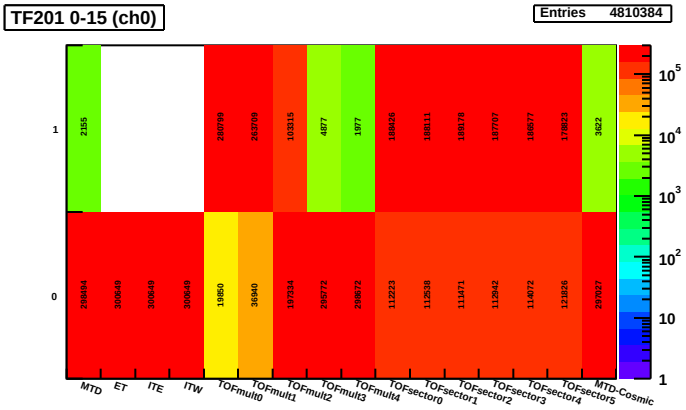

Input to FE002 QT board
Entries9620768
ADC
channel


Input to FE003 QT board
Entries9620768
ADC
channel

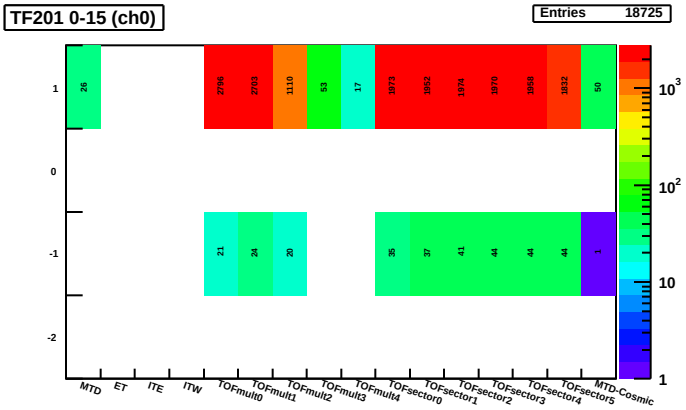

Input to FE004 QT board
Entries9620768
ADC
channel


Input to FPE L1 DSM
Entries1202596
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

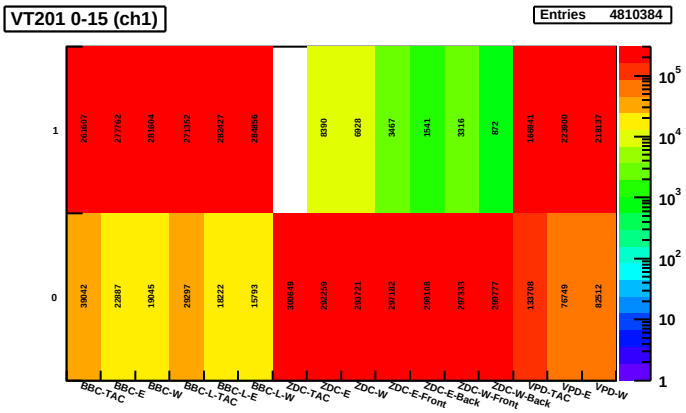
TF201 0-15 (ch0)



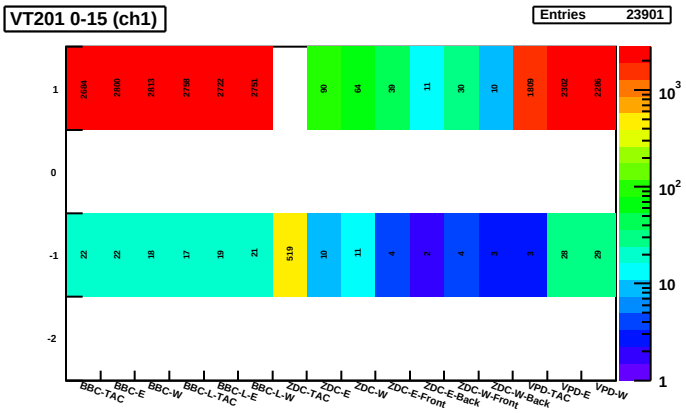
TF201 0-15 (ch0)



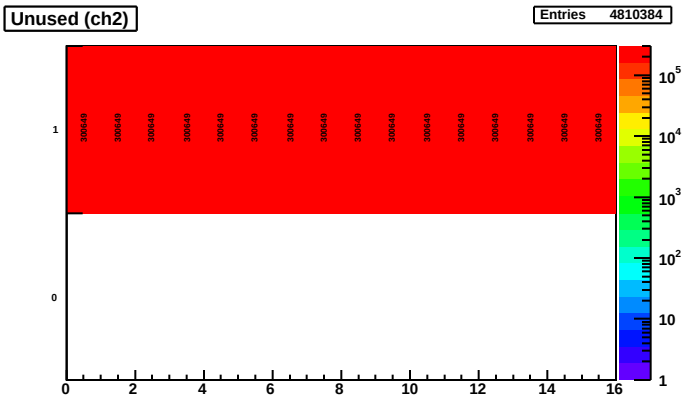
VT201 0-15 (ch1)

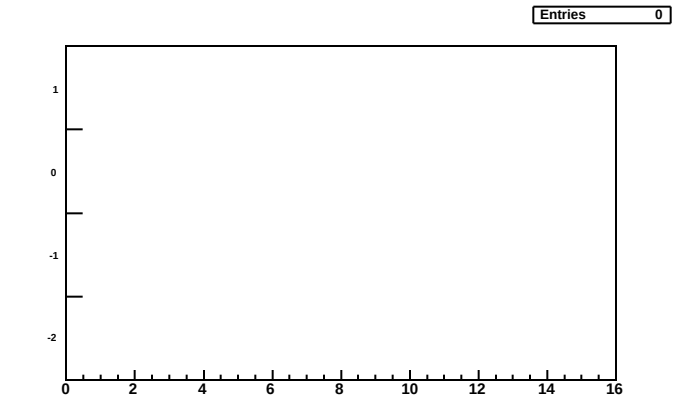


VT201 0-15 (ch1)

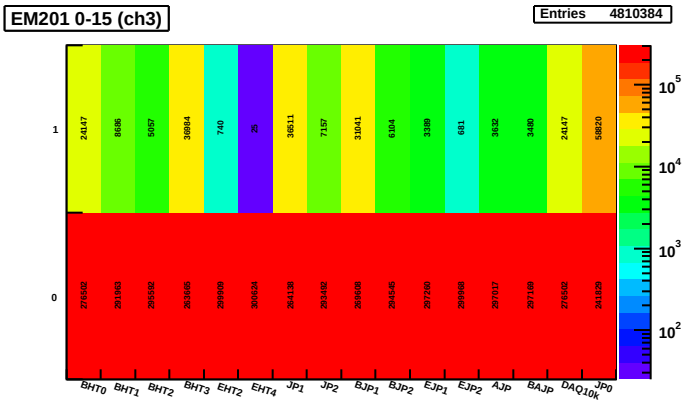


Unused (ch2)

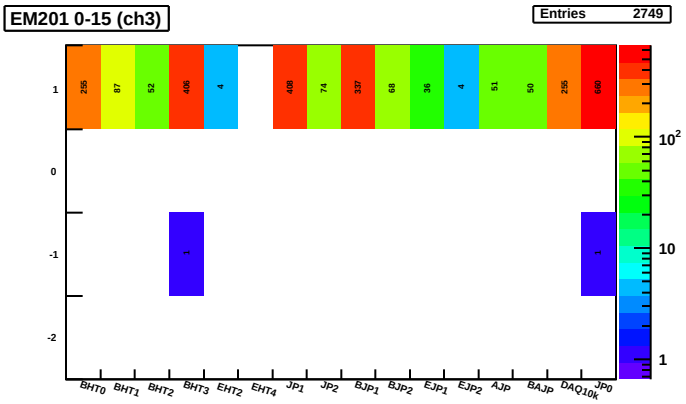




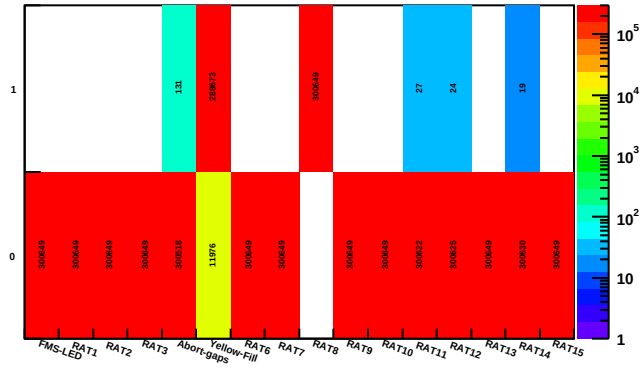
EM201 0-15 (ch3)

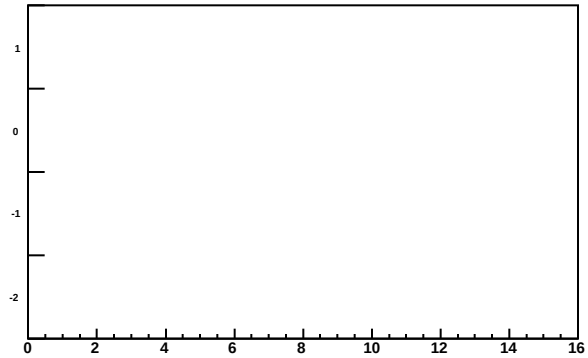


EM201 0-15 (ch3)

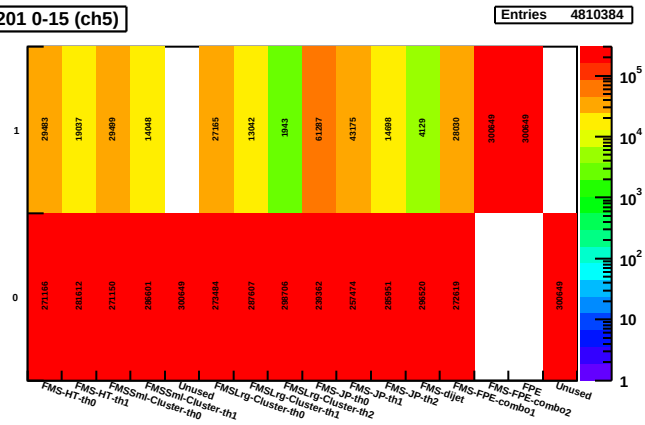


RAT board (ch4)

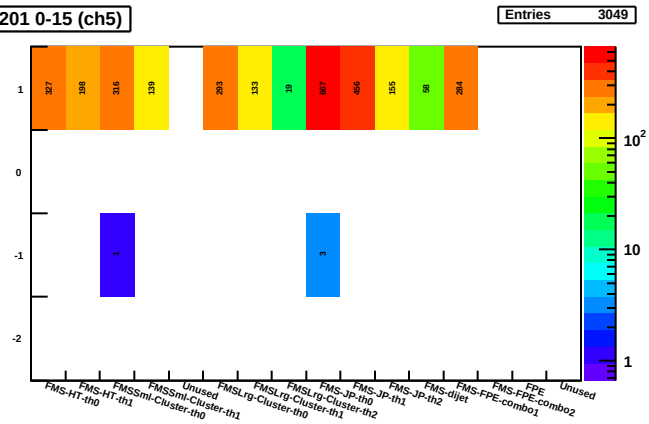




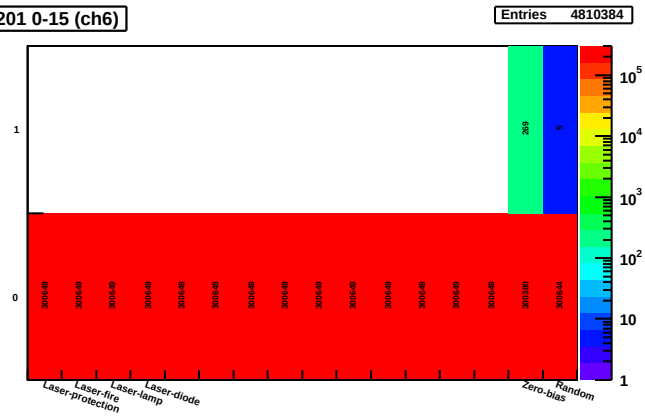
FP201 0-15 (ch5)

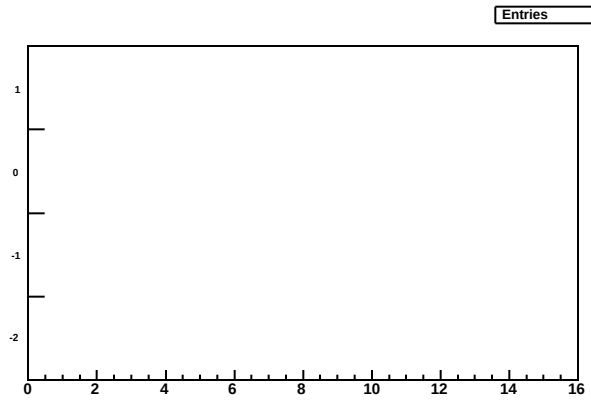
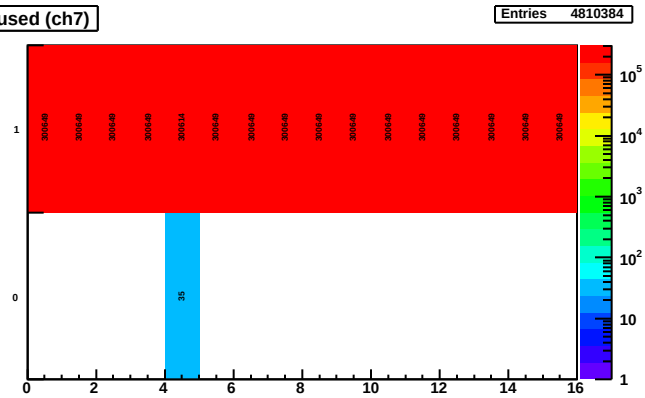
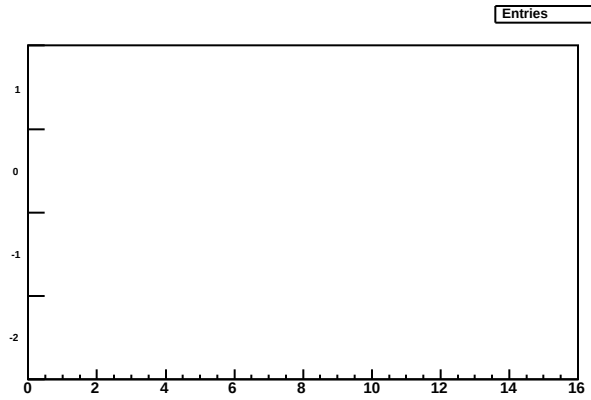


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)