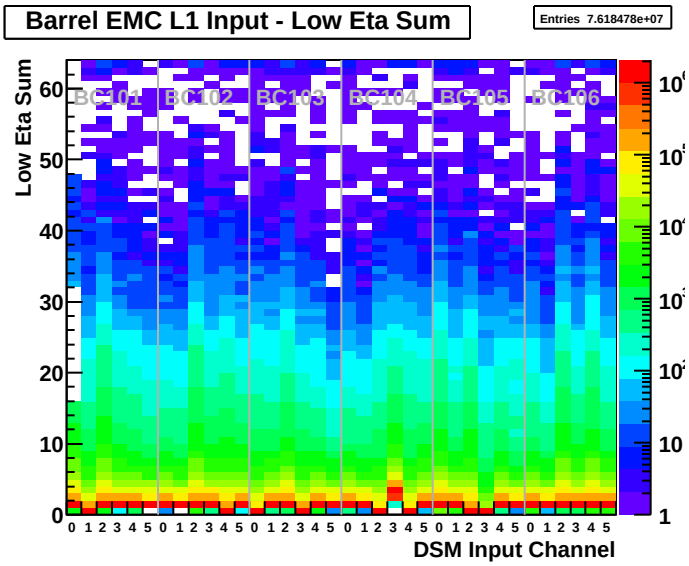
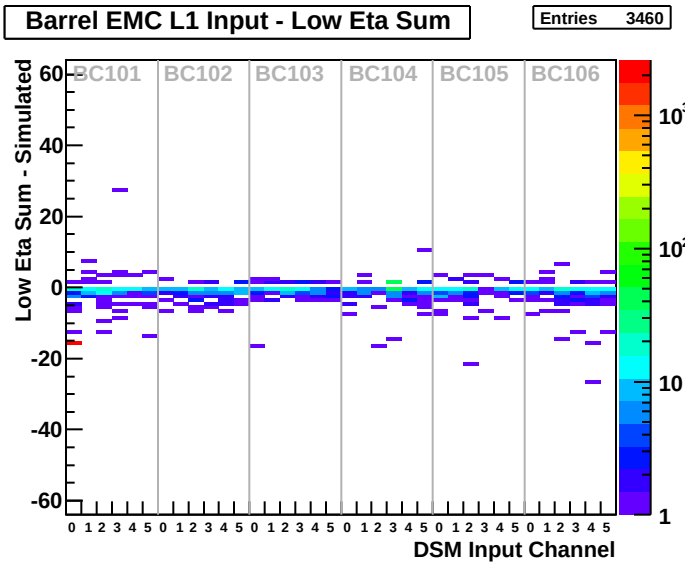
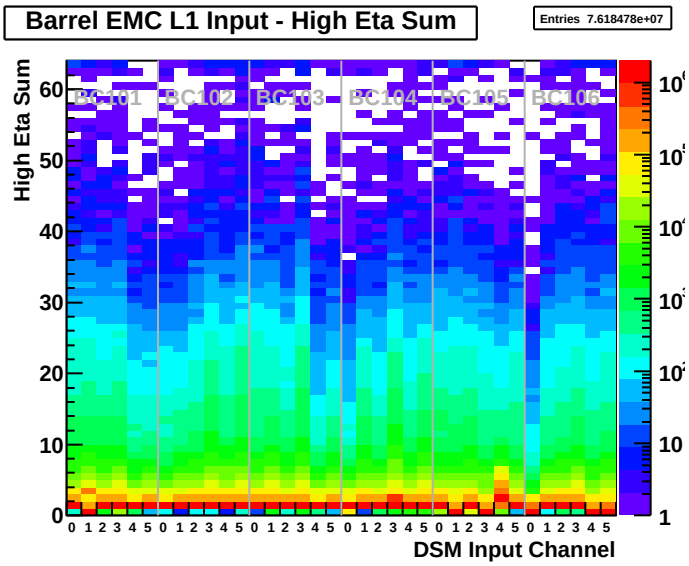
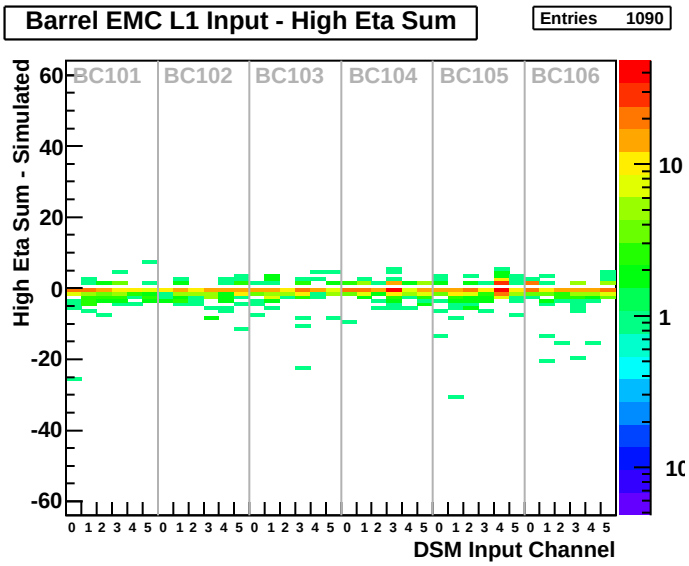
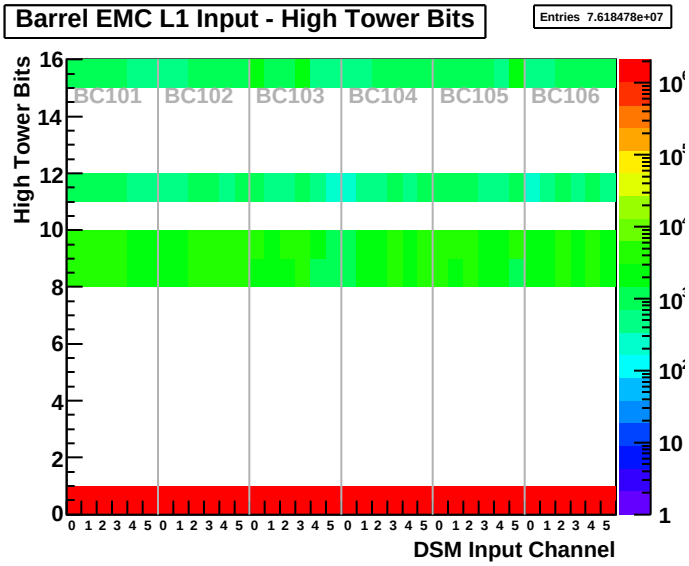
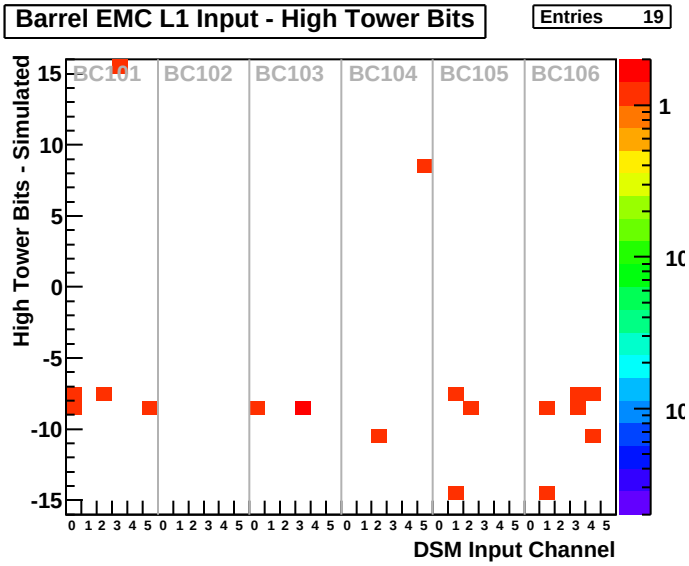

Barrel EMC L1 Input - Low Eta Sum
Entries 7.618478e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

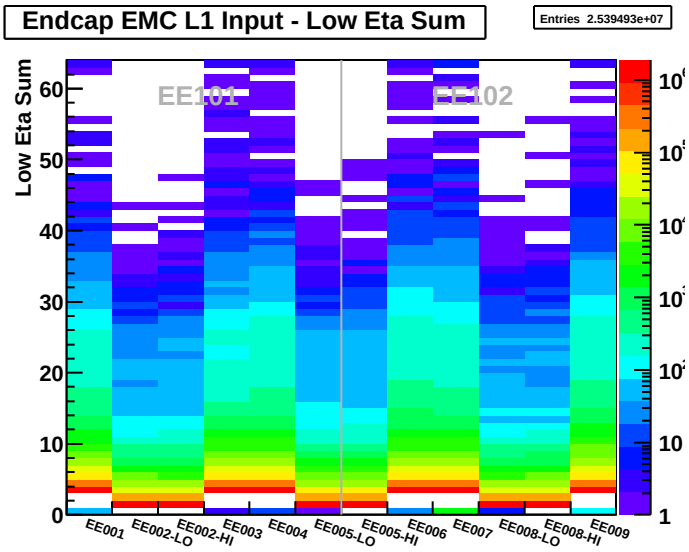

Barrel EMC L1 Input - Low Eta Sum
Entries 3460
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

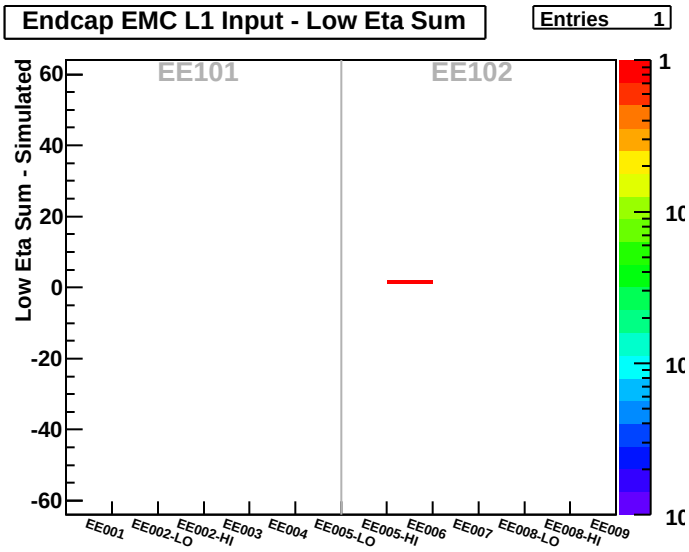

Barrel EMC L1 Input - High Eta Sum
Entries 7.618478e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

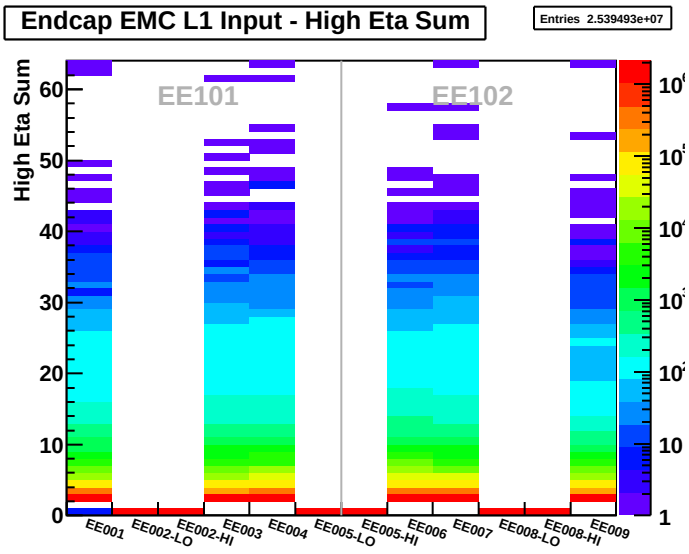

Barrel EMC L1 Input - High Eta Sum
Entries 1090
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

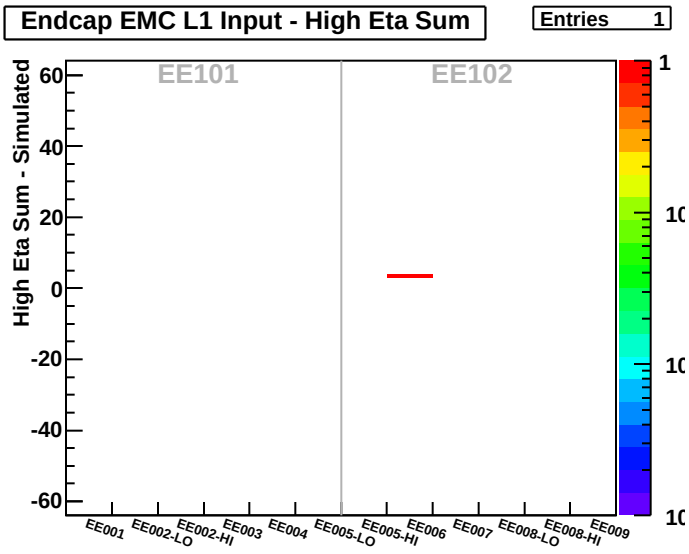

Barrel EMC L1 Input - High Tower Bits
Entries 7.618478e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

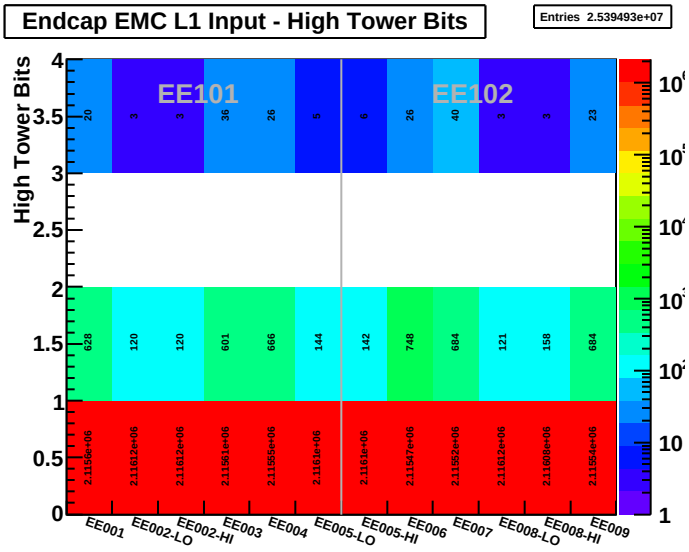

Barrel EMC L1 Input - High Tower Bits
Entries 19
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

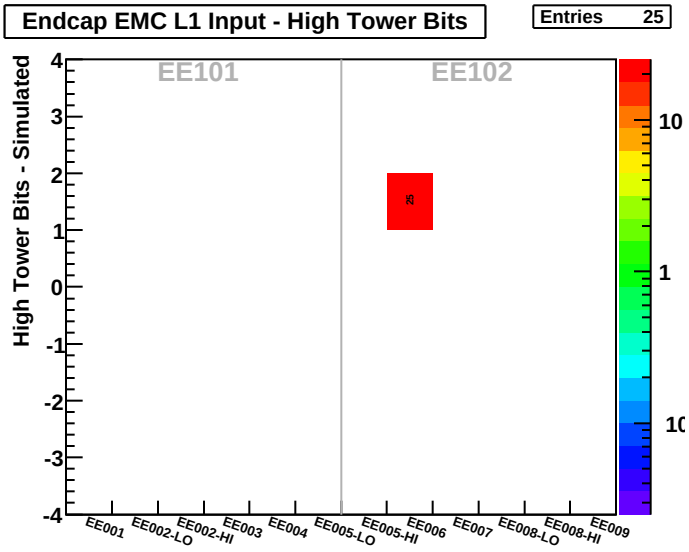

Endcap EMC L1 Input - Low Eta Sum
Entries 2.539493e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

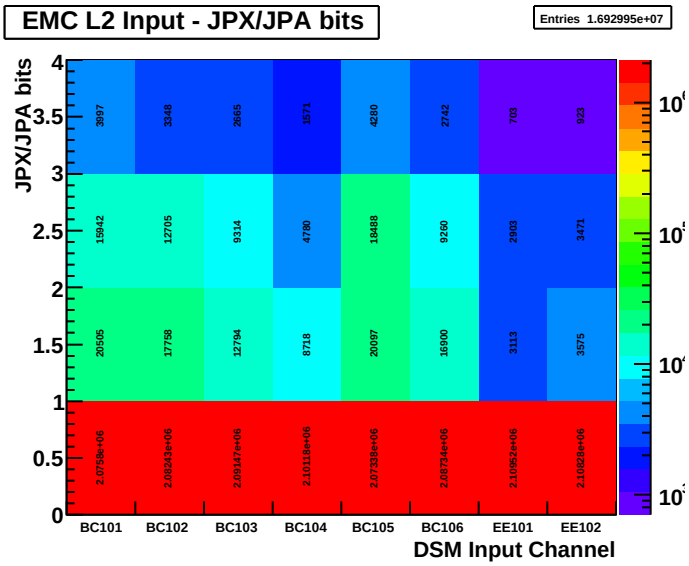

Endcap EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

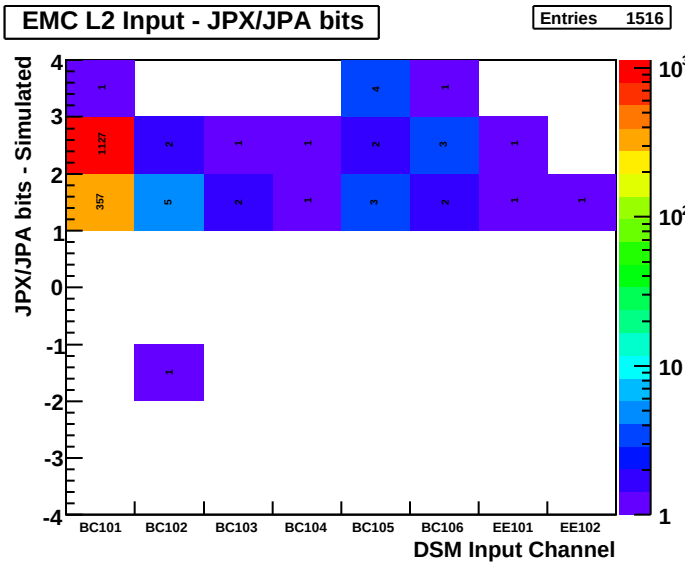

Endcap EMC L1 Input - High Eta Sum
Entries 2.539493e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

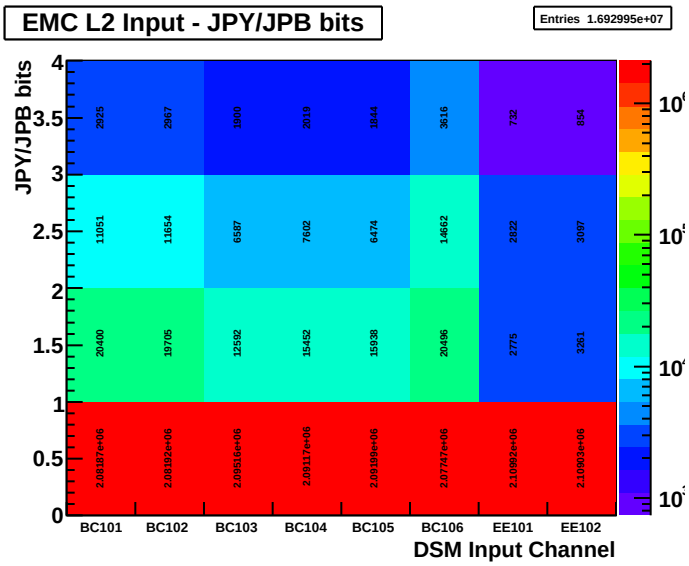

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

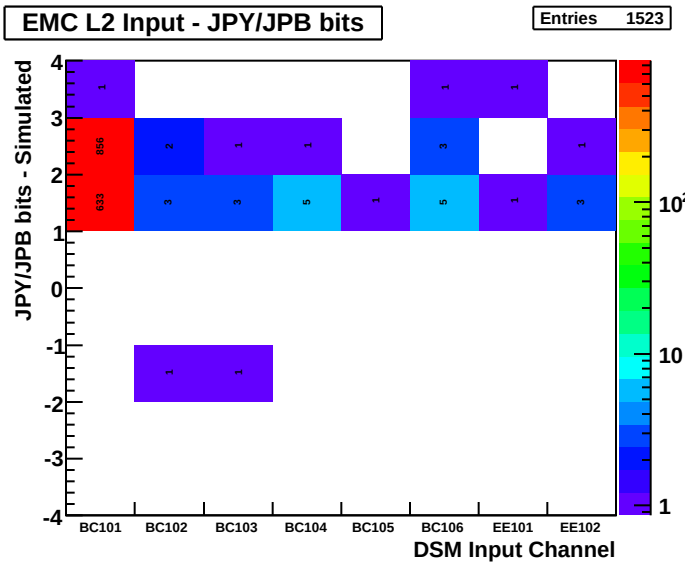

Endcap EMC L1 Input - High Tower Bits
Entries 2.539493e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

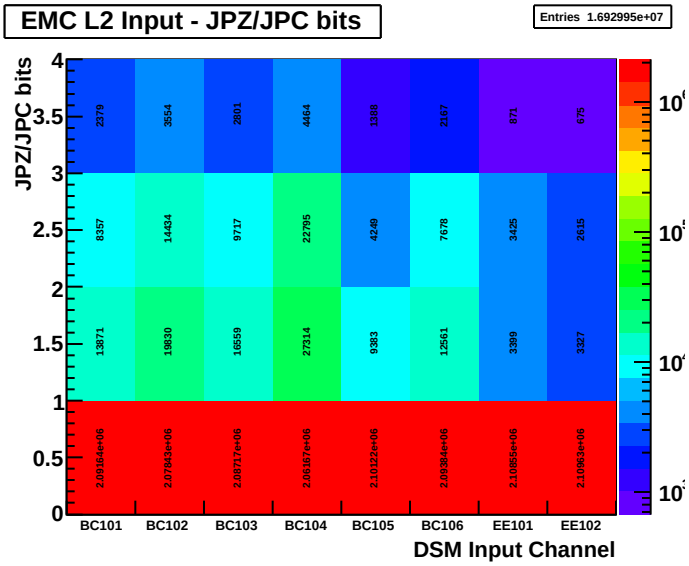

Endcap EMC L1 Input - High Tower Bits
Entries 25
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

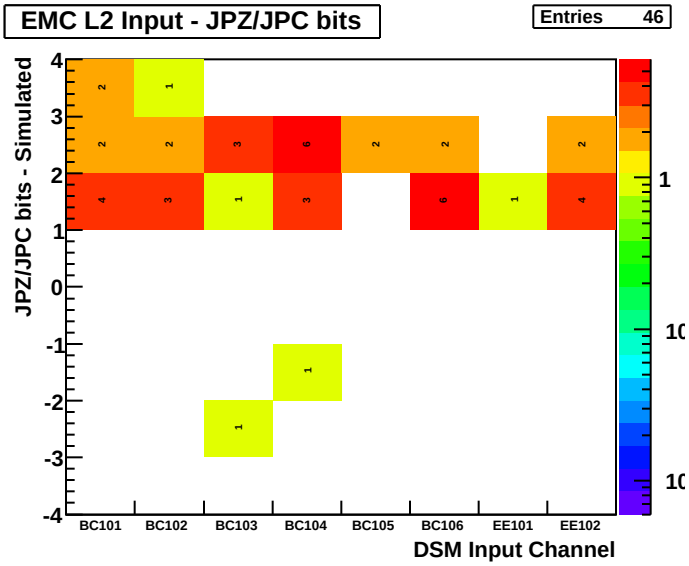

EMC L2 Input - JPX/JPA bits
Entries 1.692995e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

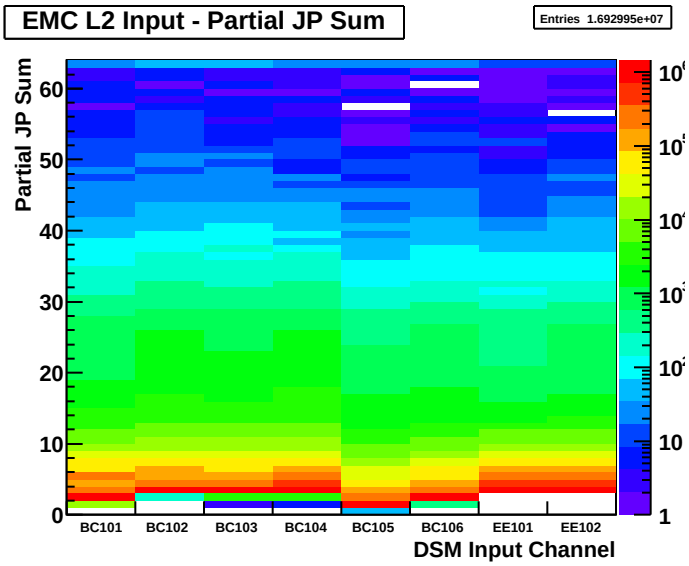

EMC L2 Input - JPX/JPA bits
Entries 1516
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

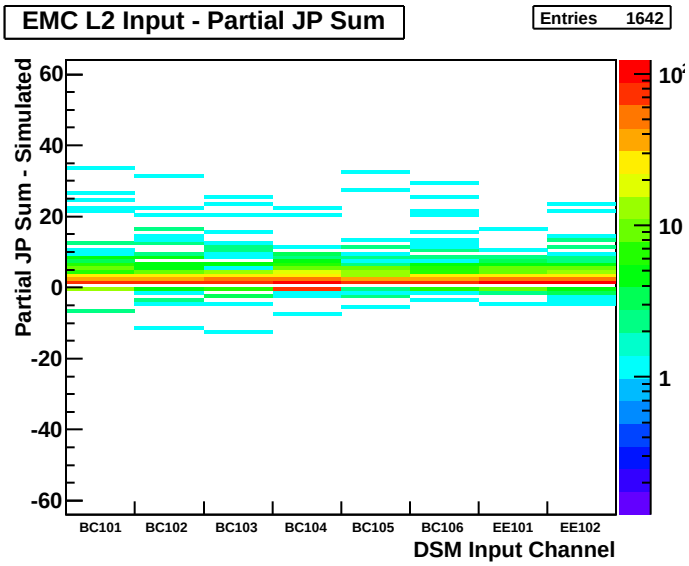

EMC L2 Input - JPY/JPB bits
Entries 1.692995e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

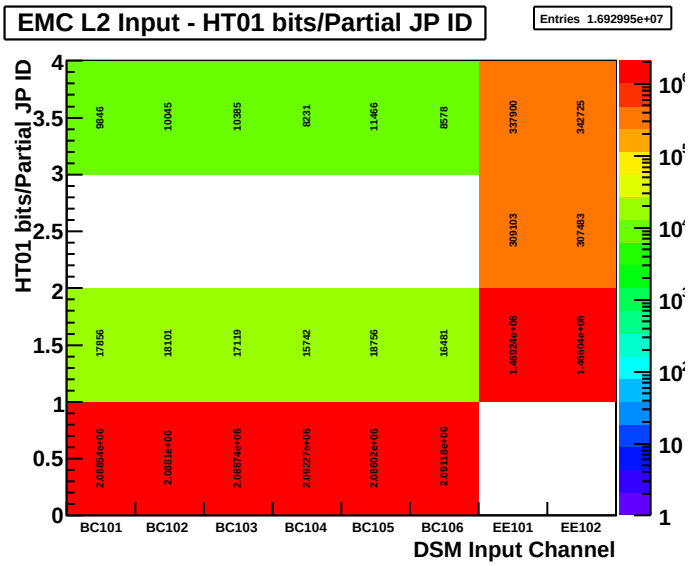

EMC L2 Input - JPY/JPB bits
Entries 1523
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

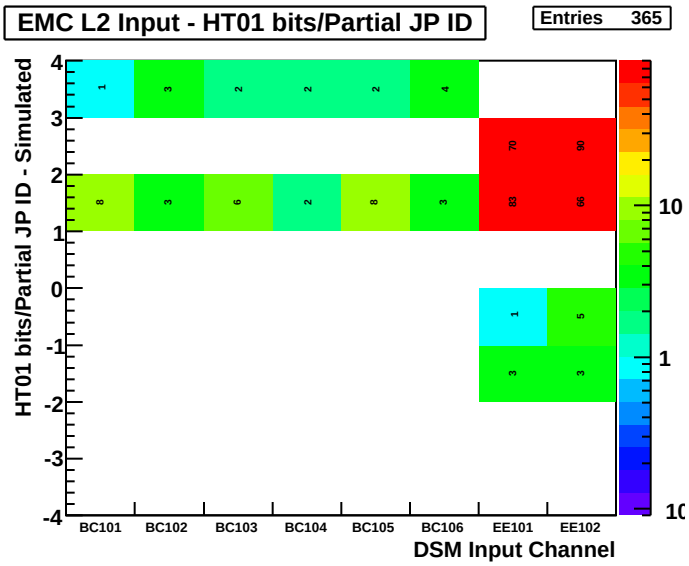

EMC L2 Input - JPZ/JPC bits
Entries 1.692995e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

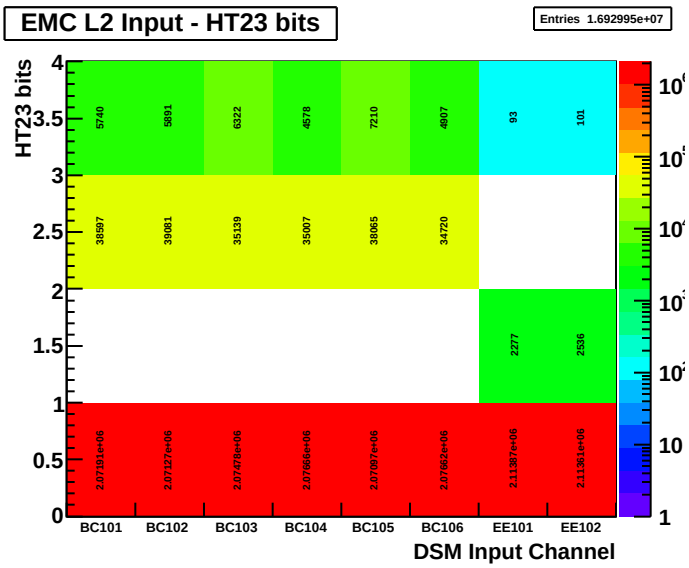

EMC L2 Input - JPZ/JPC bits
Entries 46
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

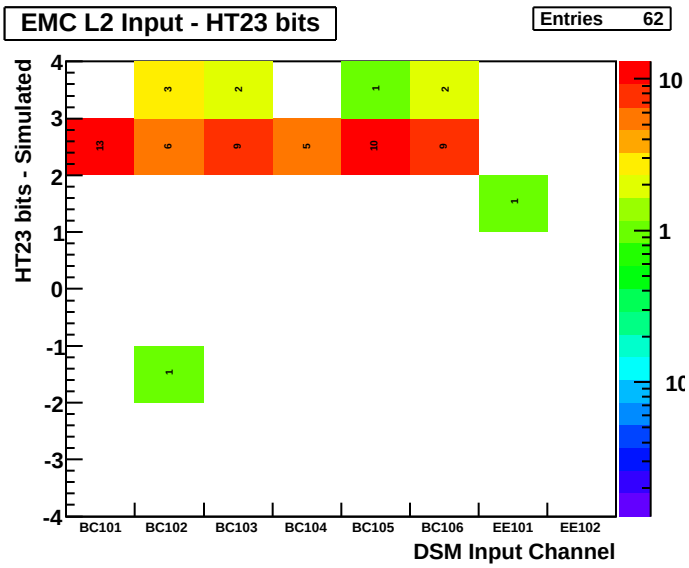

EMC L2 Input - Partial JP Sum
Entries 1.692995e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

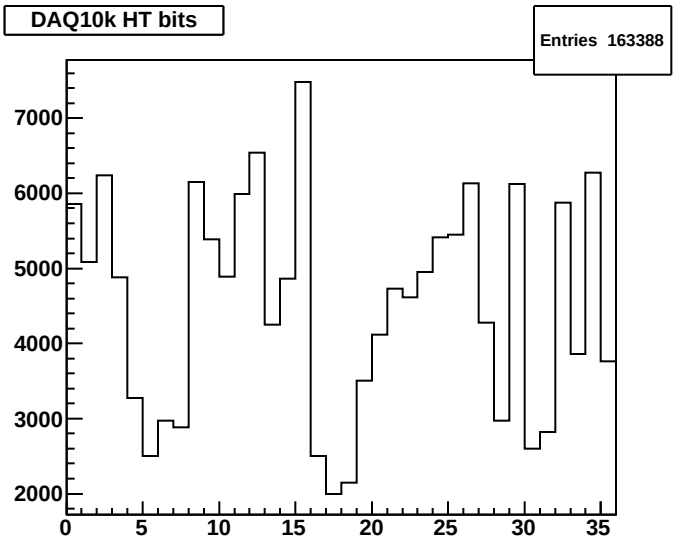

EMC L2 Input - Partial JP Sum
Entries 1642
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

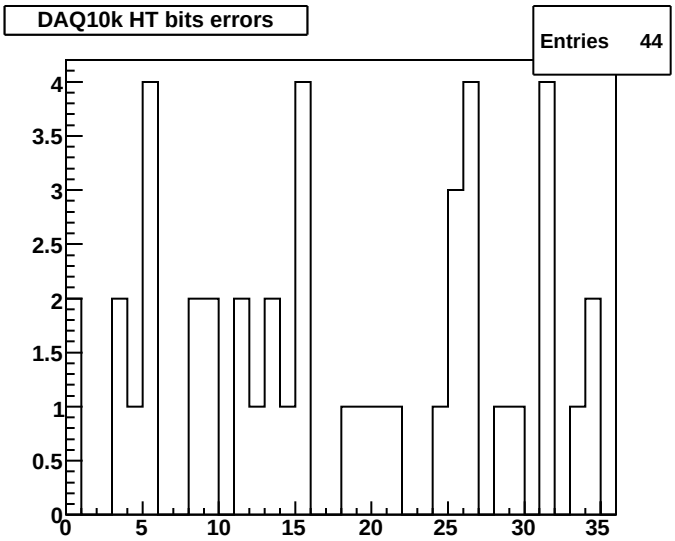

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.692995e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

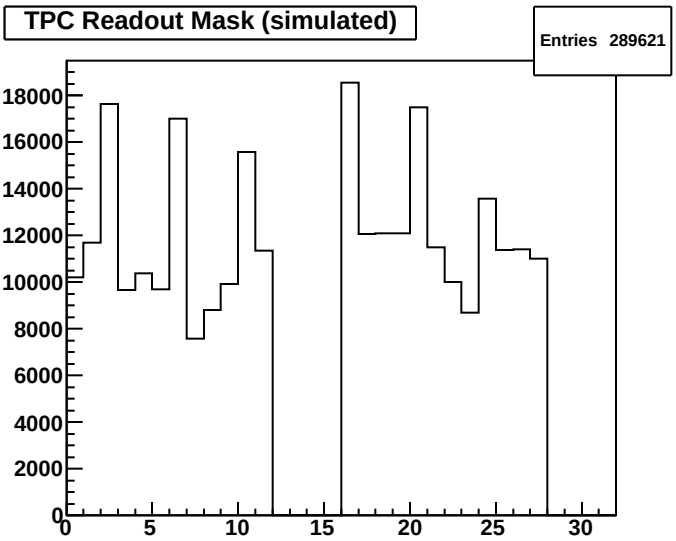

EMC L2 Input - HT01 bits/Partial JP ID
Entries 365
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

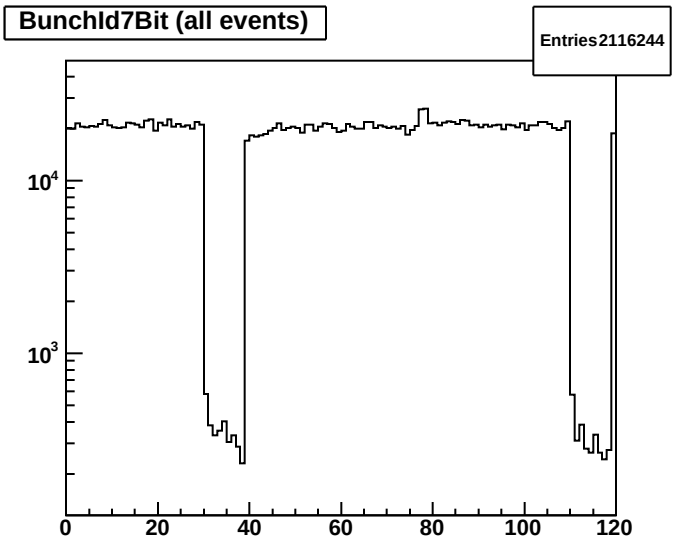

EMC L2 Input - HT23 bits
Entries 1.692995e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

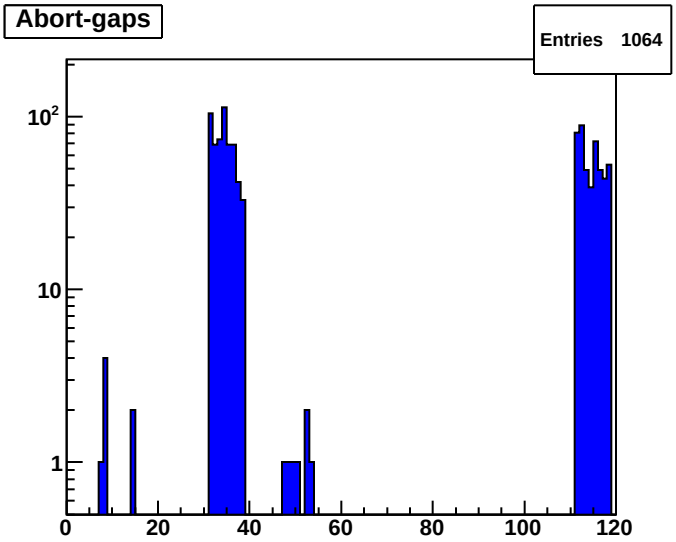

EMC L2 Input - HT23 bits
Entries 62
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

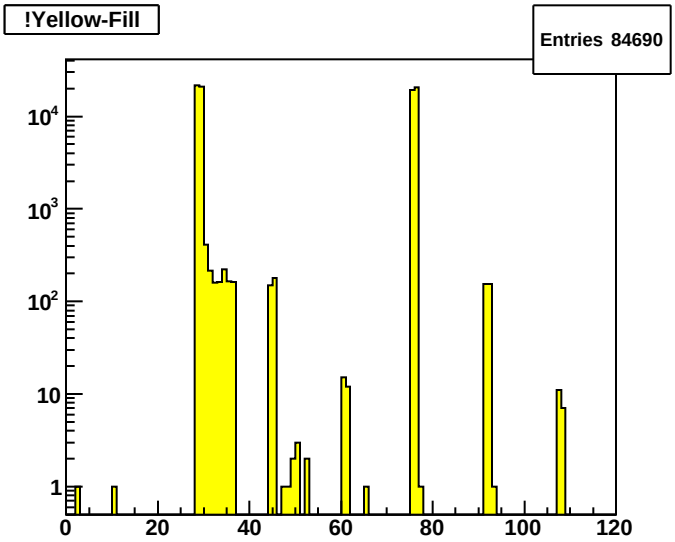

DAQ10k HT bits
Entries 163388

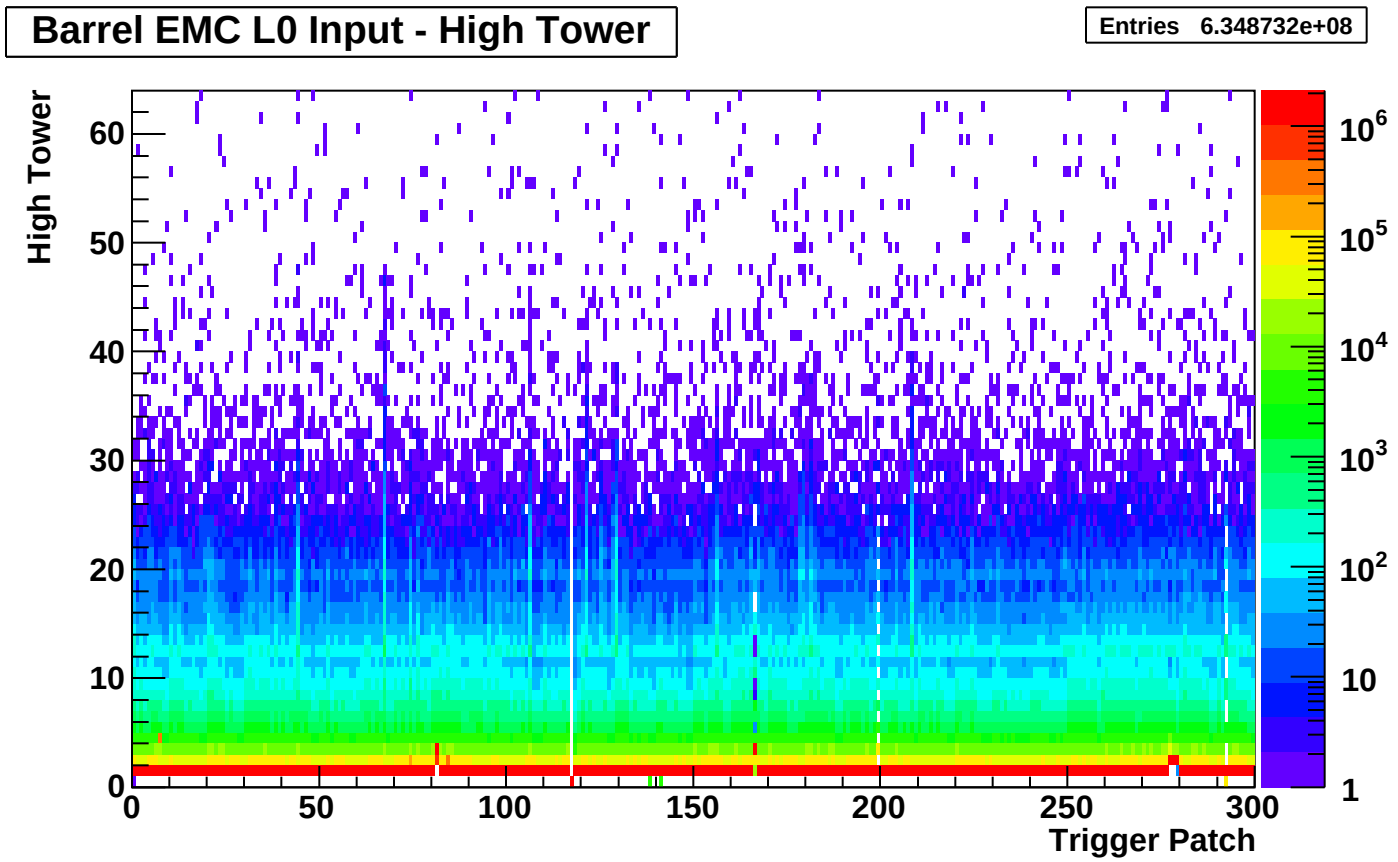

DAQ10k HT bits errors
Entries 44

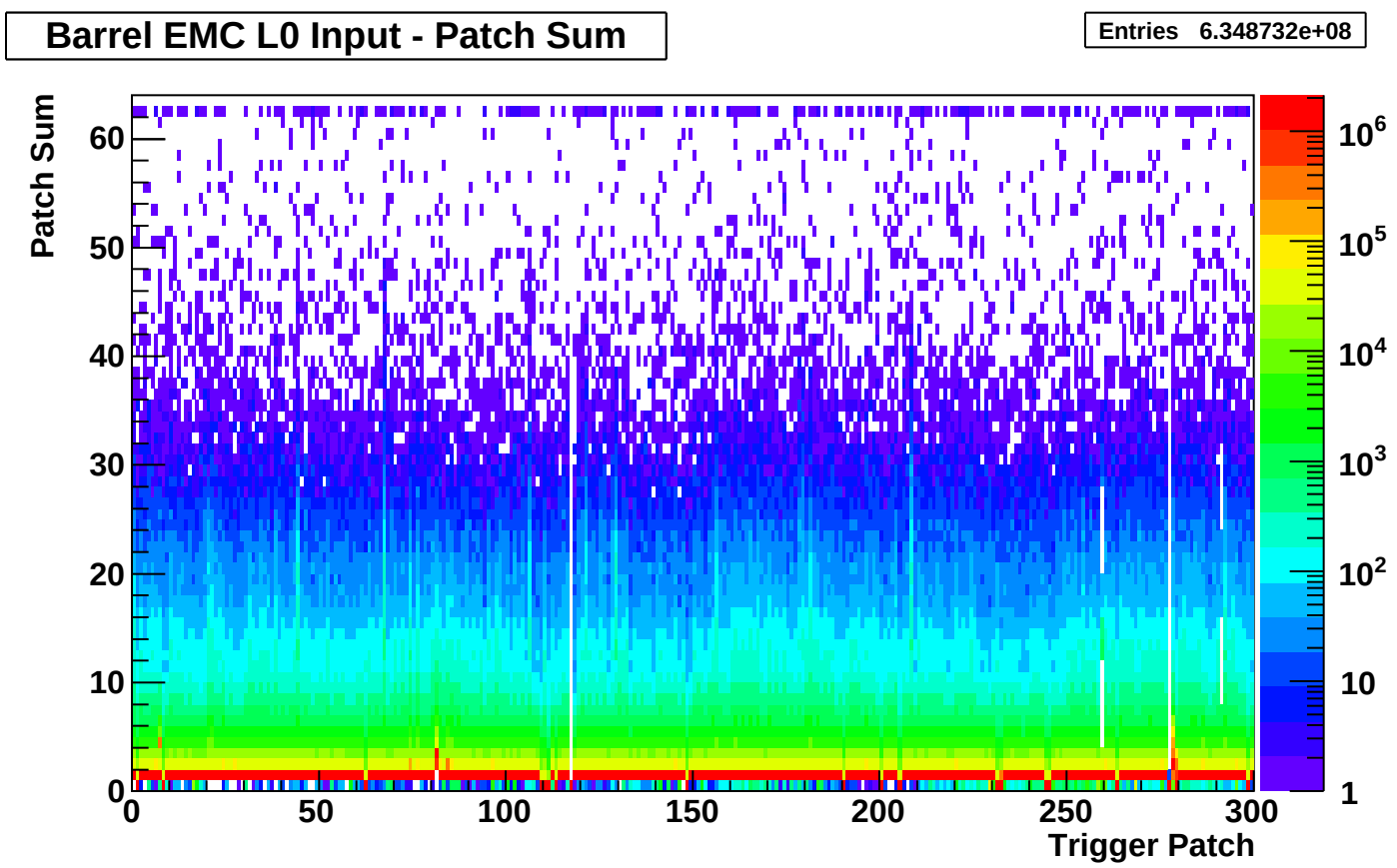

TPC Readout Mask (simulated)
Entries 289621

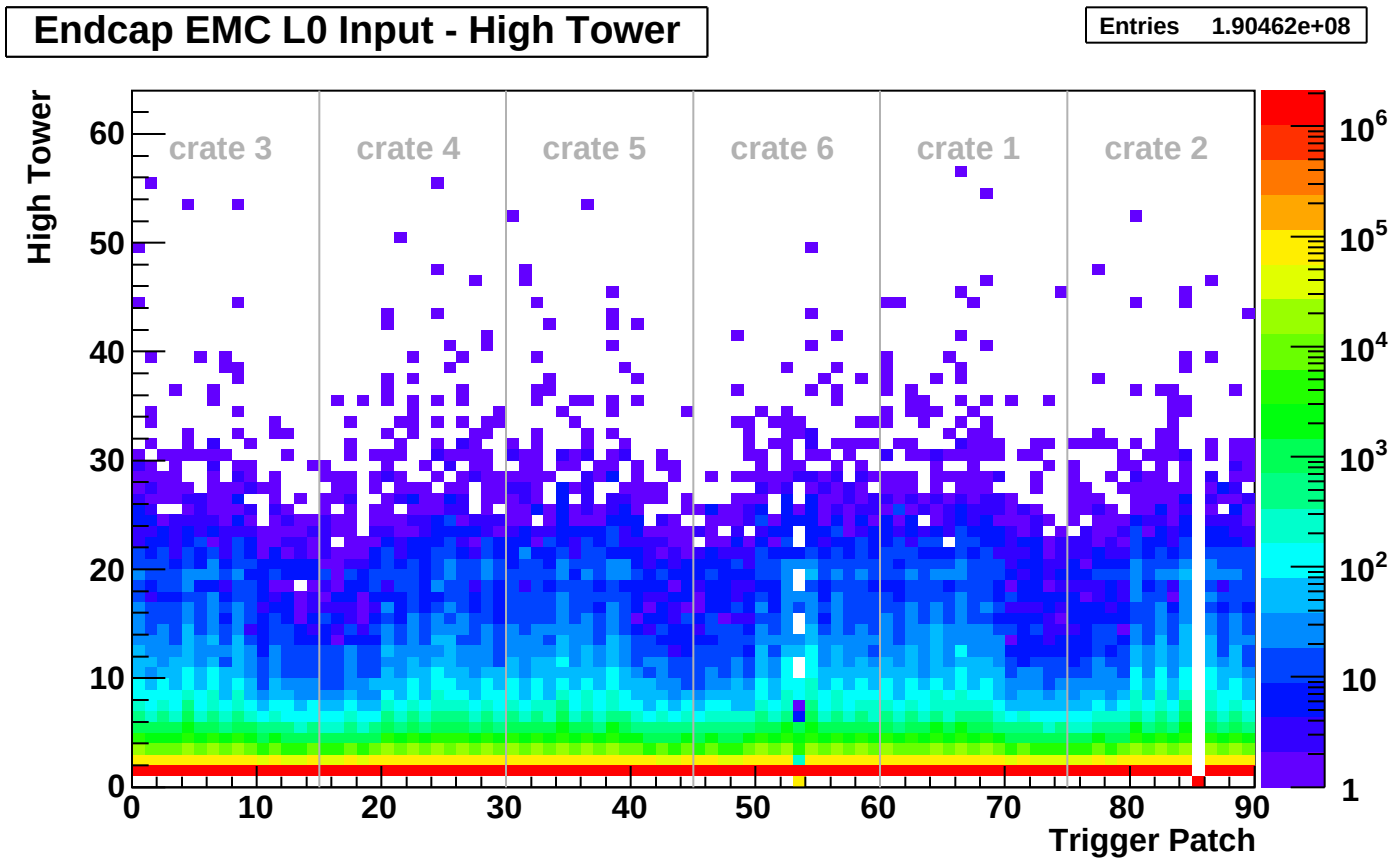

BunchId7Bit (all events)
Entries 2116244

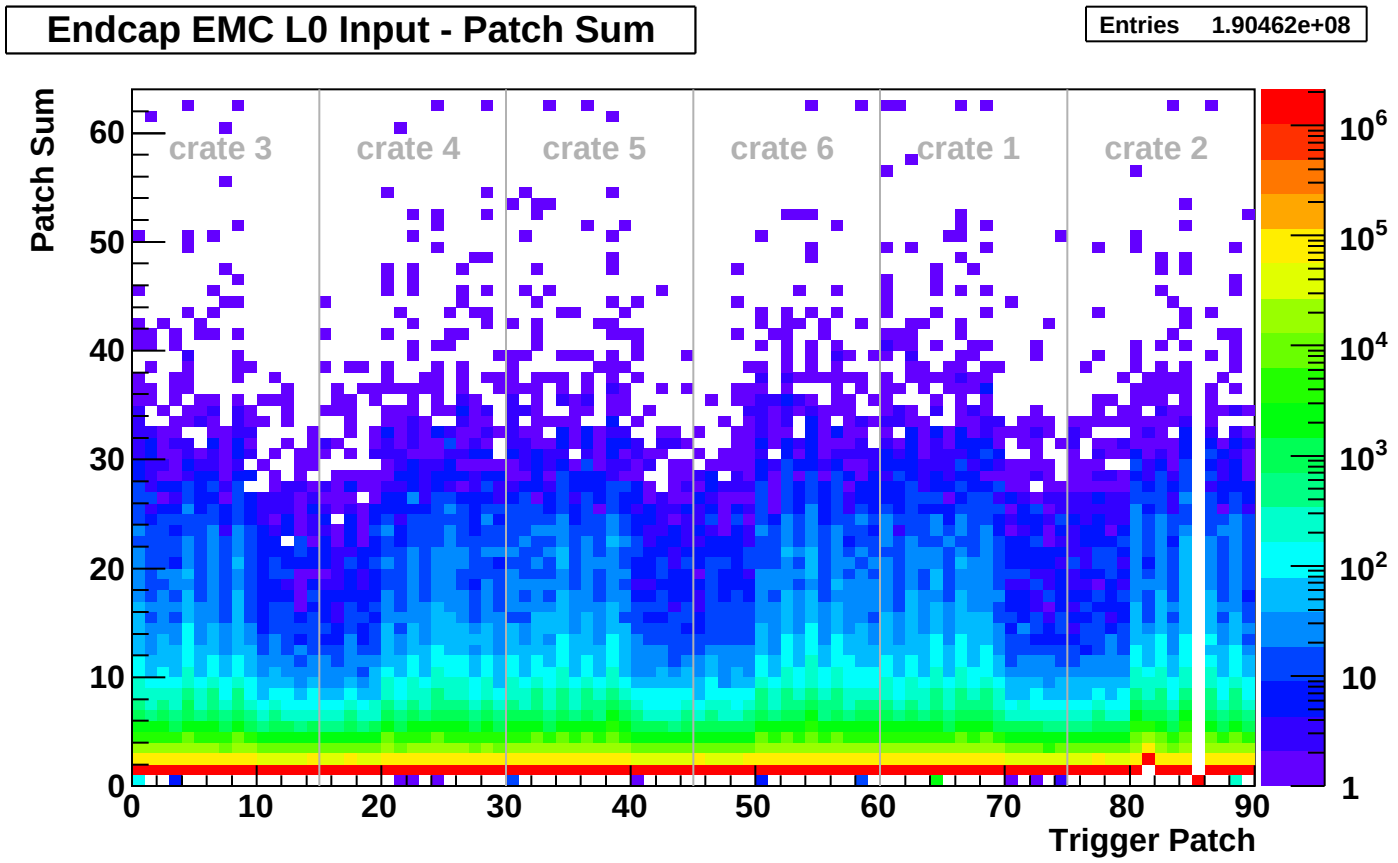

Abort-gaps
Entries 1064

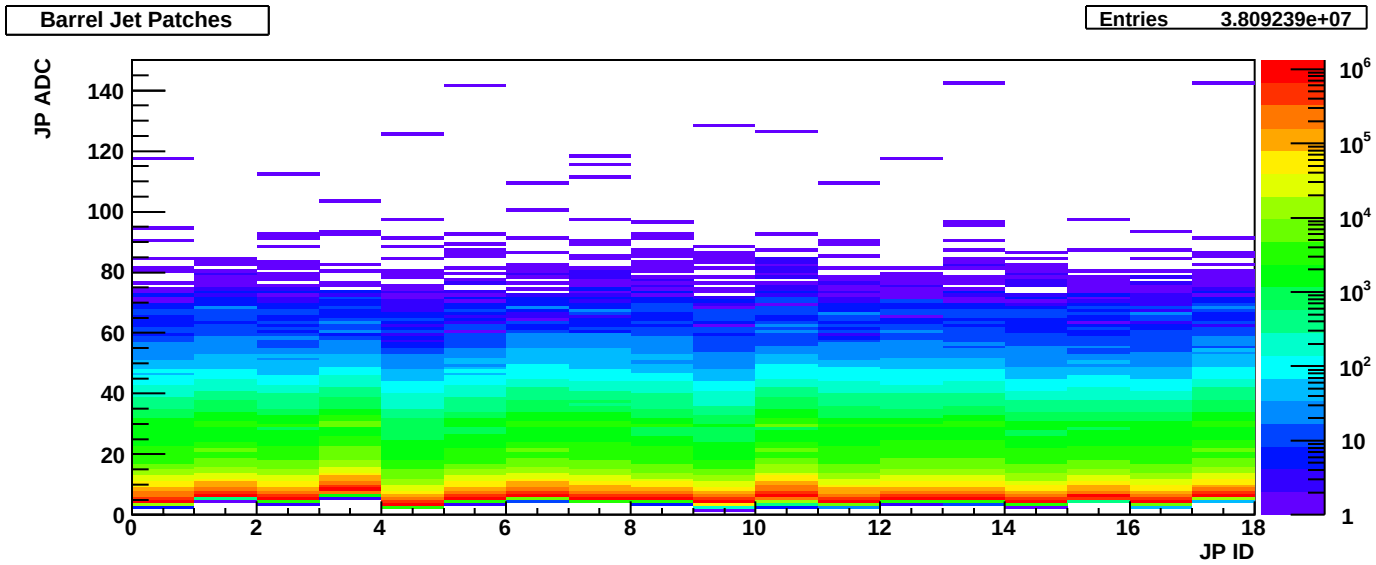

!Yellow-Fill
Entries 84690

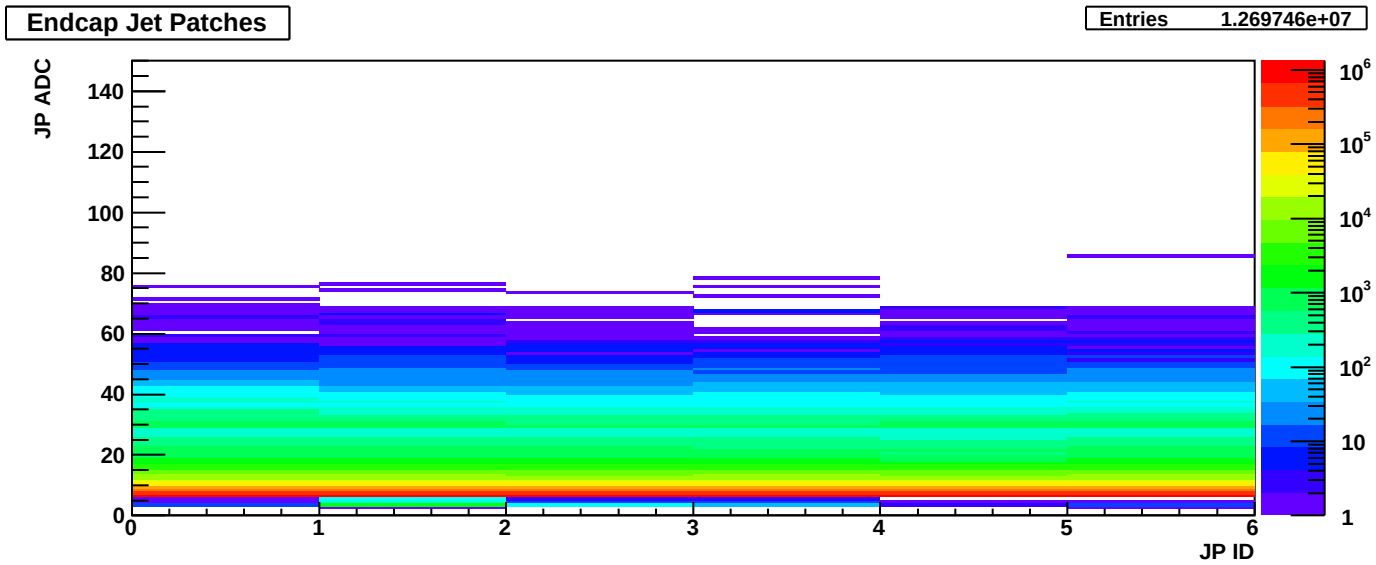

Barrel EMC L0 Input - High Tower
Entries 6.348732e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

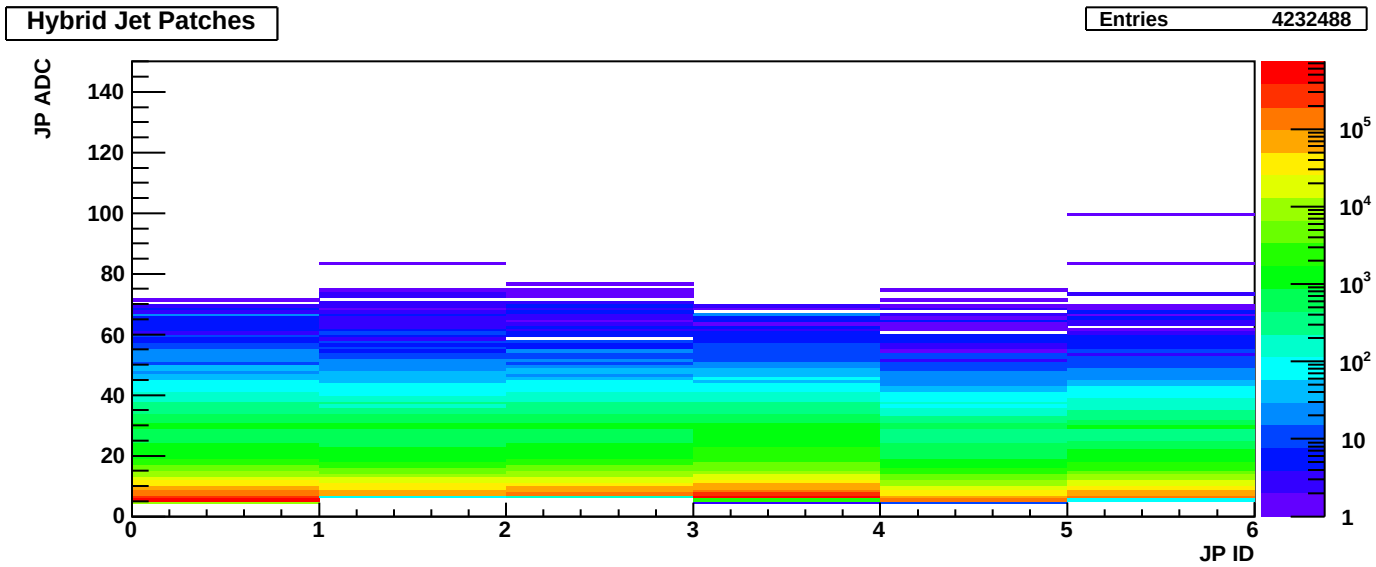

Barrel EMC L0 Input - Patch Sum
Entries 6.348732e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

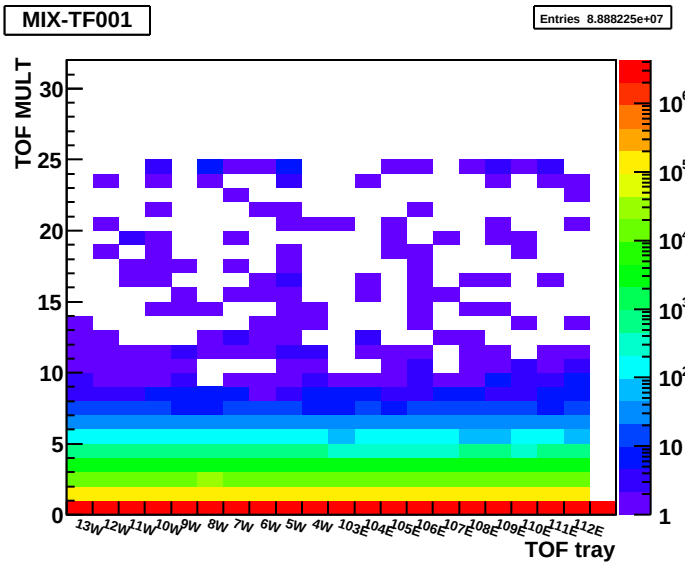

Endcap EMC L0 Input - High Tower
Entries 1.90462e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

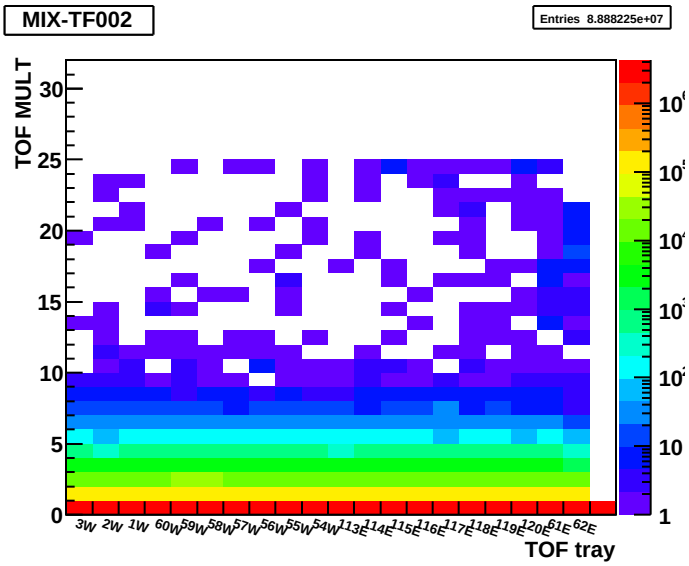

Endcap EMC L0 Input - Patch Sum
Entries 1.90462e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

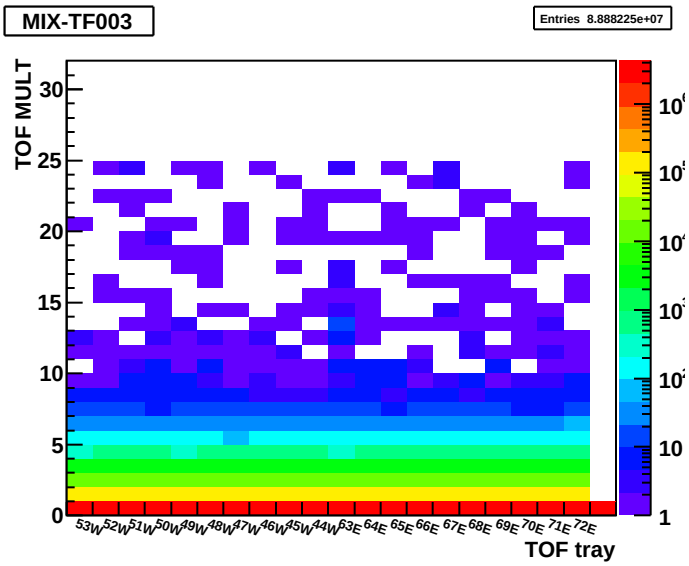

Barrel Jet Patches
Entries 3.809239e+07
JP ADC
JP ID

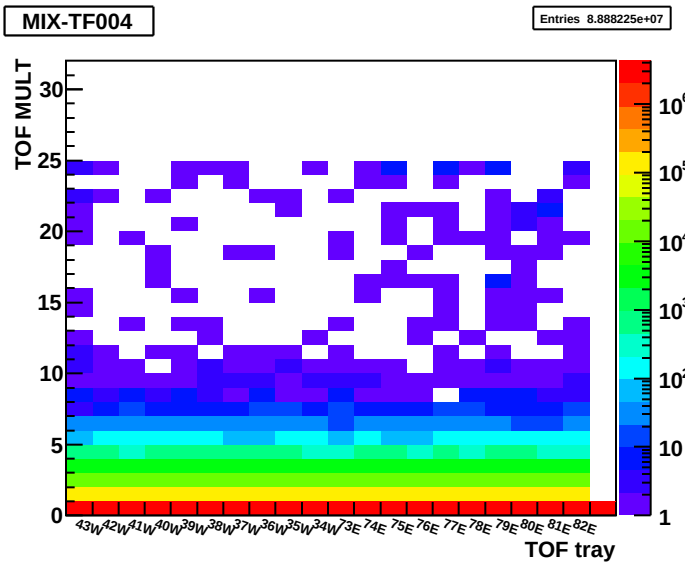

Endcap Jet Patches
Entries 1.269746e+07
JP ADC
JP ID

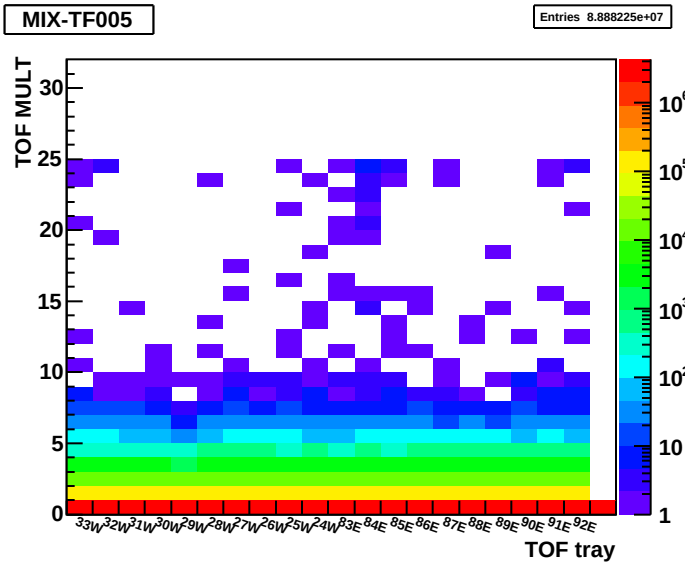

Hybrid Jet Patches
Entries 4232488
JP ADC
JP ID

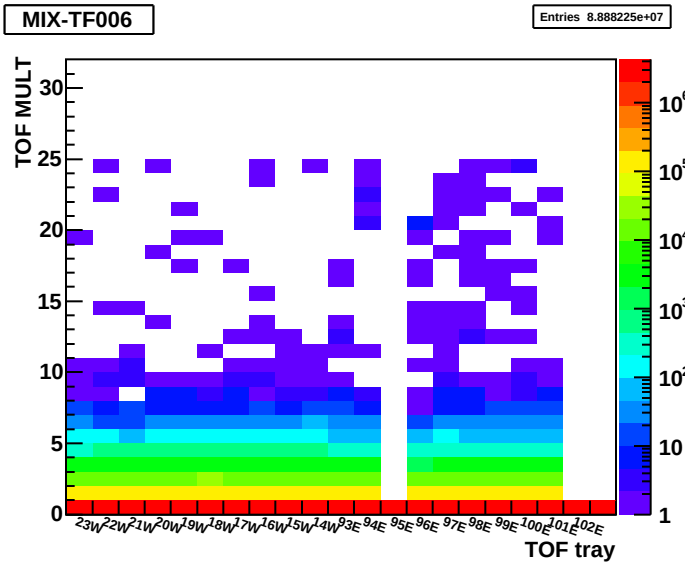

MIX-TF001
Entries 8.888225e+07
TOF MULT
TOF tray

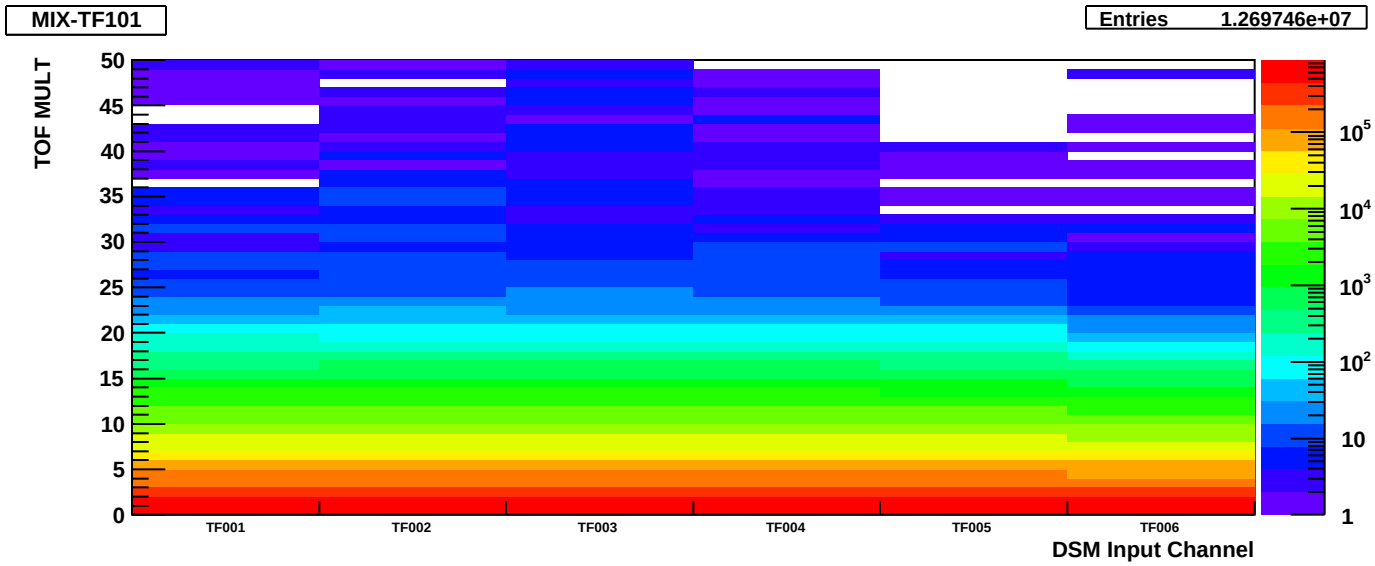

MIX-TF002
Entries 8.888225e+07
TOF MULT
TOF tray

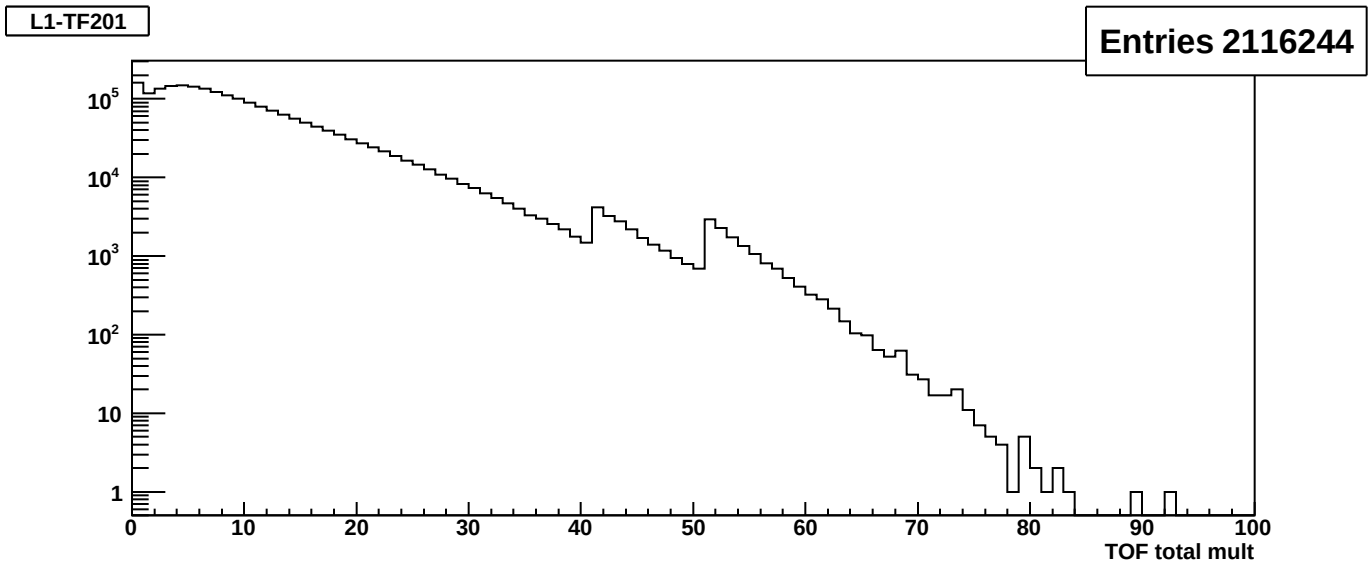

MIX-TF003
Entries 8.888225e+07
TOF MULT
TOF tray

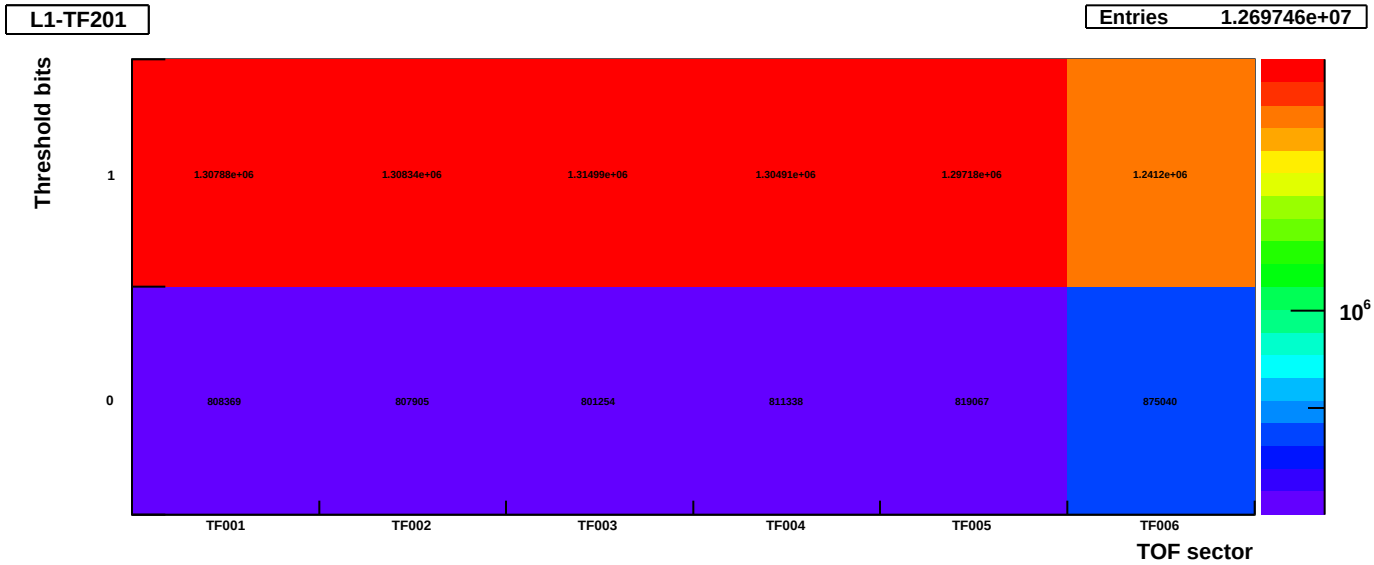

MIX-TF004
Entries 8.888225e+07
TOF MULT
TOF tray

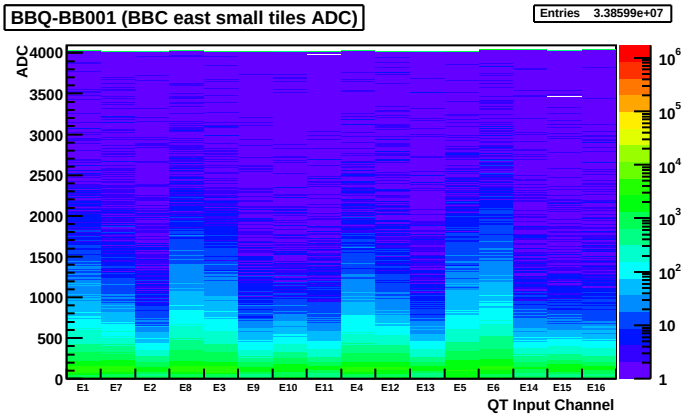

MIX-TF005
Entries 8.888225e+07
TOF MULT
TOF tray

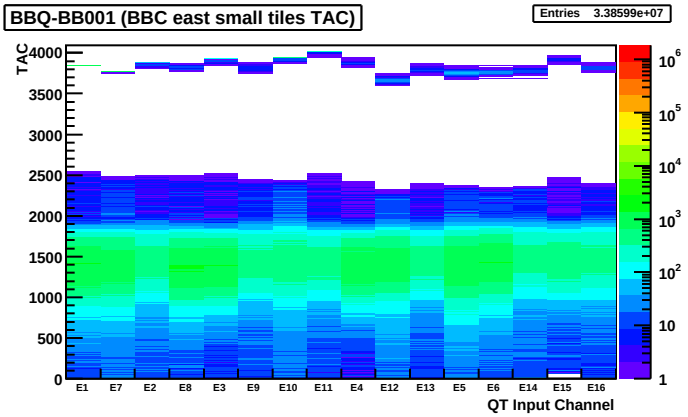

MIX-TF006
Entries 8.888225e+07
TOF MULT
TOF tray

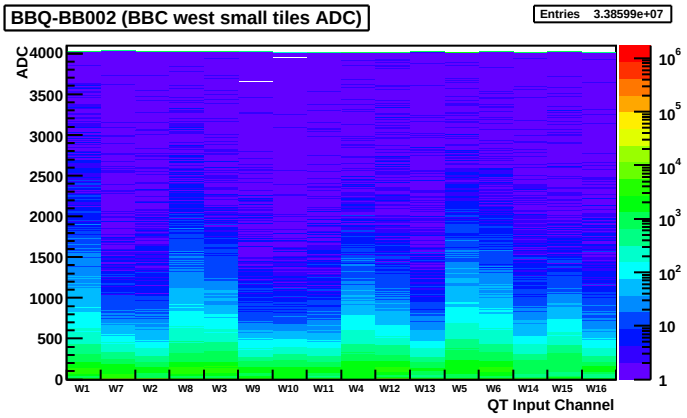

MIX-TF101
Entries 1.269746e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

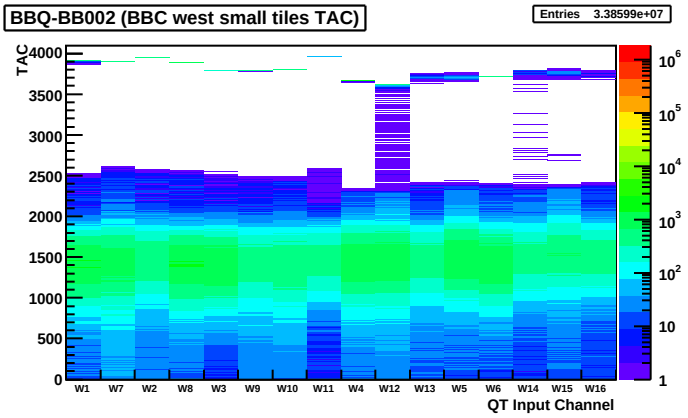

L1-TF201
Entries 2116244
TOF total mult

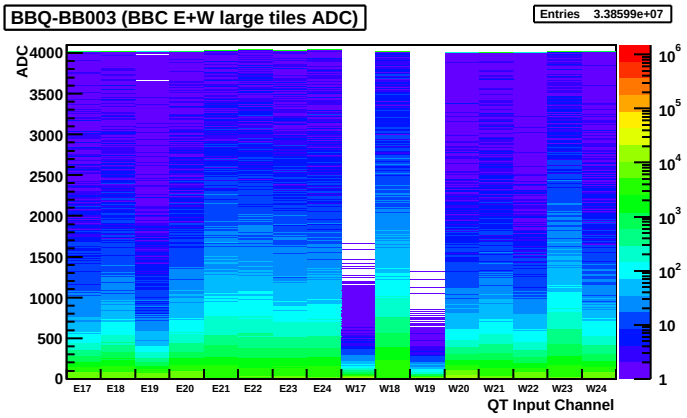

L1-TF201
Entries 1.269746e+07
Threshold bits
1.30788e+06
1.30834e+06
1.31499e+06
1.30491e+06
1.29718e+06
1.2412e+06
808369
807905
801254
811338
819067
875040
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

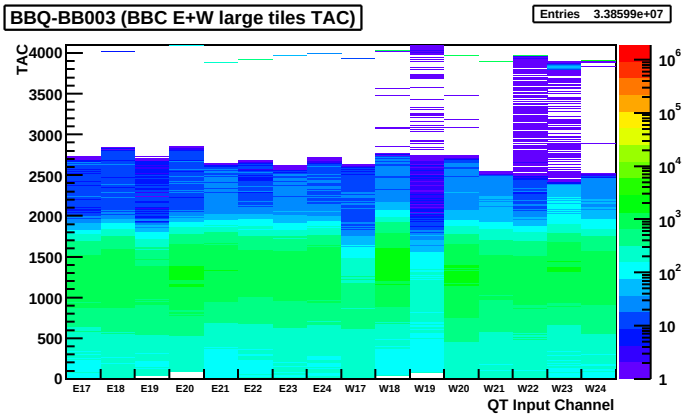

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.38599e+07
ADC
QT Input Channel

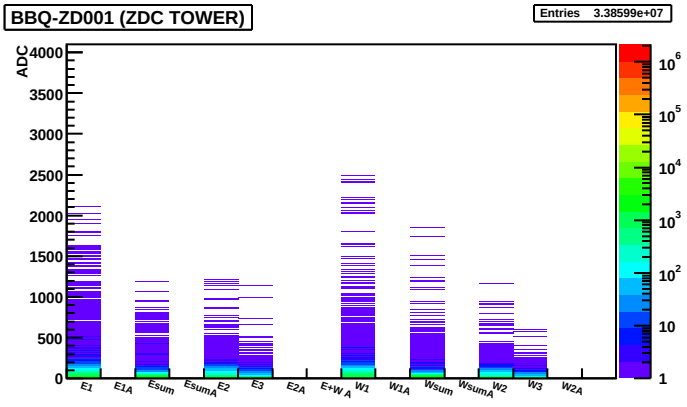

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.38599e+07
TAC
QT Input Channel

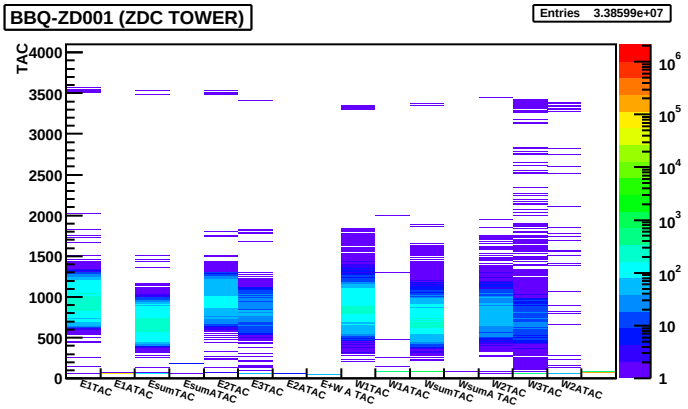

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.38599e+07
ADC
QT Input Channel

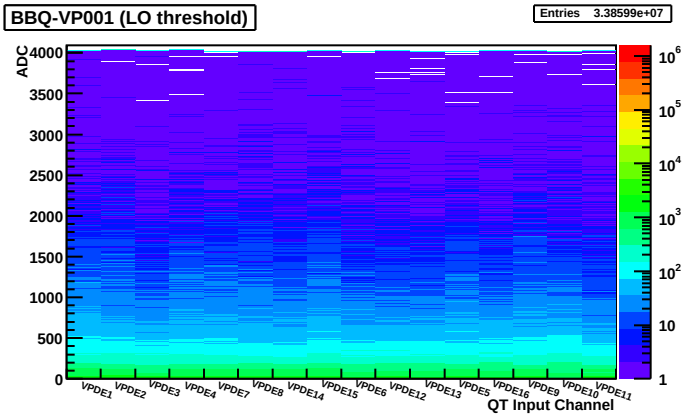

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.38599e+07
TAC
QT Input Channel

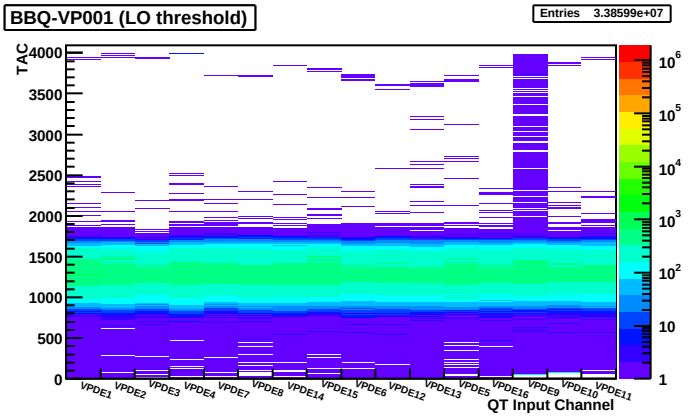

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.38599e+07
ADC
QT Input Channel

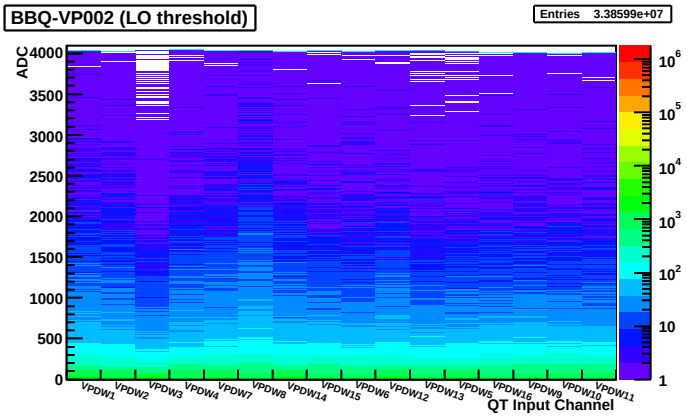

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.38599e+07
TAC
QT Input Channel

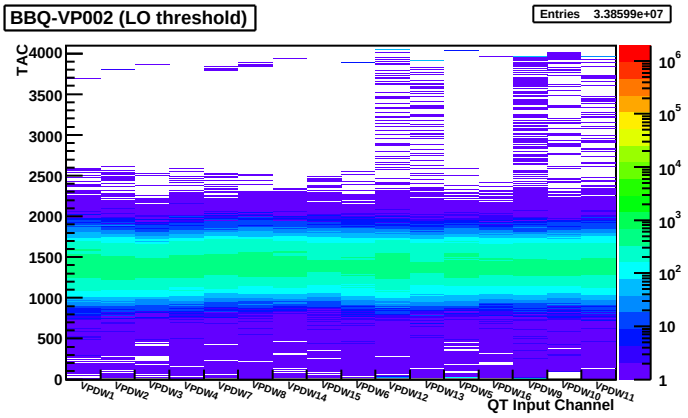

BBQ-ZD001 (ZDC TOWER)
Entries 3.38599e+07
ADC

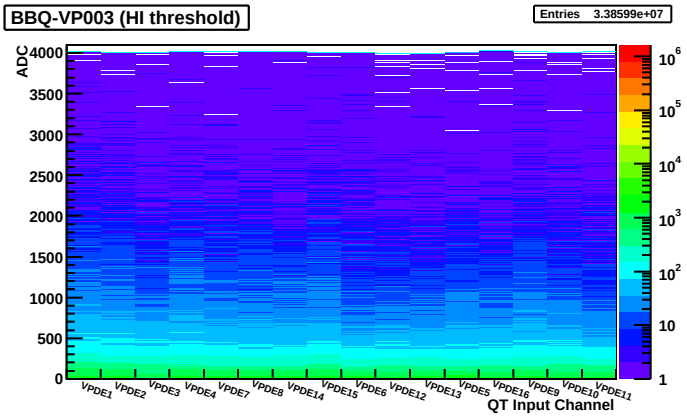

BBQ-ZD001 (ZDC TOWER)
Entries 3.38599e+07
TAC

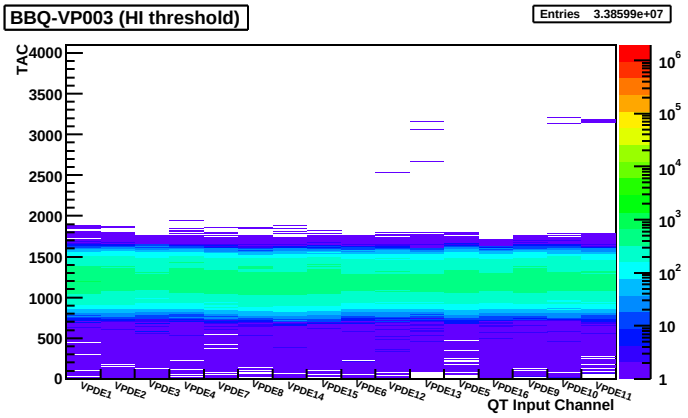

BBQ-VP001 (LO threshold)
Entries 3.38599e+07
ADC
QT Input Channel

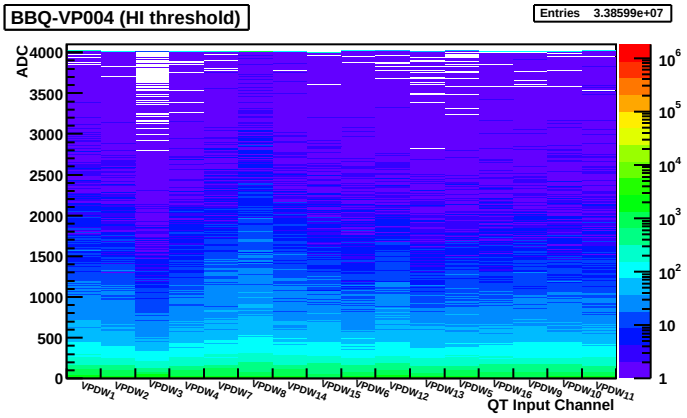

BBQ-VP001 (LO threshold)
Entries 3.38599e+07
TAC
QT Input Channel

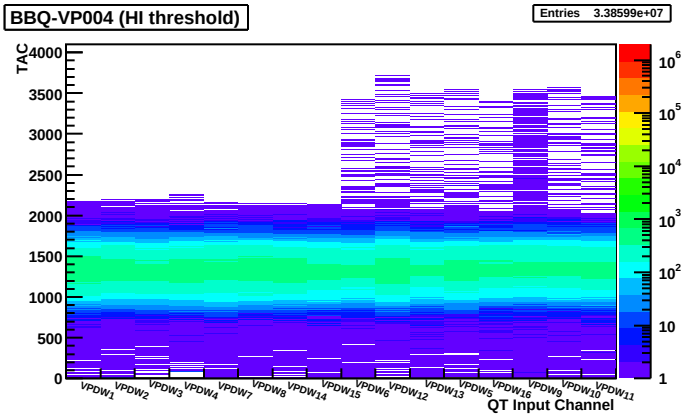

BBQ-VP002 (LO threshold)
Entries 3.38599e+07
ADC
QT Input Channel

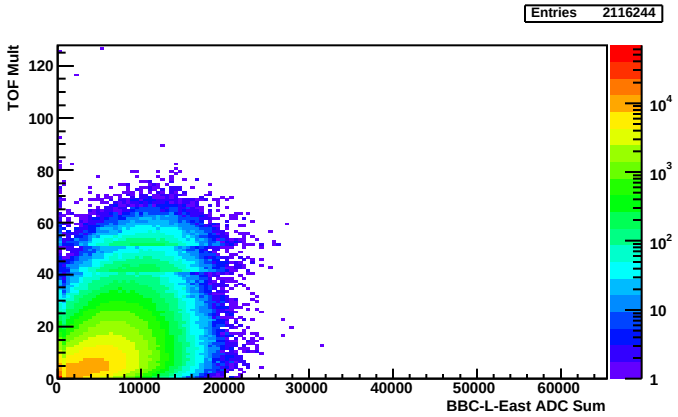

BBQ-VP002 (LO threshold)
Entries 3.38599e+07
TAC
QT Input Channel

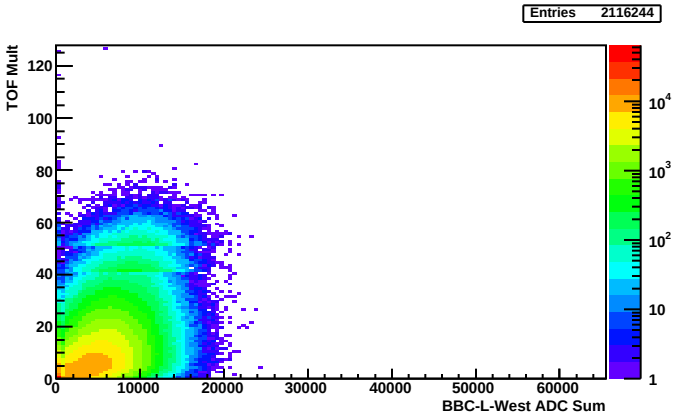

BBQ-VP003 (HI threshold)
Entries 3.38599e+07
ADC
QT Input Channel

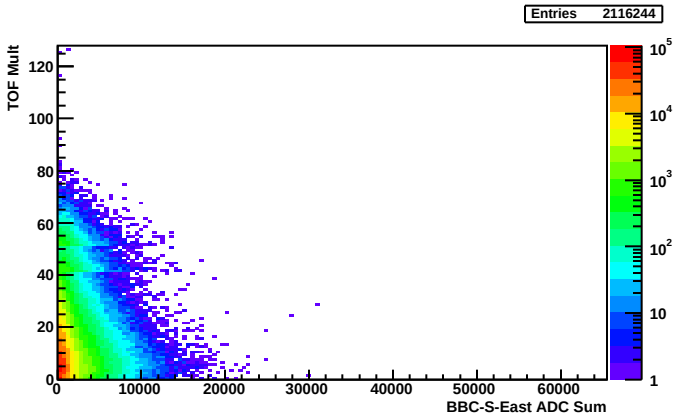

BBQ-VP003 (HI threshold)
Entries 3.38599e+07
TAC
QT Input Channel

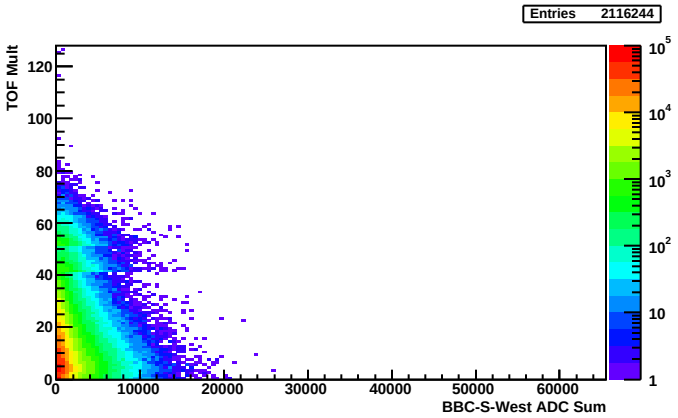

BBQ-VP004 (HI threshold)
Entries 3.38599e+07
ADC
QT Input Channel

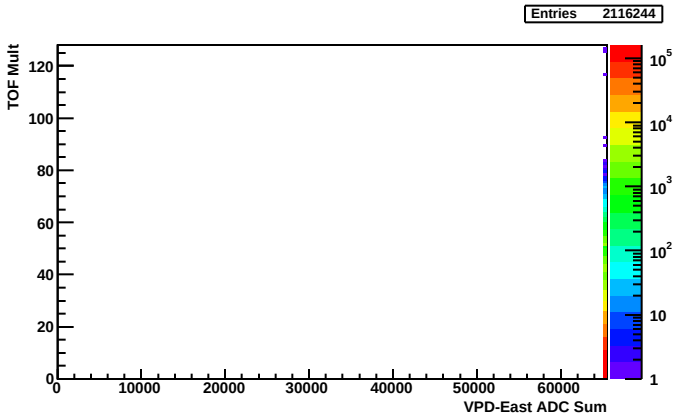

BBQ-VP004 (HI threshold)
Entries 3.38599e+07
TAC
QT Input Channel

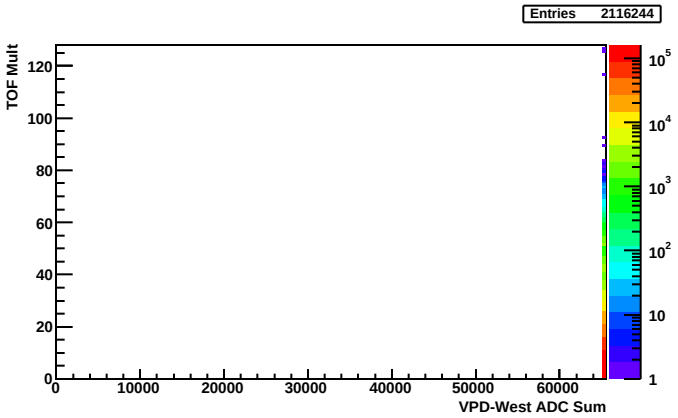

Entries 2116244
TOF Mult
BBC-L-East ADC Sum

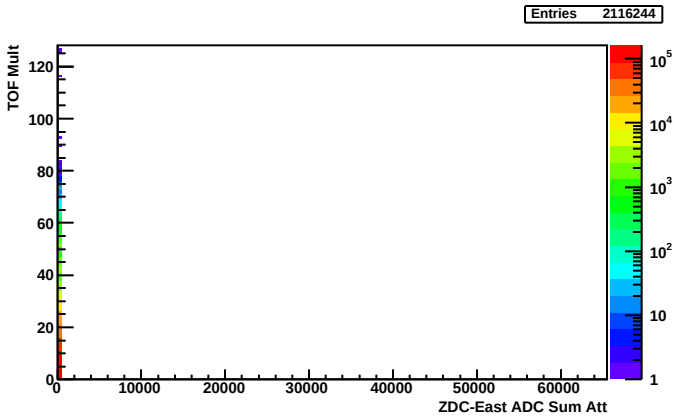

Entries 2116244
TOF Mult
BBC-L-West ADC Sum

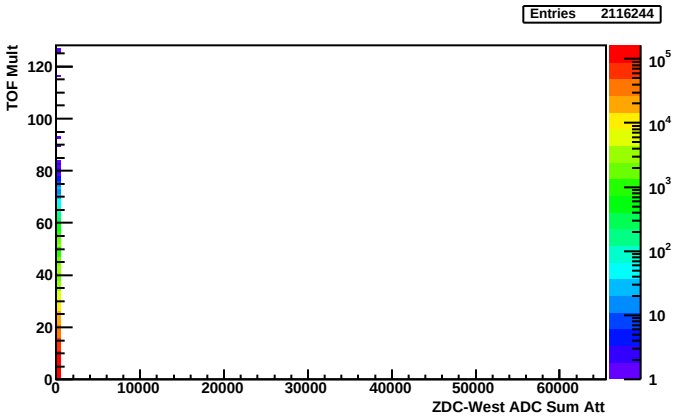

Entries 2116244
TOF Mult
BBC-S-East ADC Sum

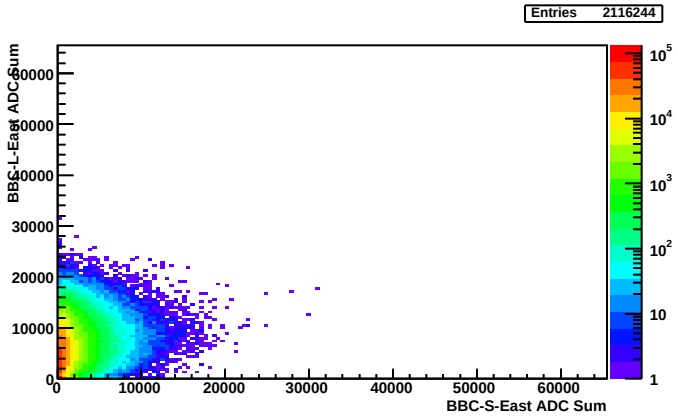

Entries 2116244
TOF Mult
BBC-S-West ADC Sum

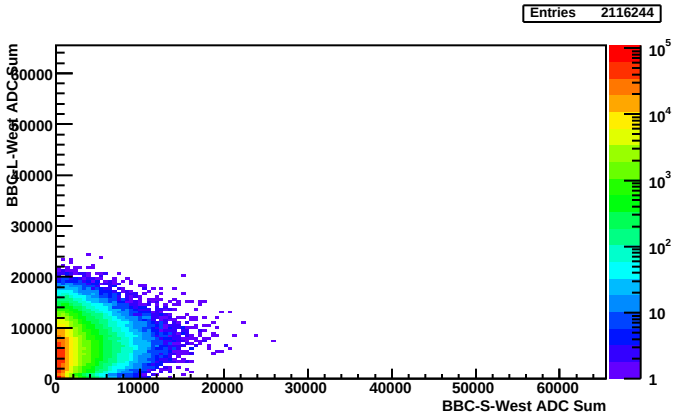

Entries 2116244
TOF Mult
VPD-East ADC Sum

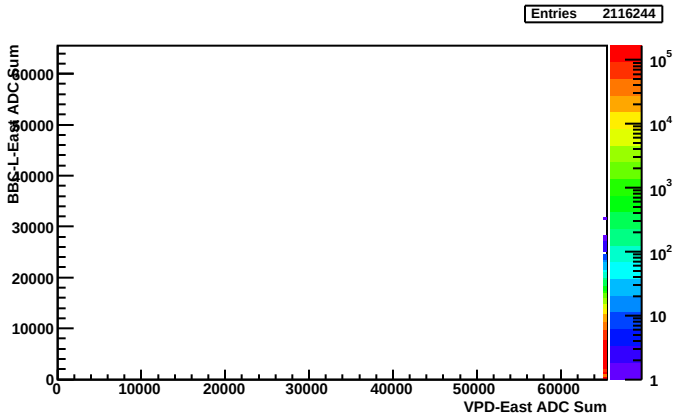

Entries 2116244
TOF Mult
VPD-West ADC Sum


Entries 2116244
TOF Mult
ZDC-East ADC Sum Att


Entries 2116244
TOF Mult
ZDC-West ADC Sum Att


Entries 2116244
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2116244
BBC-L-West ADC Sum
BBC-S-West ADC Sum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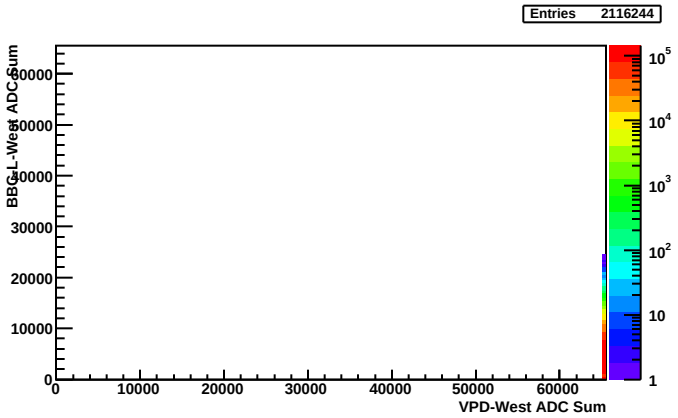
BBC-L-East ADC Sum
VPD-East ADC Sum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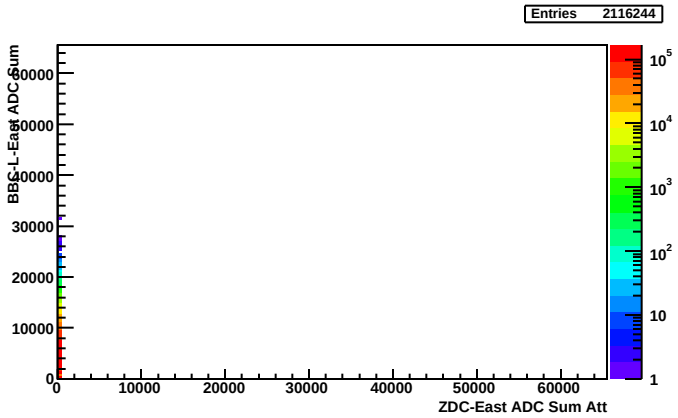
BBC-L-West ADC Sum
VPD-West ADC Sum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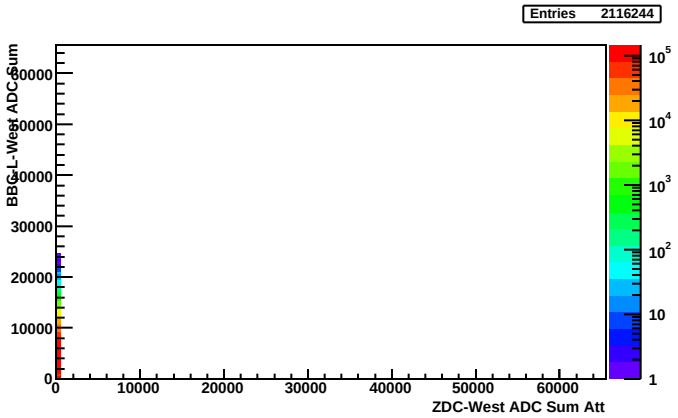
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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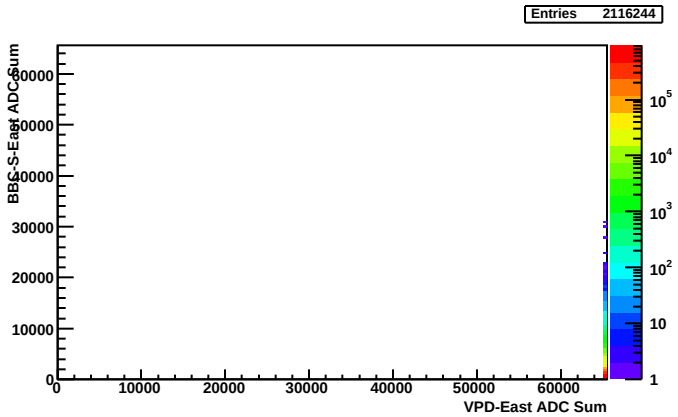
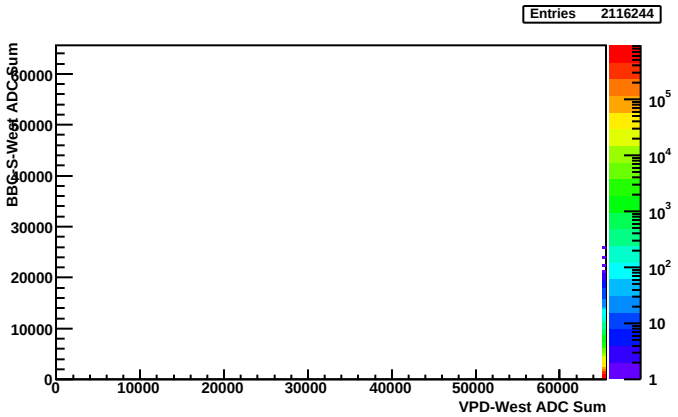
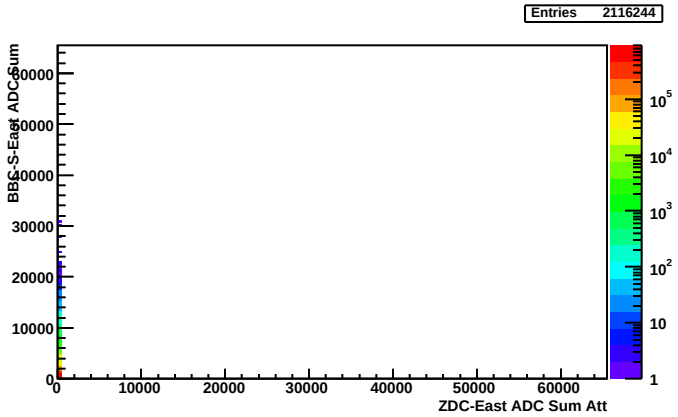
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2116244
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2116244
BBC-S-West ADC Sum
VPD-West ADC Sum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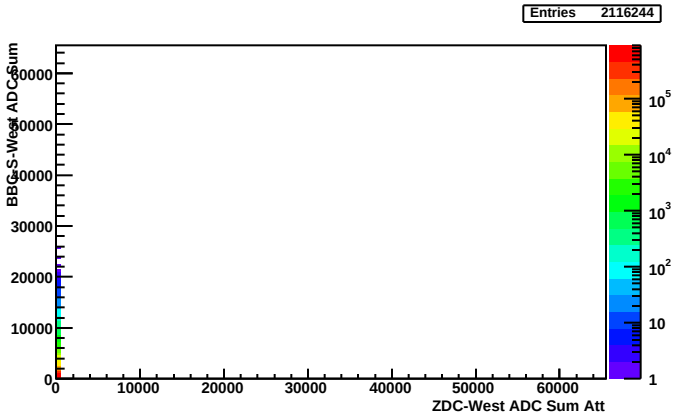
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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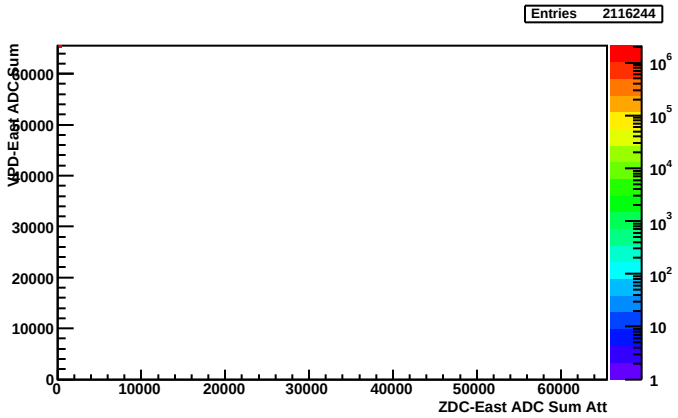
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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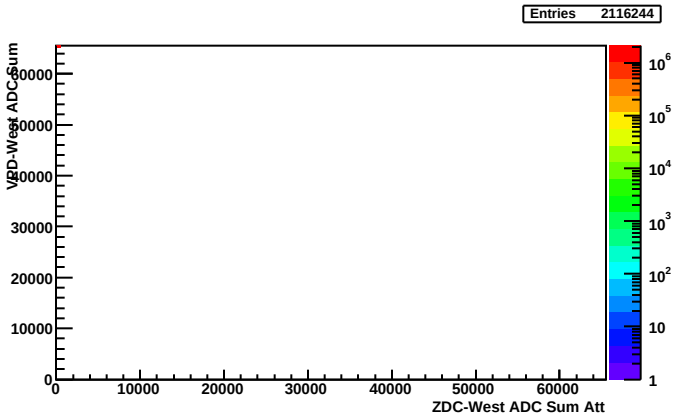
VPD-East ADC Sum
ZDC-East ADC Sum Att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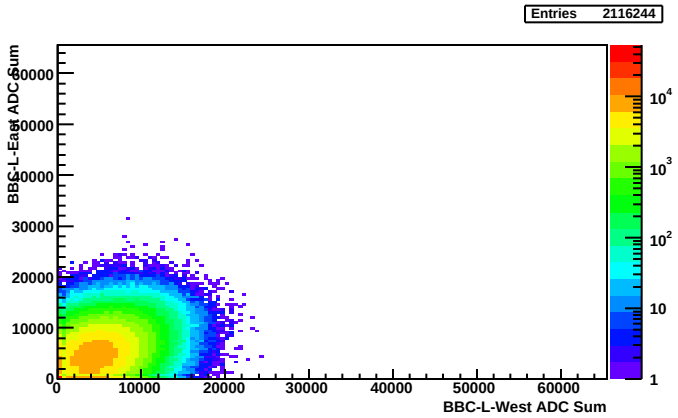
ZDC-West ADC Sum Att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BBC-L-East ADC Sum
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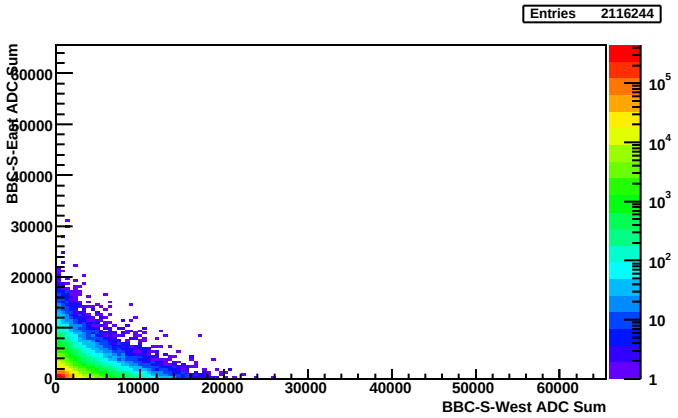
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BBC-S-East ADC Sum
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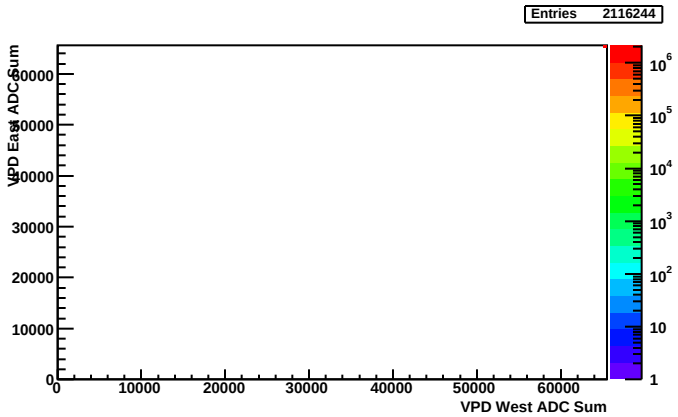
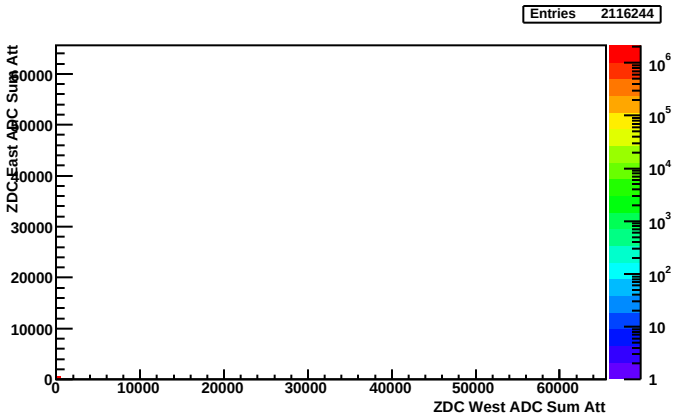
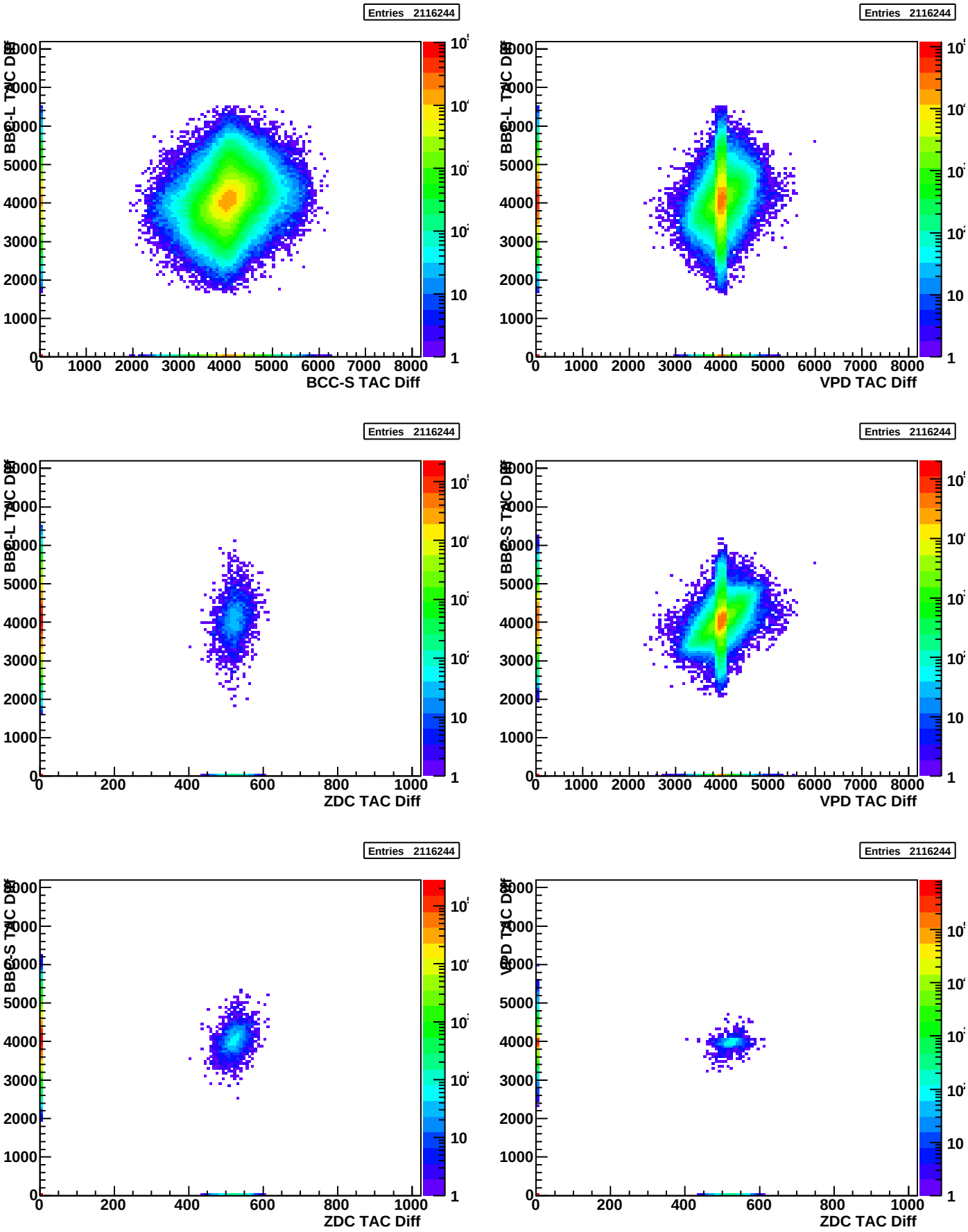
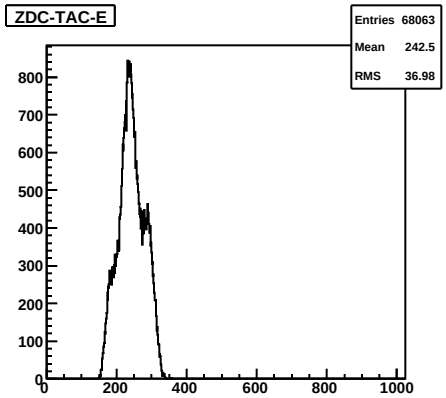
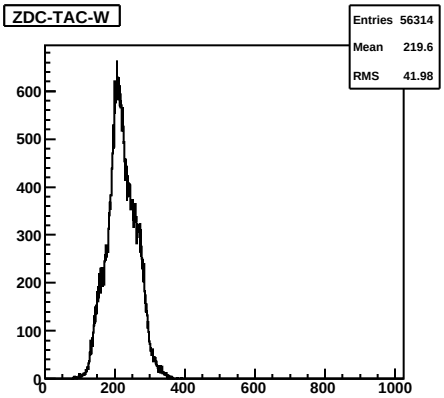
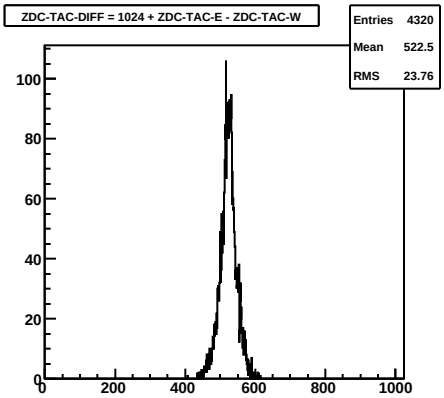

Entries 2116244
VPD East ADC Sum
VPD West ADC Sum

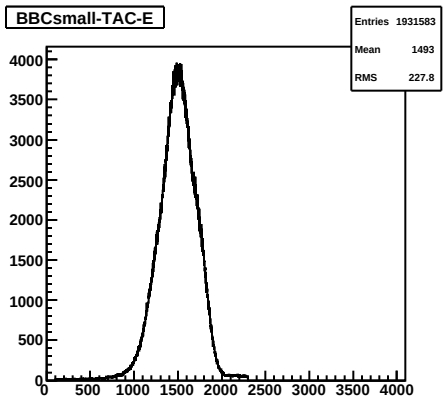

Entries 2116244
ZDC East ADC Sum Att
ZDC West ADC Sum Att

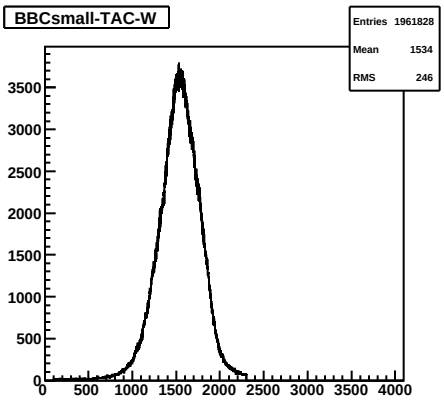

Entries 2116244
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2116244
BBC-L TAC Diff
VPD TAC Diff
Entries 2116244
BBC-L TAC Diff
ZDC TAC Diff
Entries 2116244
BBC-S TAC Diff
VPD TAC Diff
Entries 2116244
BBC-S TAC Diff
ZDC TAC Diff
Entries 2116244
VPD TAC Diff
ZDC TAC Diff

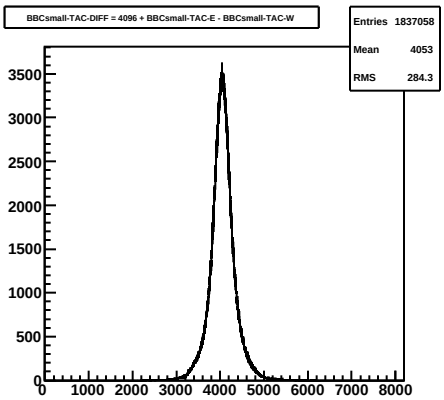

ZDC-TAC-E
Entries 68063
Mean 242.5
RMS 36.98

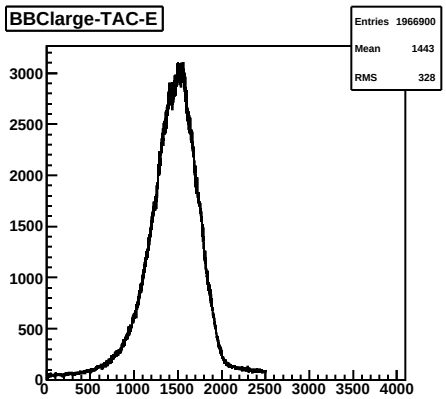

ZDC-TAC-W
Entries 56314
Mean 219.6
RMS 41.98

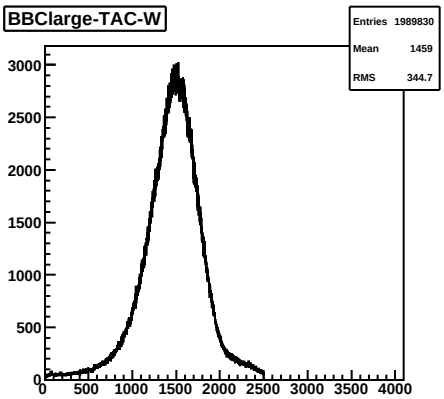

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4320
Mean 522.5
RMS 23.76

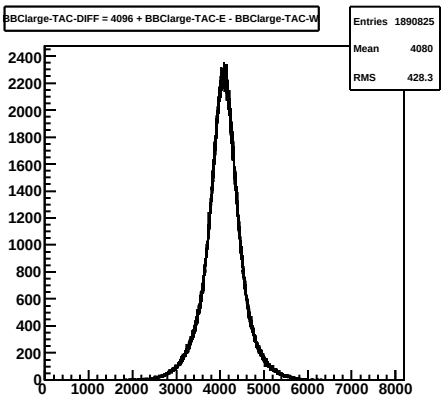

BBCsmall-TAC-E
Entries 1931583
Mean 1493
RMS 227.8

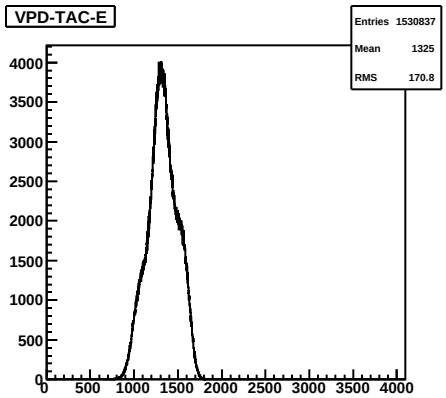

BBCsmall-TAC-W
Entries 1961828
Mean 1534
RMS 246

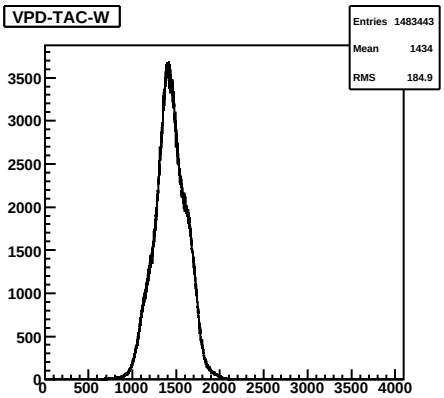

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1837058
Mean 4053
RMS 284.3

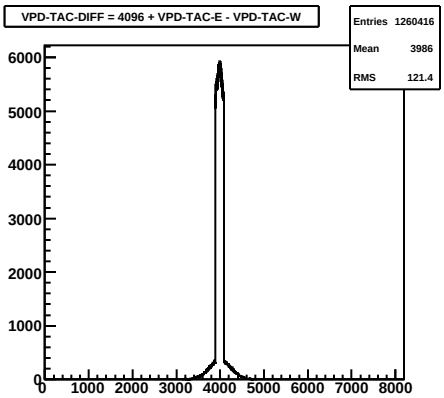

BBClarge-TAC-E
Entries 1966900
Mean 1443
RMS 328

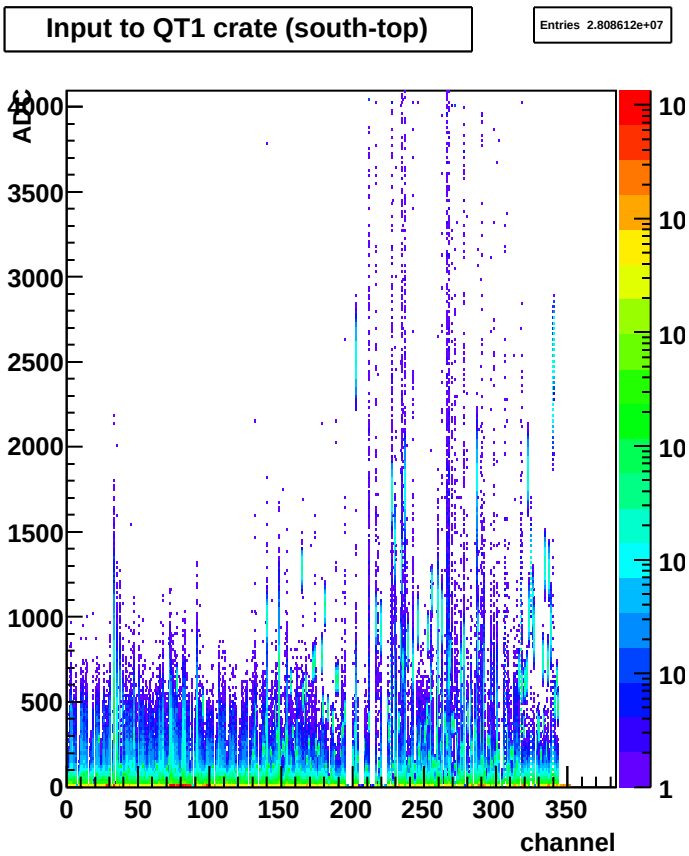

BBClarge-TAC-W
Entries 1989830
Mean 1459
RMS 344.7

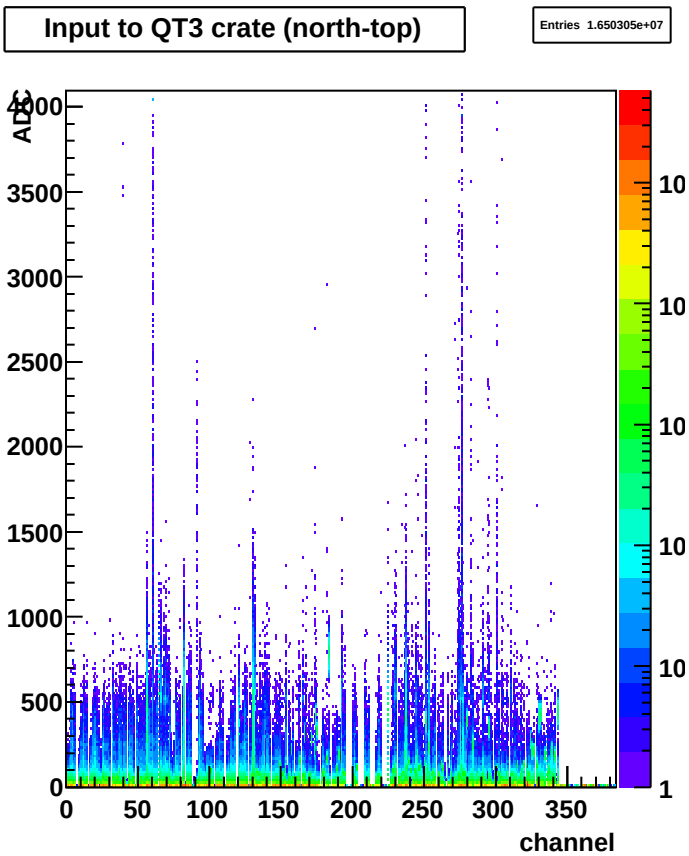

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1890825
Mean 4080
RMS 428.3

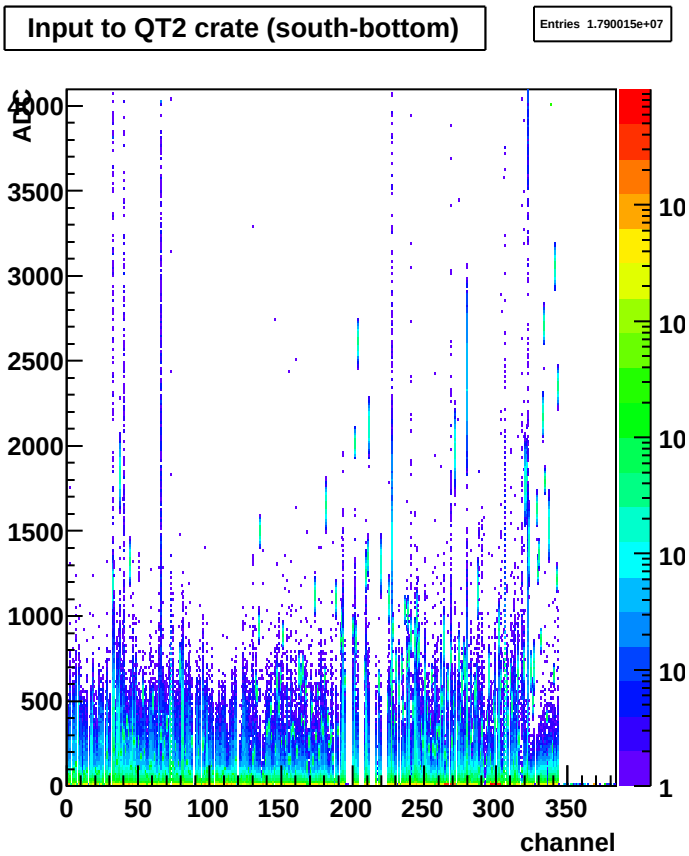

VPD-TAC-E
Entries 1530837
Mean 1325
RMS 170.8

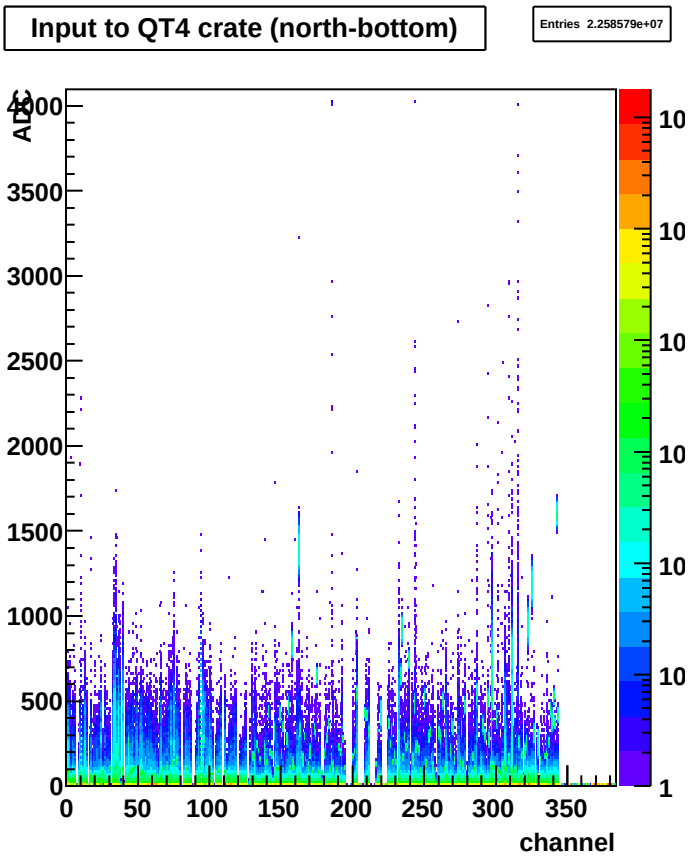

VPD-TAC-W
Entries 1483443
Mean 1434
RMS 184.9

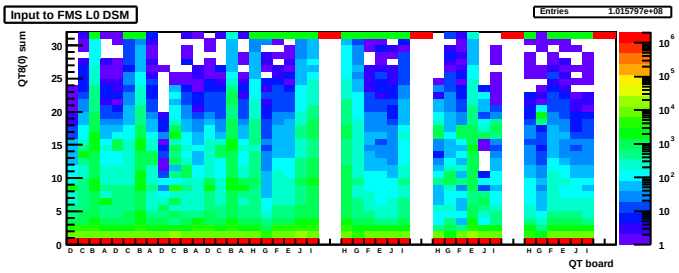

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1260416
Mean 3986
RMS 121.4

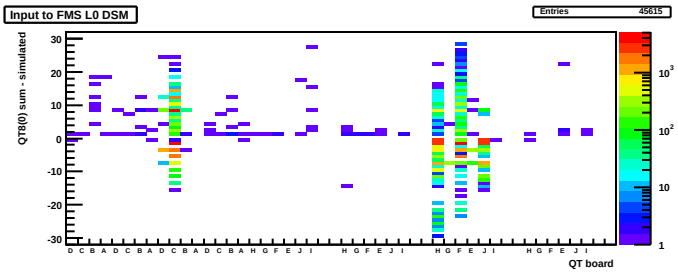

Input to QT1 crate (south-top)
Entries 2.808612e+07
ADC
channel

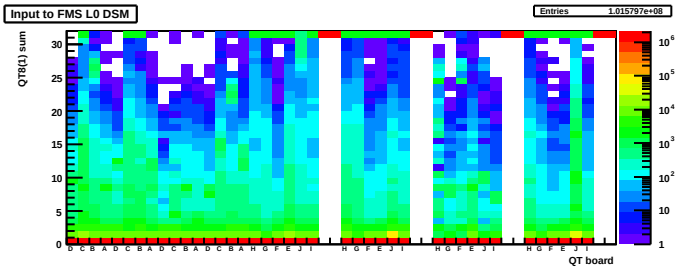

Input to QT3 crate (north-top)
Entries 1.650305e+07
ADC
channel

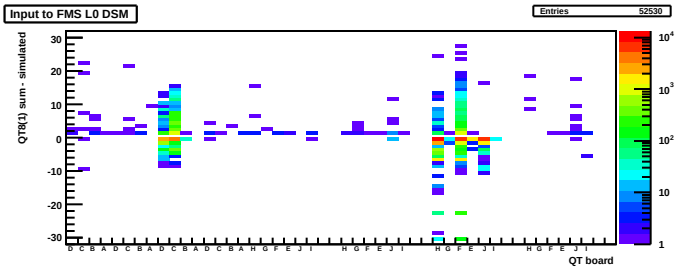

Input to QT2 crate (south-bottom)
Entries 1.790015e+07
ADC
channel

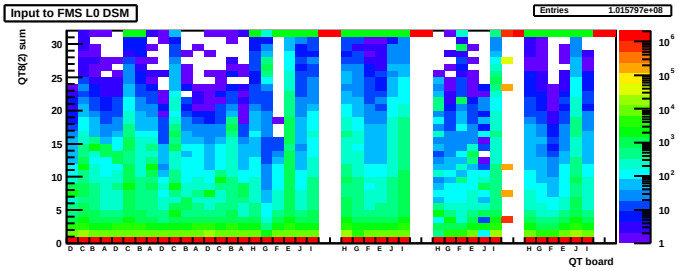

Input to QT4 crate (north-bottom)
Entries 2.258579e+07
ADC
channel

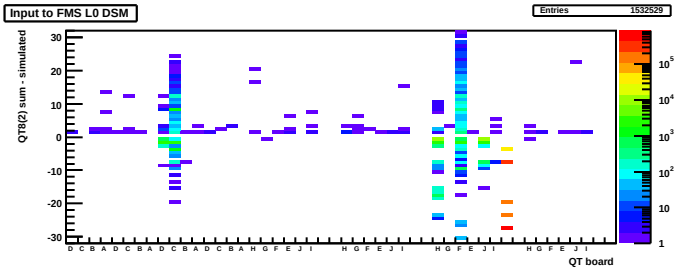

Input to FMS L0 DSM
Entries 1.015797e+08
QT8(0) sum
QT board

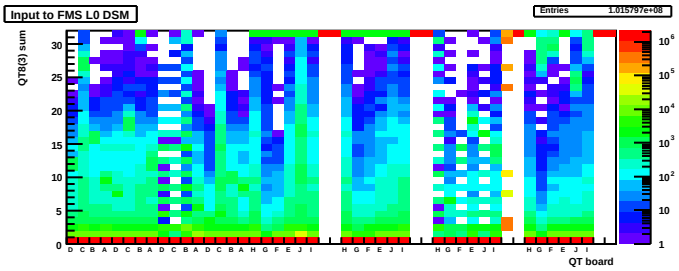

Input to FMS L0 DSM
Entries 45615
QT8(0) sum - simulated
QT board

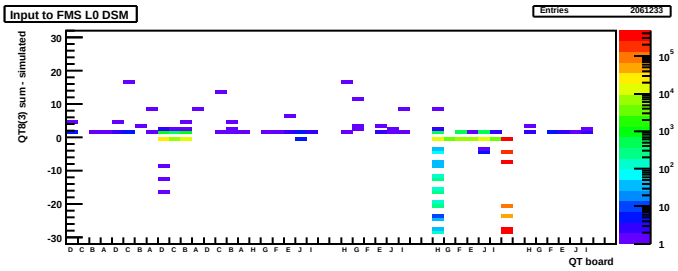

Input to FMS L0 DSM
Entries 1.015797e+08
QT8(1) sum
QT board

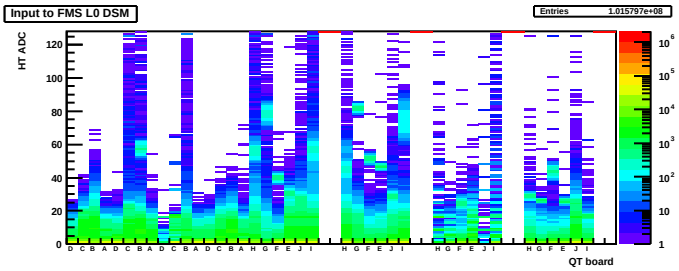

Input to FMS L0 DSM
Entries 52530
QT8(1) sum - simulated
QT board

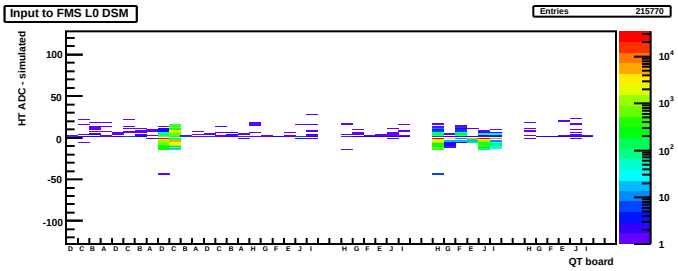

Input to FMS L0 DSM
Entries 1.015797e+08
QT8(2) sum
QT board

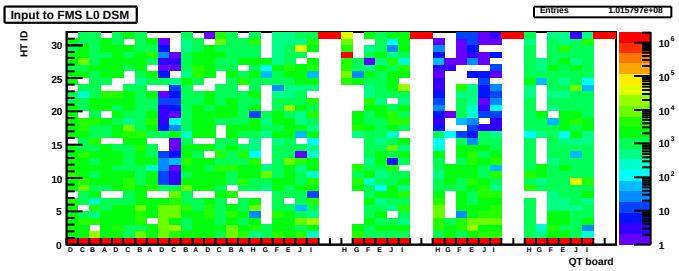

Input to FMS L0 DSM
Entries 1532529
QT8(2) sum - simulated
QT board

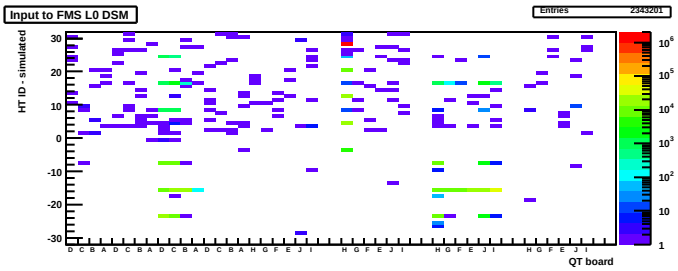

Input to FMS L0 DSM
Entries 1.015797e+08
QT8(3) sum
QT board

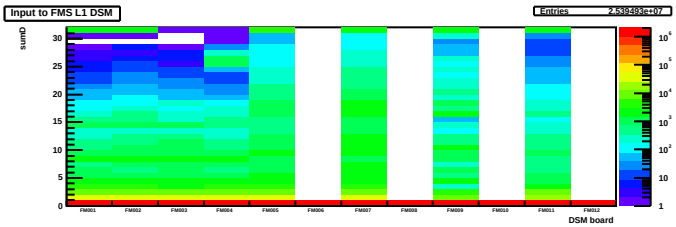

Input to FMS L0 DSM
Entries 2061233
QT8(3) sum - simulated
QT board

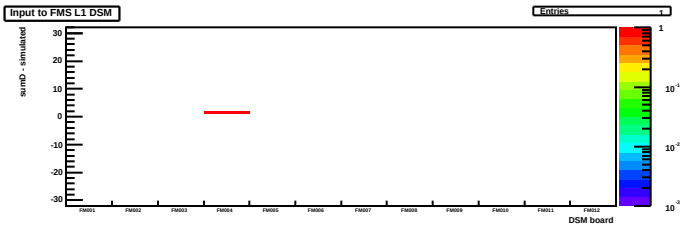

Input to FMS L0 DSM
Entries 1.015797e+08
HT ADC
QT board

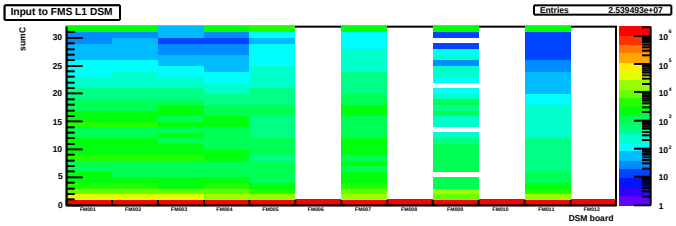

Input to FMS L0 DSM
Entries 215770
HT ADC - simulated
QT board

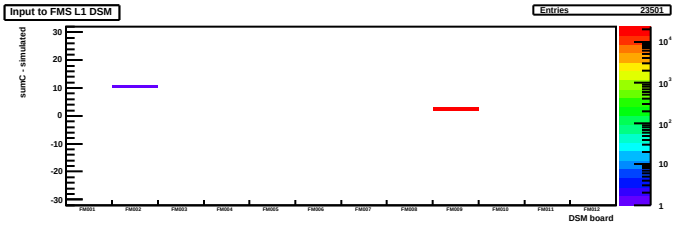

Input to FMS L0 DSM
Entries 1.015797e+08
HT ID
QT board

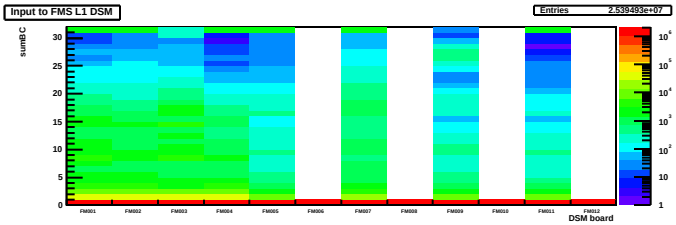

Input to FMS L0 DSM
Entries 2343201
HT ID - simulated
QT board

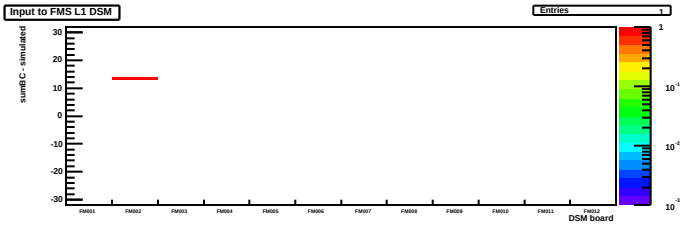

Input to FMS L1 DSM
Entries 2.539493e+07
sumD
DSM board

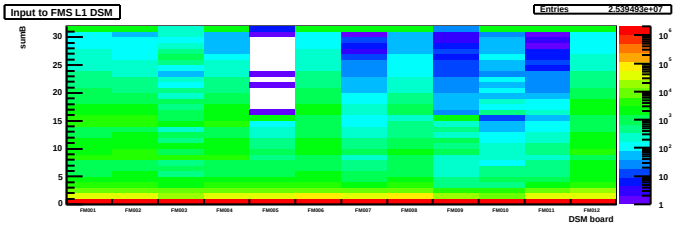

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

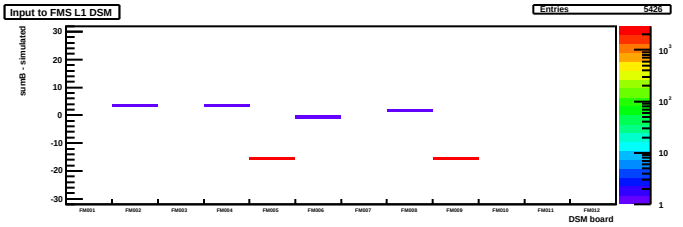

Input to FMS L1 DSM
Entries 2.539493e+07
sumC
DSM board

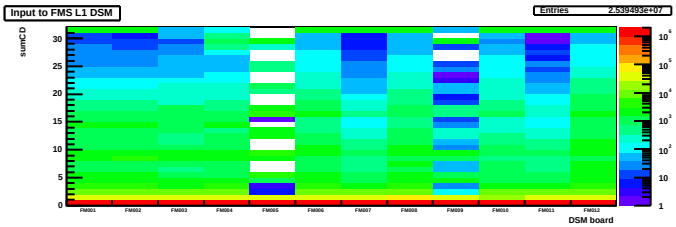

Input to FMS L1 DSM
Entries 23501
sumC - simulated
DSM board

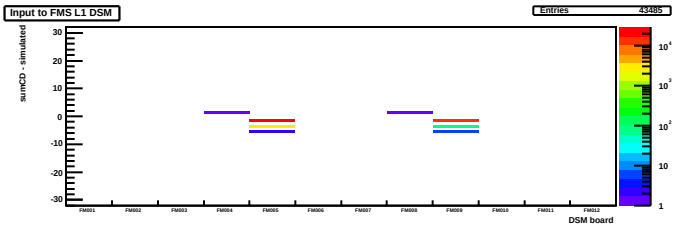

Input to FMS L1 DSM
Entries 2.539493e+07
sumBC
DSM board

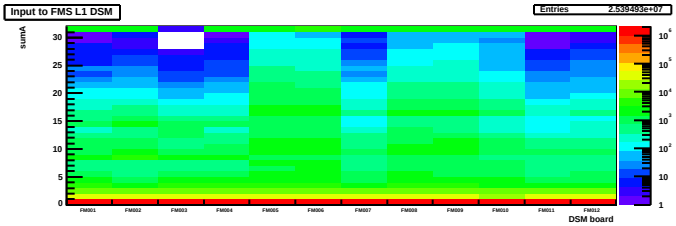

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

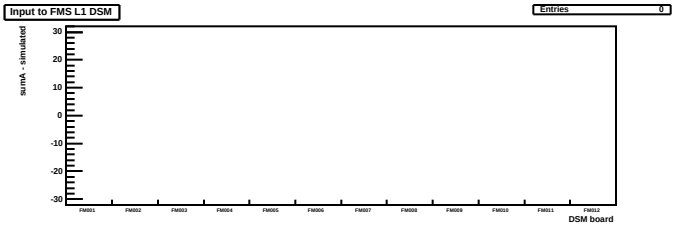

Input to FMS L1 DSM
Entries 2.539493e+07
sumB
DSM board

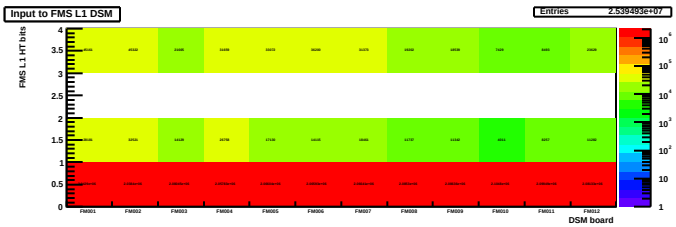

Input to FMS L1 DSM
Entries 5426
sumB - simulated
DSM board

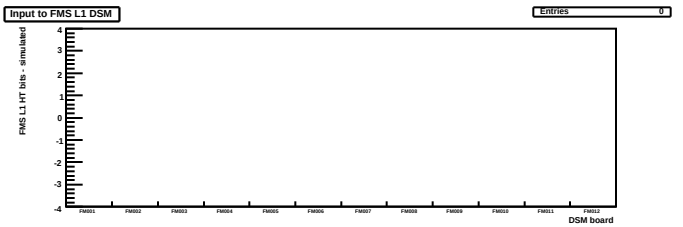

Input to FMS L1 DSM
Entries 2.539493e+07
sumCD
DSM board

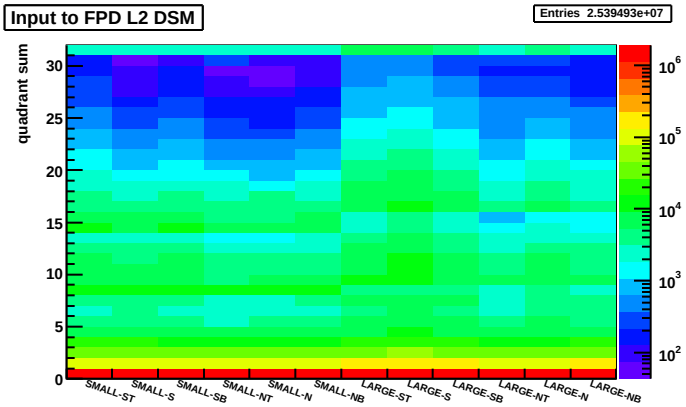

Input to FMS L1 DSM
Entries 43485
sumCD - simulated
DSM board

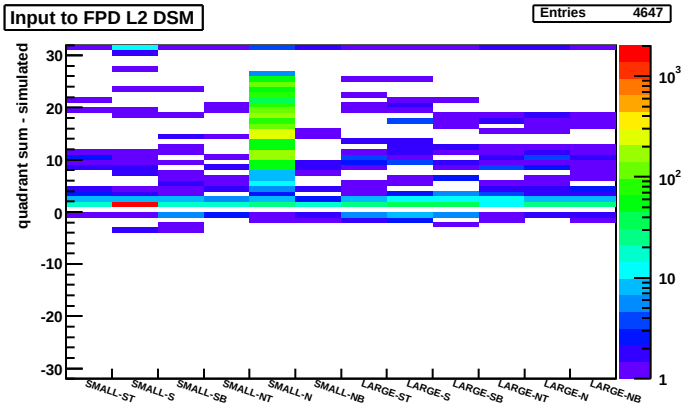

Input to FMS L1 DSM
Entries 2.539493e+07
sumA
DSM board

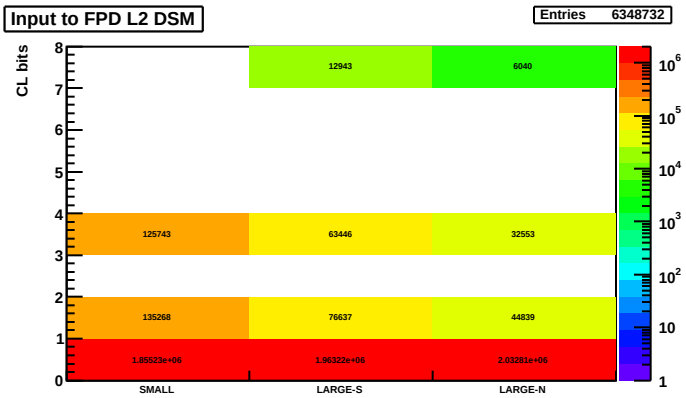

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

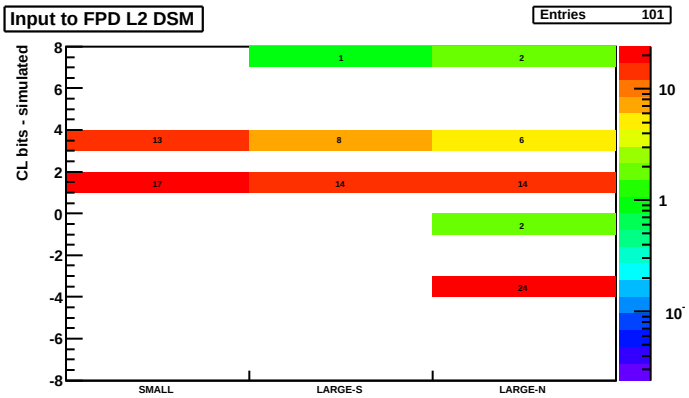

Input to FMS L1 DSM
Entries 2.539493e+07
FMS L1 HT bits
DSM board

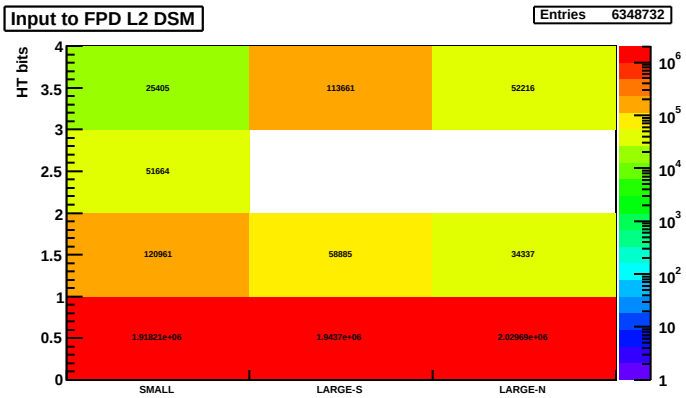

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

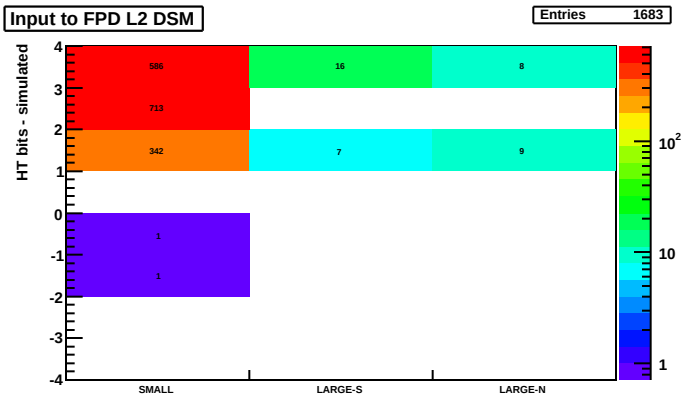

Input to FPD L2 DSM
Entries 2.539493e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

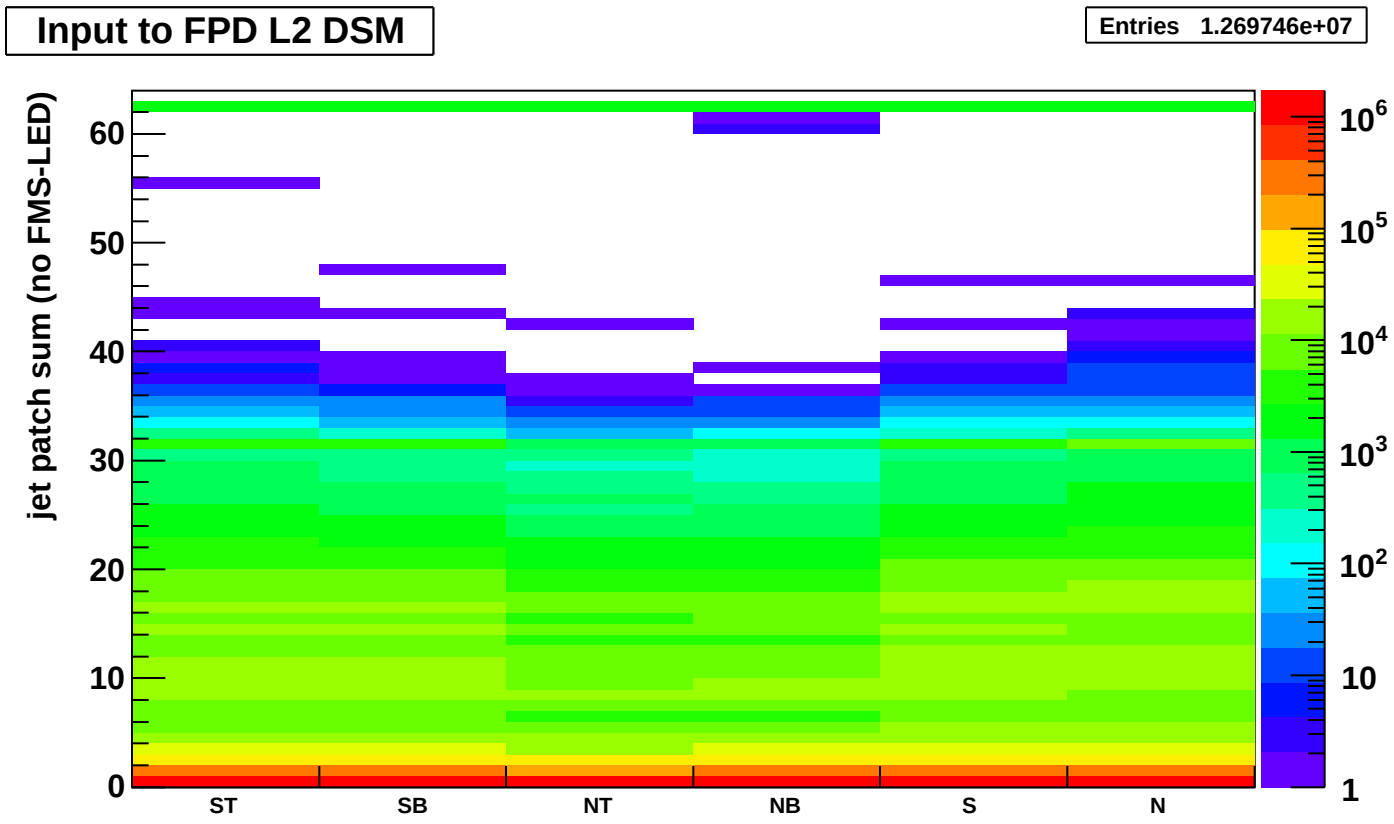

Input to FPD L2 DSM
Entries 4647
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

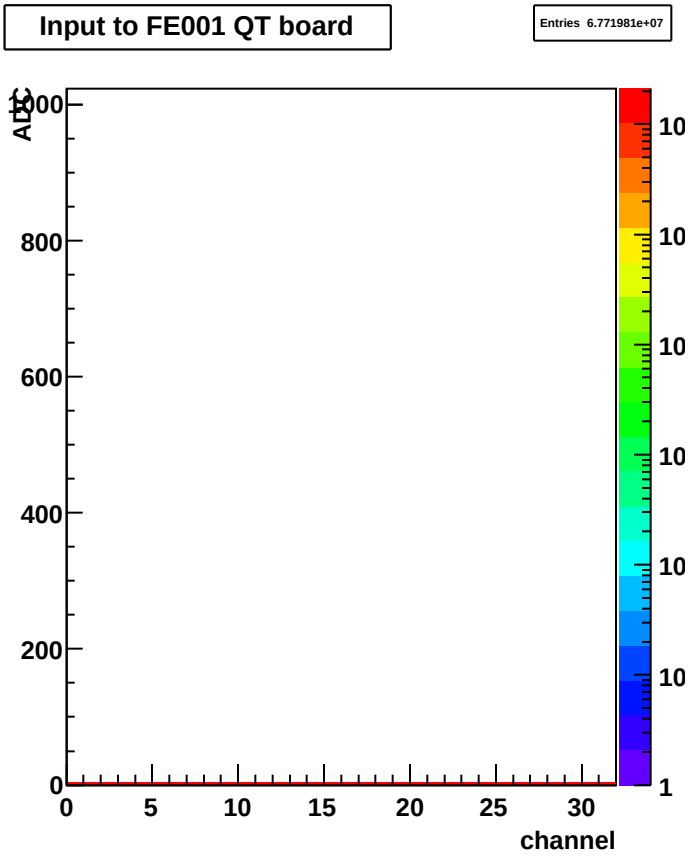

Input to FPD L2 DSM
Entries 6348732
CL bits
12943
6040
125743
63446
32553
135268
76637
44839
1.85523e+06
1.96322e+06
2.03281e+06
SMALL
LARGE-S
LARGE-N

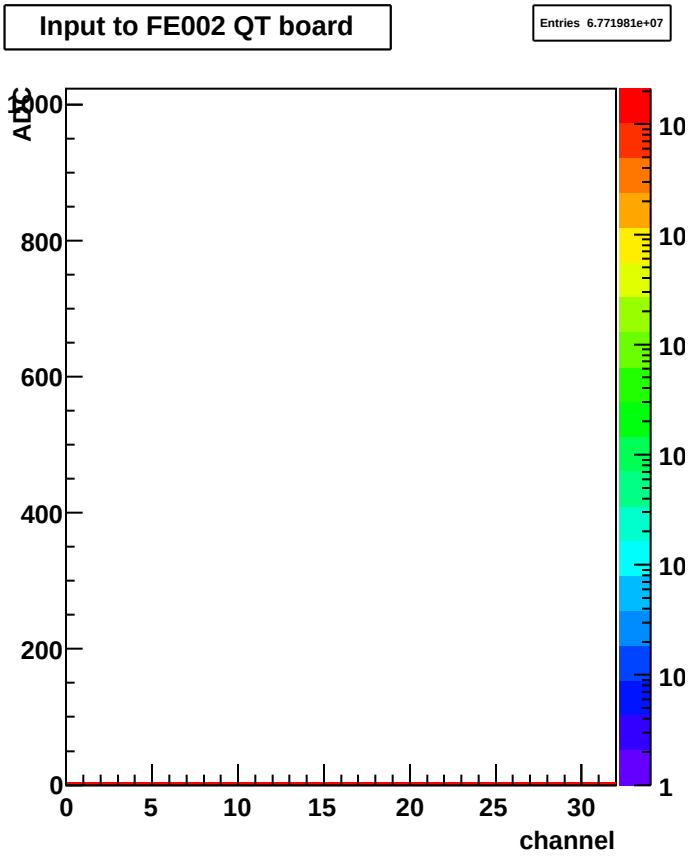

Input to FPD L2 DSM
Entries 101
CL bits - simulated
1
2
13
8
6
17
14
14
2
24
SMALL
LARGE-S
LARGE-N

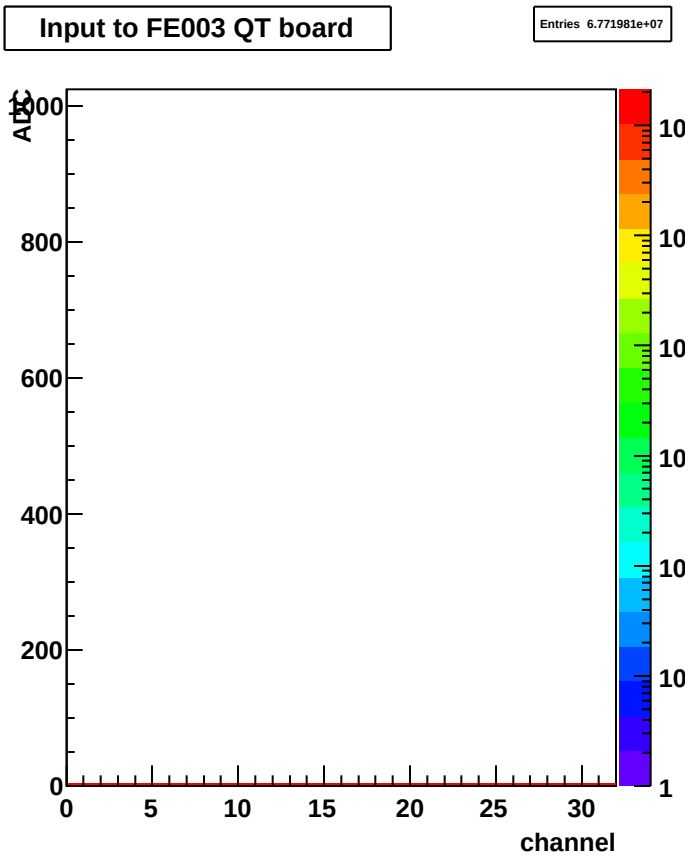

Input to FPD L2 DSM
Entries 6348732
HT bits
25405
113661
52216
51664
120961
58885
34337
1.91821e+06
1.9437e+06
2.02969e+06
SMALL
LARGE-S
LARGE-N

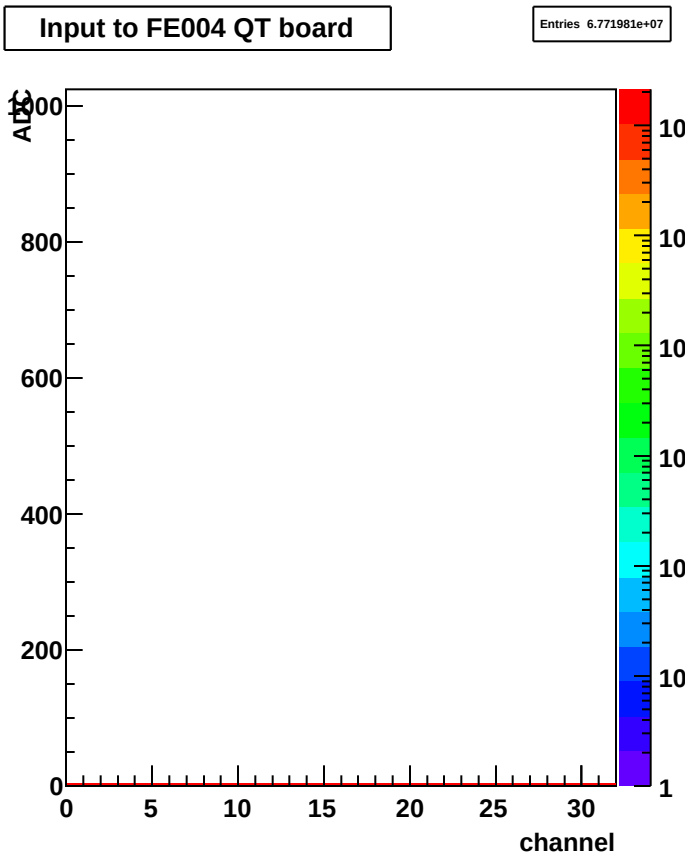

Input to FPD L2 DSM
Entries 1683
HT bits - simulated
586
16
8
713
342
7
9
1
1
SMALL
LARGE-S
LARGE-N

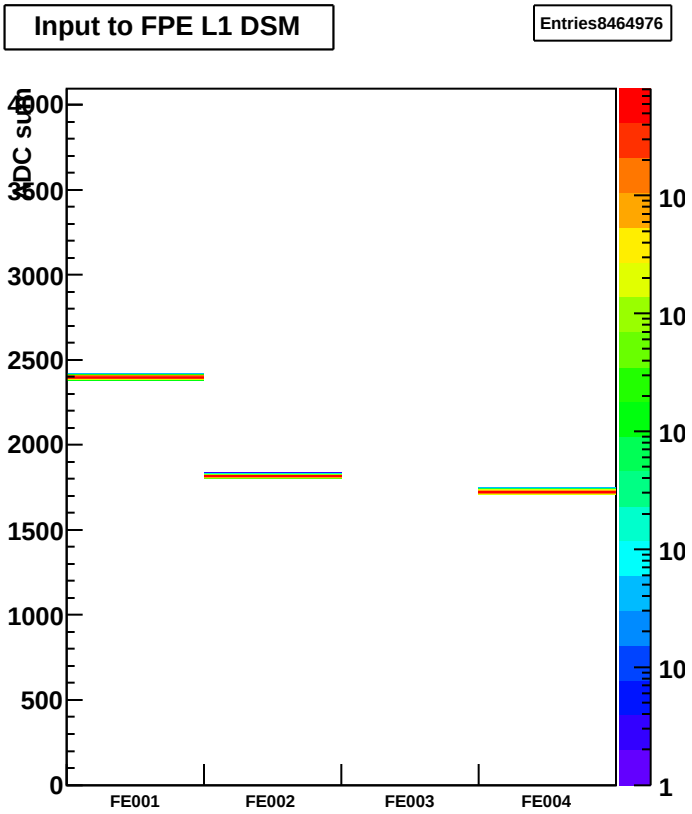

Input to FPD L2 DSM
Entries 1.269746e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.771981e+07
ADC
channel

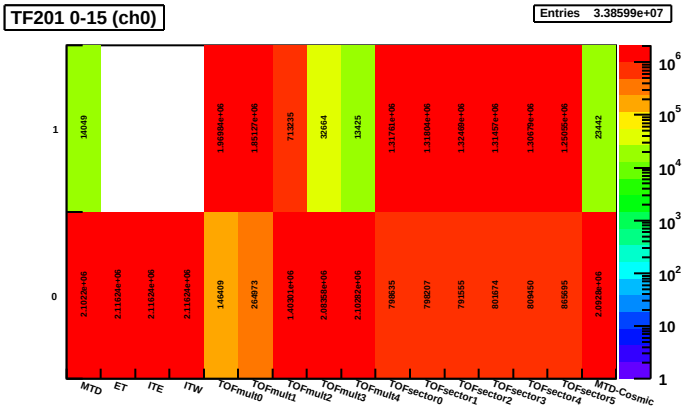

Input to FE002 QT board
Entries 6.771981e+07
ADC
channel


Input to FE003 QT board
Entries 6.771981e+07
ADC
channel

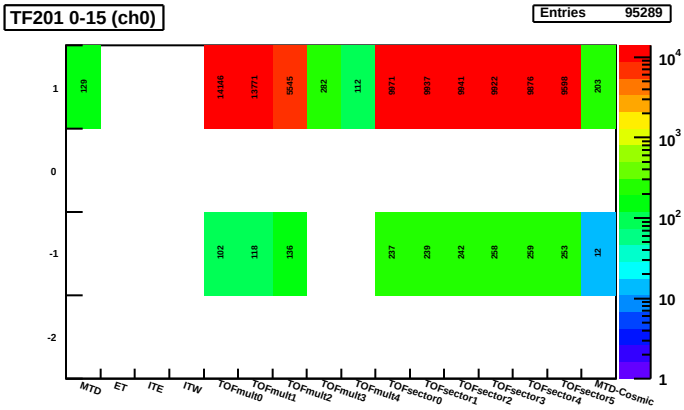

Input to FE004 QT board
Entries 6.771981e+07
ADC
channel


Input to FPE L1 DSM
Entries8464976
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

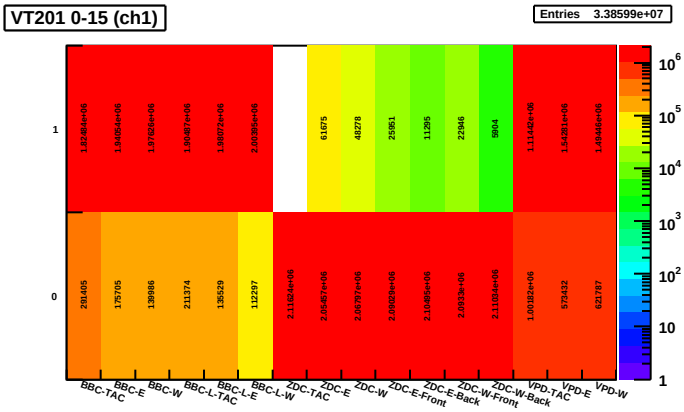
TF201 0-15 (ch0)



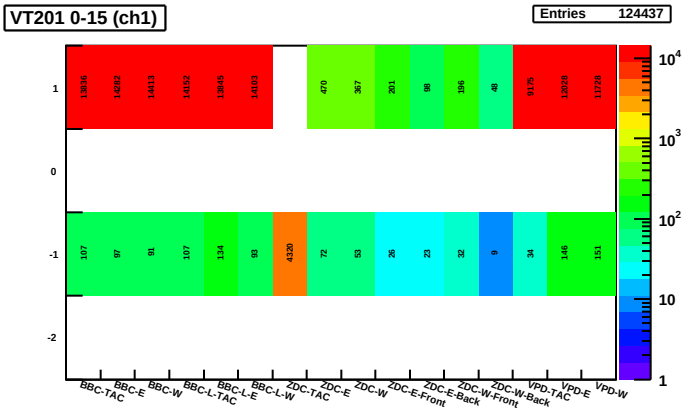
TF201 0-15 (ch0)



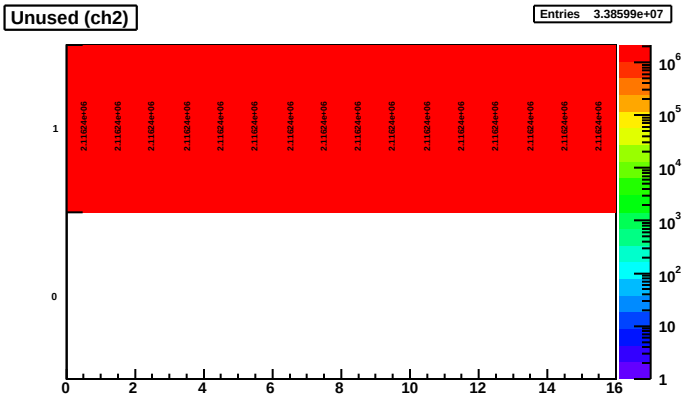
VT201 0-15 (ch1)

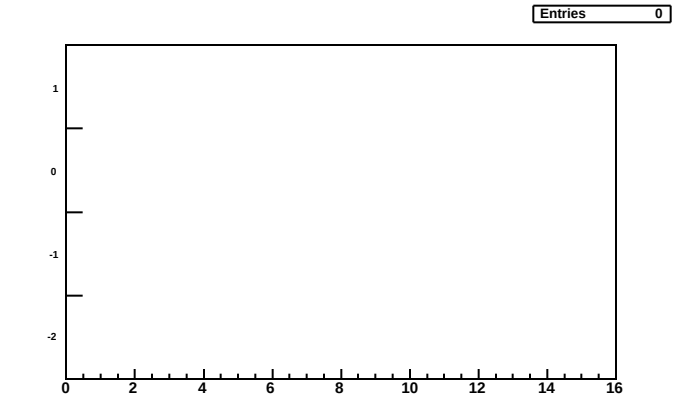


VT201 0-15 (ch1)

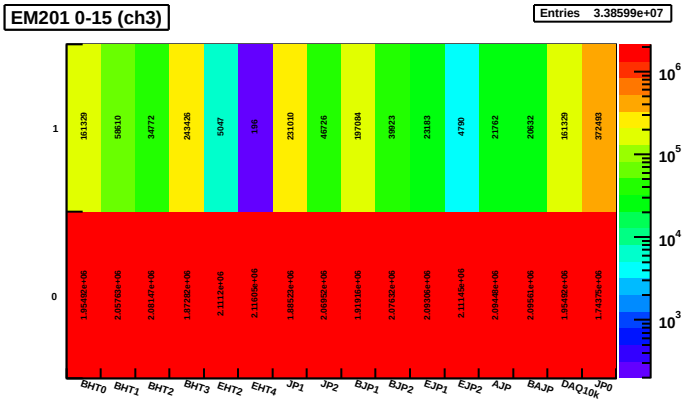


Unused (ch2)





EM201 0-15 (ch3)



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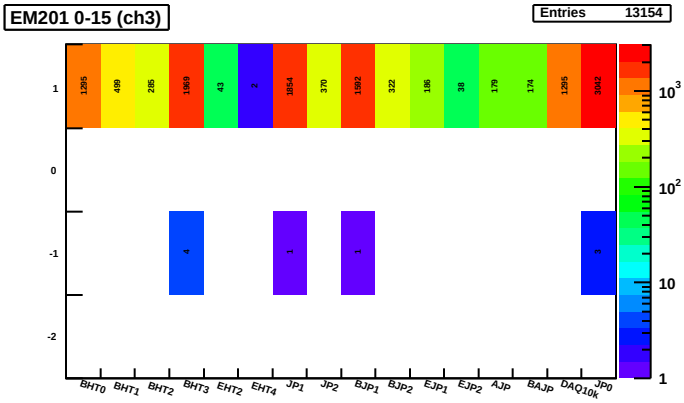
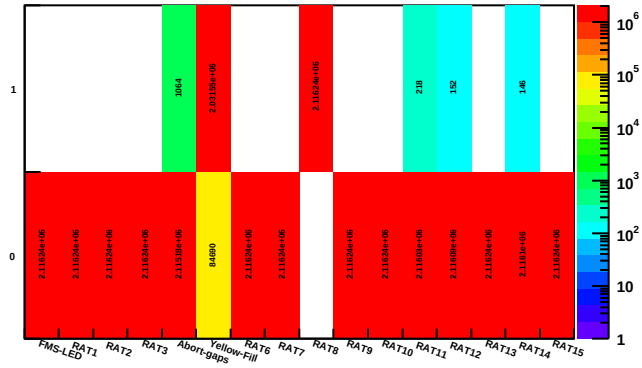
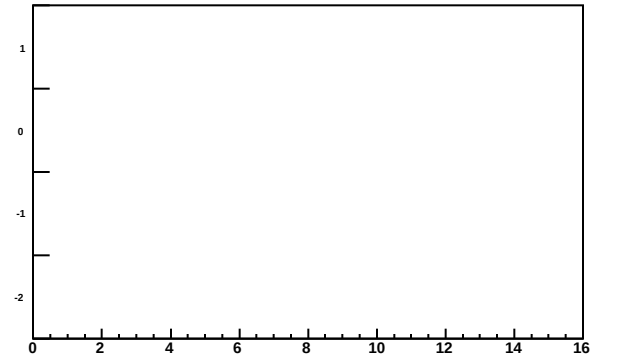


RAT board (ch4)





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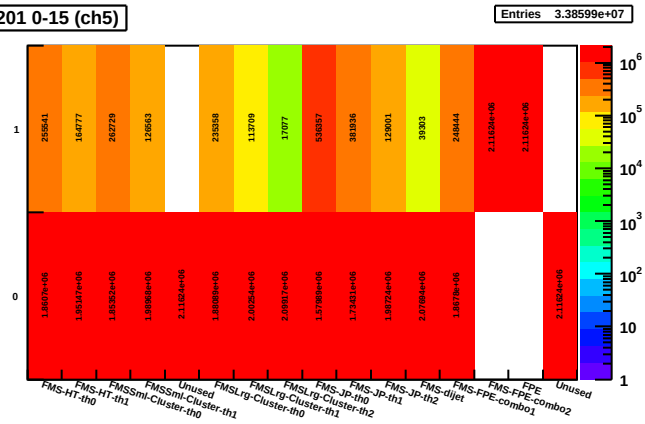

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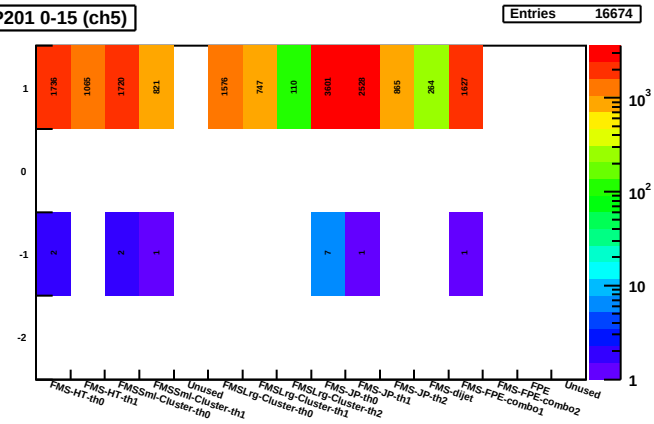

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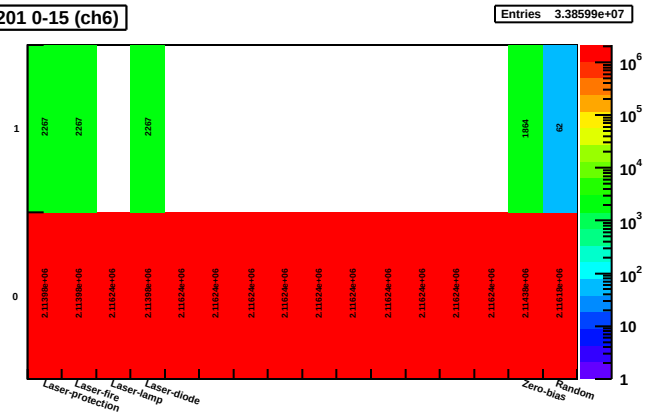

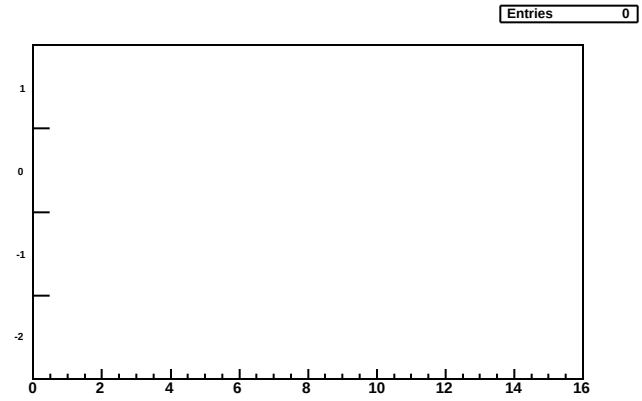
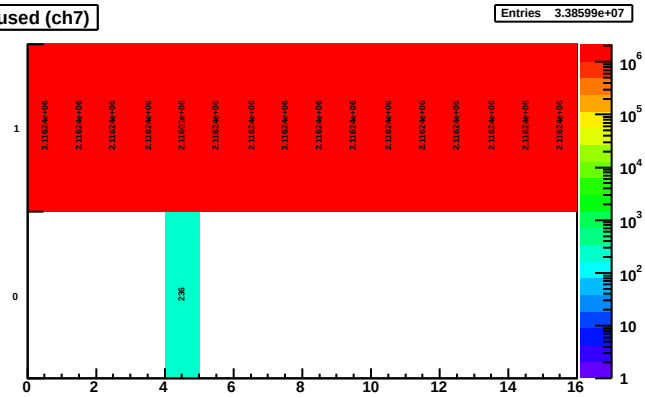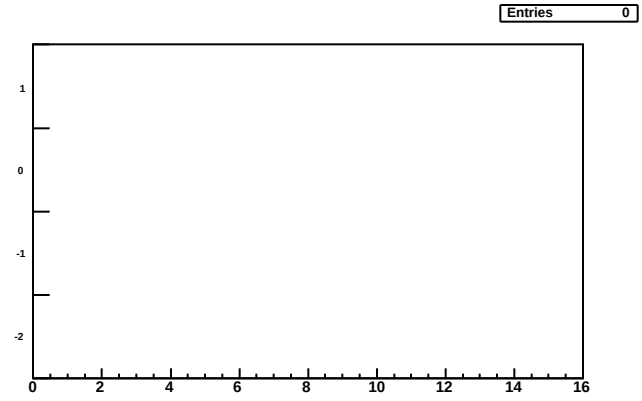
Unused (ch7)