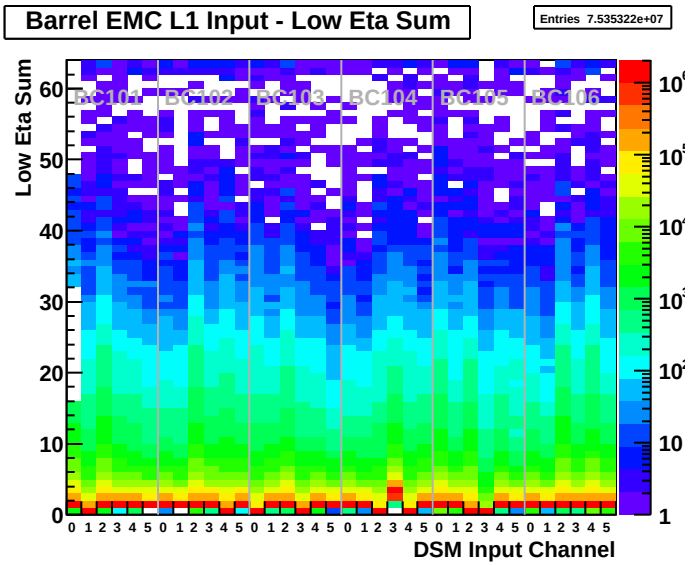
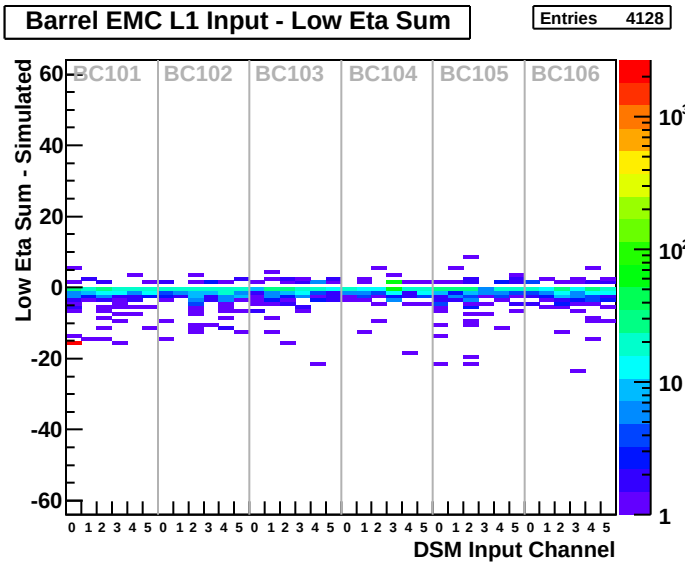
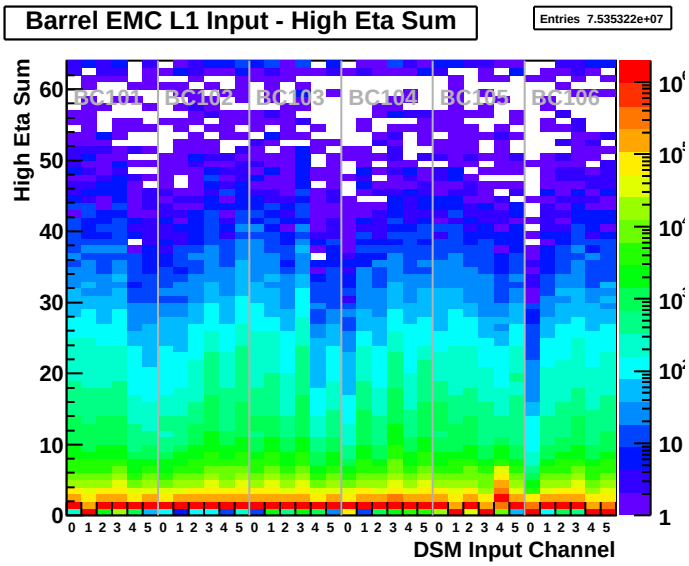
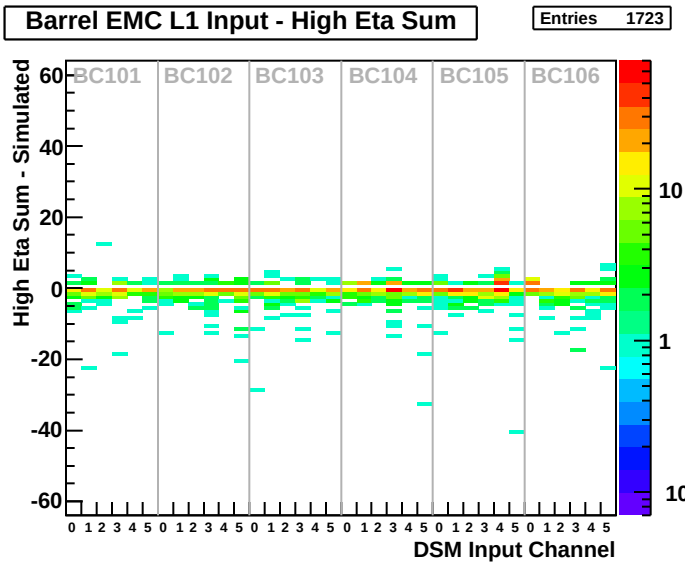
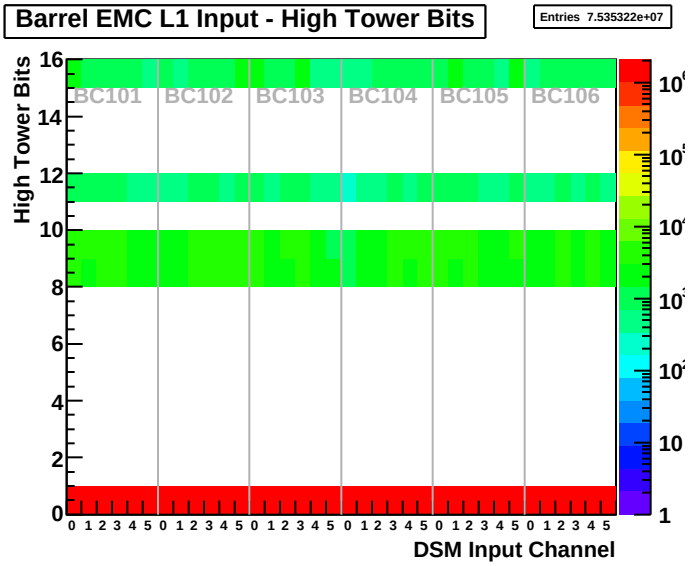
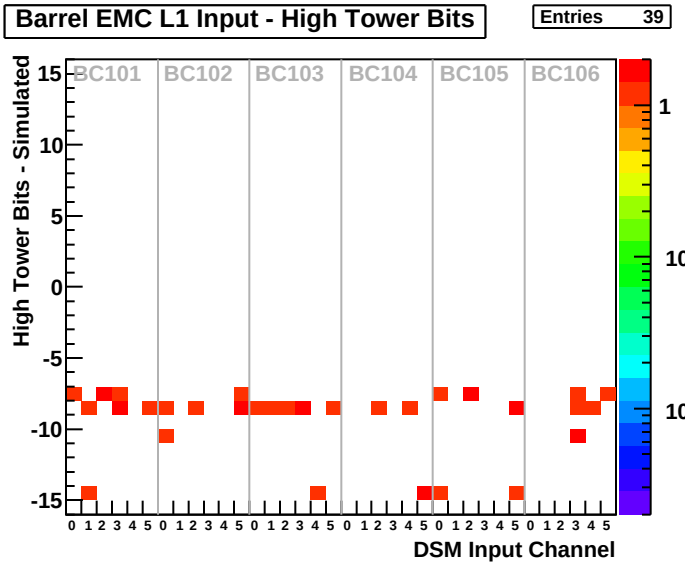

Barrel EMC L1 Input - Low Eta Sum
Entries 7.535322e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

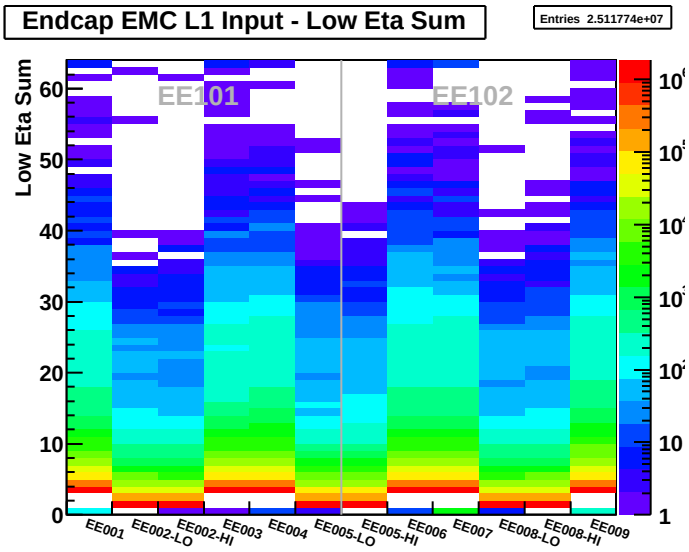

Barrel EMC L1 Input - Low Eta Sum
Entries 4128
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

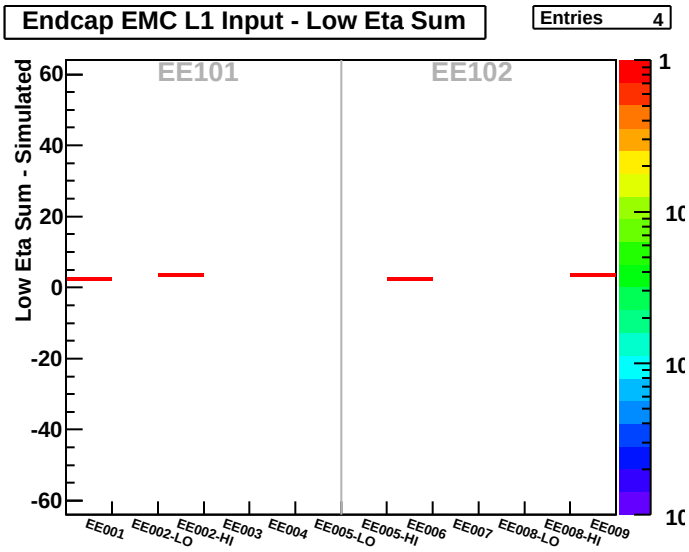

Barrel EMC L1 Input - High Eta Sum
Entries 7.535322e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

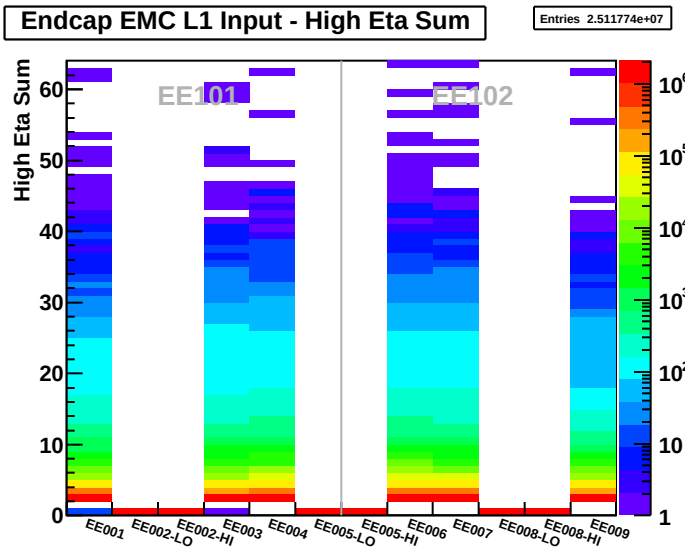

Barrel EMC L1 Input - High Eta Sum
Entries 1723
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

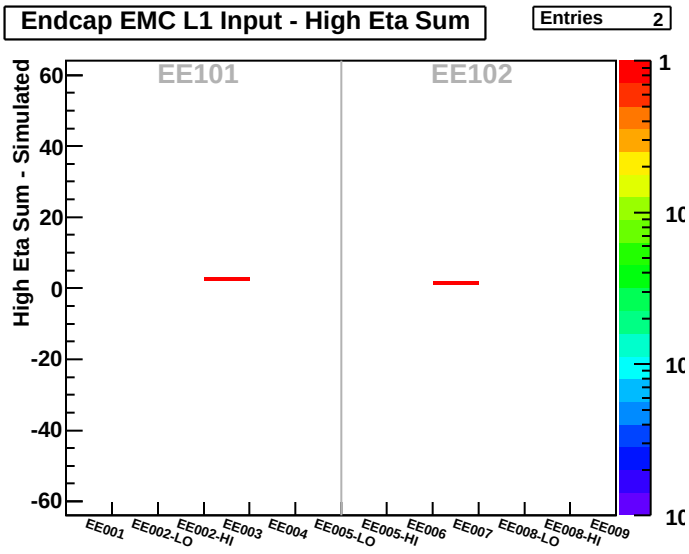

Barrel EMC L1 Input - High Tower Bits
Entries 7.535322e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

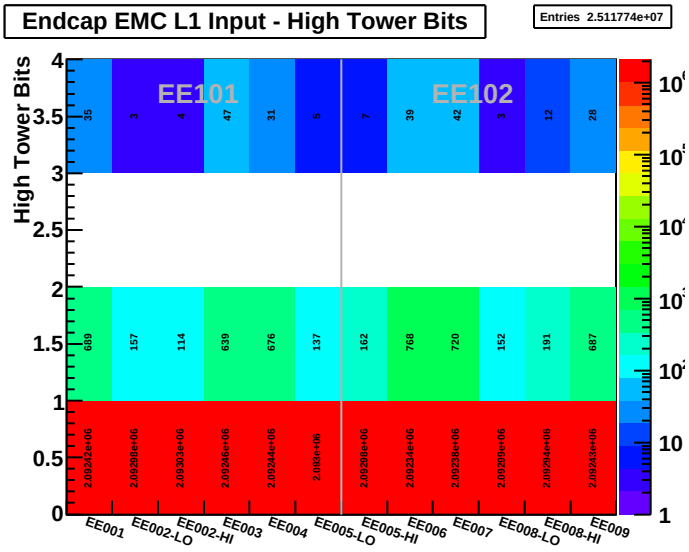

Barrel EMC L1 Input - High Tower Bits
Entries 39
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

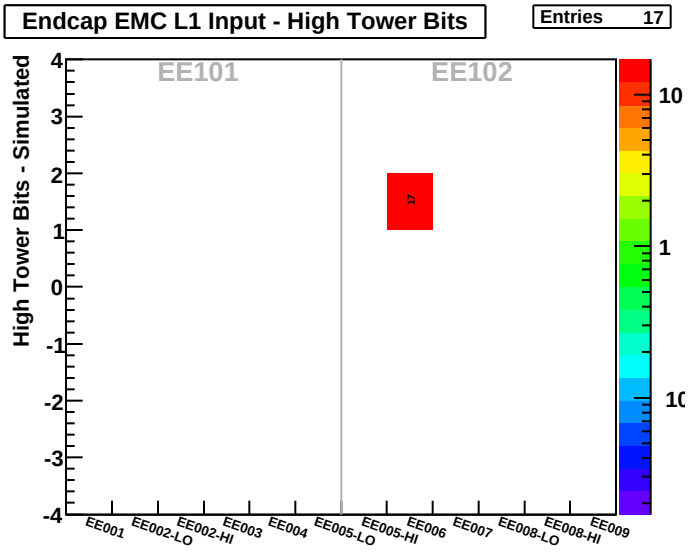

Endcap EMC L1 Input - Low Eta Sum
Entries 2.511774e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

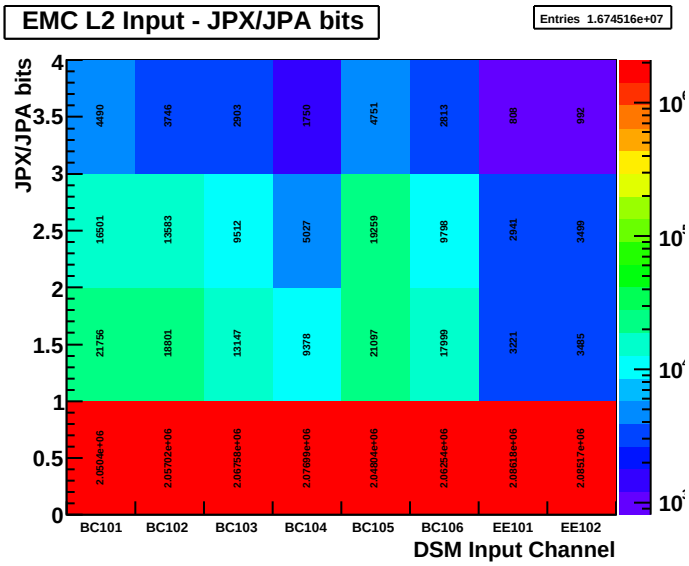

Endcap EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

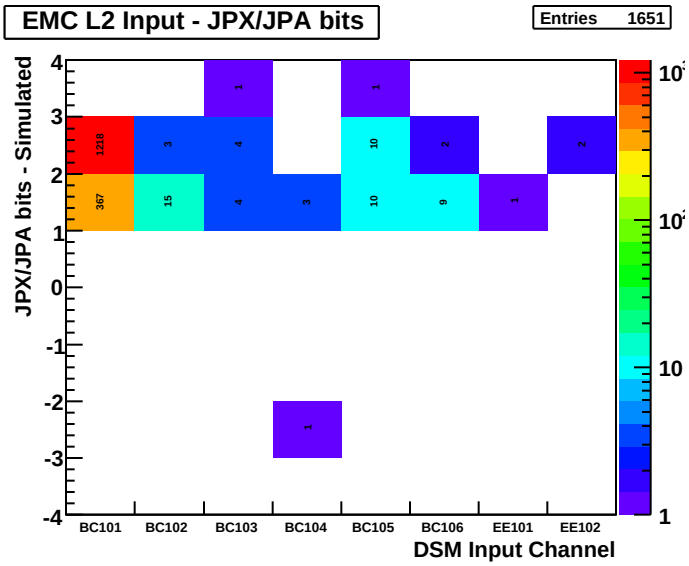

Endcap EMC L1 Input - High Eta Sum
Entries 2.511774e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

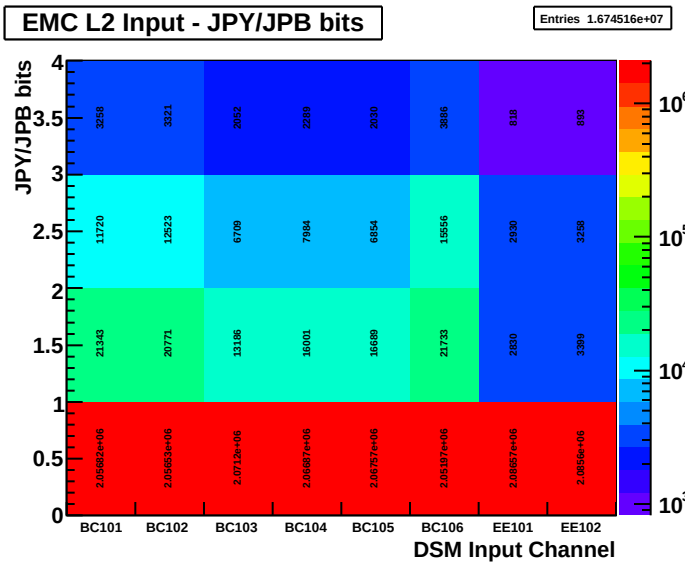

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

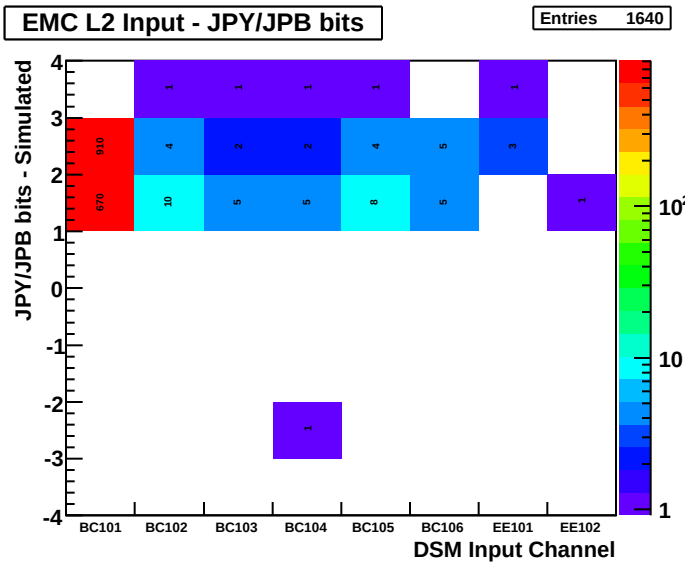

Endcap EMC L1 Input - High Tower Bits
Entries 2.511774e+07
High Tower Bits
EE101
EE102
35 3 4 47 31 5 7 39 42 3 12 28
689 157 114 639 676 137 162 768 720 152 191 687
2.09242e+06 2.09239e+06 2.09303e+06 2.09246e+06 2.09244e+06 2.093e+06 2.09238e+06 2.09234e+06 2.09238e+06 2.09239e+06 2.09234e+06 2.09243e+06
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

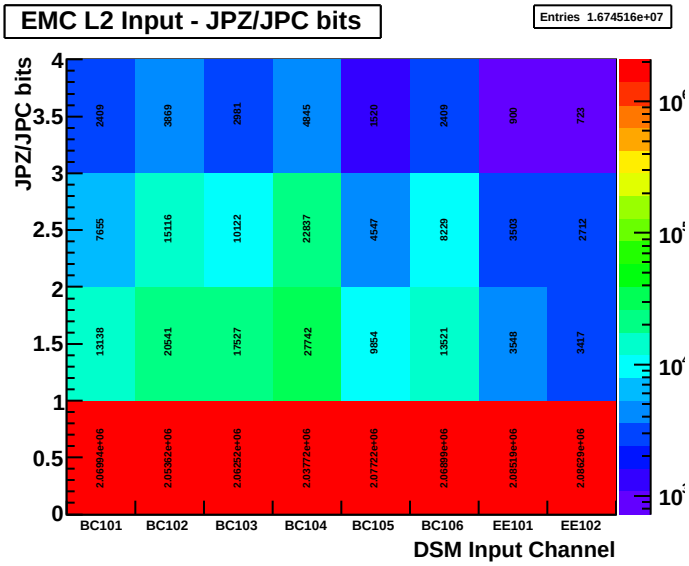

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
17
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

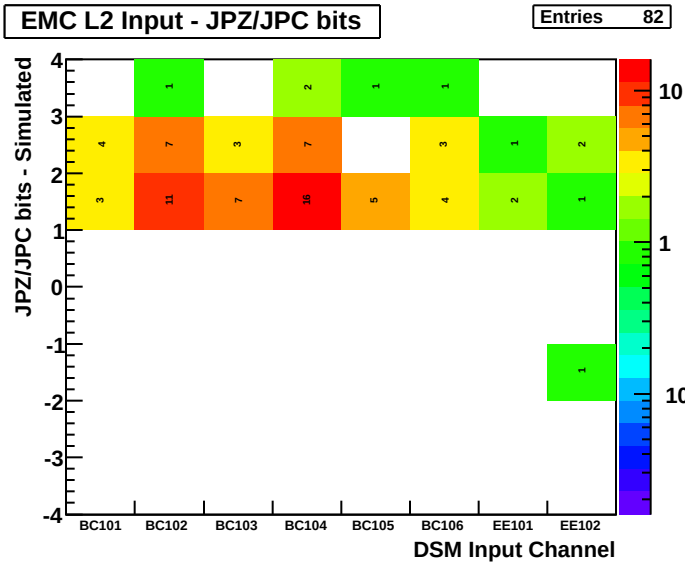

EMC L2 Input - JPX/JPA bits
Entries 1.674516e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

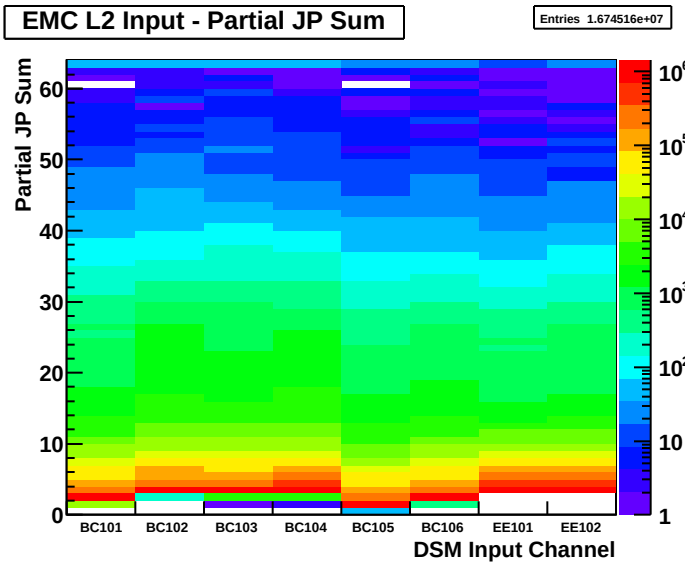

EMC L2 Input - JPX/JPA bits
Entries 1651
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

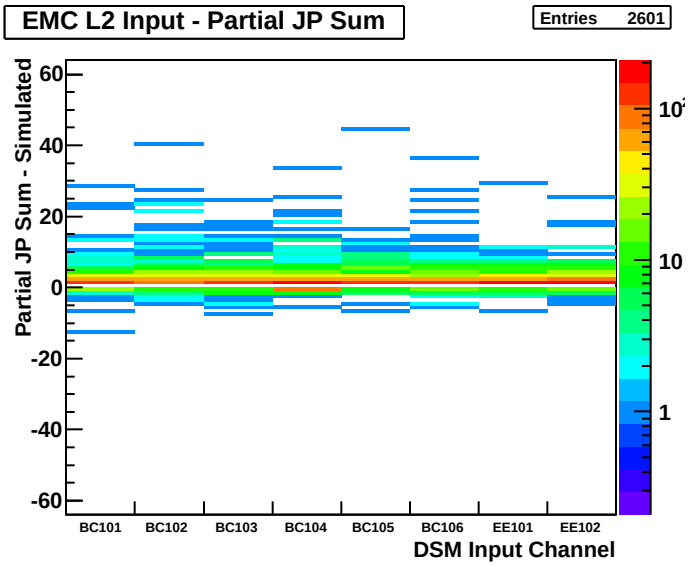

EMC L2 Input - JPY/JPB bits
Entries 1.674516e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

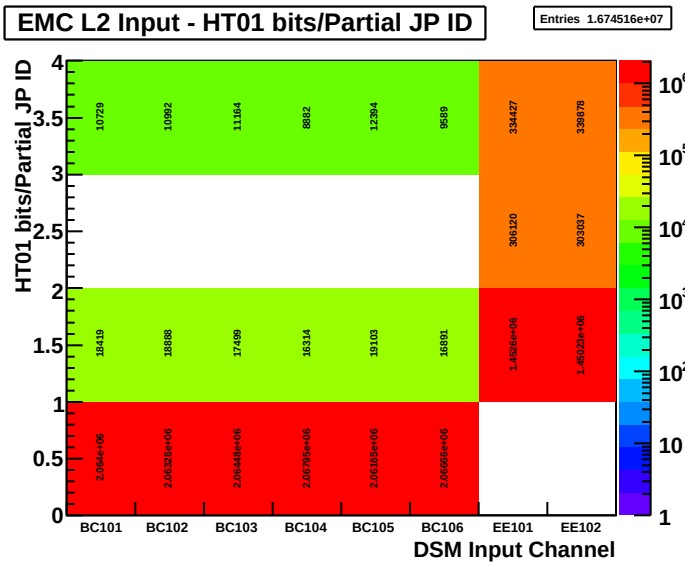

EMC L2 Input - JPY/JPB bits
Entries 1640
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

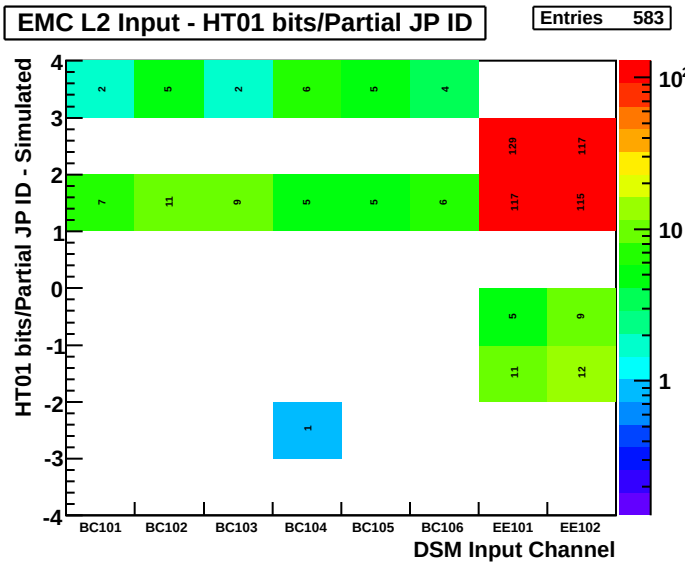

EMC L2 Input - JPZ/JPC bits
Entries 1.674516e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

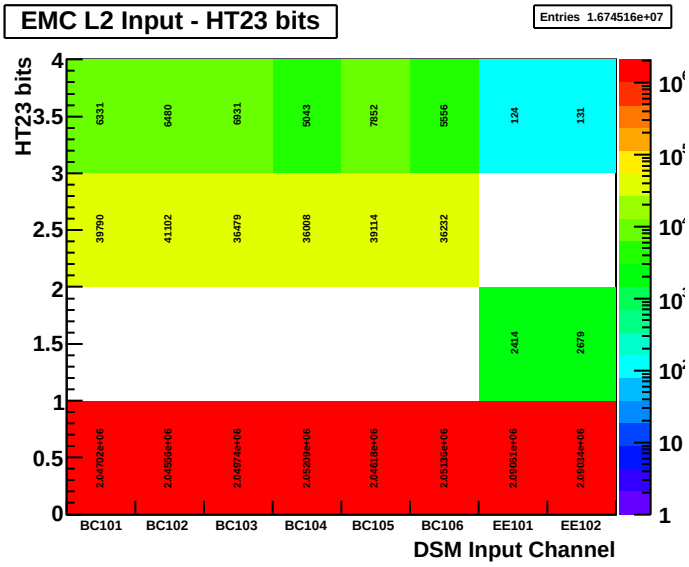

EMC L2 Input - JPZ/JPC bits
Entries 82
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

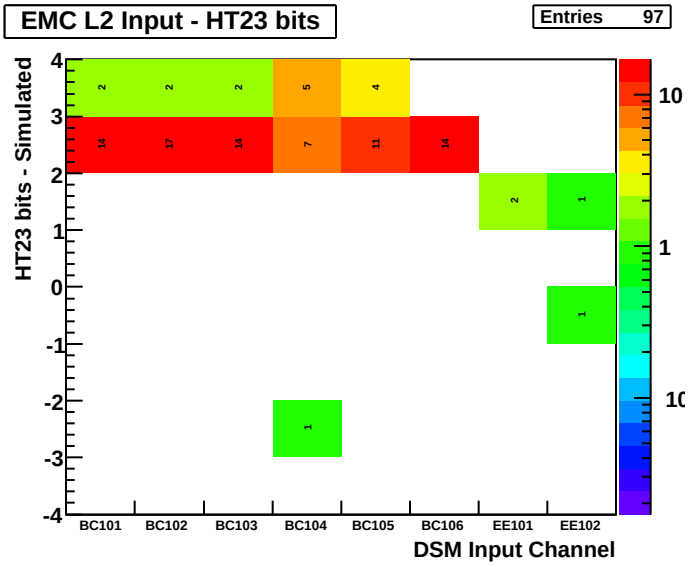

EMC L2 Input - Partial JP Sum
Entries 1.674516e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

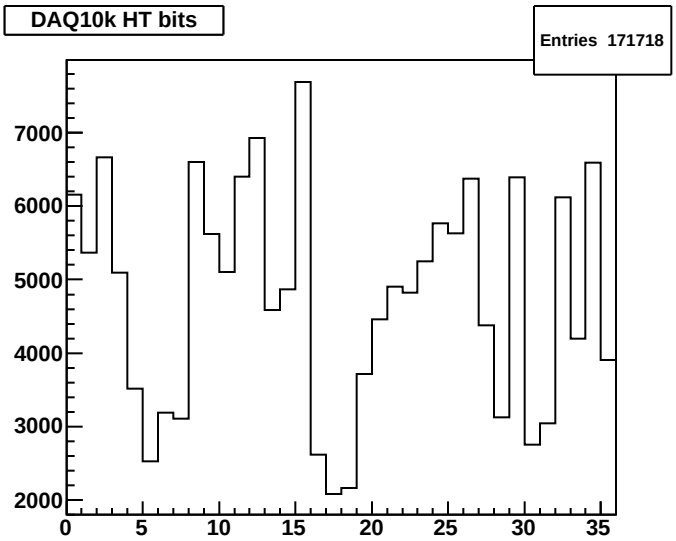

EMC L2 Input - Partial JP Sum
Entries 2601
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

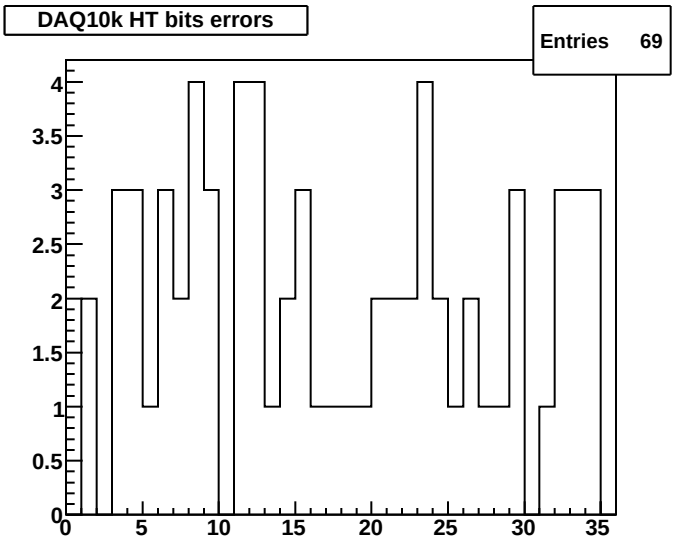

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.674516e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

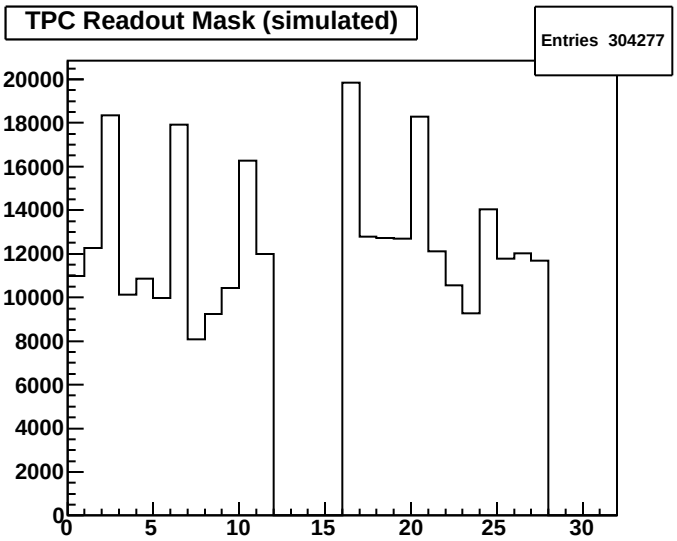

EMC L2 Input - HT01 bits/Partial JP ID
Entries 583
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

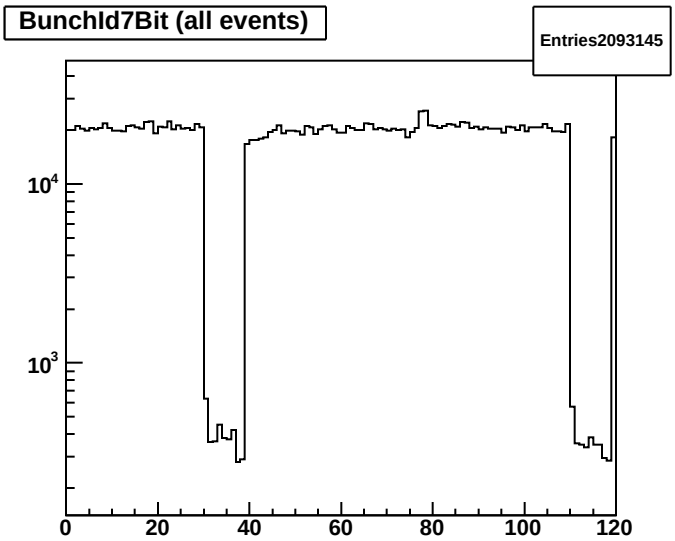

EMC L2 Input - HT23 bits
Entries 1.674516e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

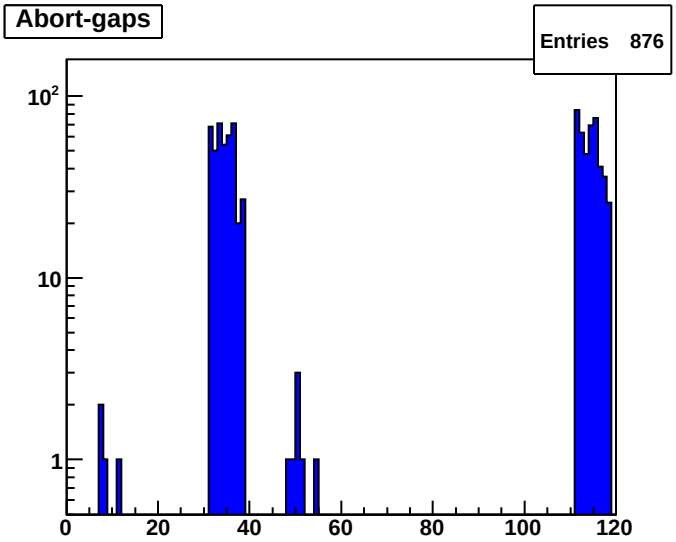

EMC L2 Input - HT23 bits
Entries 97
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

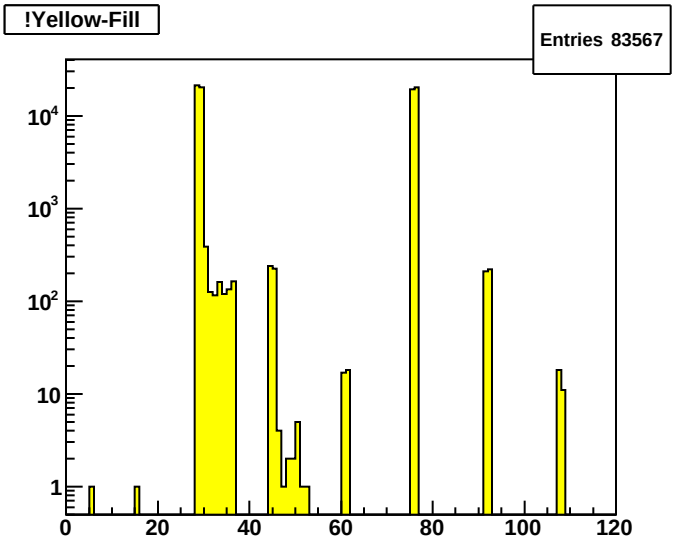

DAQ10k HT bits
Entries 171718

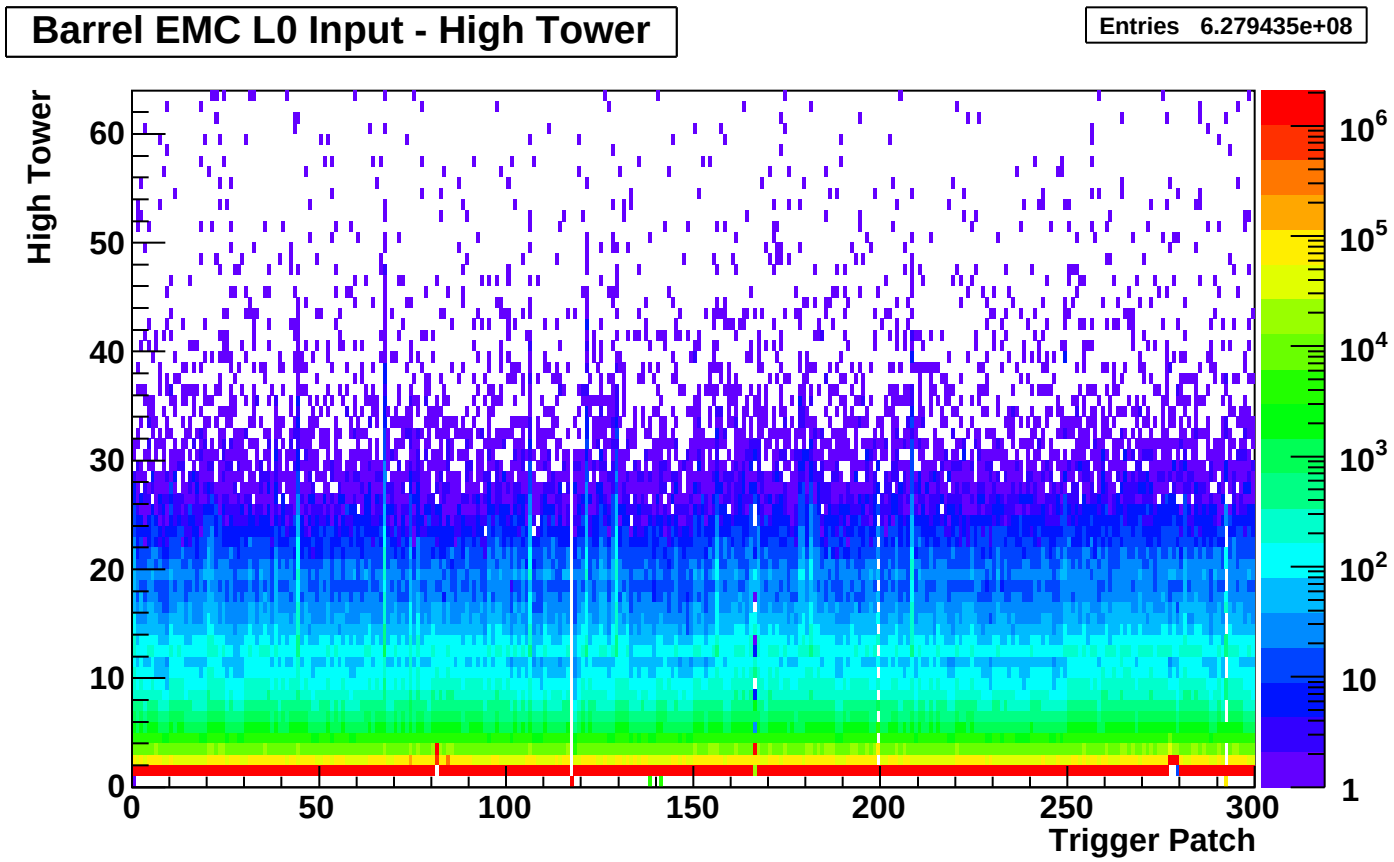

DAQ10k HT bits errors
Entries 69

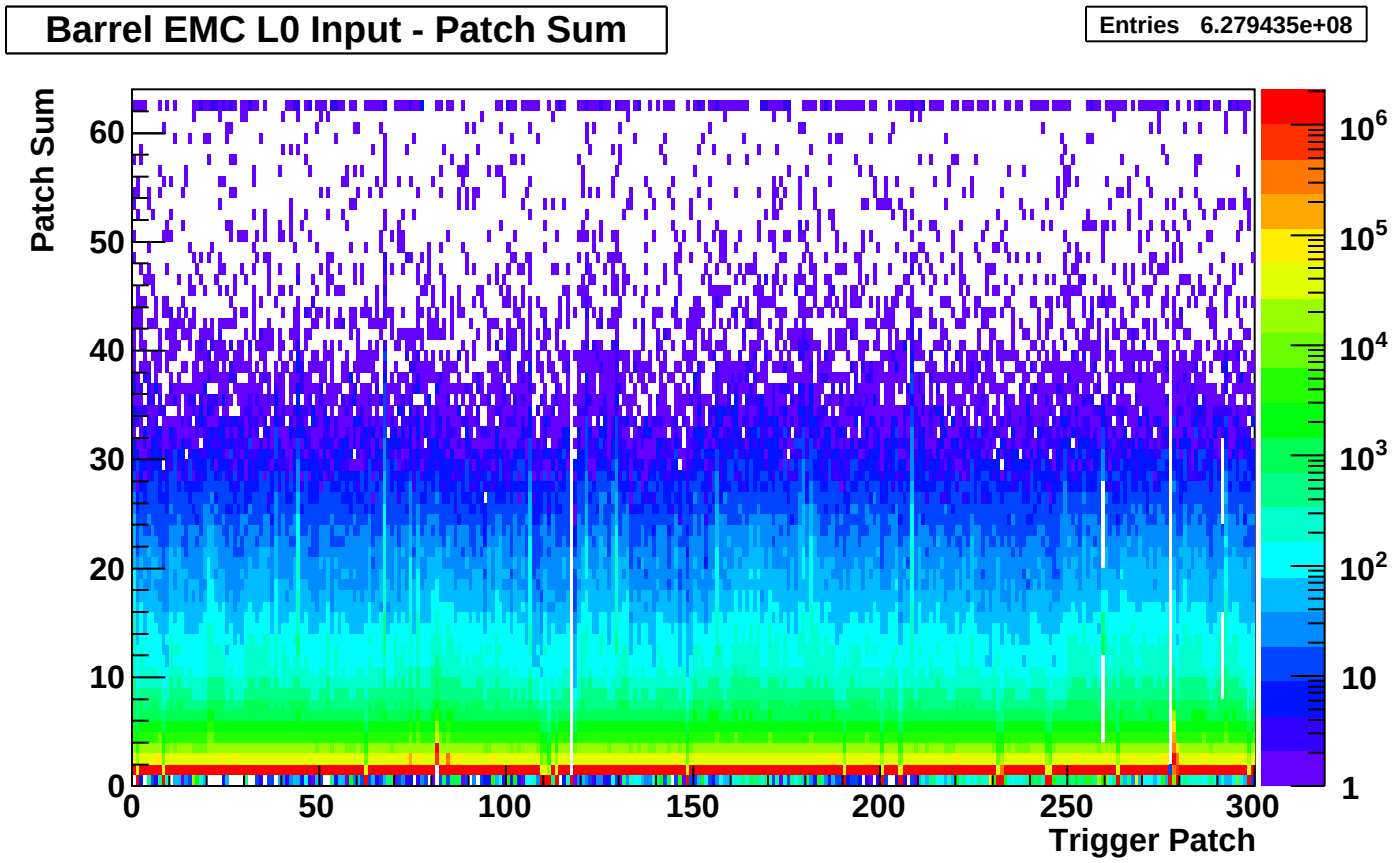

TPC Readout Mask (simulated)
Entries 304277

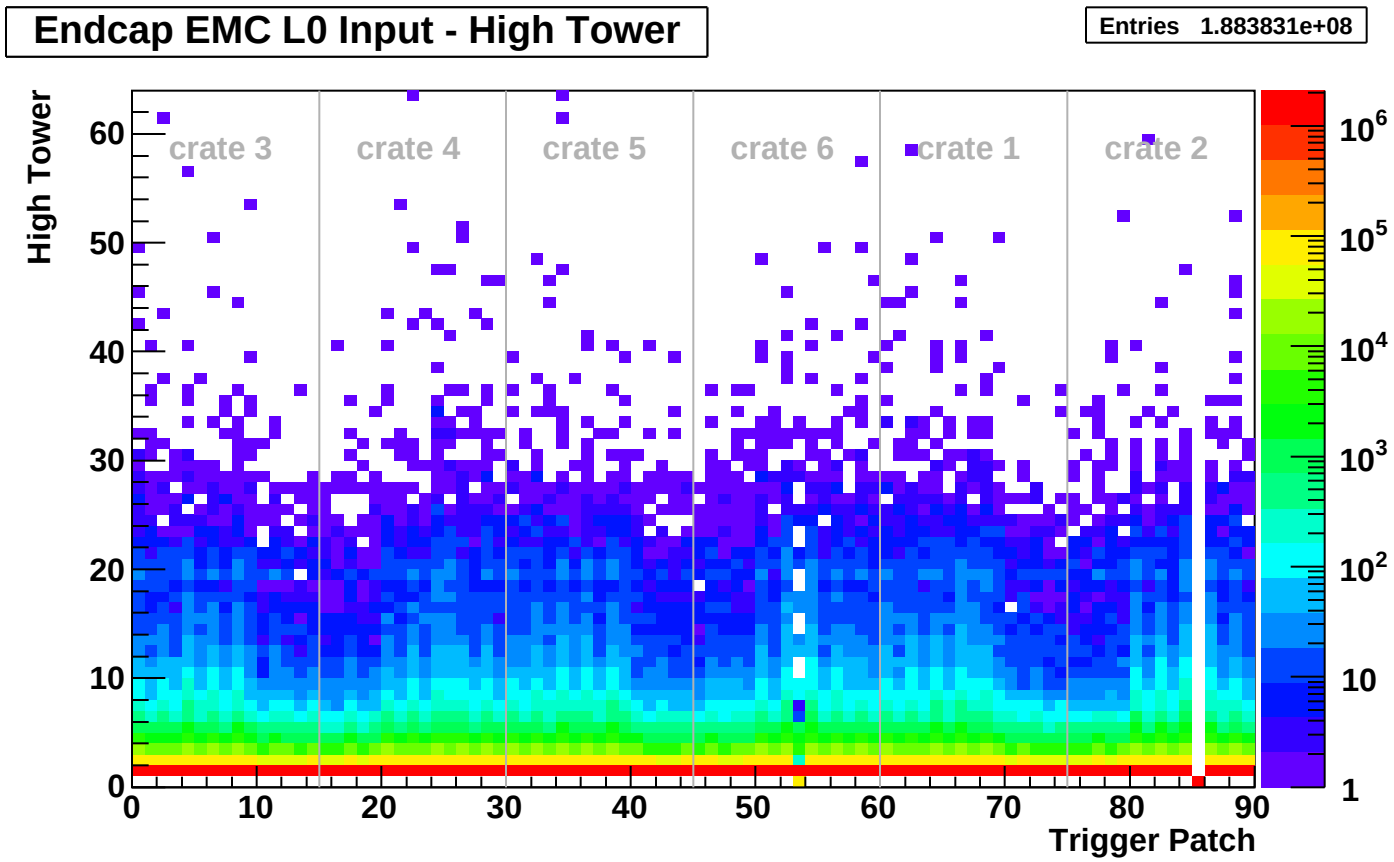

BunchId7Bit (all events)
Entries2093145

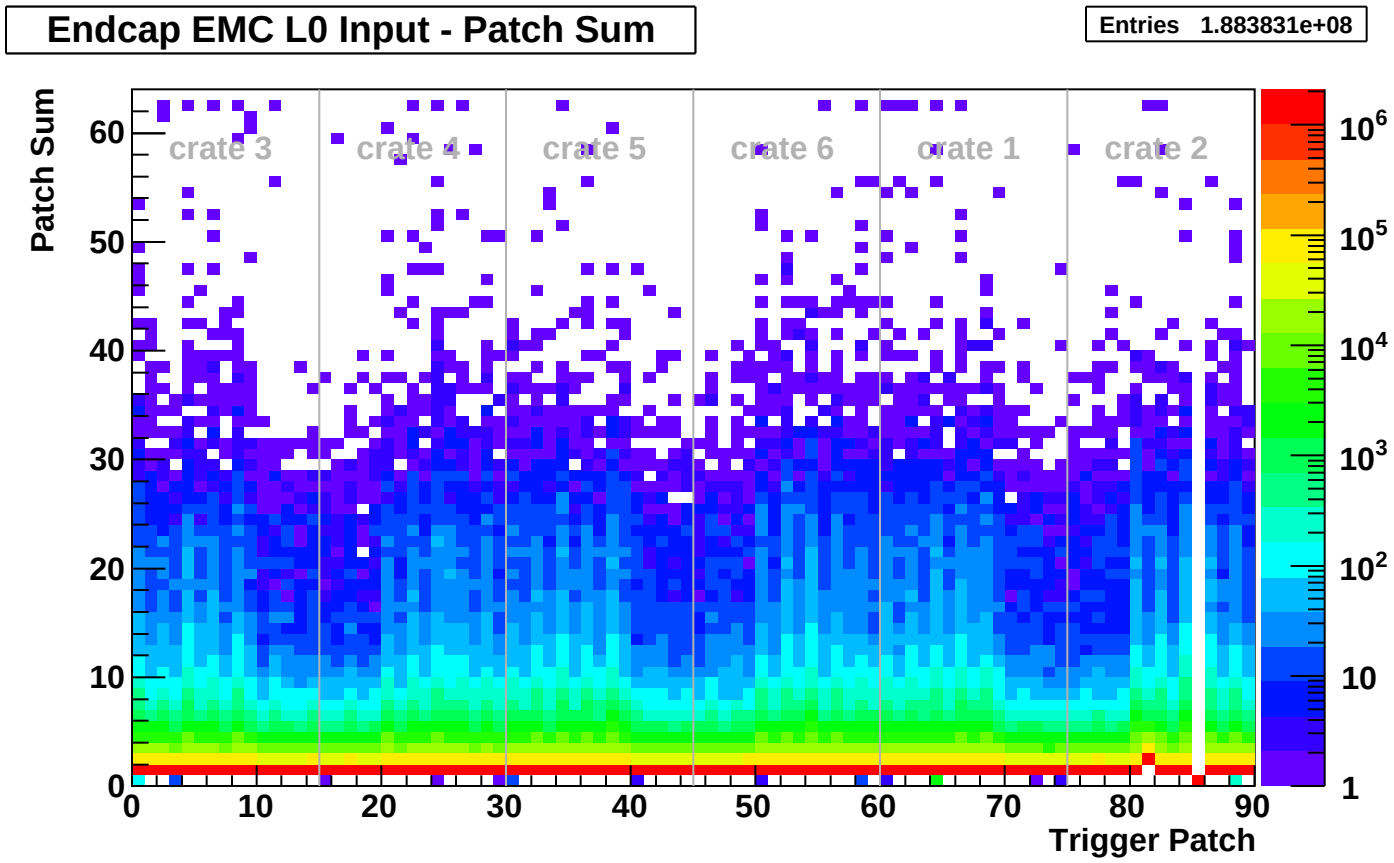

Abort-gaps
Entries 876

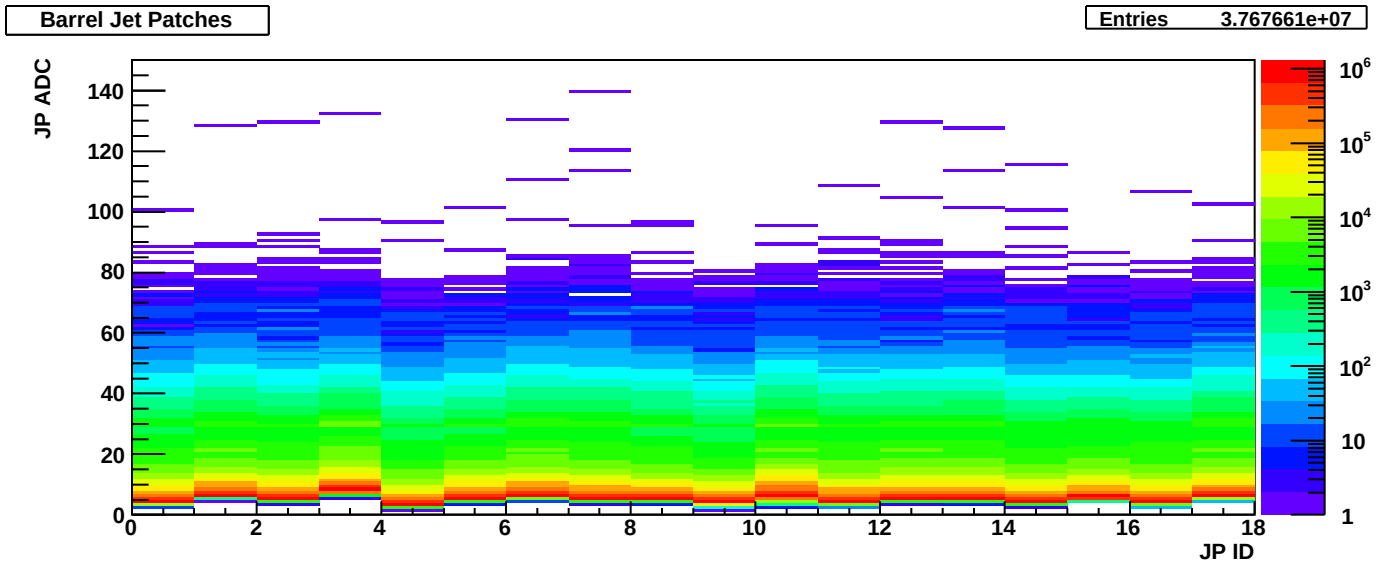

!Yellow-Fill
Entries 83567

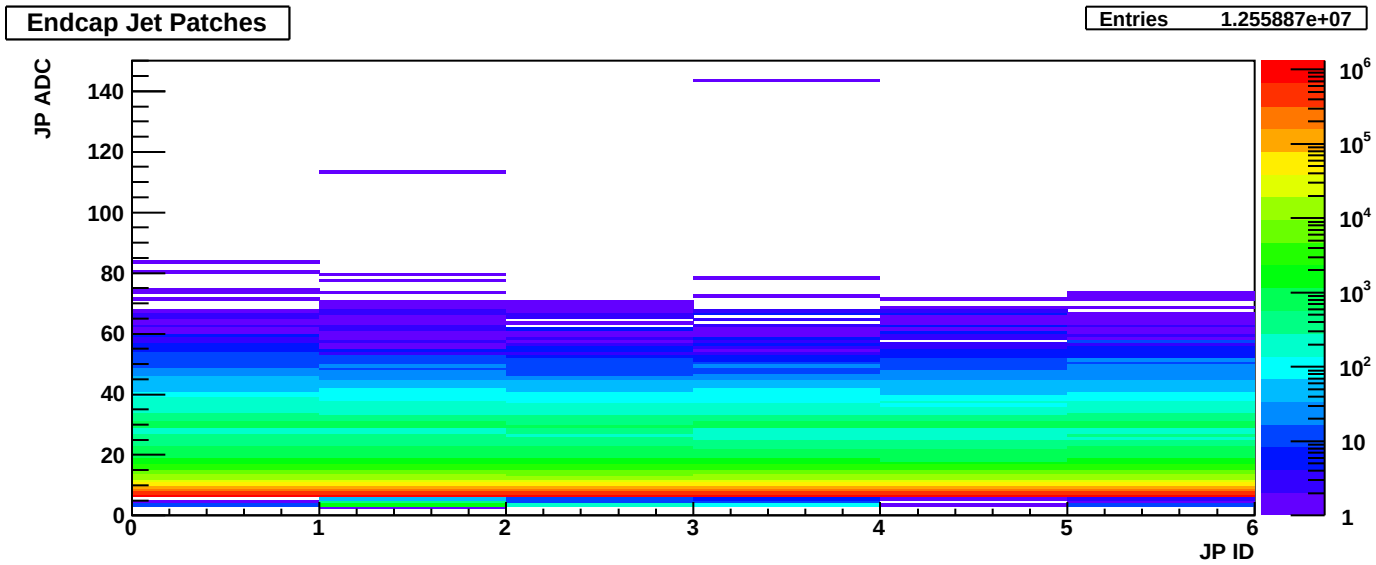

Barrel EMC L0 Input - High Tower
Entries 6.279435e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

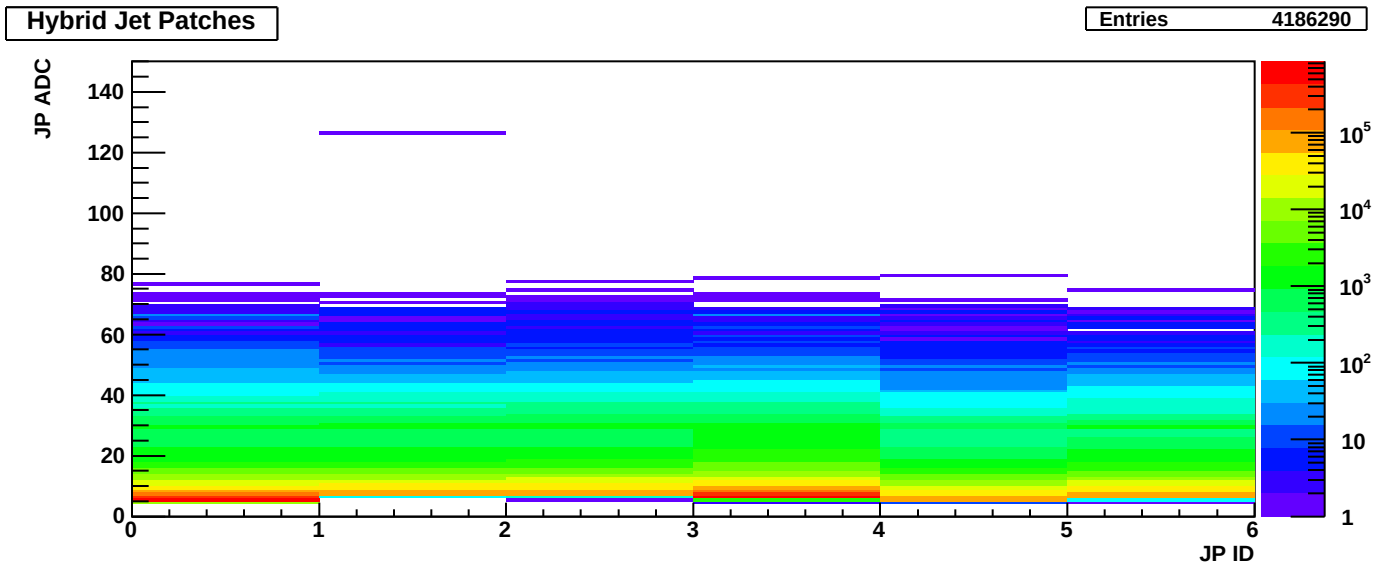

Barrel EMC L0 Input - Patch Sum
Entries 6.279435e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

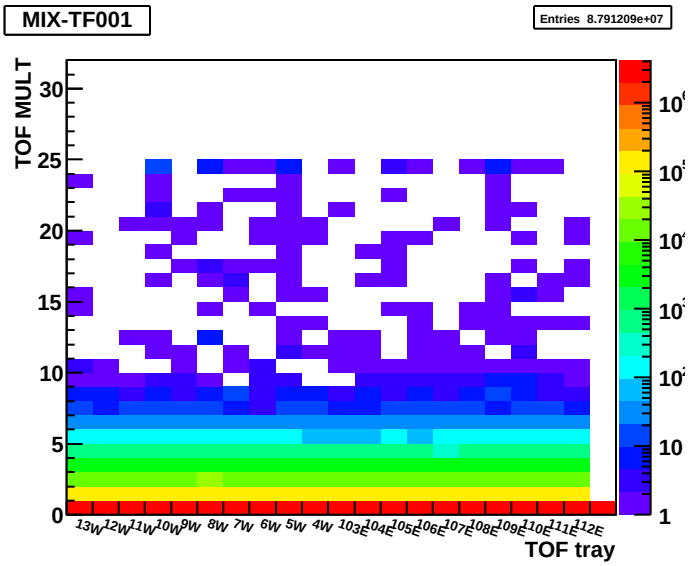

Endcap EMC L0 Input - High Tower
Entries 1.883831e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

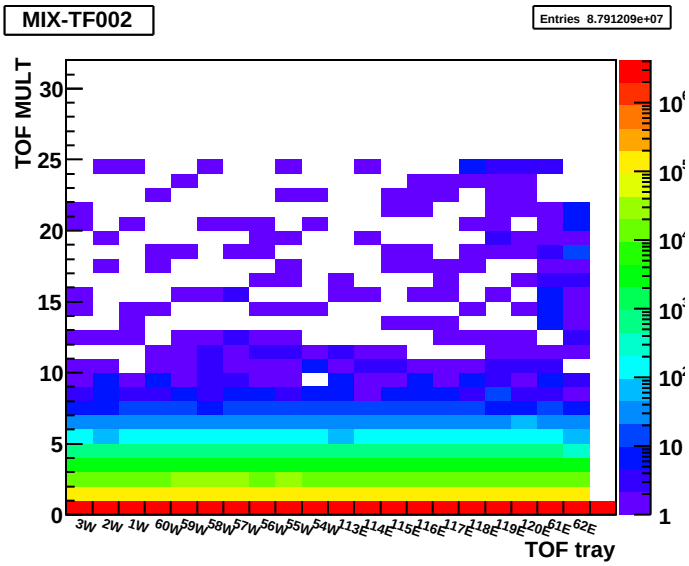

Endcap EMC L0 Input - Patch Sum
Entries 1.883831e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

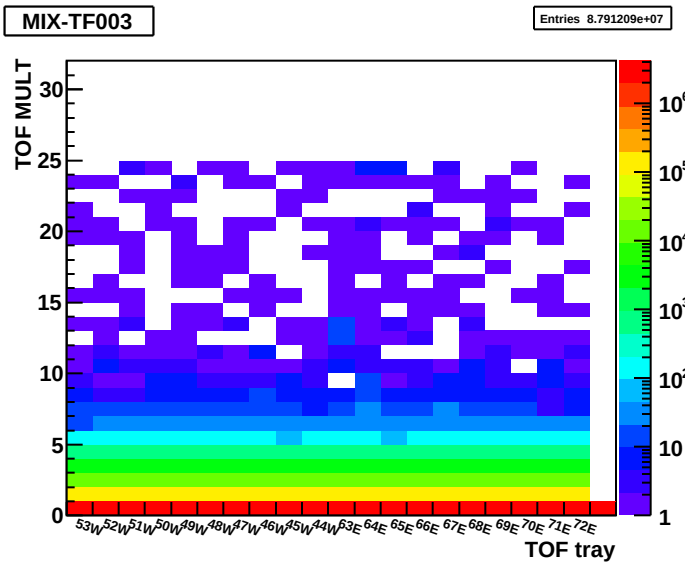

Barrel Jet Patches
Entries 3.767661e+07
JP ADC
JP ID

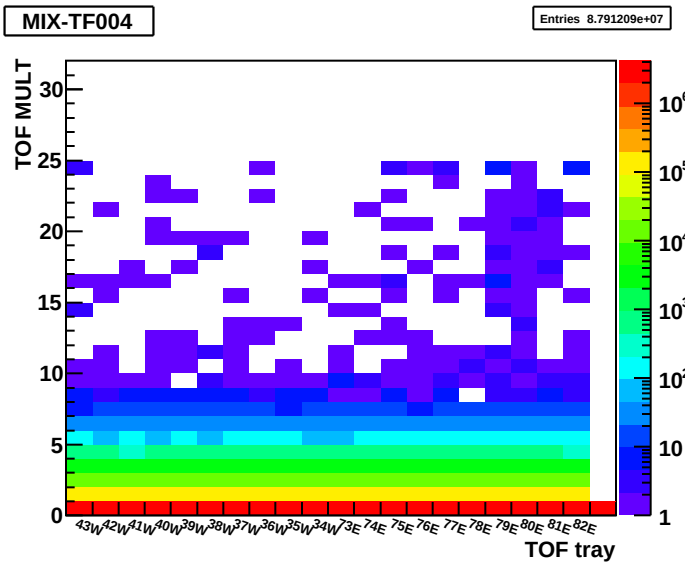

Endcap Jet Patches
Entries 1.255887e+07
JP ADC
JP ID

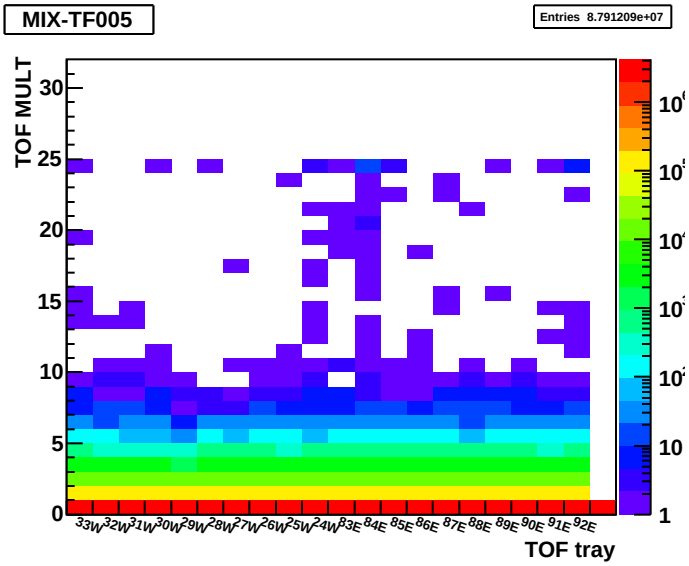

Hybrid Jet Patches
Entries 4186290
JP ADC
JP ID

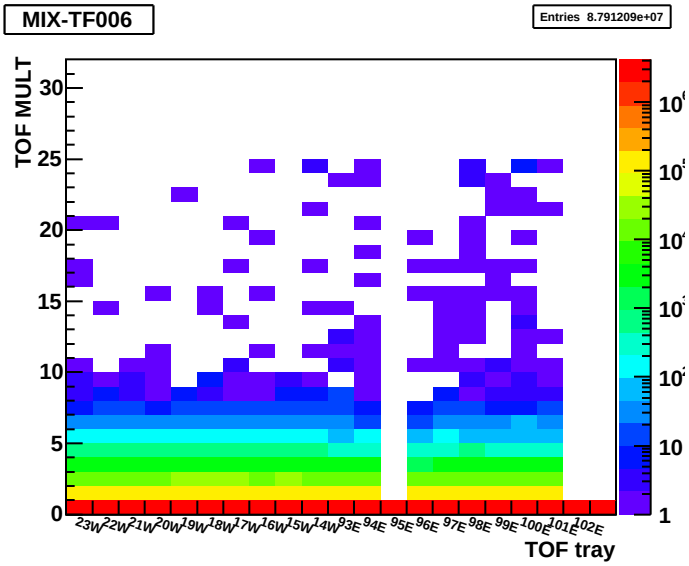

MIX-TF001
Entries 8.791209e+07
TOF MULT
TOF tray

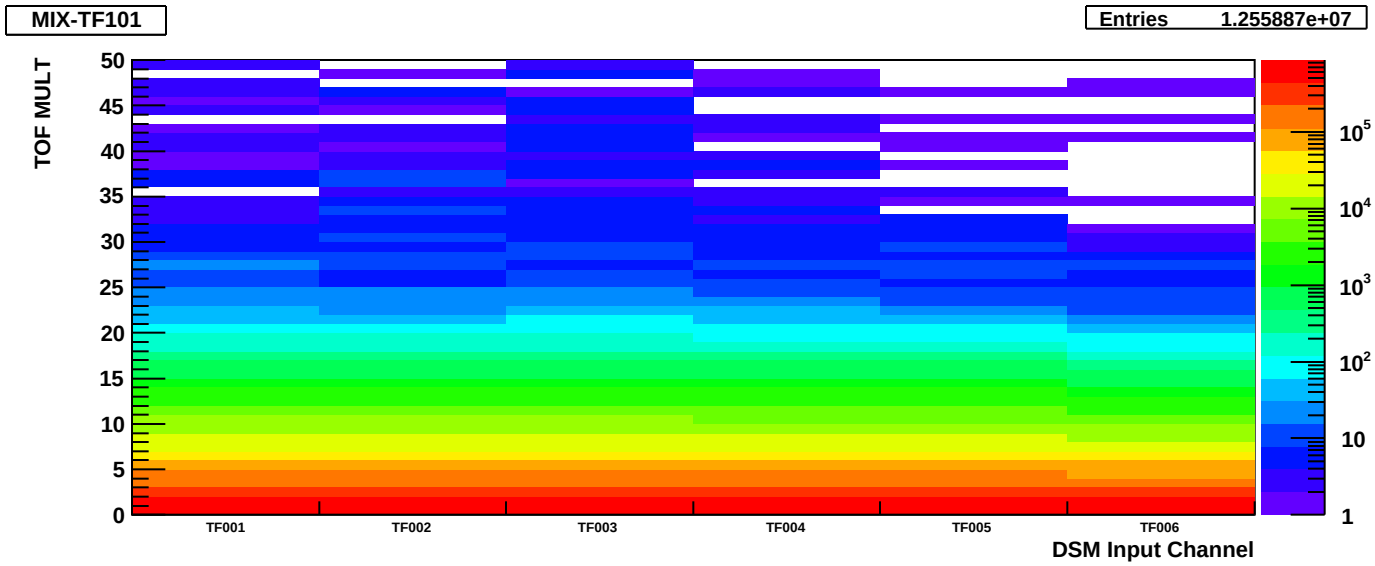

MIX-TF002
Entries 8.791209e+07
TOF MULT
TOF tray

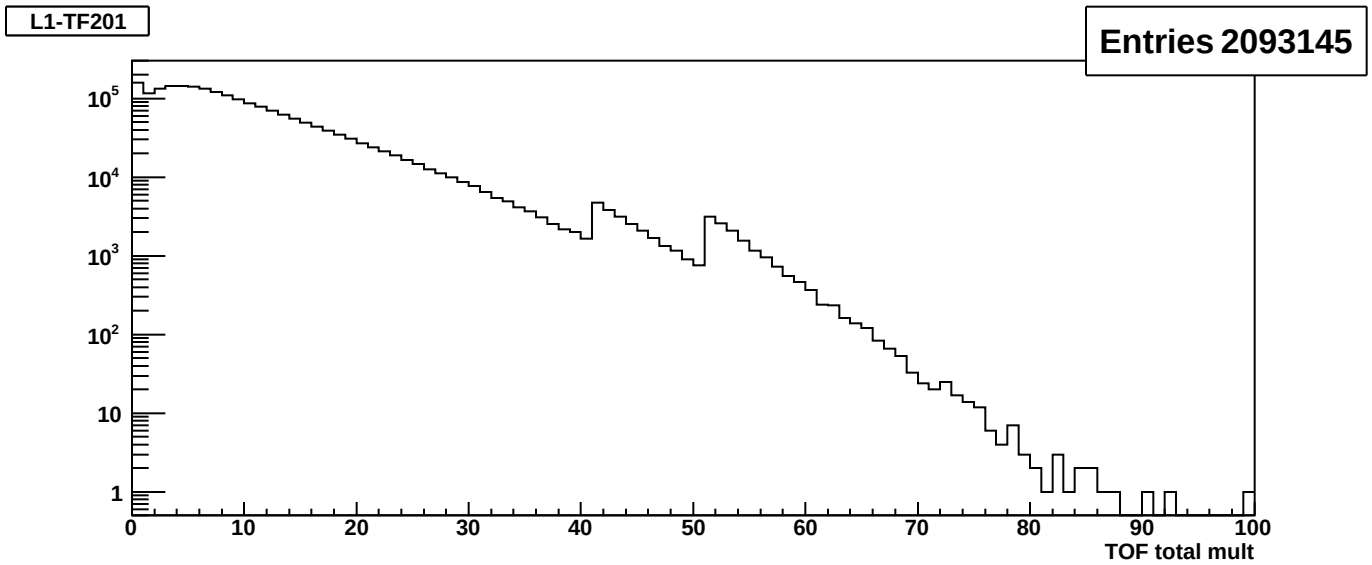

MIX-TF003
Entries 8.791209e+07
TOF MULT
TOF tray

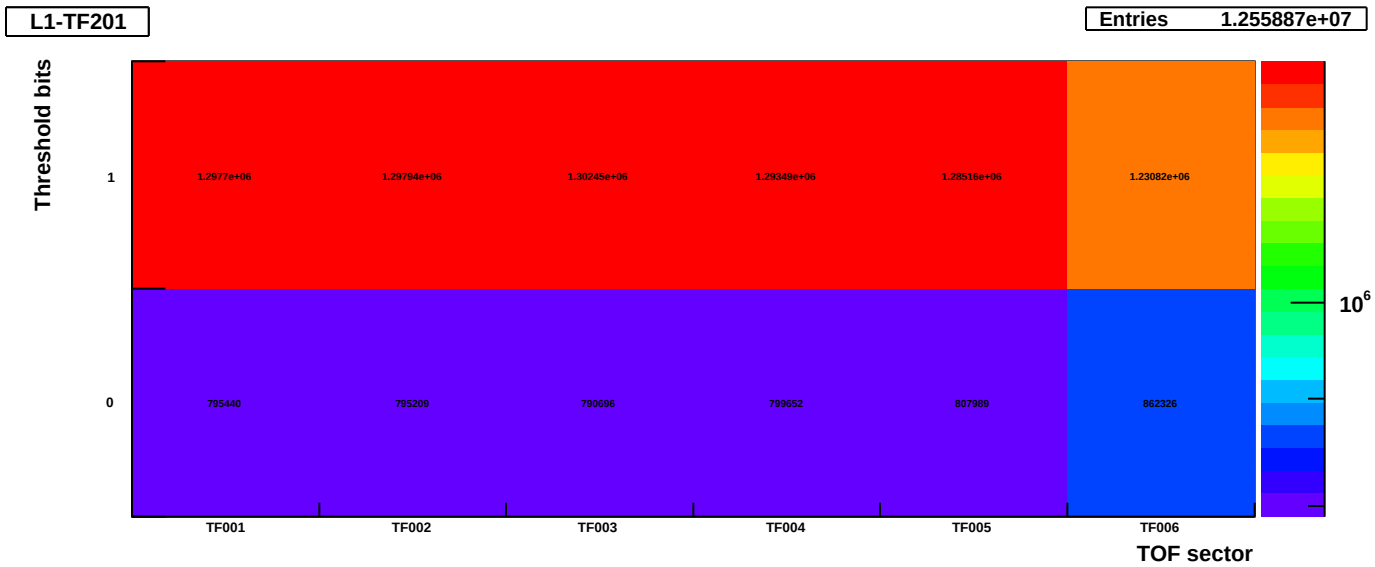

MIX-TF004
Entries 8.791209e+07
TOF MULT
TOF tray

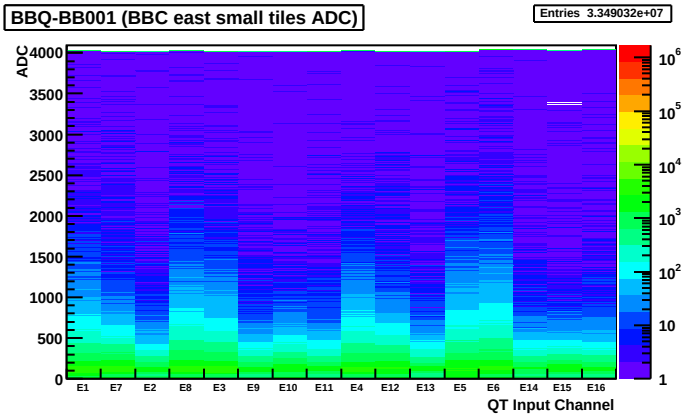

MIX-TF005
Entries 8.791209e+07
TOF MULT
TOF tray

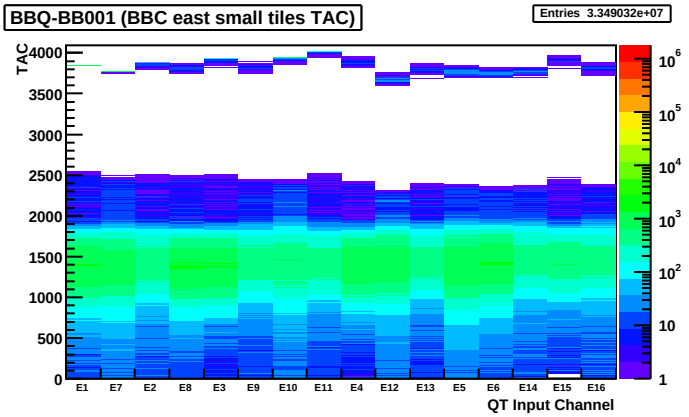

MIX-TF006
Entries 8.791209e+07
TOF MULT
TOF tray

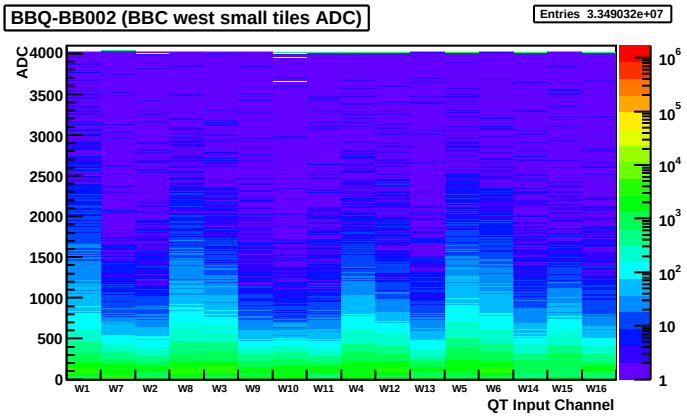

MIX-TF101
Entries 1.255887e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

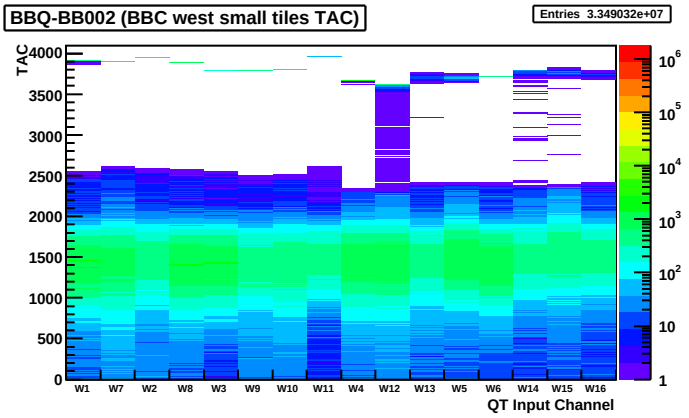

L1-TF201
Entries 2093145
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

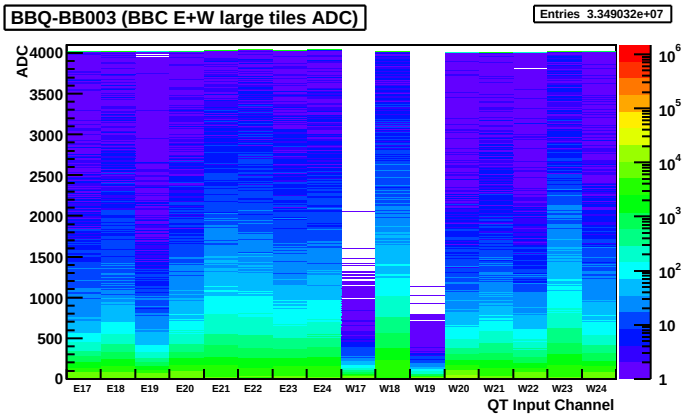

L1-TF201
Entries 1.255887e+07
Threshold bits
1
0
1.2977e+06
1.29794e+06
1.30245e+06
1.29349e+06
1.28516e+06
1.23082e+06
795440
795209
790696
799652
807989
862326
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

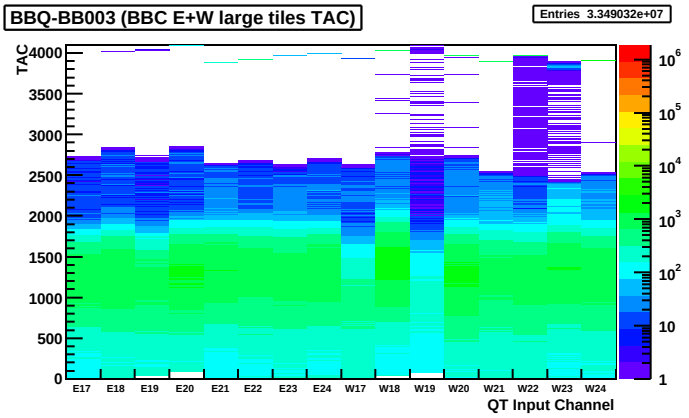

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.349032e+07
ADC
QT Input Channel

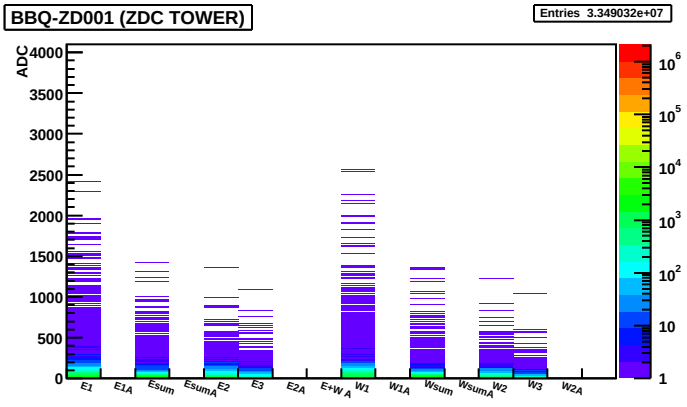

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.349032e+07
TAC
QT Input Channel

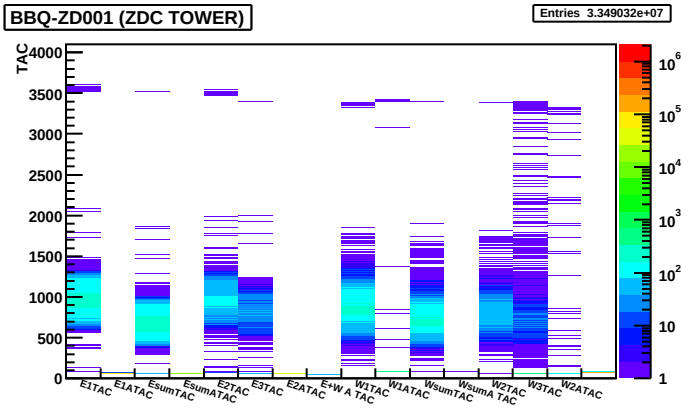

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.349032e+07
ADC
QT Input Channel

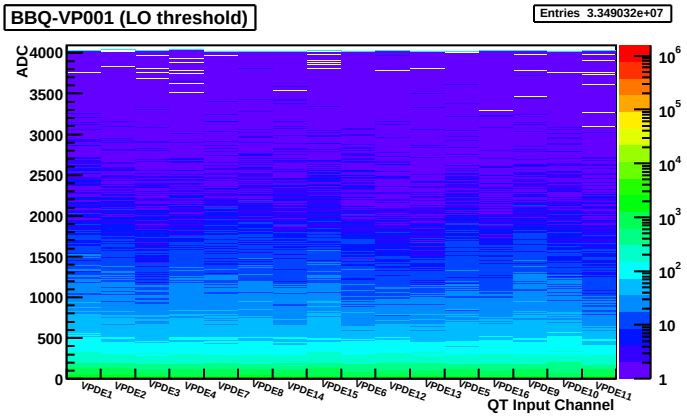

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.349032e+07
TAC
QT Input Channel

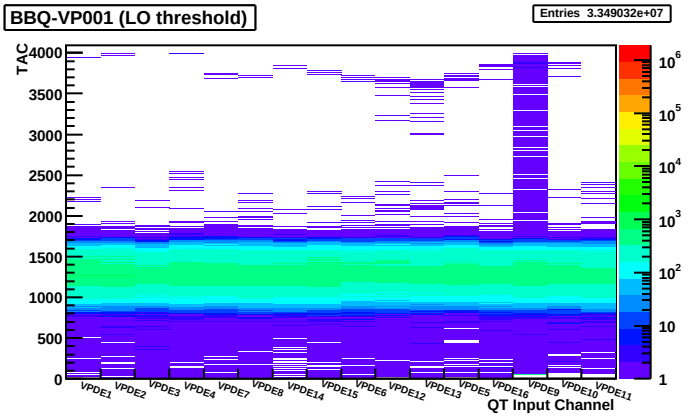

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.349032e+07
ADC
QT Input Channel

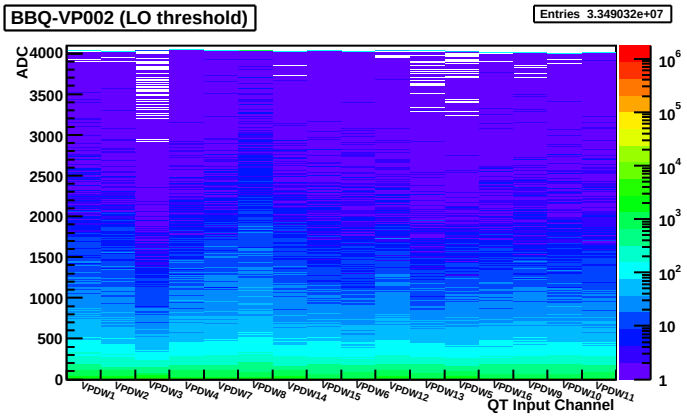

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.349032e+07
TAC
QT Input Channel

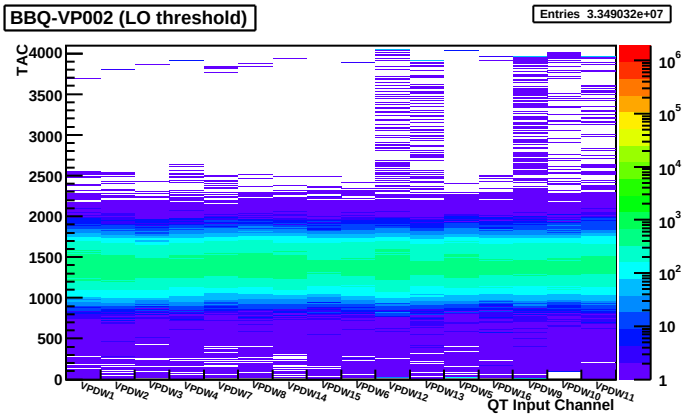

BBQ-ZD001 (ZDC TOWER)
Entries 3.349032e+07
ADC

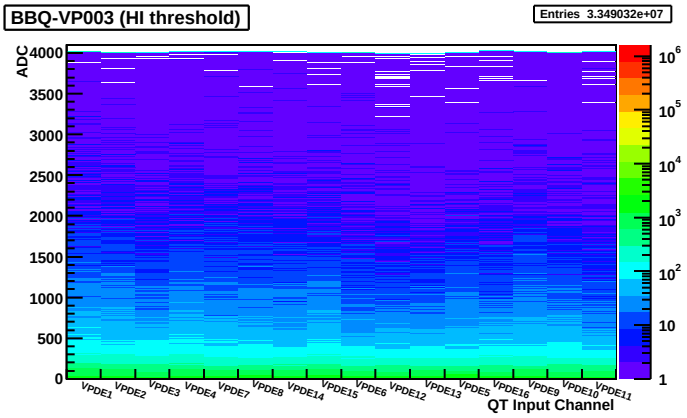

BBQ-ZD001 (ZDC TOWER)
Entries 3.349032e+07
TAC

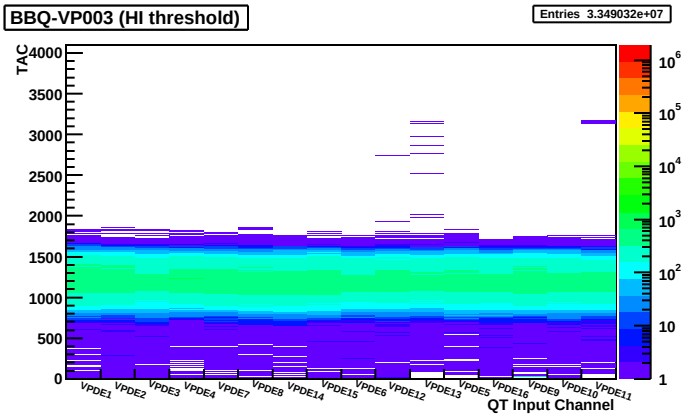

BBQ-VP001 (LO threshold)
Entries 3.349032e+07
ADC
QT Input Channel

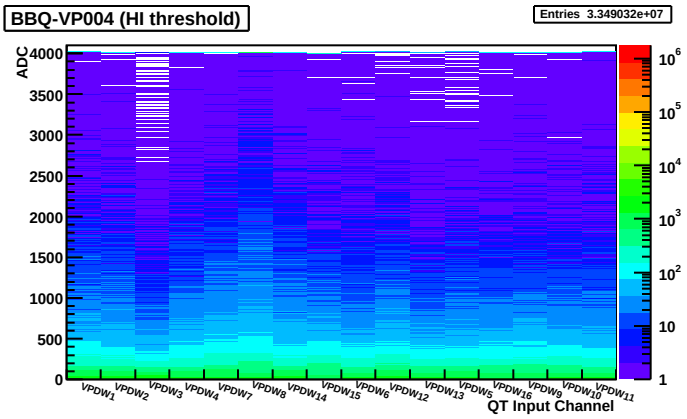

BBQ-VP001 (LO threshold)
Entries 3.349032e+07
TAC
QT Input Channel

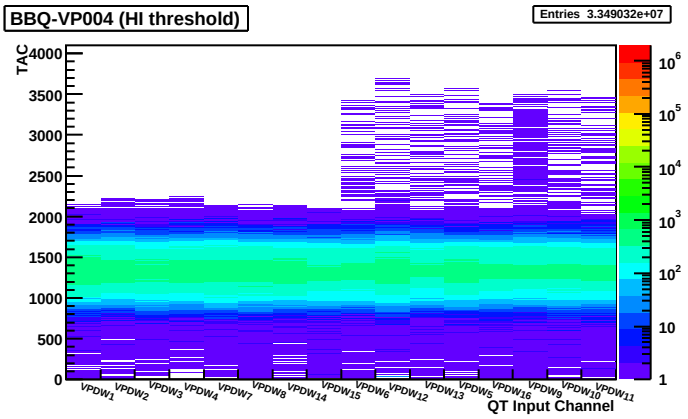

BBQ-VP002 (LO threshold)
Entries 3.349032e+07
ADC
QT Input Channel

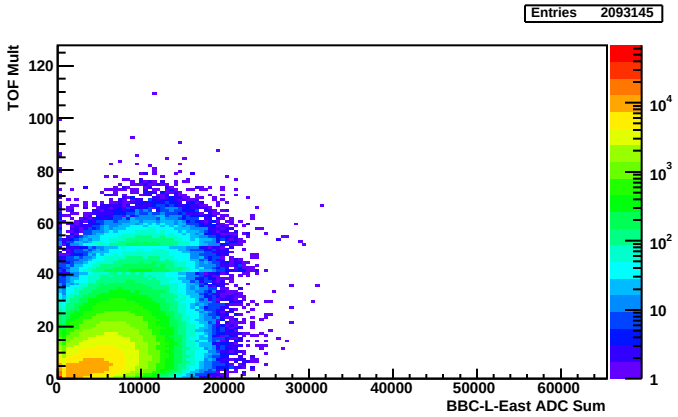

BBQ-VP002 (LO threshold)
Entries 3.349032e+07
TAC
QT Input Channel

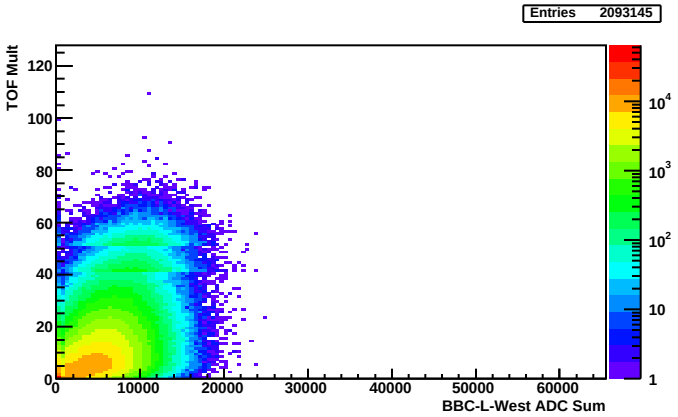

BBQ-VP003 (HI threshold)
Entries 3.349032e+07
ADC
QT Input Channel

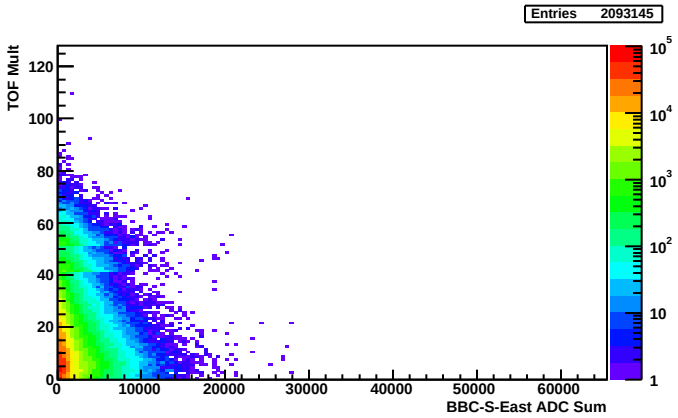

BBQ-VP003 (HI threshold)
Entries 3.349032e+07
TAC
QT Input Channel

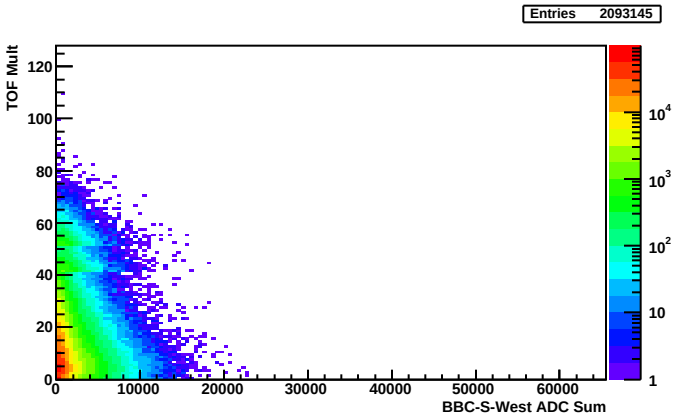

BBQ-VP004 (HI threshold)
Entries 3.349032e+07
ADC
QT Input Channel

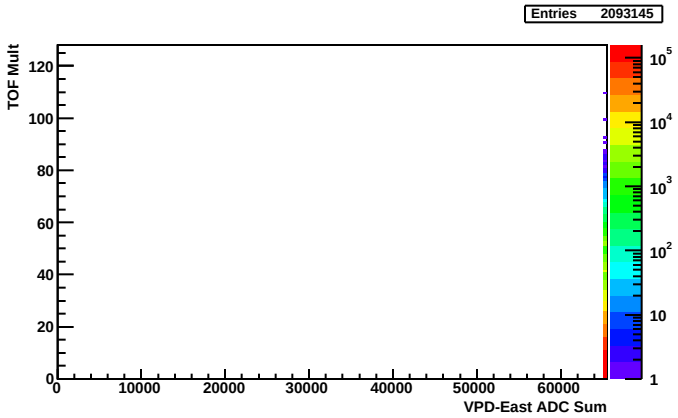

BBQ-VP004 (HI threshold)
Entries 3.349032e+07
TAC
QT Input Channel

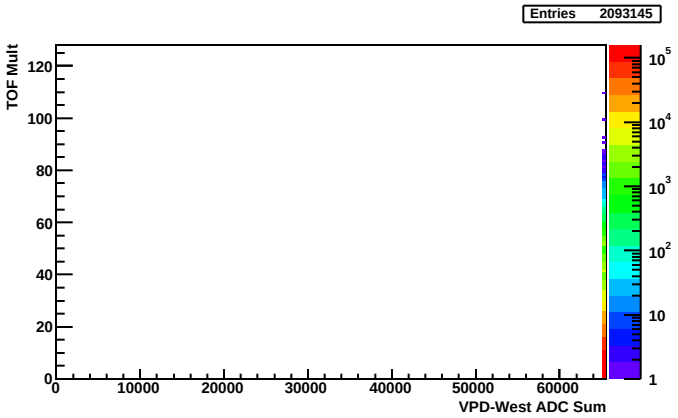

Entries 2093145
TOF Mult
BBC-L-East ADC Sum

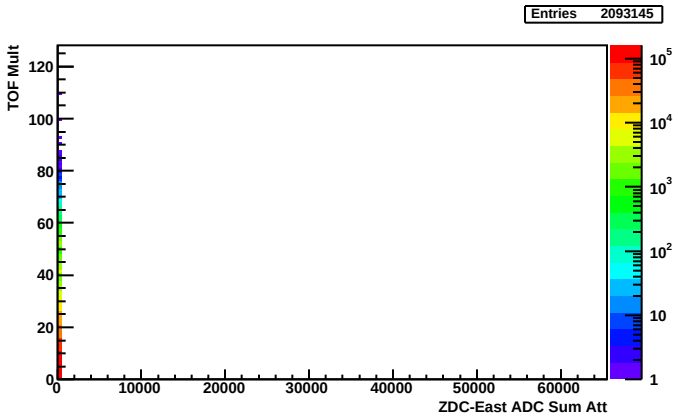

Entries 2093145
TOF Mult
BBC-L-West ADC Sum

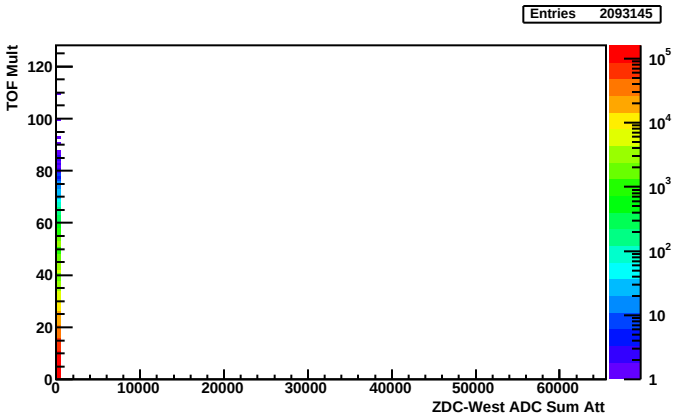

Entries 2093145
TOF Mult
BBC-S-East ADC Sum

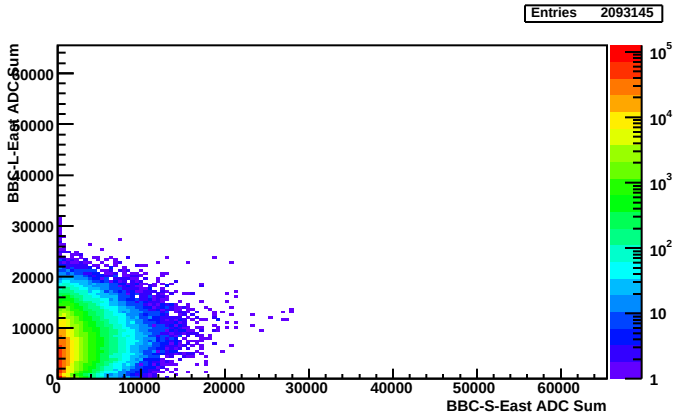

Entries 2093145
TOF Mult
BBC-S-West ADC Sum

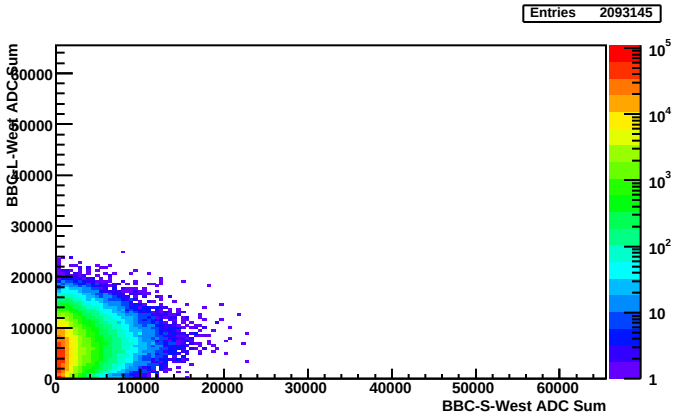

Entries 2093145
TOF Mult
VPD-East ADC Sum

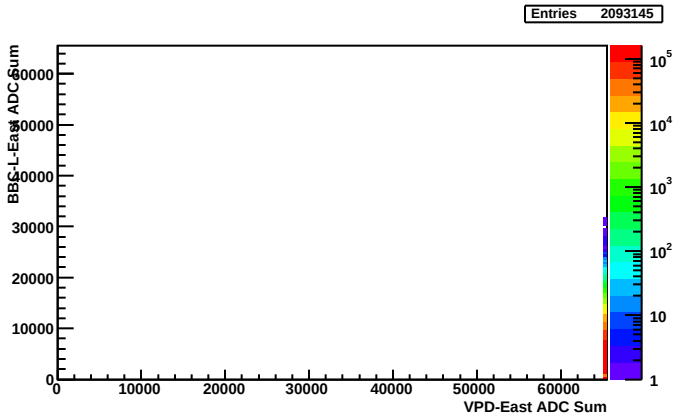

Entries 2093145
TOF Mult
VPD-West ADC Sum

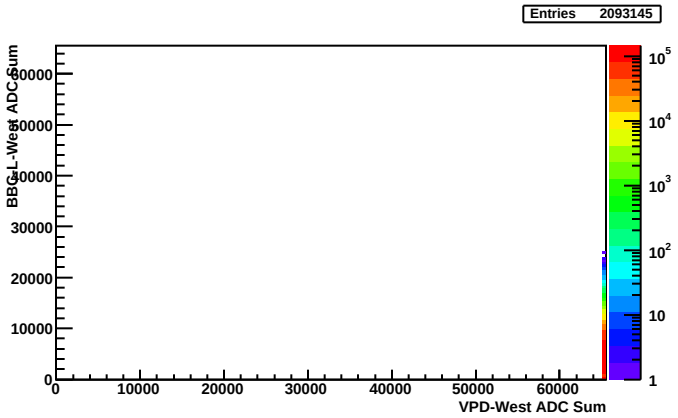

Entries 2093145
TOF Mult
ZDC-East ADC Sum Att

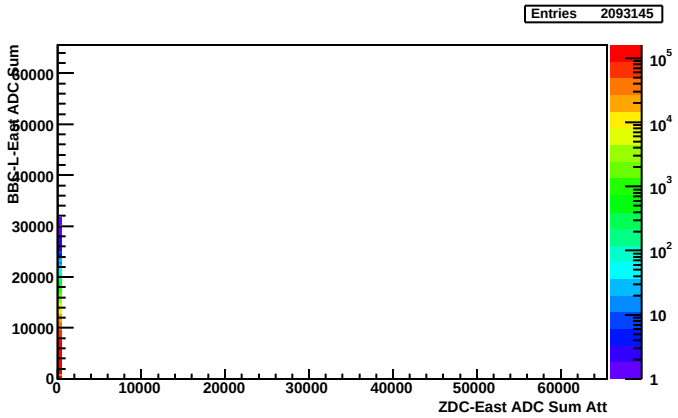

Entries 2093145
TOF Mult
ZDC-West ADC Sum Att

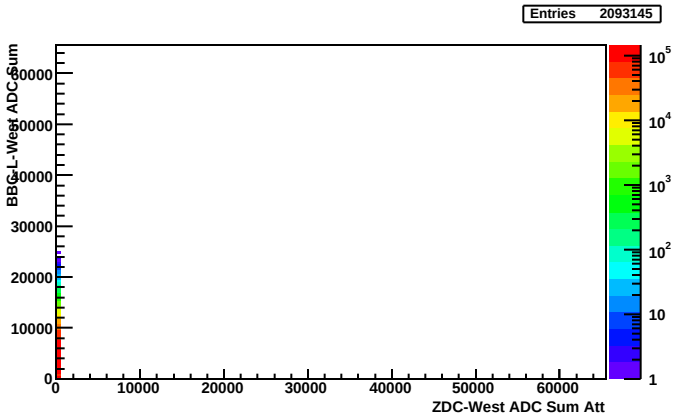

Entries 2093145
BBC-L-East ADC Sum
BBC-S-East ADC Sum

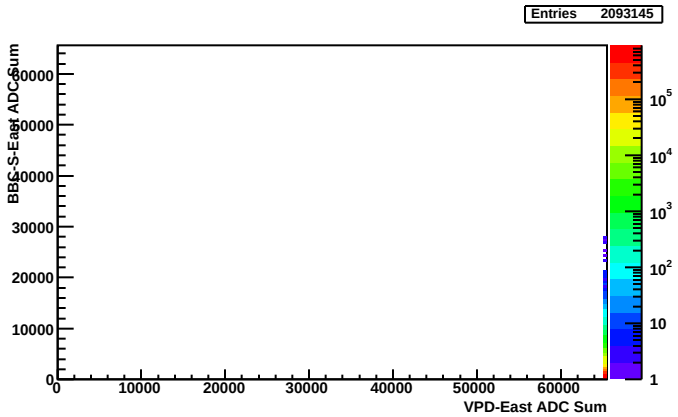

Entries 2093145
BBC-L-West ADC Sum
BBC-S-West ADC Sum

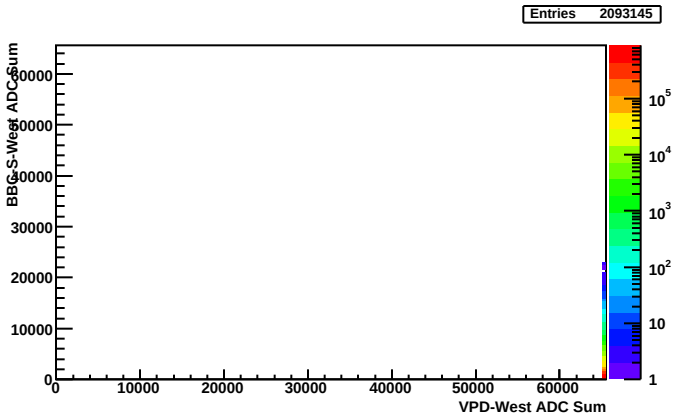

Entries 2093145
BBC-L-East ADC Sum
VPD-East ADC Sum

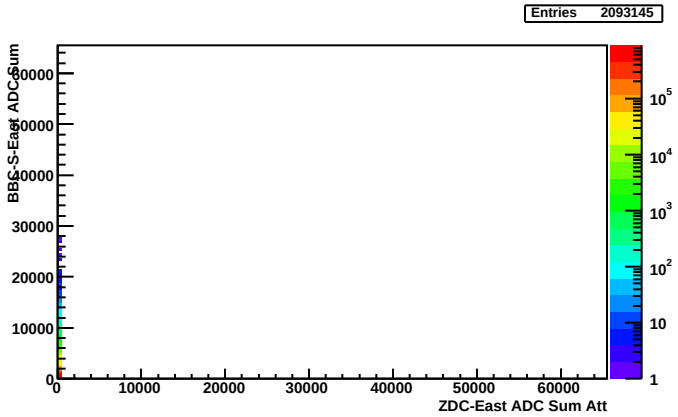

Entries 2093145
BBC-L-West ADC Sum
VPD-West ADC Sum

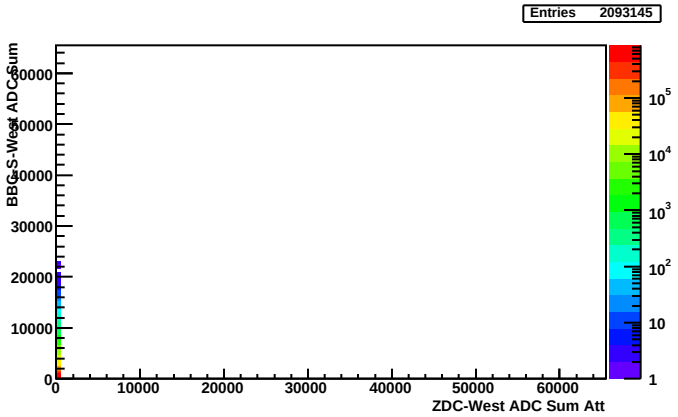

Entries 2093145
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

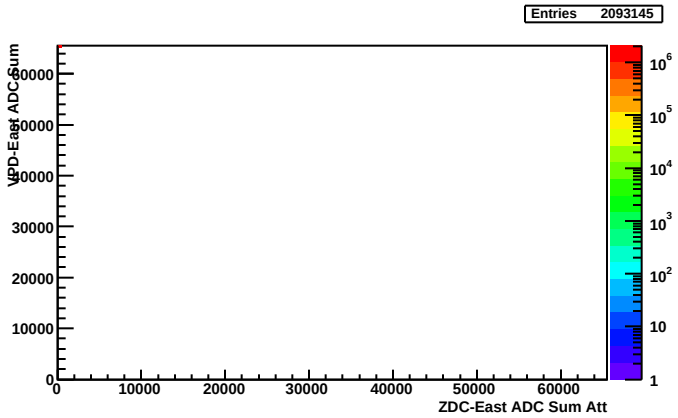

Entries 2093145
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

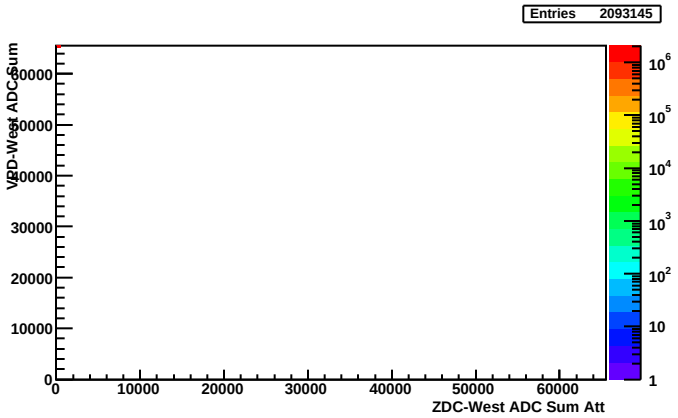

Entries 2093145
BBC-S-East ADC Sum
VPD-East ADC Sum

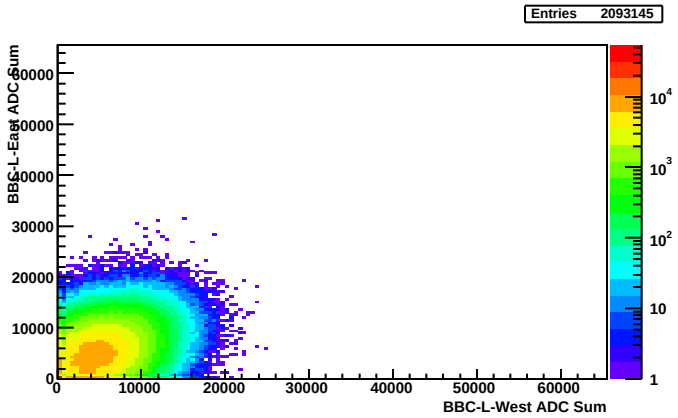

Entries 2093145
BBC-S-West ADC Sum
VPD-West ADC Sum

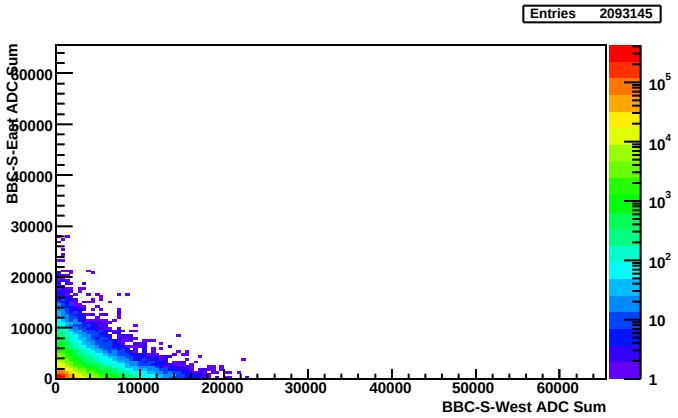

Entries 2093145
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

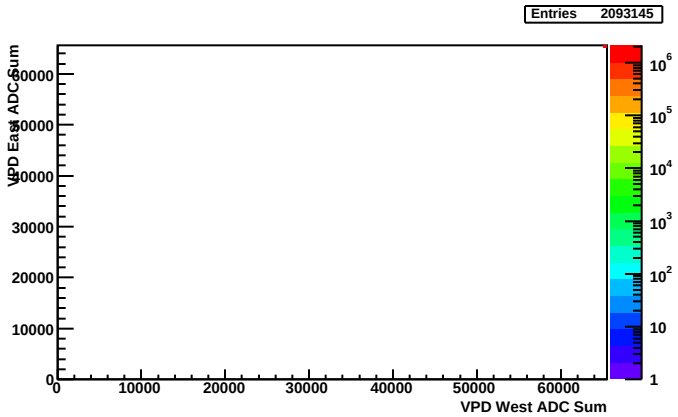

Entries 2093145
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

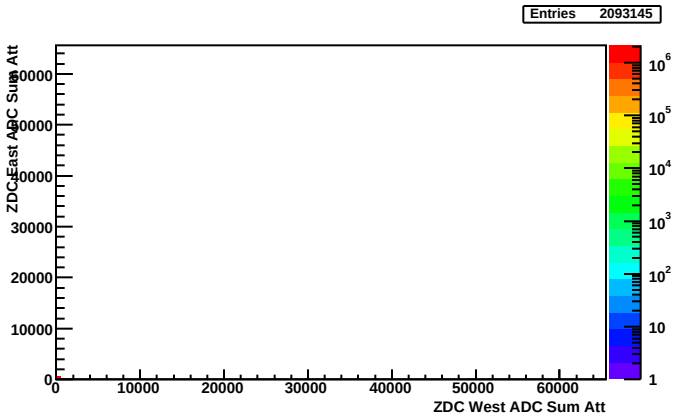

Entries 2093145
VPD-East ADC Sum
ZDC-East ADC Sum Att

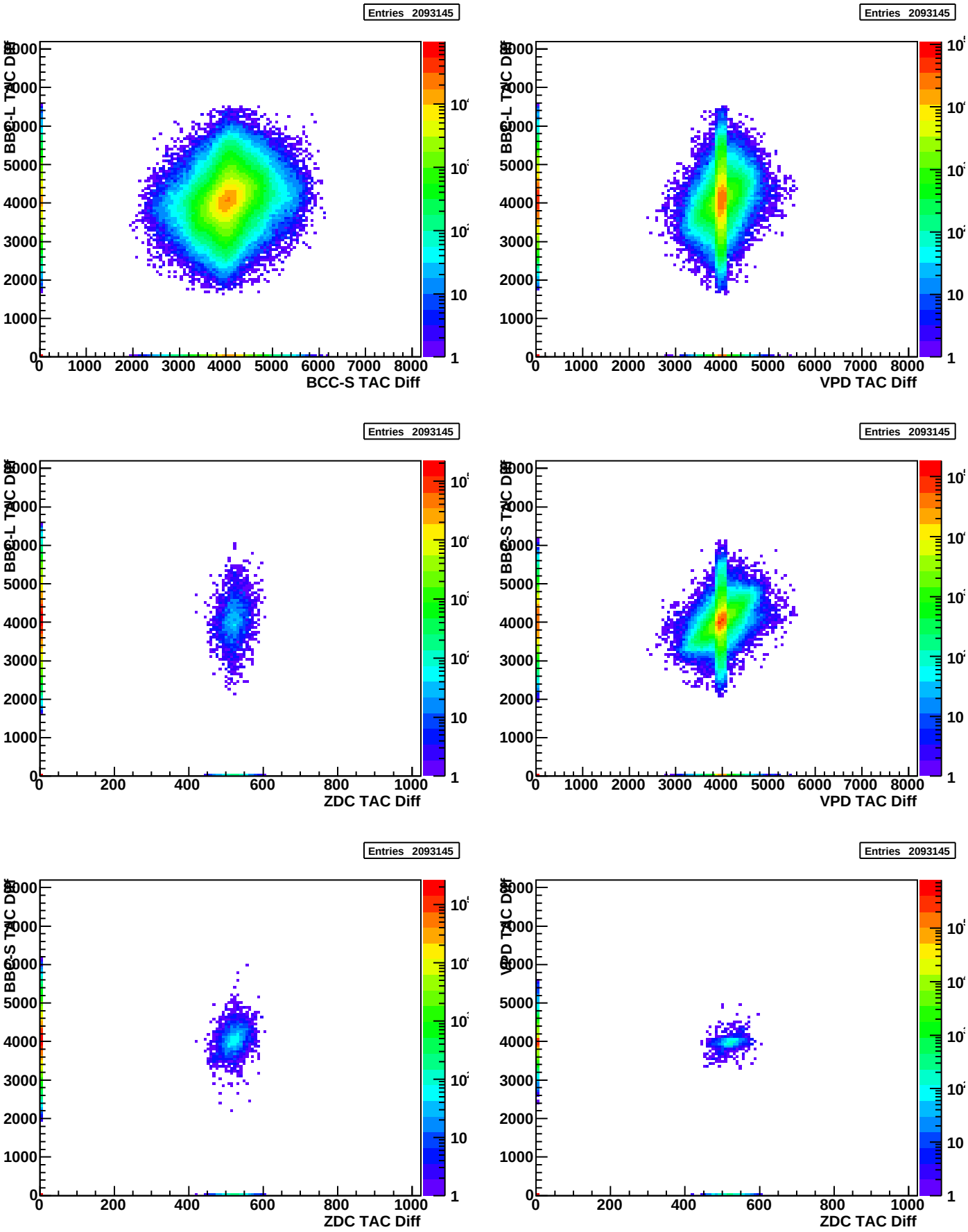

Entries 2093145
VPD-West ADC Sum
ZDC-West ADC Sum Att

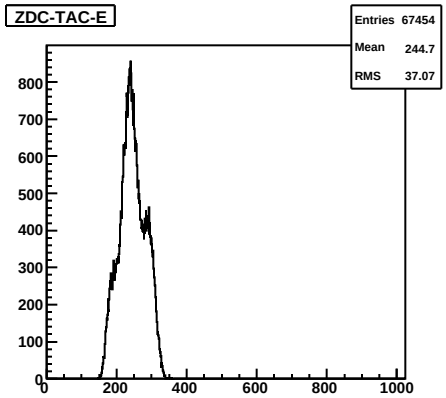

Entries 2093145
BBC-L-East ADC Sum
BBC-L-West ADC Sum

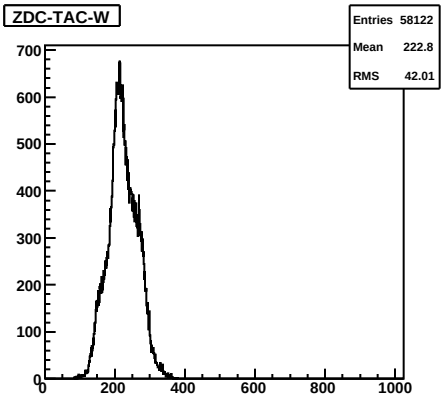

Entries 2093145
BBC-S-East ADC Sum
BBC-S-West ADC Sum

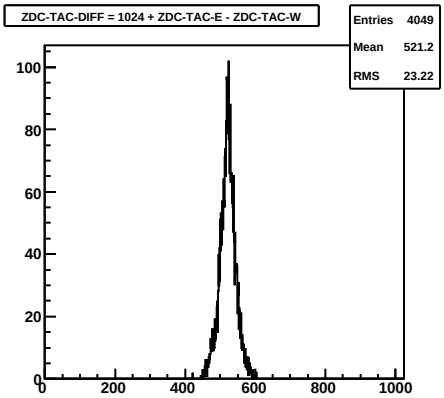

Entries 2093145
VPD East ADC Sum
VPD West ADC Sum

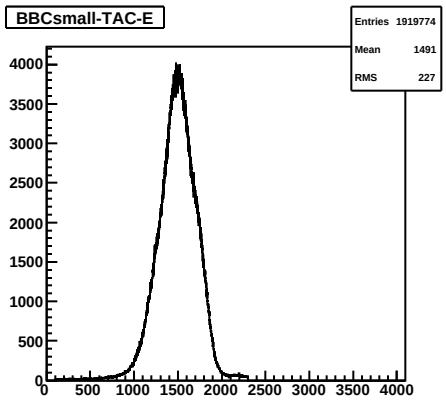

Entries 2093145
ZDC East ADC Sum Att
ZDC West ADC Sum Att

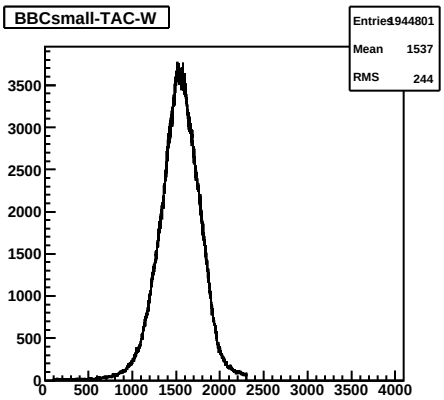

Entries 2093145
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2093145
BBC-L TAC Diff
VPD TAC Diff
Entries 2093145
BBC-L TAC Diff
ZDC TAC Diff
Entries 2093145
BBC-S TAC Diff
VPD TAC Diff
Entries 2093145
BBC-S TAC Diff
ZDC TAC Diff
Entries 2093145
VPD TAC Diff
ZDC TAC Diff

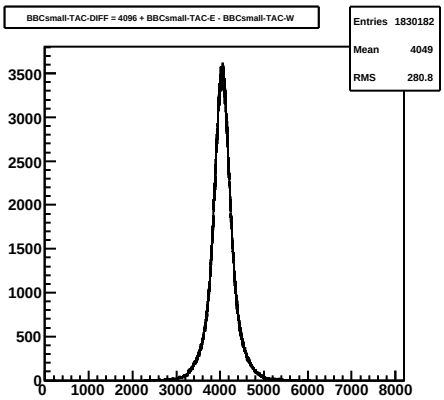

ZDC-TAC-E
Entries 67454
Mean 244.7
RMS 37.07

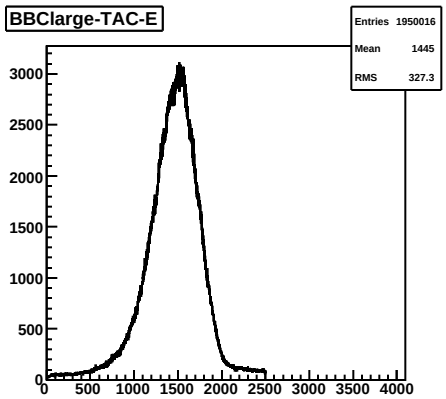

ZDC-TAC-W
Entries 58122
Mean 222.8
RMS 42.01

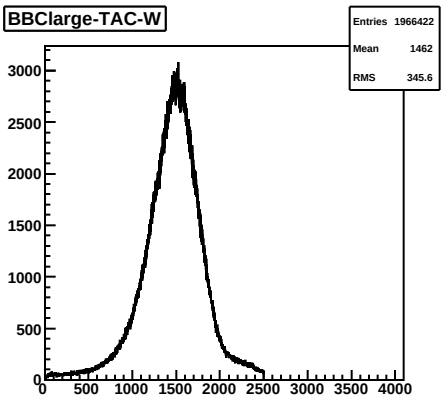

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4049
Mean 521.2
RMS 23.22

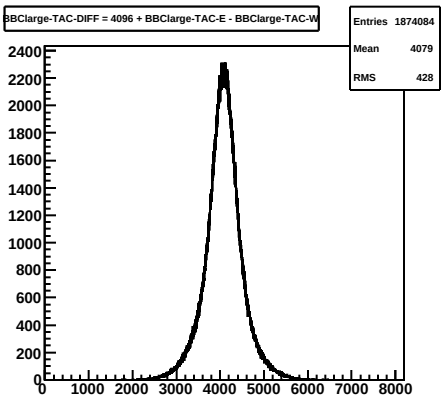

BBCsmall-TAC-E
Entries 1919774
Mean 1491
RMS 227

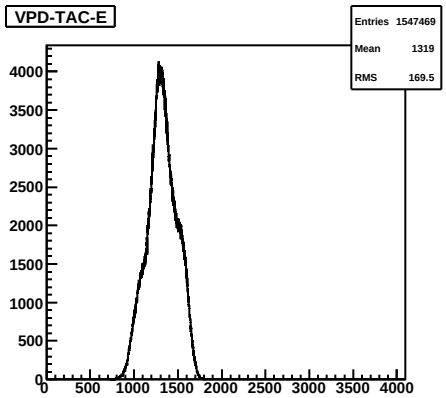

BBCsmall-TAC-W
Entries 1944801
Mean 1537
RMS 244

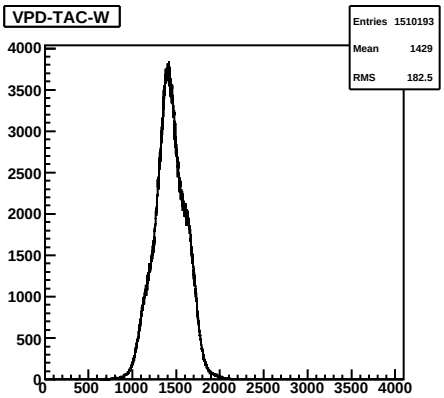

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1830182
Mean 4049
RMS 280.8

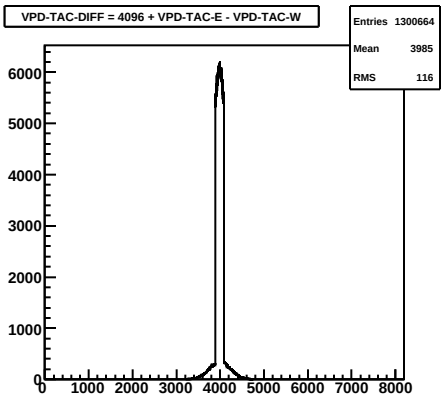

BBClarge-TAC-E
Entries 1950016
Mean 1445
RMS 327.3

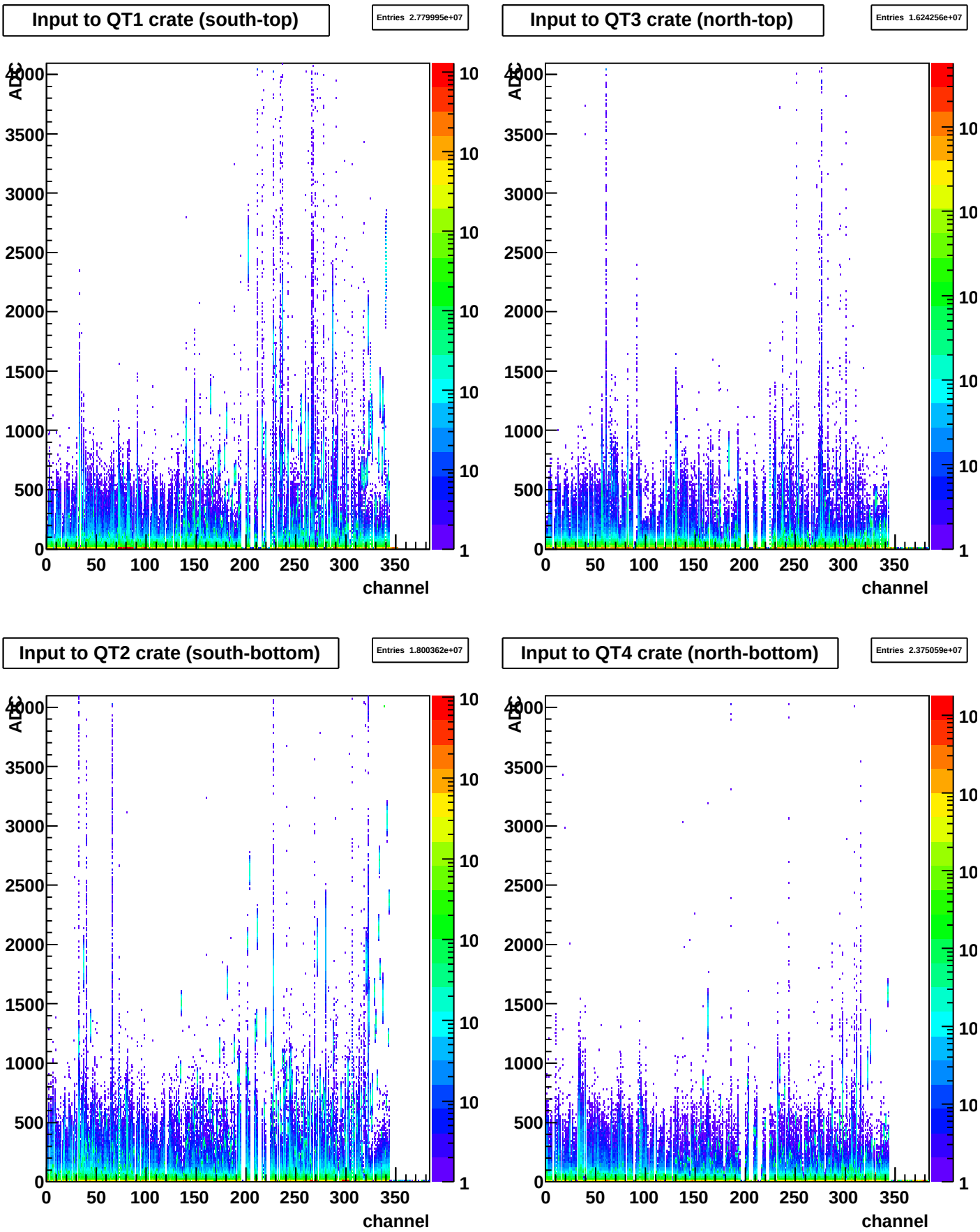

BBClarge-TAC-W
Entries 1966422
Mean 1462
RMS 345.6

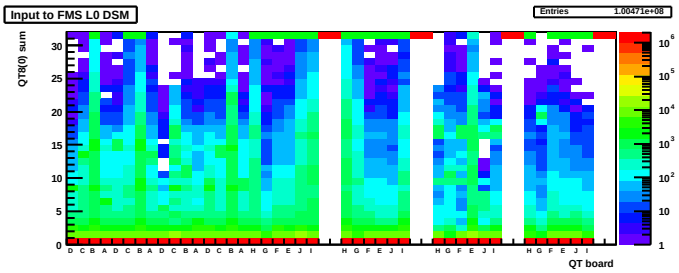

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1874084
Mean 4079
RMS 428

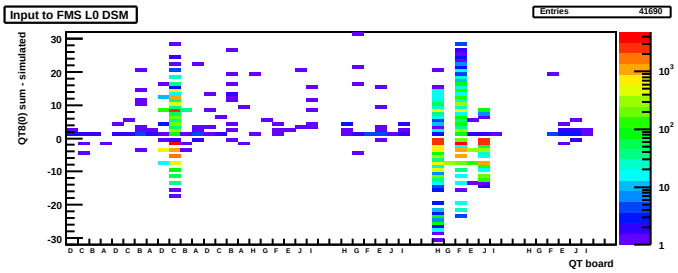

VPD-TAC-E
Entries 1547469
Mean 1319
RMS 169.5

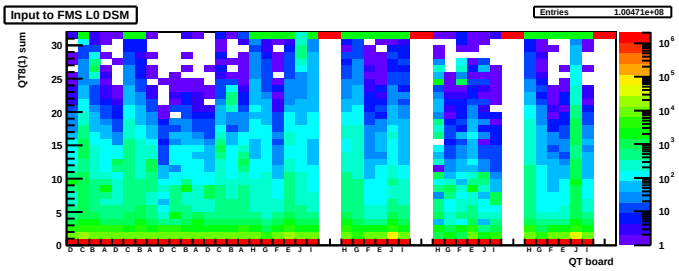

VPD-TAC-W
Entries 1510193
Mean 1429
RMS 182.5

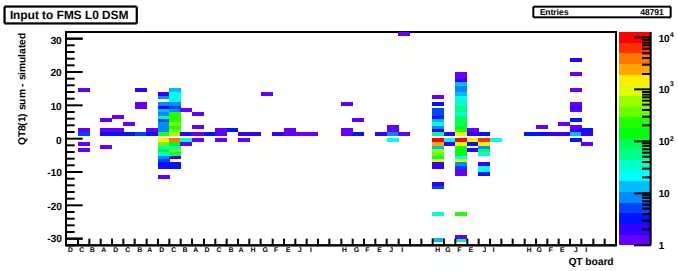

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1300664
Mean 3985
RMS 116

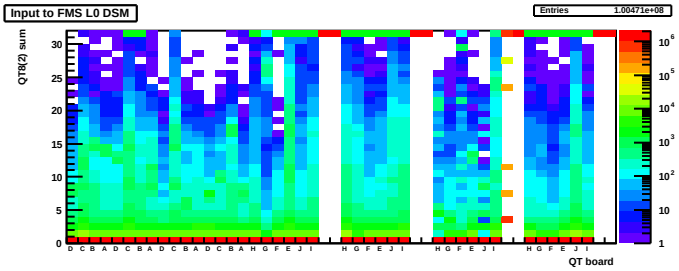

Input to QT1 crate (south-top)
Entries 2.779995e+07
Input to QT3 crate (north-top)
Entries 1.624256e+07
Input to QT2 crate (south-bottom)
Entries 1.800362e+07
Input to QT4 crate (north-bottom)
Entries 2.375059e+07
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

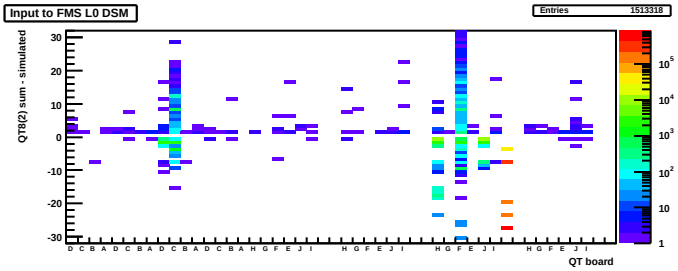

Input to FMS L0 DSM
Entries 1.00471e+08
QT8(0) sum
QT board

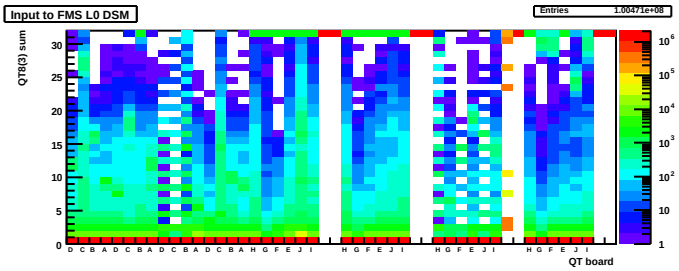

Input to FMS L0 DSM
Entries 41690
QT8(0) sum - simulated
QT board

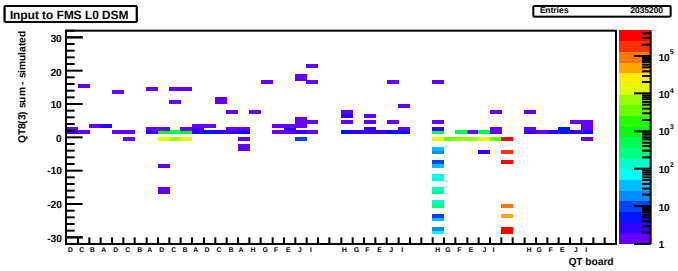

Input to FMS L0 DSM
Entries 1.00471e+08
QT8(1) sum
QT board

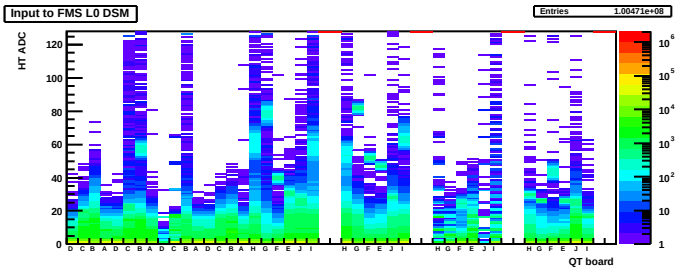

Input to FMS L0 DSM
Entries 48791
QT8(1) sum - simulated
QT board

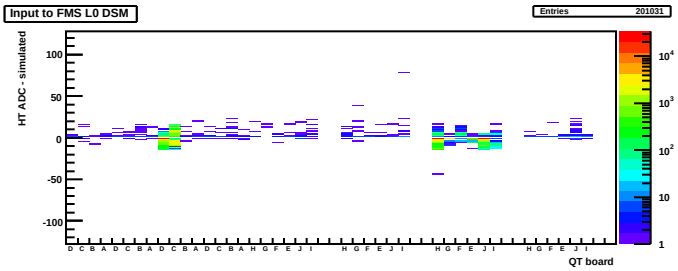

Input to FMS L0 DSM
Entries 1.00471e+08
QT8(2) sum
QT board

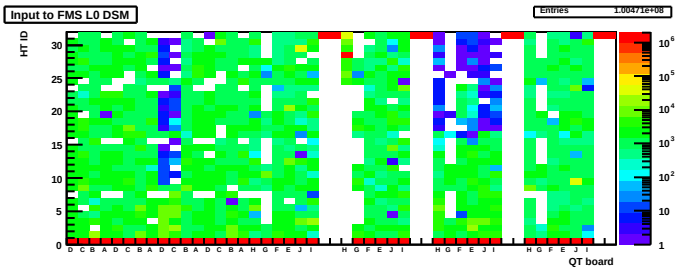

Input to FMS L0 DSM
Entries 1513318
QT8(2) sum - simulated
QT board

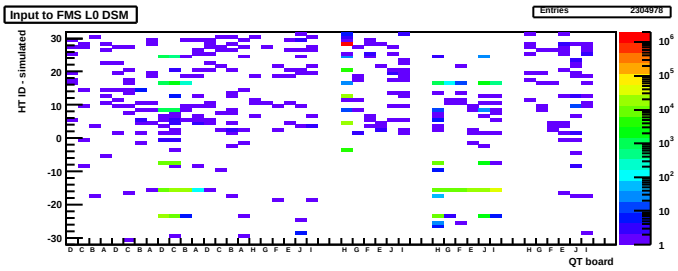

Input to FMS L0 DSM
Entries 1.00471e+08
QT8(3) sum
QT board

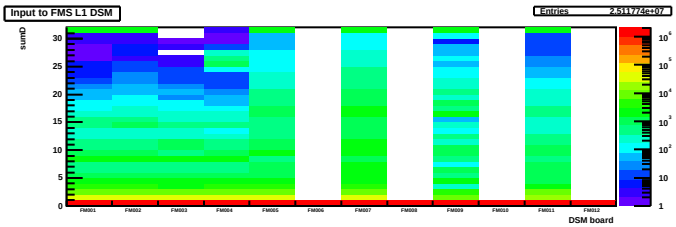

Input to FMS L0 DSM
Entries 2035200
QT8(3) sum - simulated
QT board

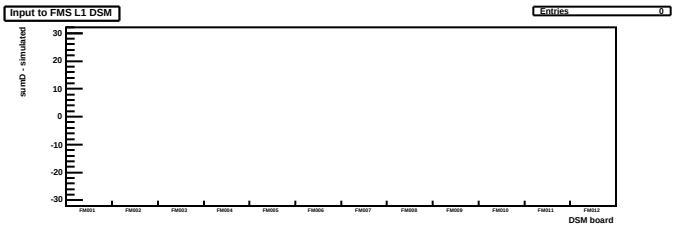

Input to FMS L0 DSM
Entries 1.00471e+08
HT ADC
QT board

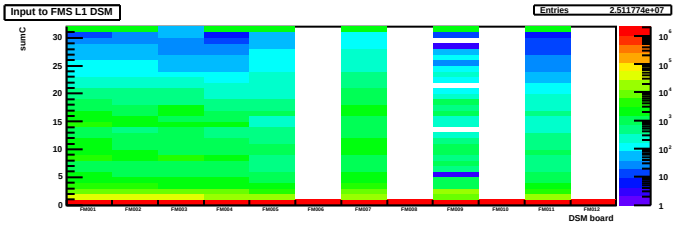

Input to FMS L0 DSM
Entries 201031
HT ADC - simulated
QT board

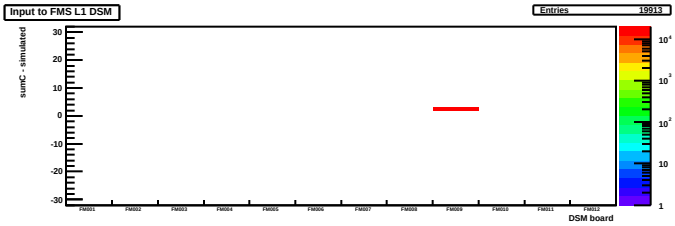

Input to FMS L0 DSM
Entries 1.00471e+08
HT ID
QT board

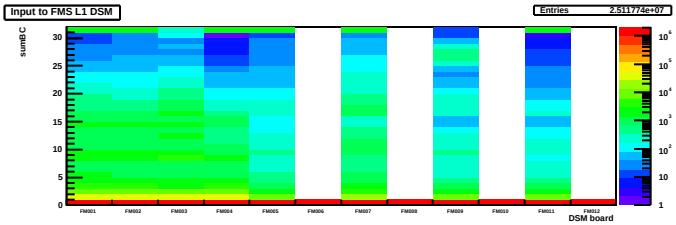

Input to FMS L0 DSM
Entries 2304978
HT ID - simulated
QT board

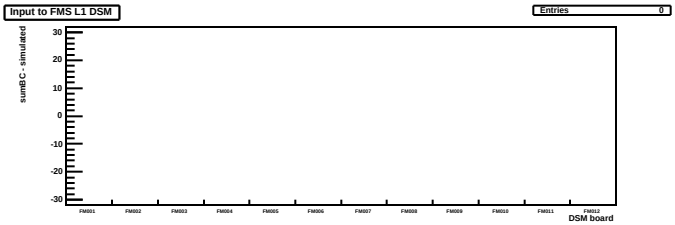

Input to FMS L1 DSM
Entries 2.511774e+07
sumD
DSM board

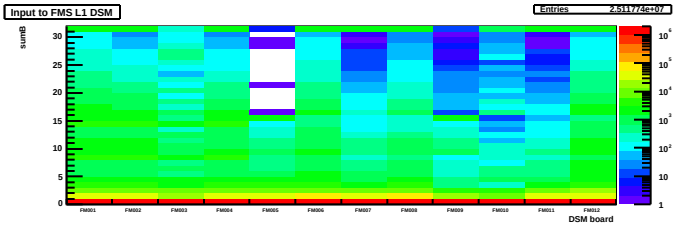

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

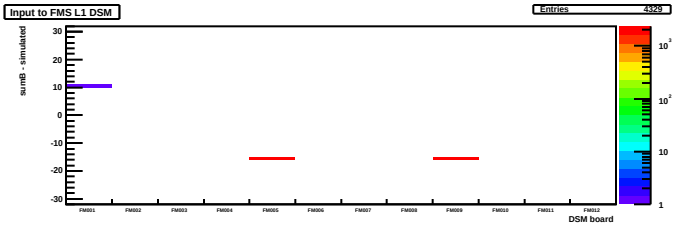

Input to FMS L1 DSM
Entries 2.511774e+07
sumC
DSM board

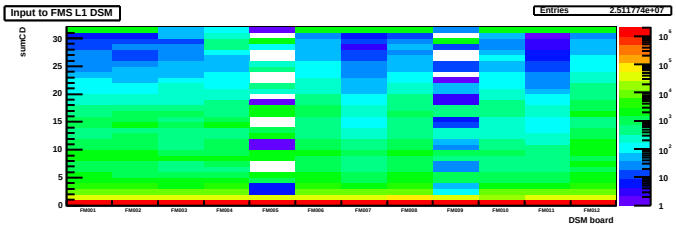

Input to FMS L1 DSM
Entries 19913
sumC - simulated
DSM board

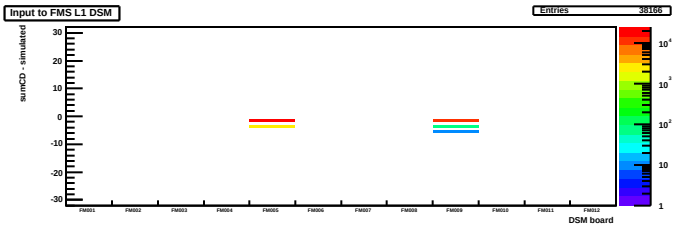

Input to FMS L1 DSM
Entries 2.511774e+07
sumBC
DSM board

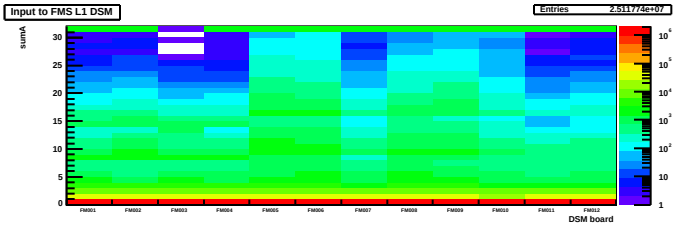

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

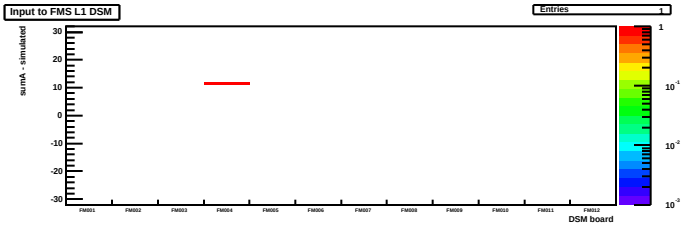

Input to FMS L1 DSM
Entries 2.511774e+07
sumB
DSM board

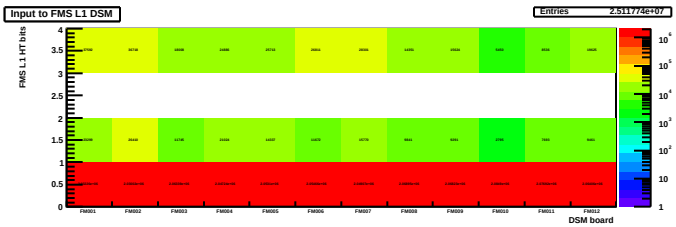

Input to FMS L1 DSM
Entries 4329
sumB - simulated
DSM board

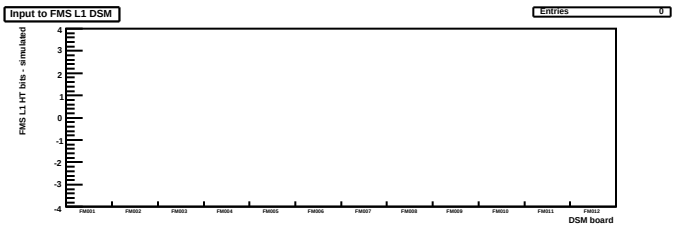

Input to FMS L1 DSM
Entries 2.511774e+07
sumCD
DSM board

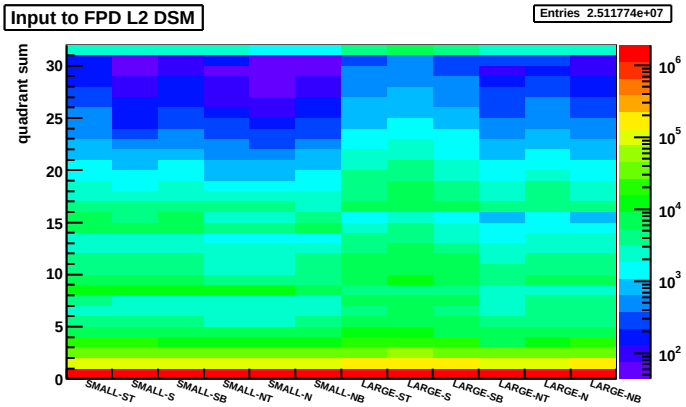

Input to FMS L1 DSM
Entries 38166
sumCD - simulated
DSM board

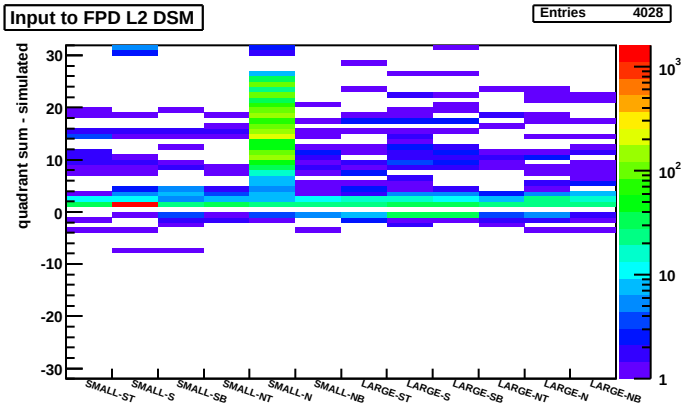

Input to FMS L1 DSM
Entries 2.511774e+07
sumA
DSM board

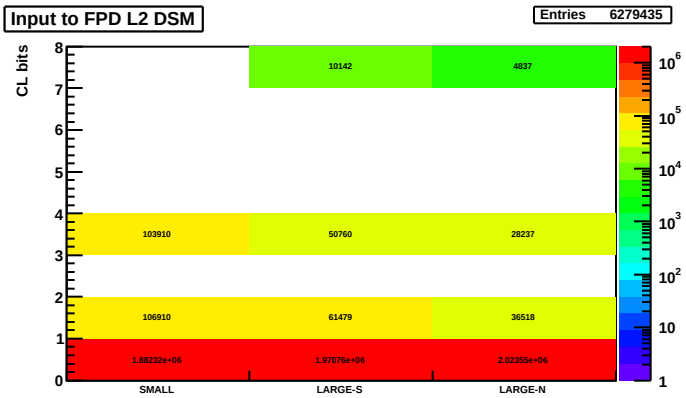

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

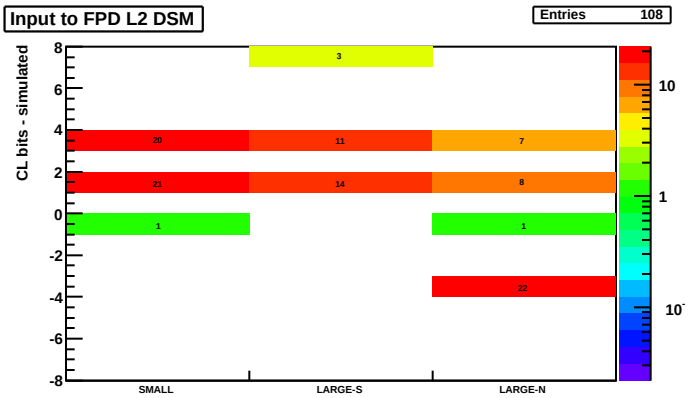

Input to FMS L1 DSM
Entries 2.511774e+07
FMS L1 HT bits
DSM board

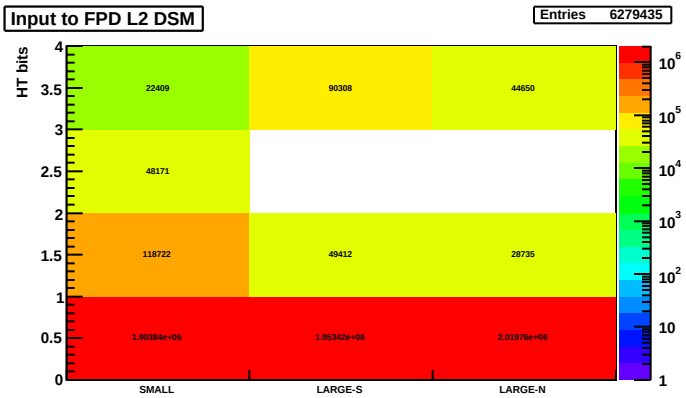

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

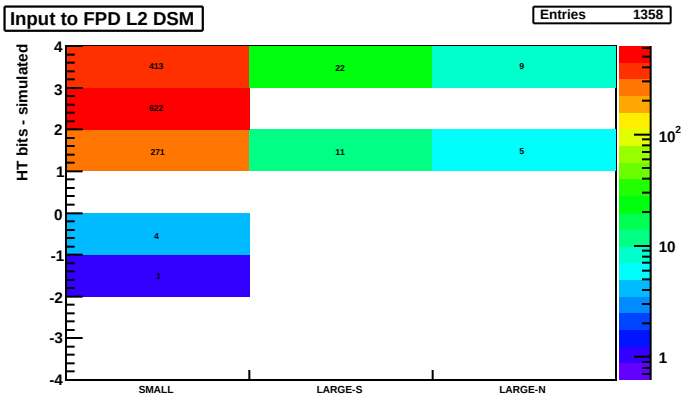

Input to FPD L2 DSM
Entries 2.511774e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

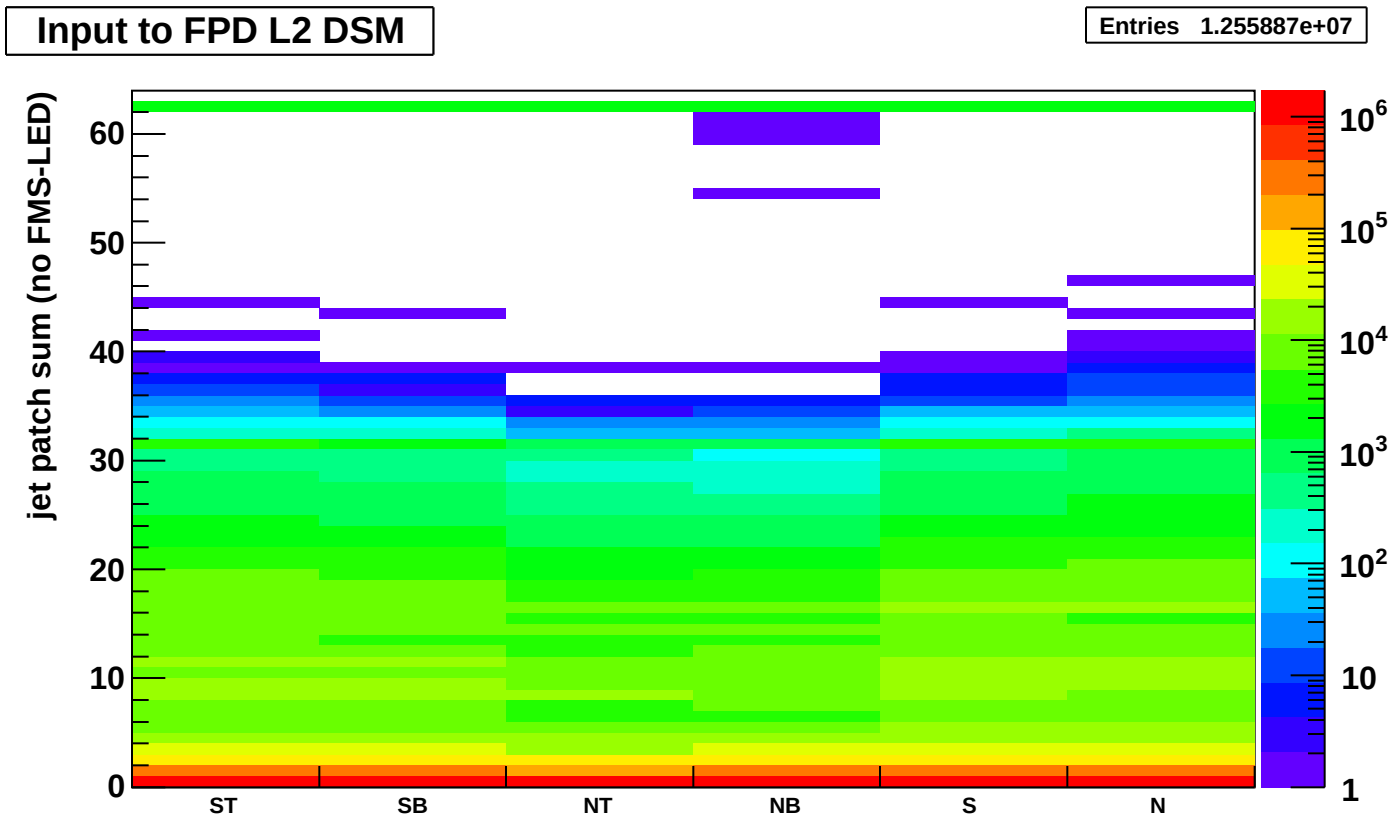

Input to FPD L2 DSM
Entries 4028
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

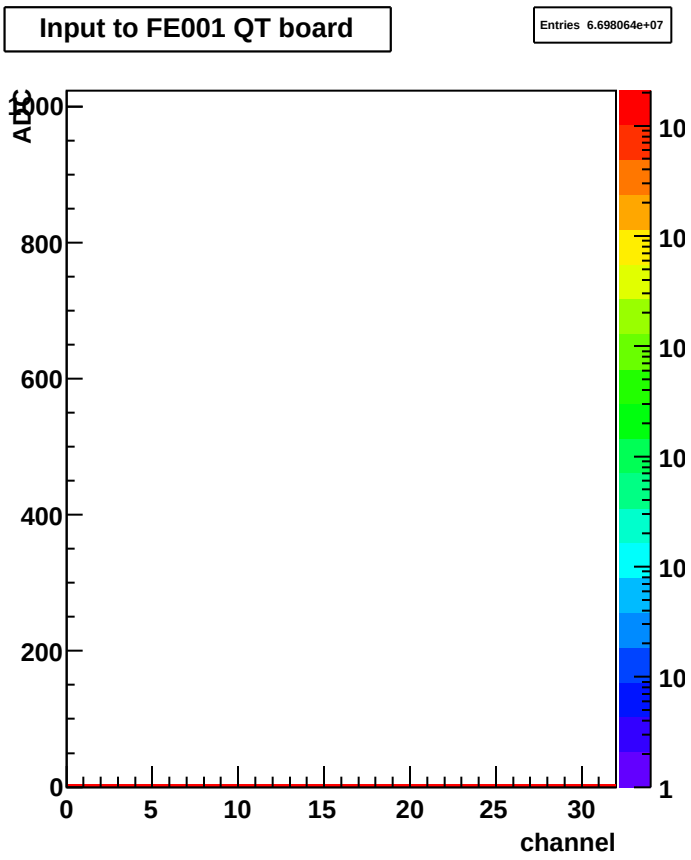

Input to FPD L2 DSM
Entries 6279435
CL bits
10142
4837
103910
50760
28237
106910
61479
36518
1.88232e+06
1.97076e+06
2.02355e+06
SMALL
LARGE-S
LARGE-N

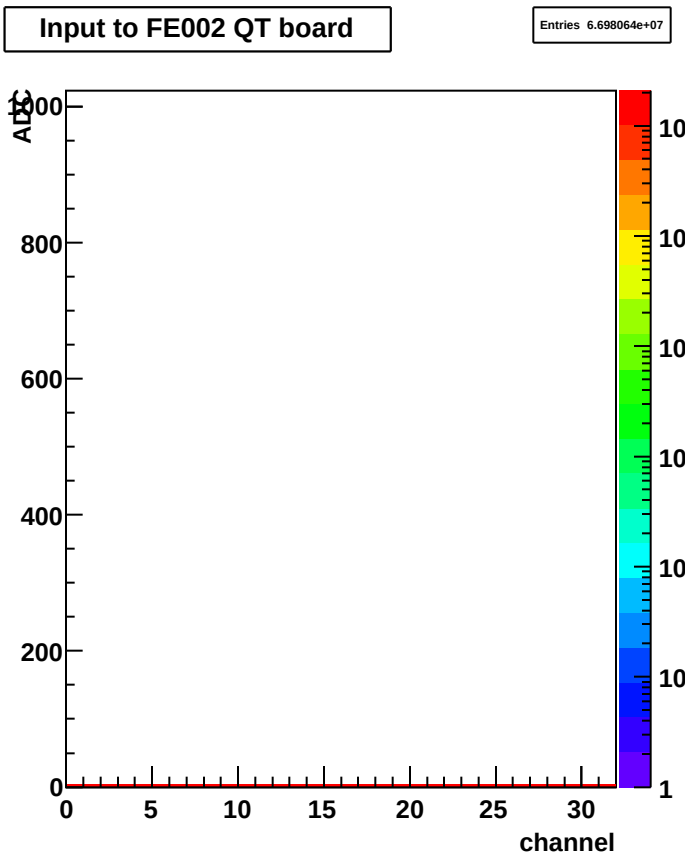

Input to FPD L2 DSM
Entries 108
CL bits - simulated
3
20
11
7
21
14
8
1
1
22
SMALL
LARGE-S
LARGE-N

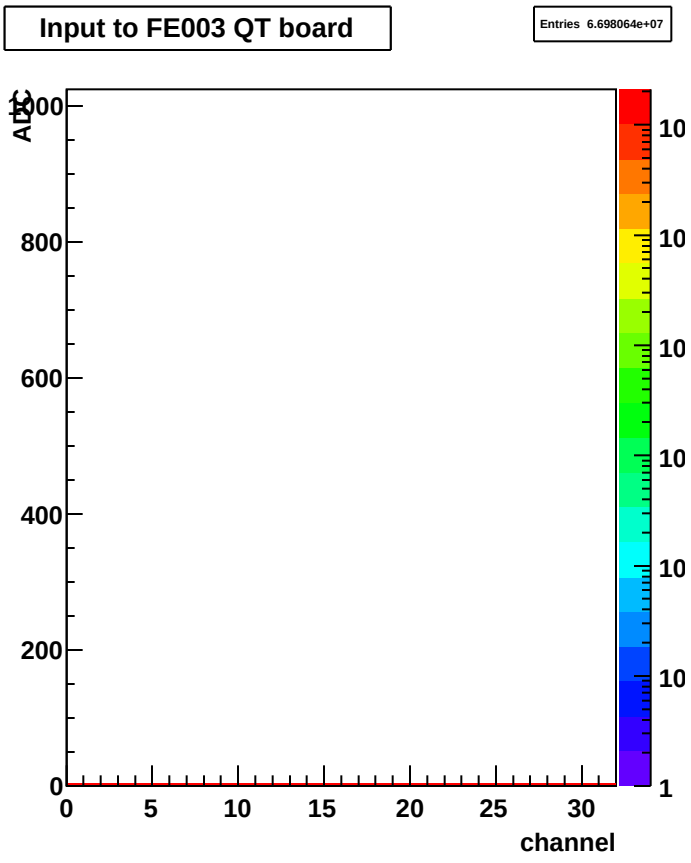

Input to FPD L2 DSM
Entries 6279435
HT bits
22409
90308
44650
48171
118722
49412
28735
1.90384e+06
1.95342e+06
2.01976e+06
SMALL
LARGE-S
LARGE-N

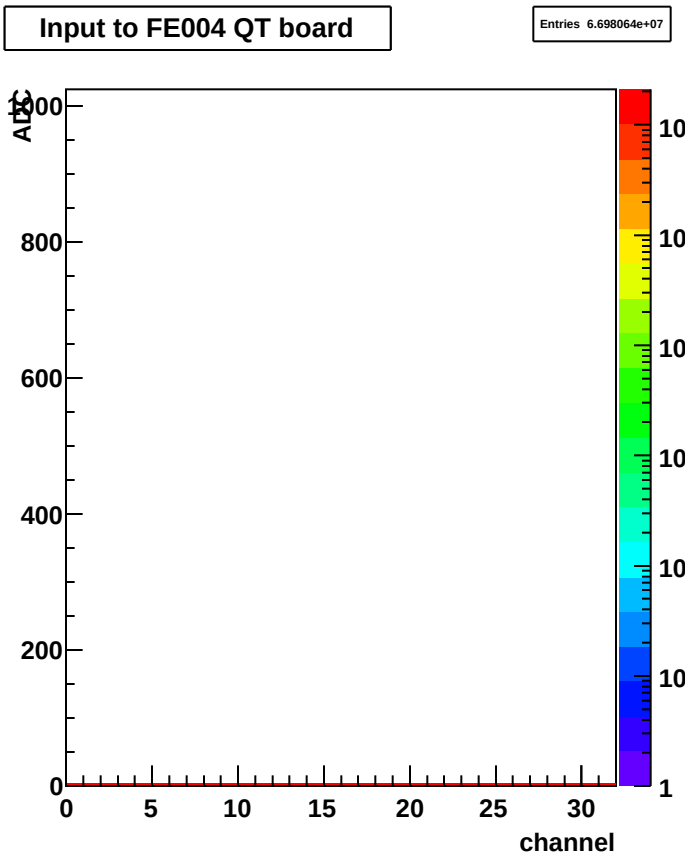

Input to FPD L2 DSM
Entries 1358
HT bits - simulated
413
22
9
622
271
11
5
4
1
SMALL
LARGE-S
LARGE-N

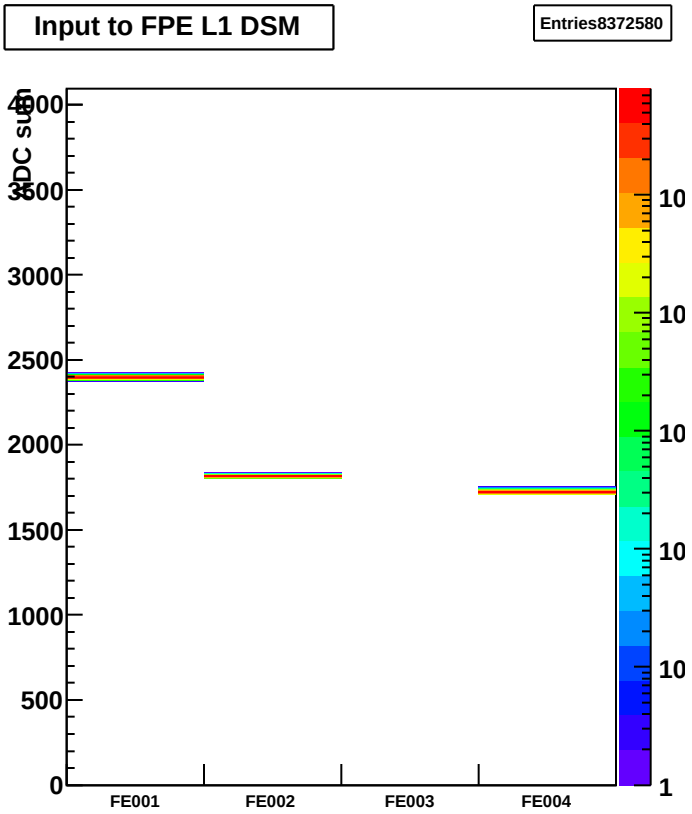

Input to FPD L2 DSM
Entries 1.255887e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.698064e+07
ADC
channel

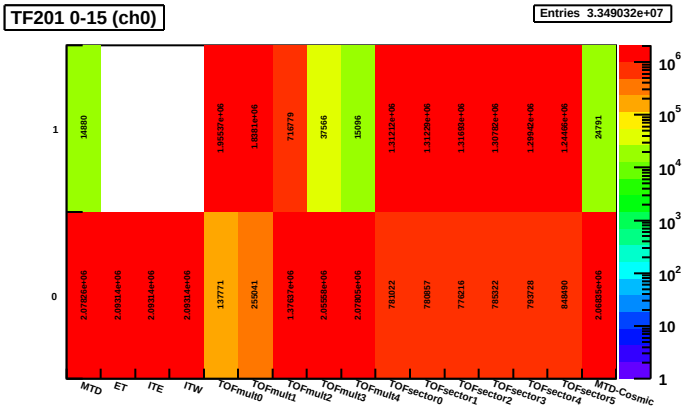

Input to FE002 QT board
Entries 6.698064e+07
ADC
channel


Input to FE003 QT board
Entries 6.698064e+07
ADC
channel

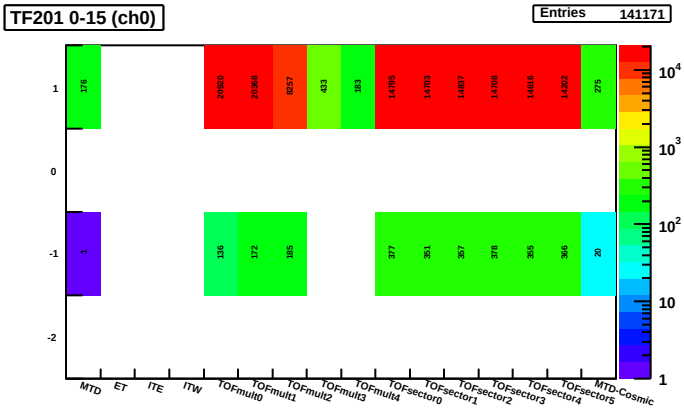

Input to FE004 QT board
Entries 6.698064e+07
ADC
channel


Input to FPE L1 DSM
Entries8372580
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

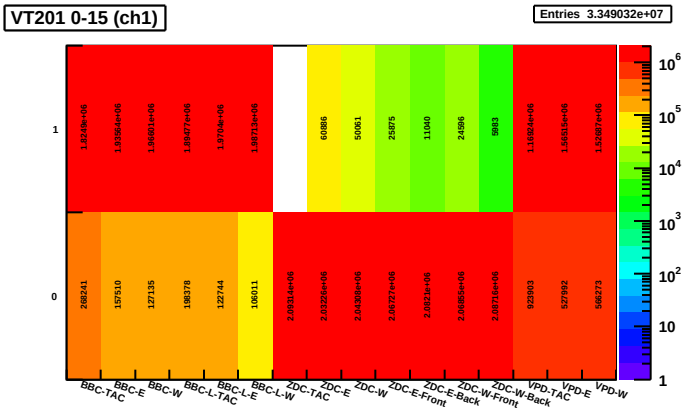
TF201 0-15 (ch0)



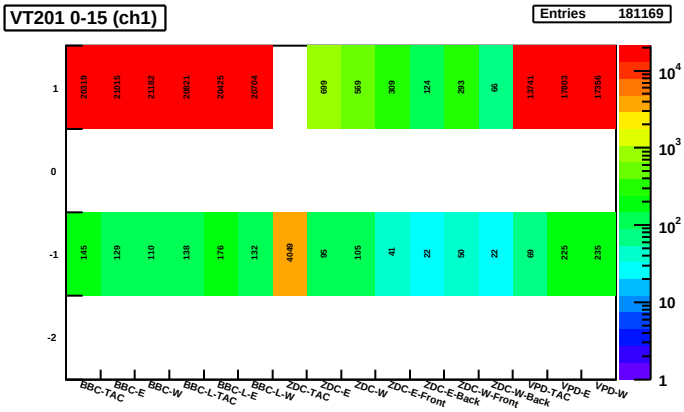
TF201 0-15 (ch0)



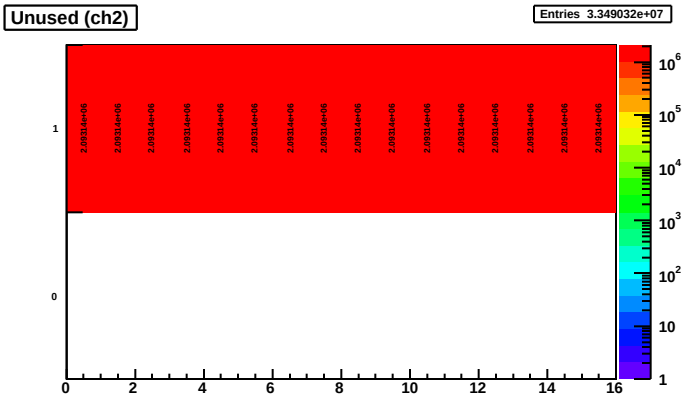
VT201 0-15 (ch1)

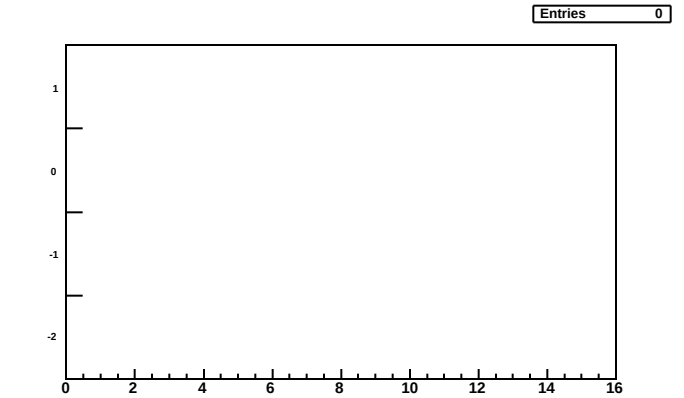


VT201 0-15 (ch1)

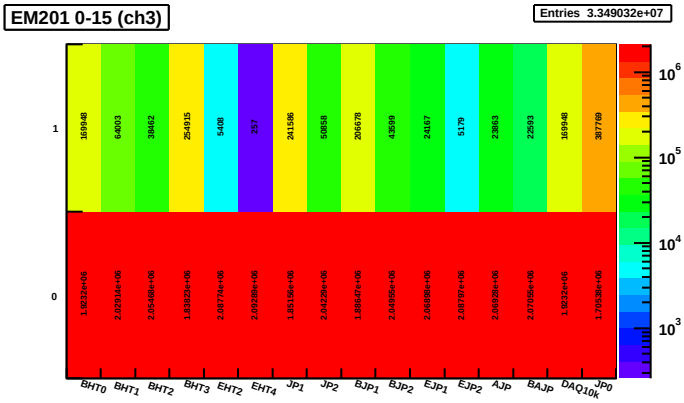


Unused (ch2)

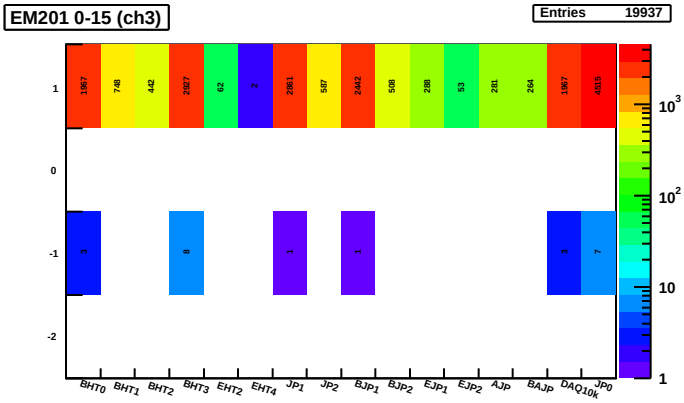




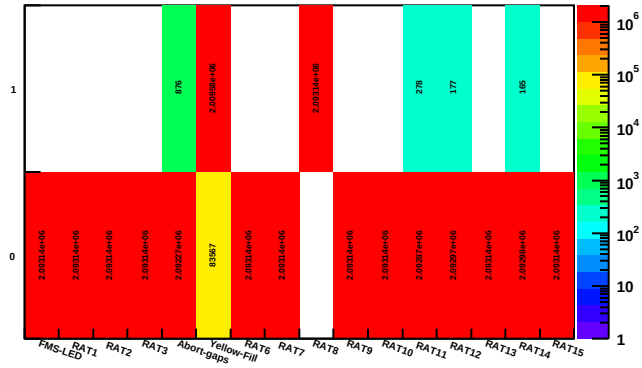
EM201 0-15 (ch3)

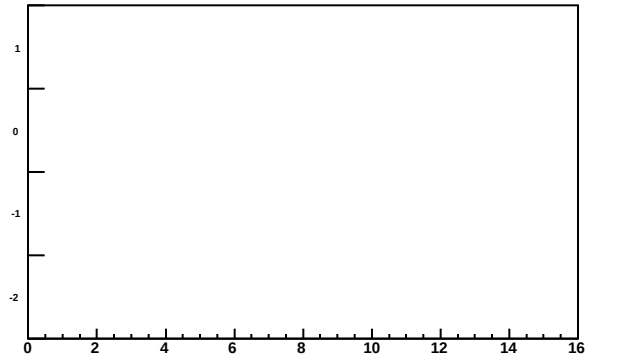


EM201 0-15 (ch3)

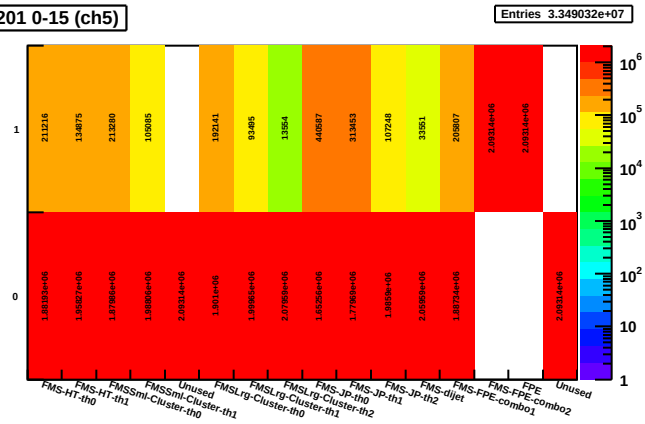


RAT board (ch4)





FP201 0-15 (ch5)



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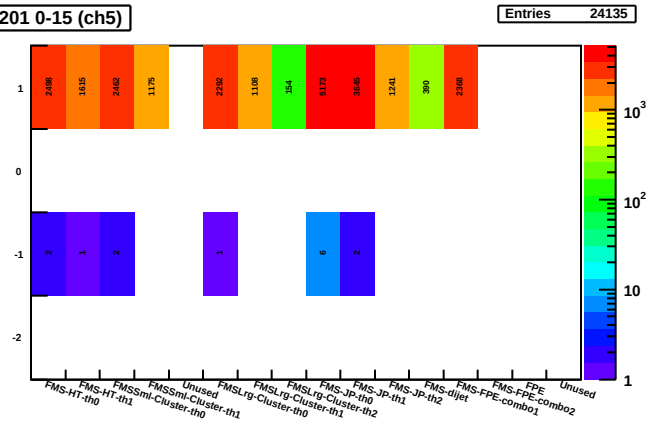
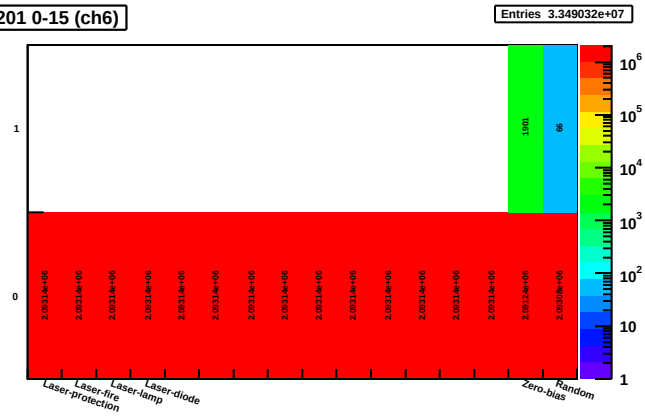
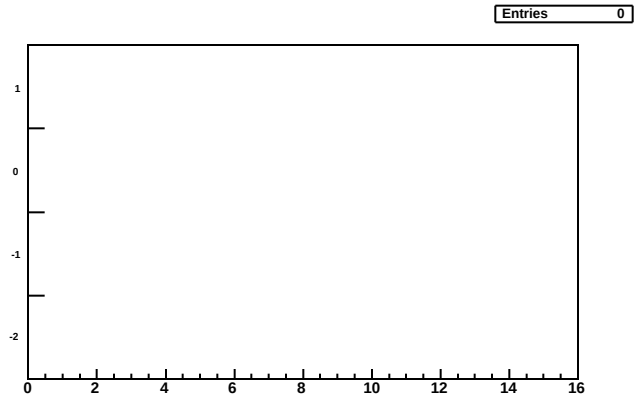
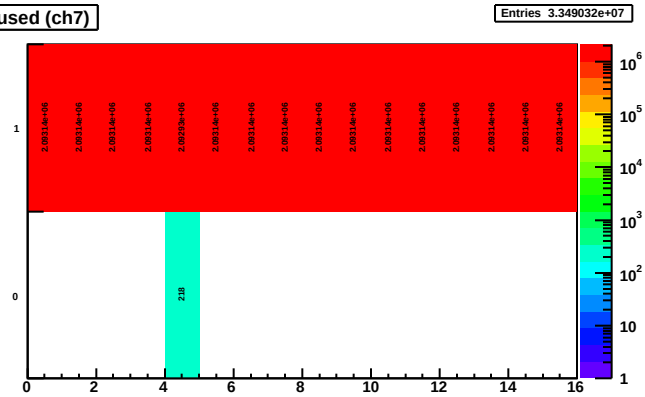
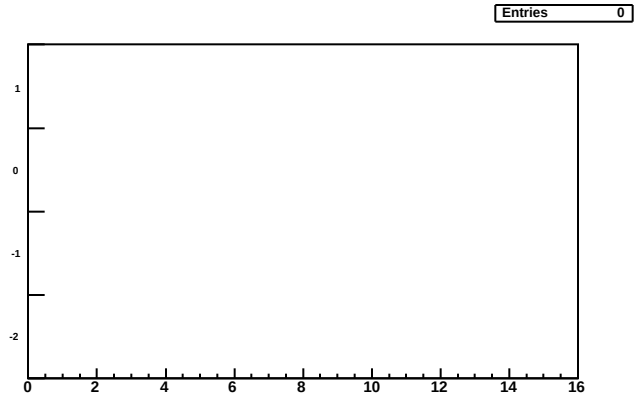


ST201 0-15 (ch6)





Unused (ch7)