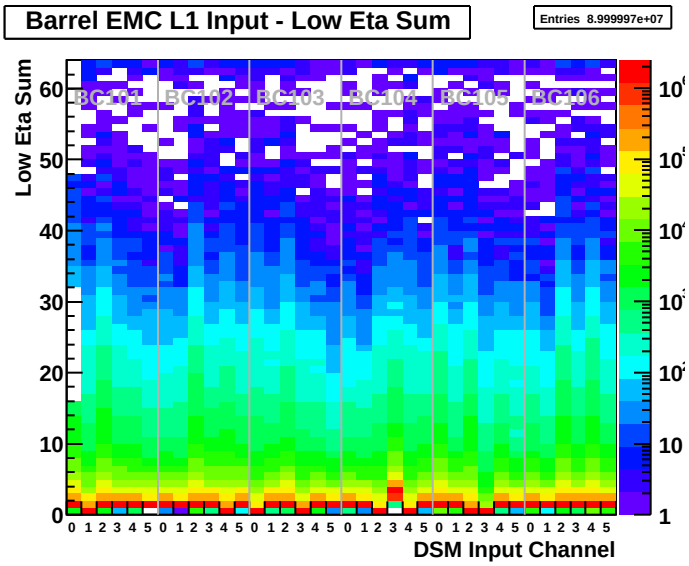
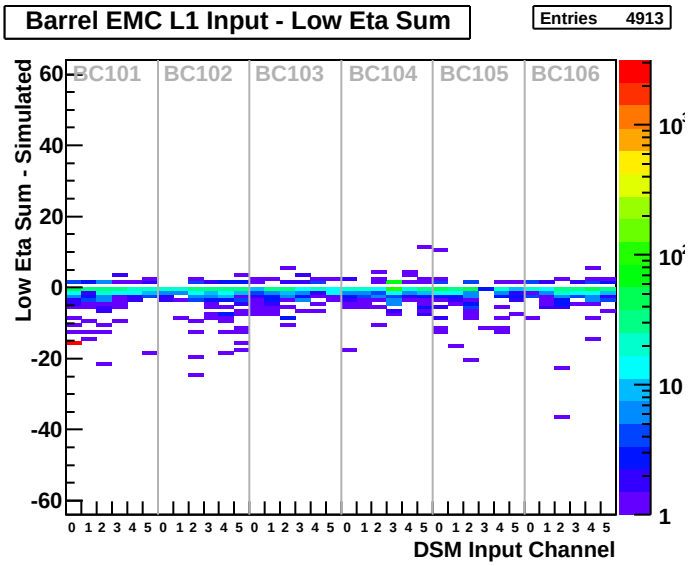
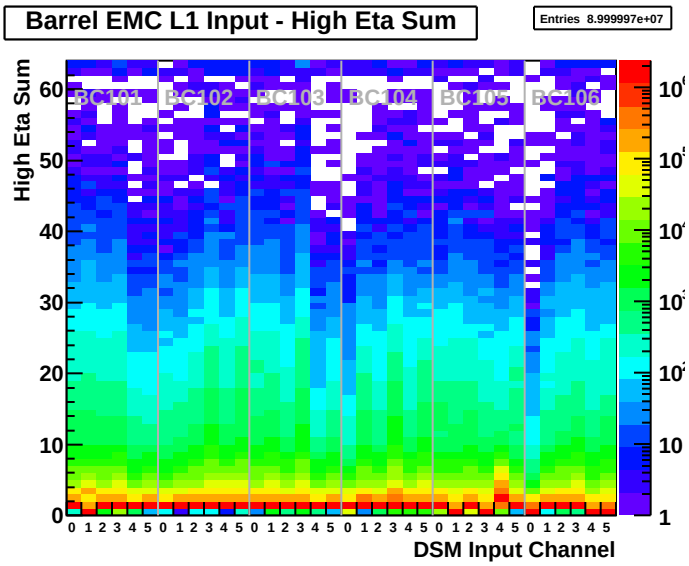
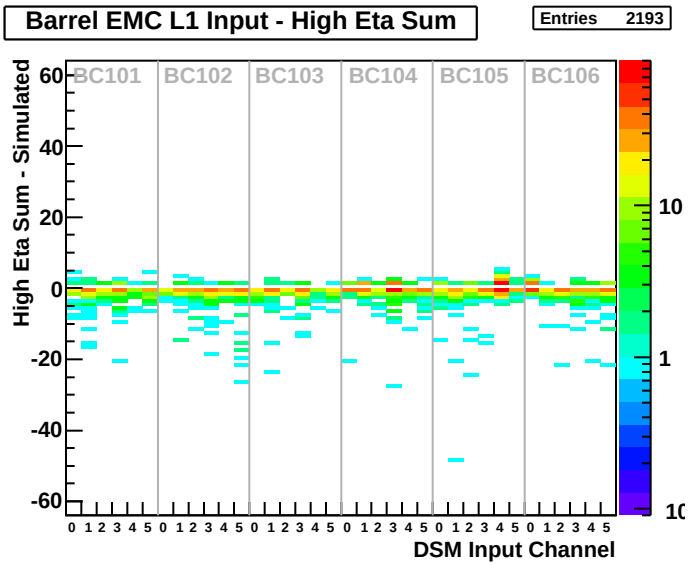
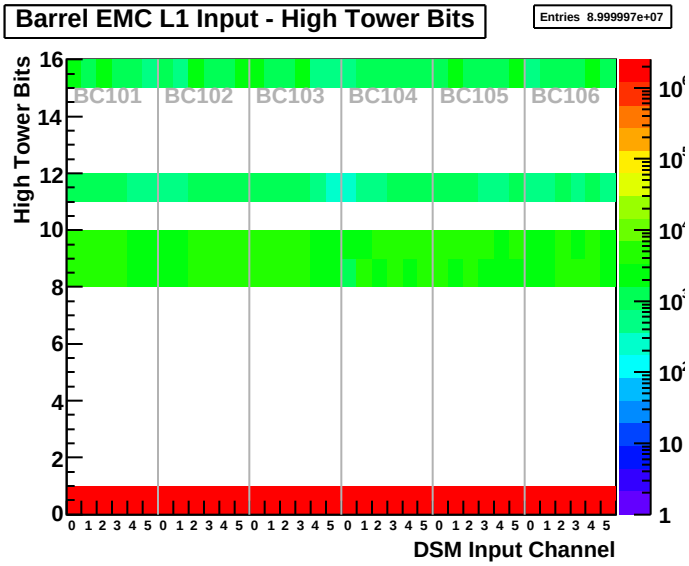
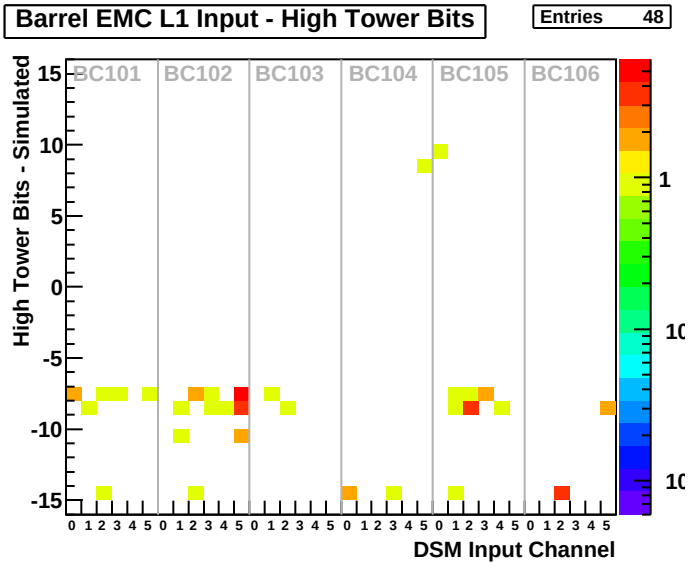

Barrel EMC L1 Input - Low Eta Sum
Entries 8.999997e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

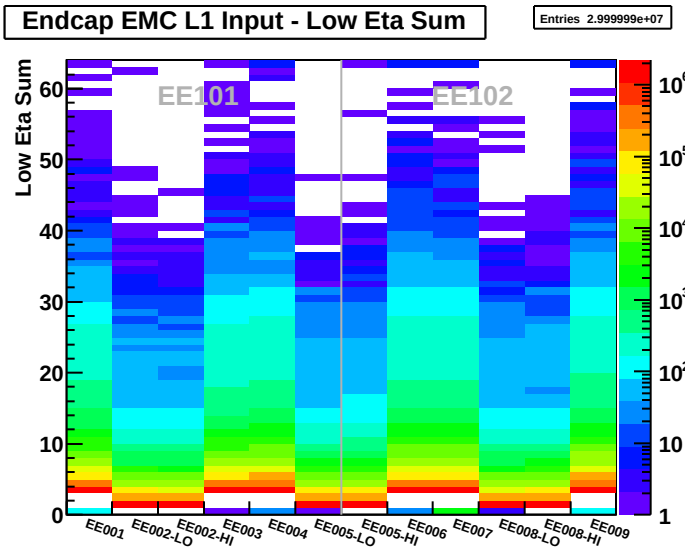

Barrel EMC L1 Input - Low Eta Sum
Entries 4913
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

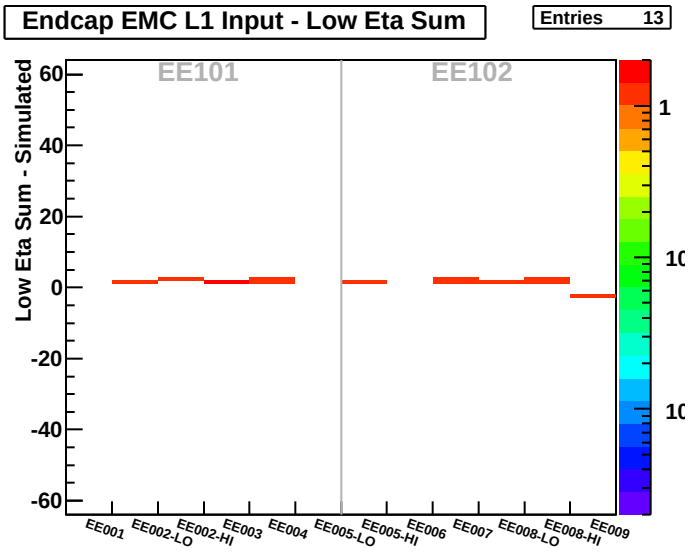

Barrel EMC L1 Input - High Eta Sum
Entries 8.999997e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

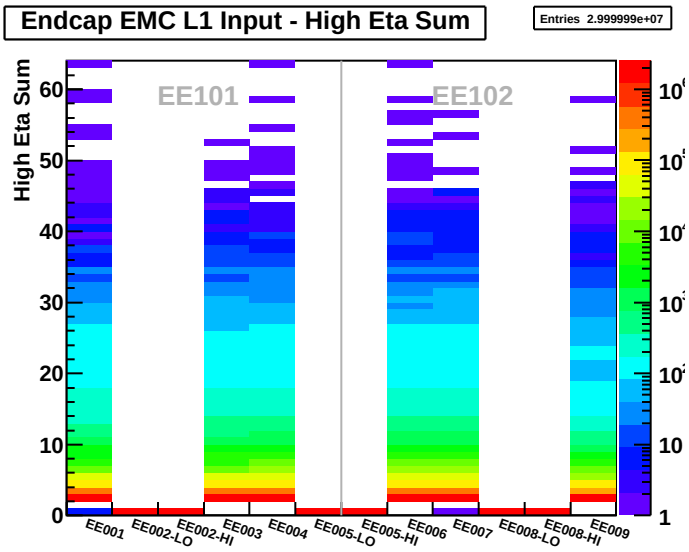

Barrel EMC L1 Input - High Eta Sum
Entries 2193
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

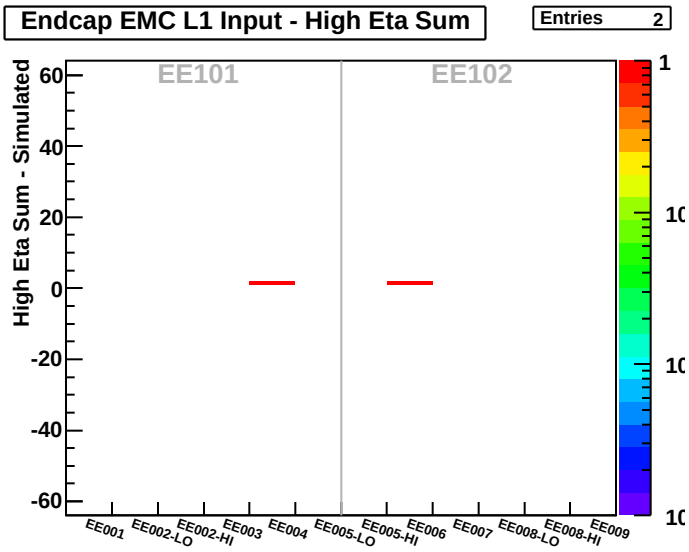

Barrel EMC L1 Input - High Tower Bits
Entries 8.999997e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

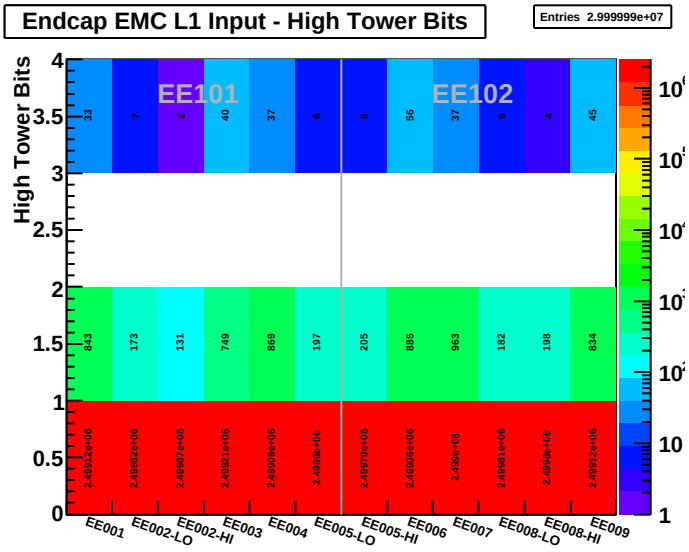

Barrel EMC L1 Input - High Tower Bits
Entries 48
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

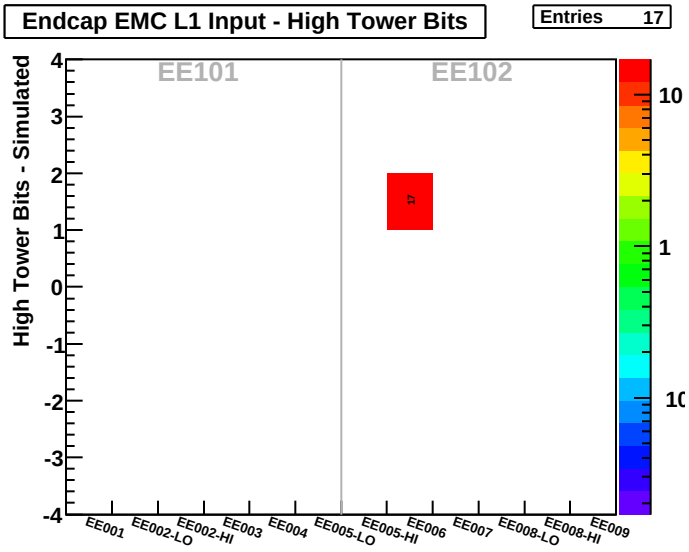

Endcap EMC L1 Input - Low Eta Sum
Entries 2.999999e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

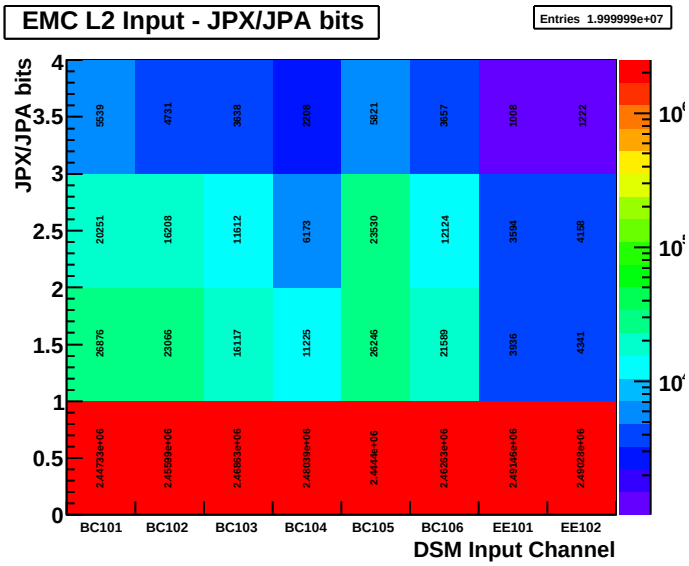

Endcap EMC L1 Input - Low Eta Sum
Entries 13
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

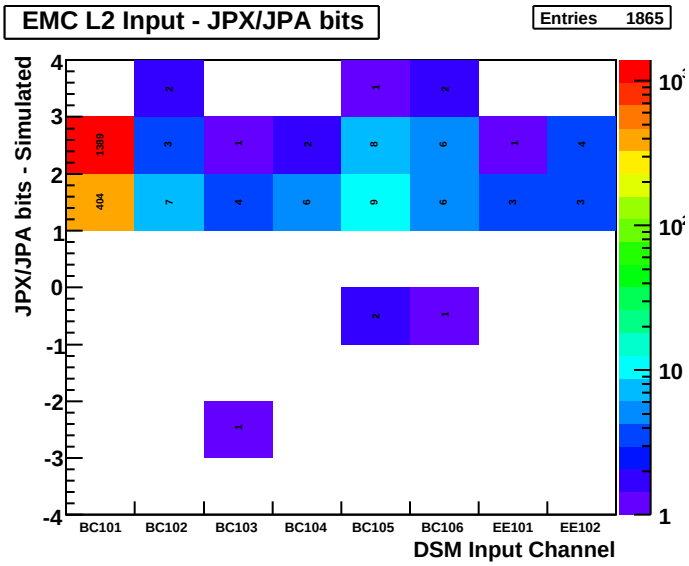

Endcap EMC L1 Input - High Eta Sum
Entries 2.999999e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

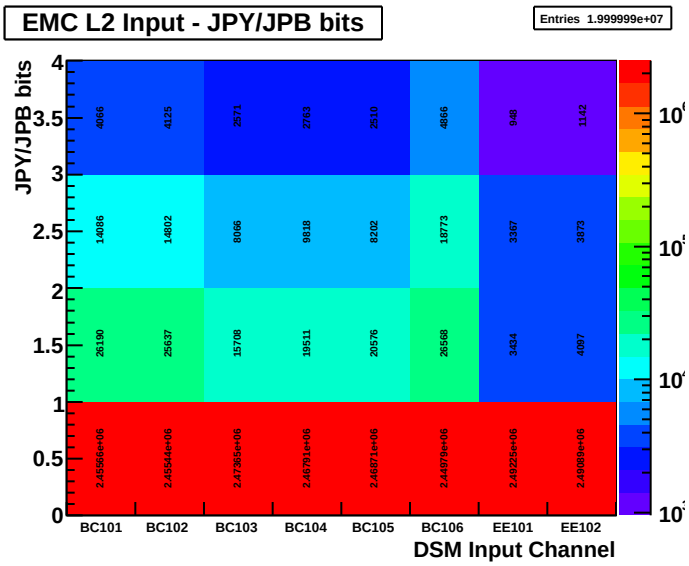

Endcap EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

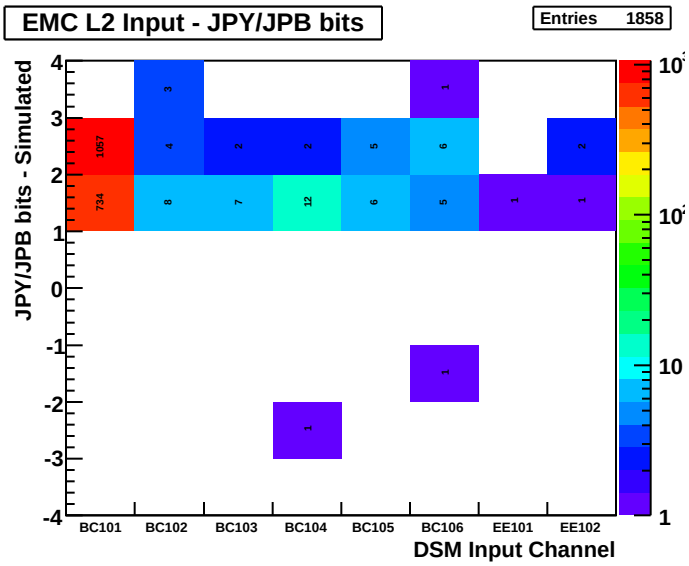

Endcap EMC L1 Input - High Tower Bits
Entries 2.999999e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

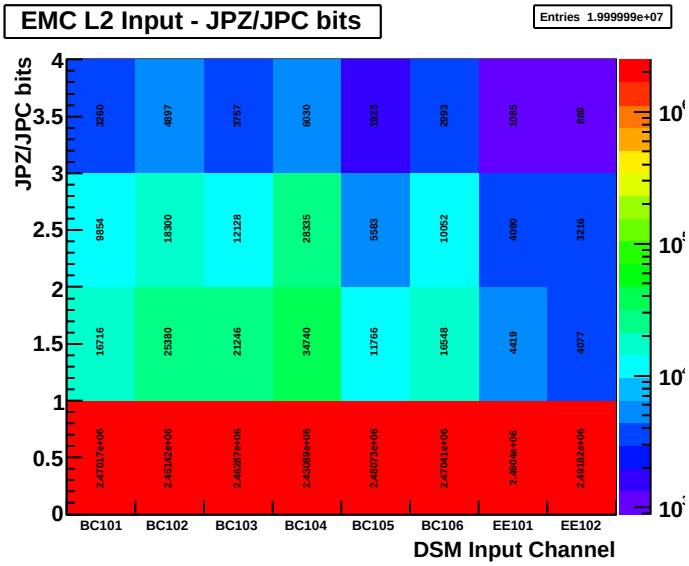

Endcap EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

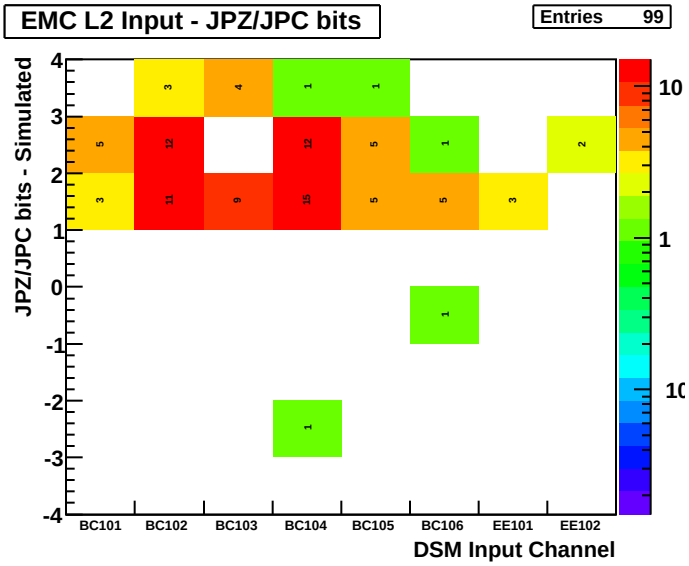

EMC L2 Input - JPX/JPA bits
Entries 1.999999e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

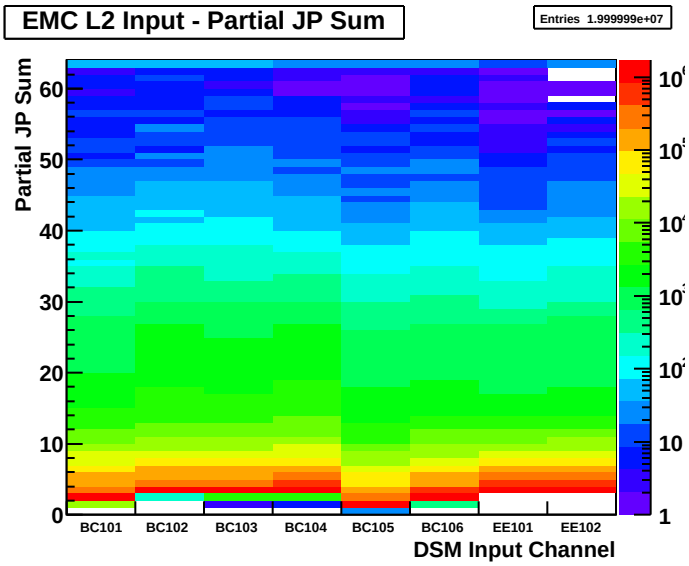

EMC L2 Input - JPX/JPA bits
Entries 1865
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

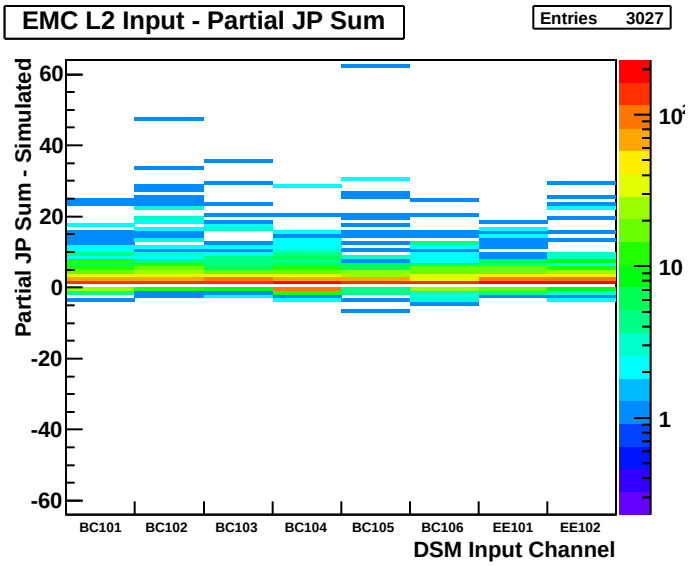

EMC L2 Input - JPY/JPB bits
Entries 1.999999e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

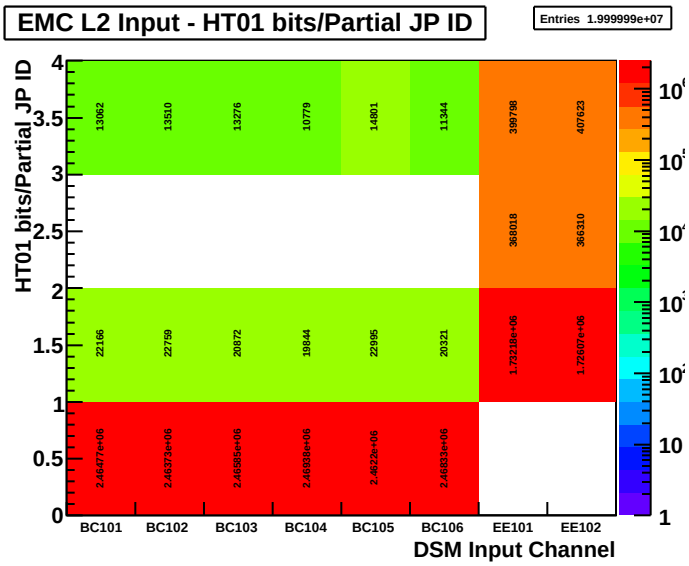

EMC L2 Input - JPY/JPB bits
Entries 1858
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

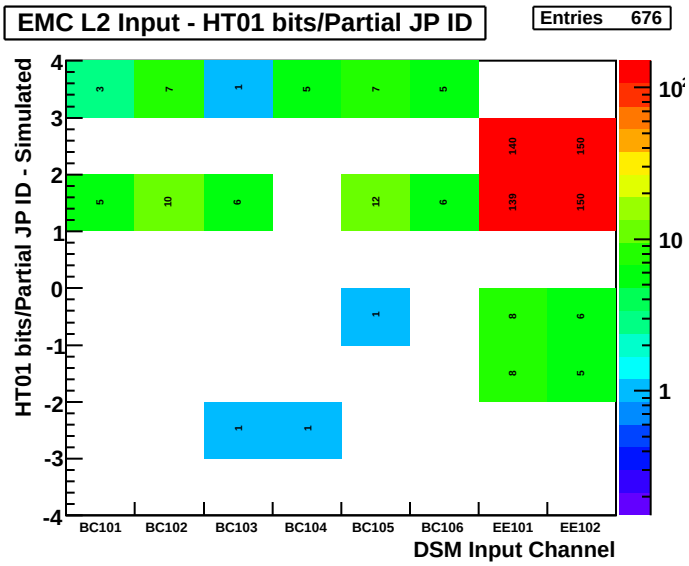

EMC L2 Input - JPZ/JPC bits
Entries 1.999999e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

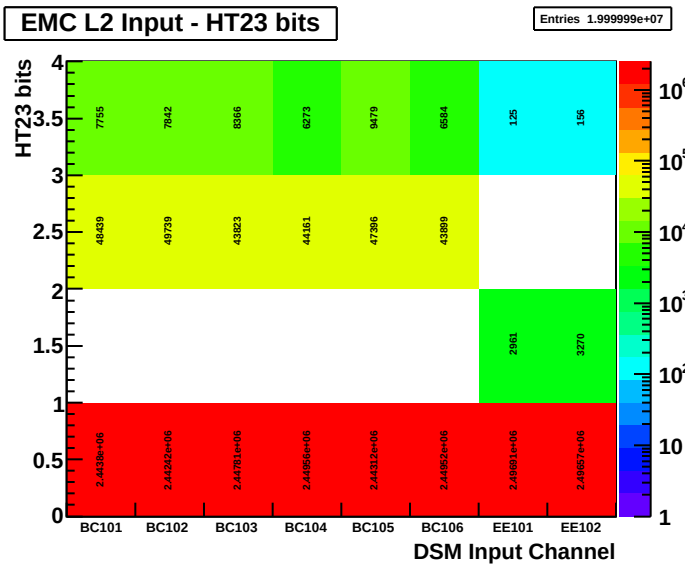

EMC L2 Input - JPZ/JPC bits
Entries 99
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

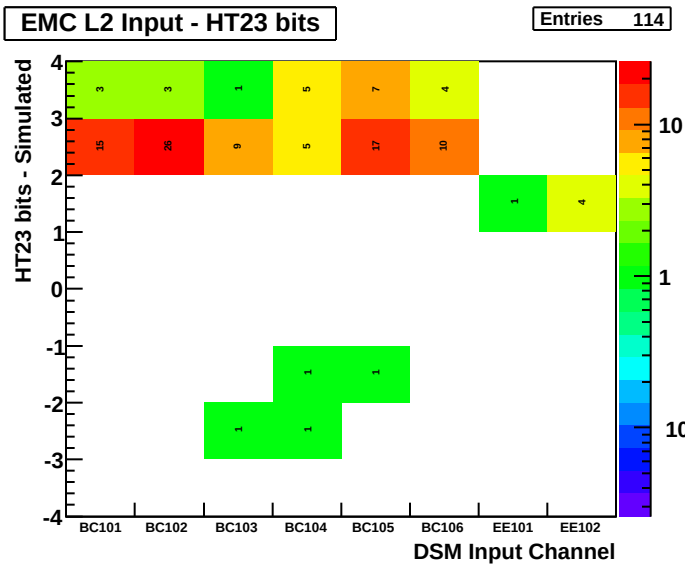

EMC L2 Input - Partial JP Sum
Entries 1.999999e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

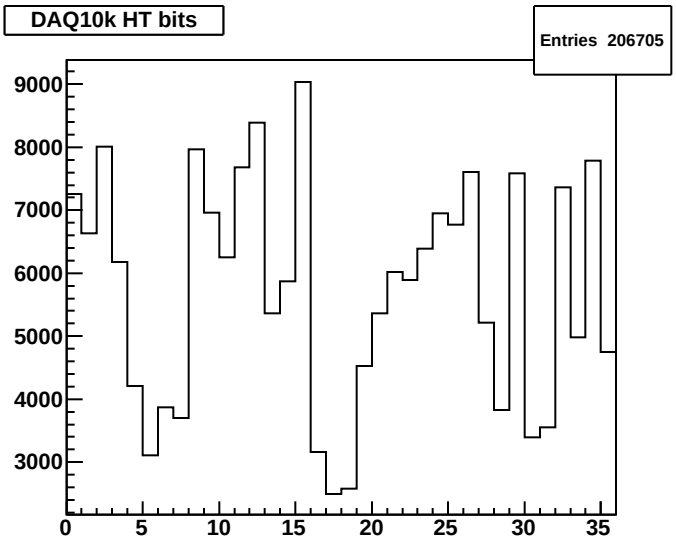

EMC L2 Input - Partial JP Sum
Entries 3027
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

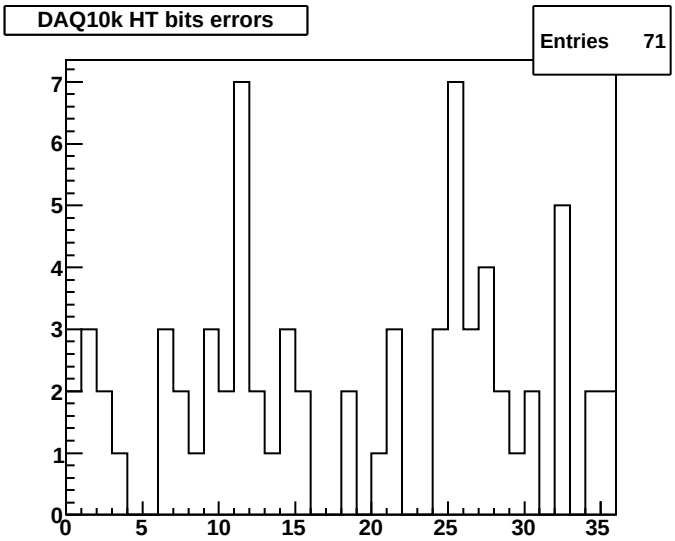

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.999999e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

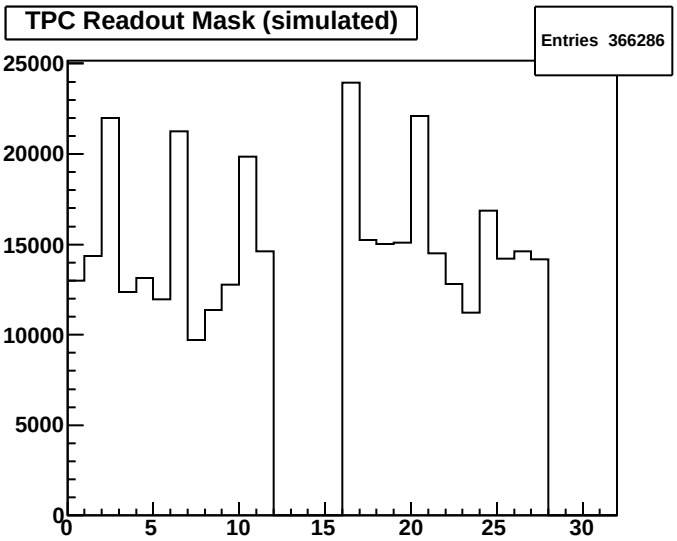

EMC L2 Input - HT01 bits/Partial JP ID
Entries 676
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

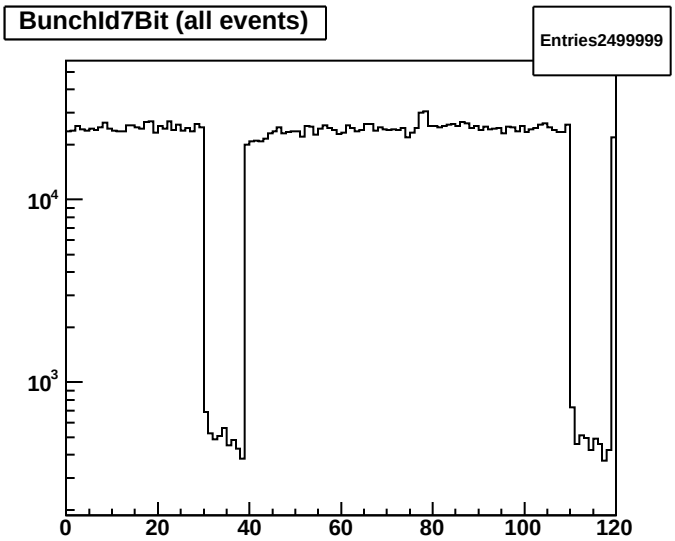

EMC L2 Input - HT23 bits
Entries 1.999999e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

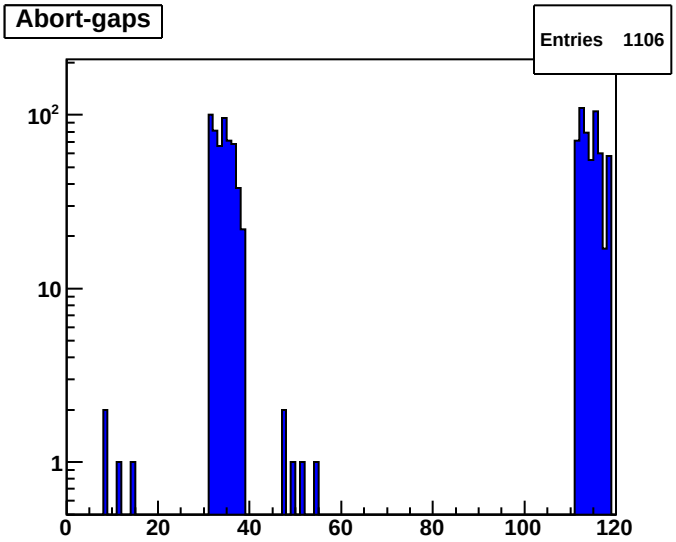

EMC L2 Input - HT23 bits
Entries 114
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

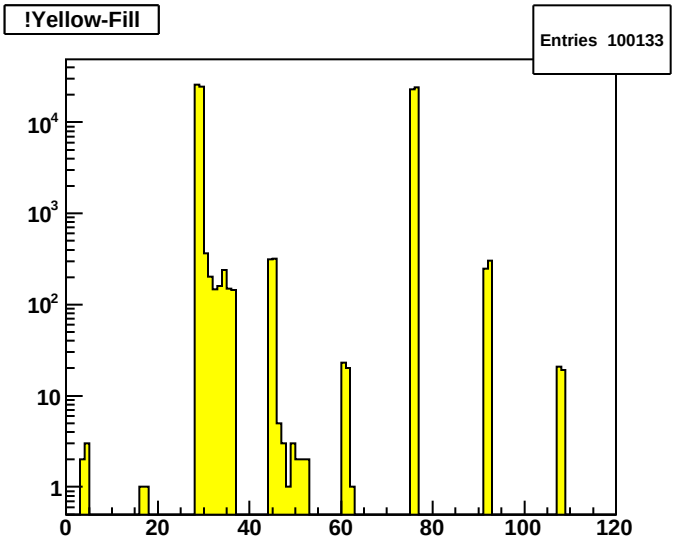

DAQ10k HT bits
Entries 206705

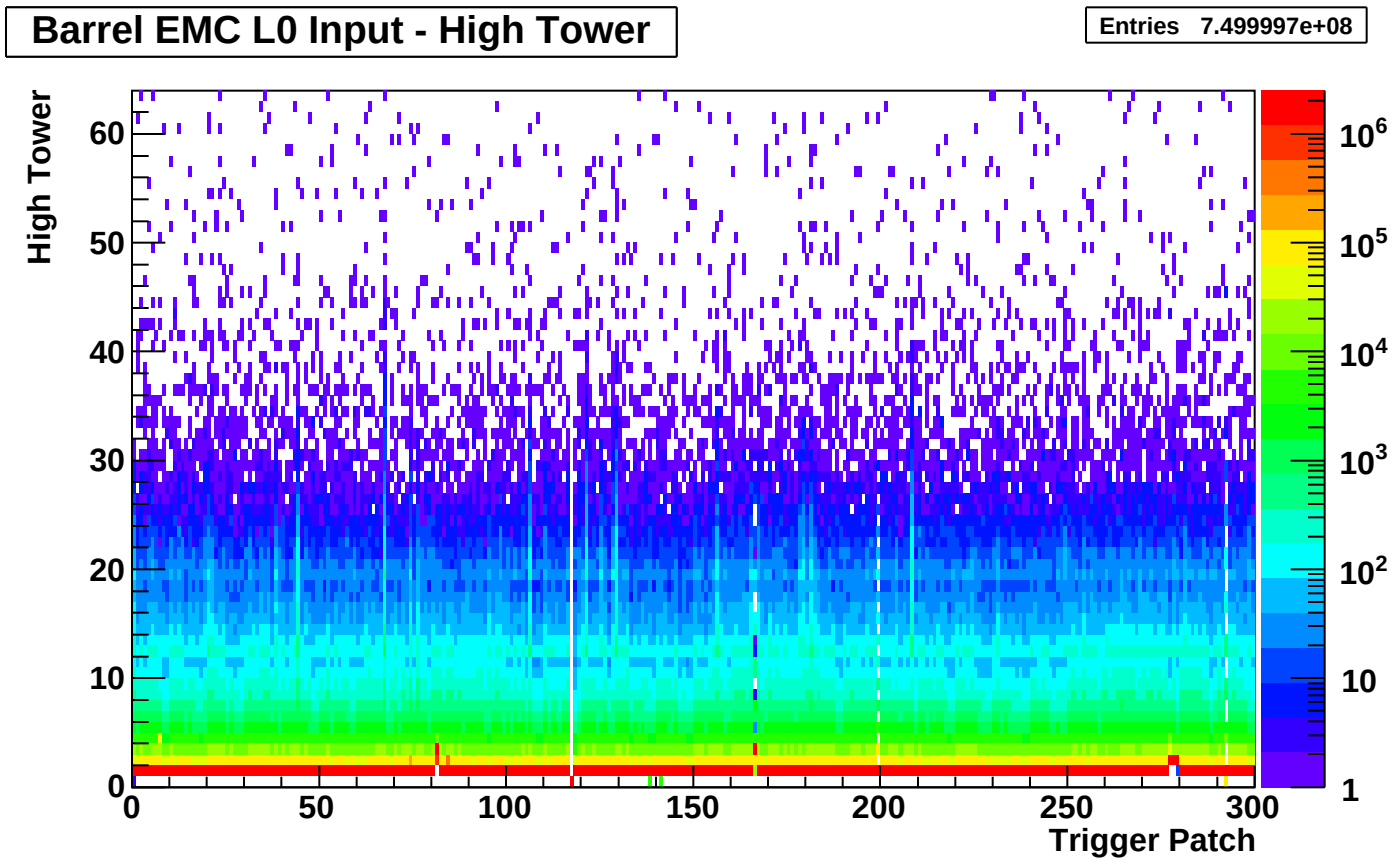

DAQ10k HT bits errors
Entries 71

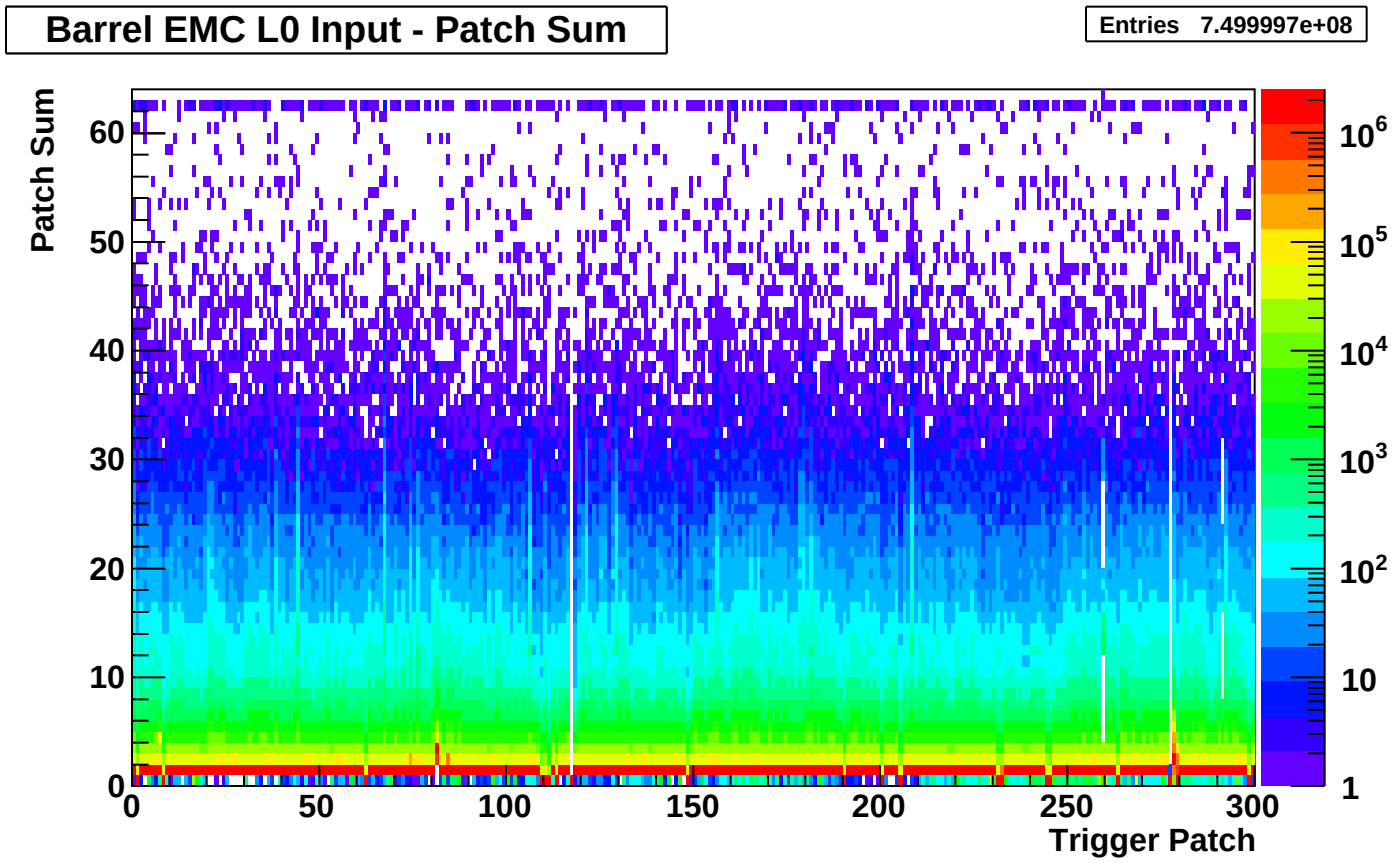

TPC Readout Mask (simulated)
Entries 366286

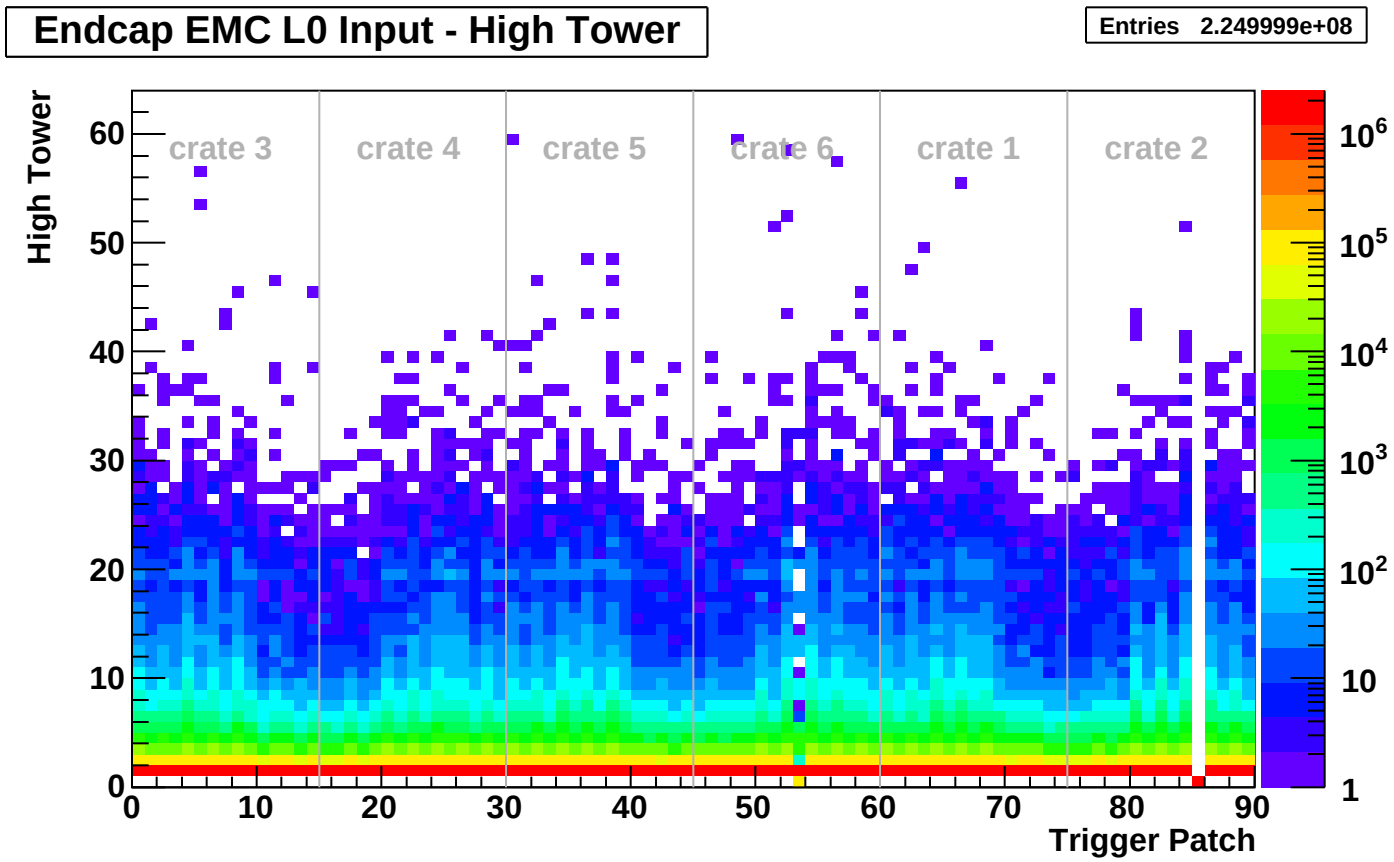

BunchId7Bit (all events)
Entries2499999

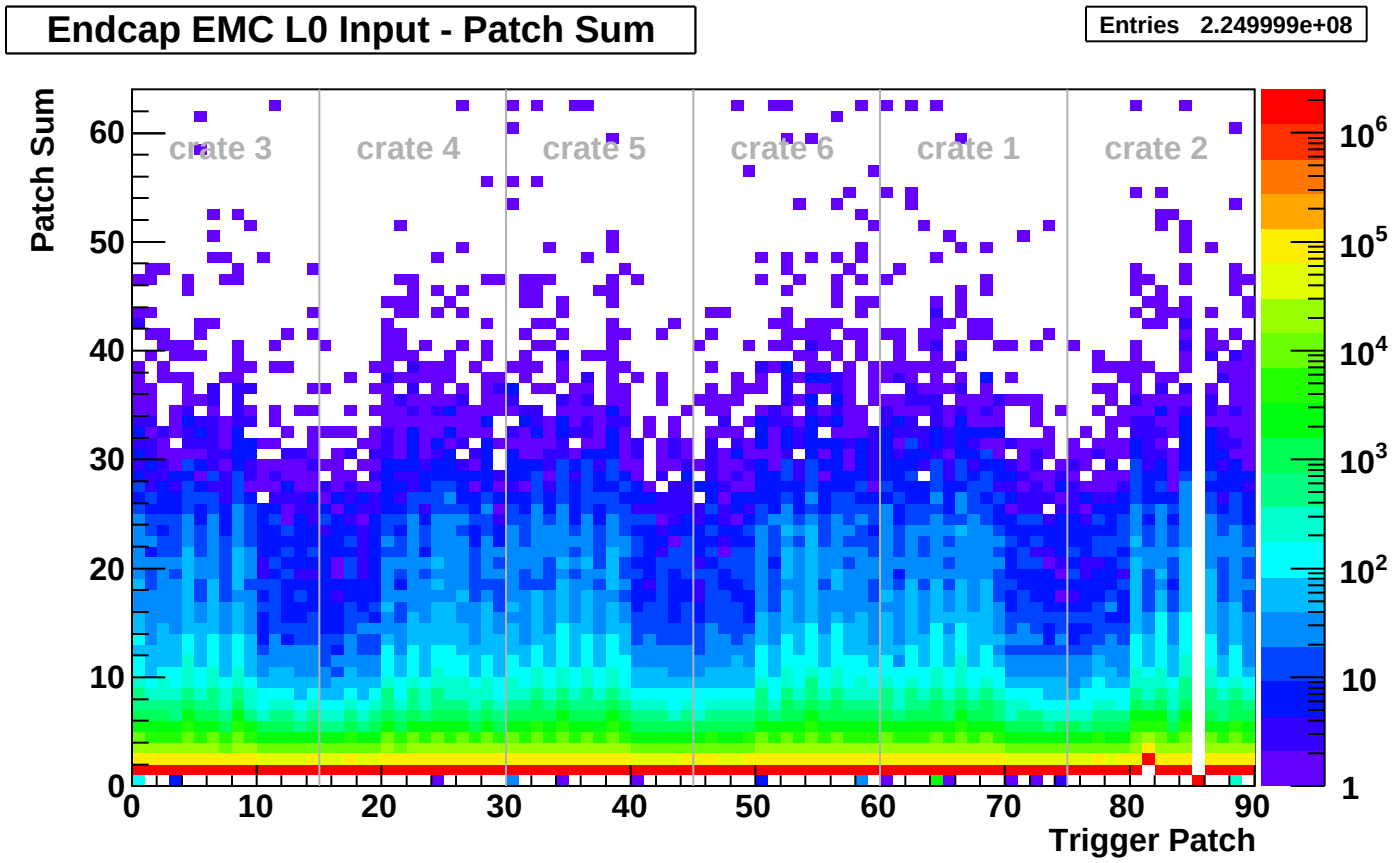

Abort-gaps
Entries 1106

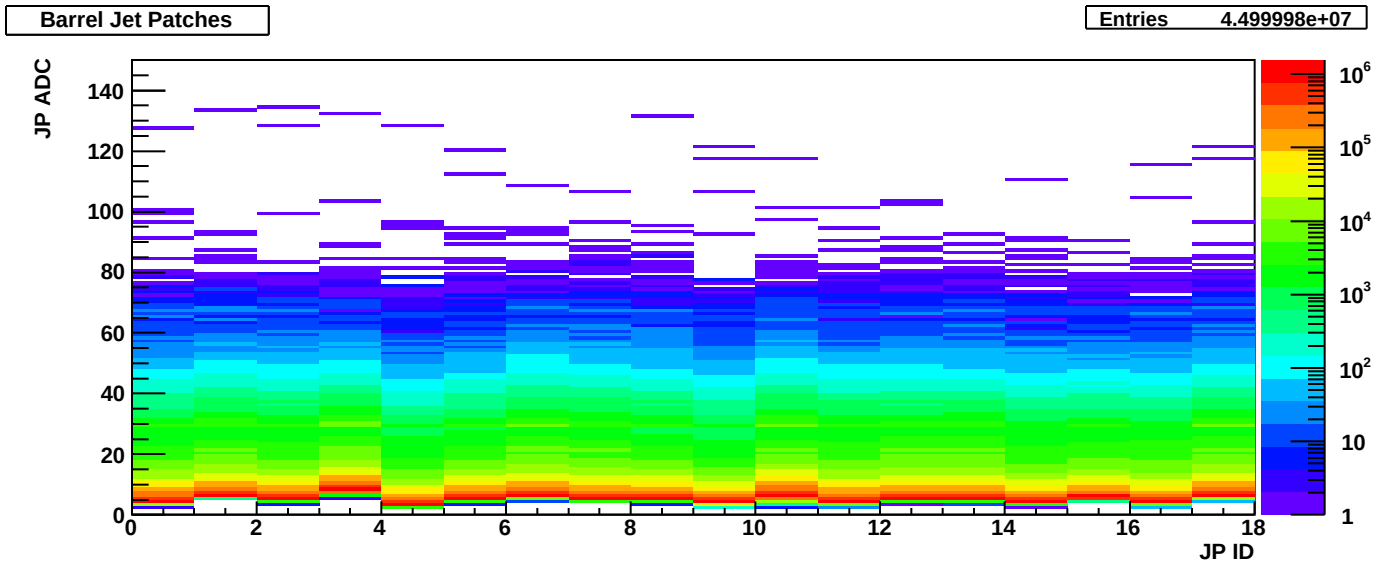

!Yellow-Fill
Entries 100133

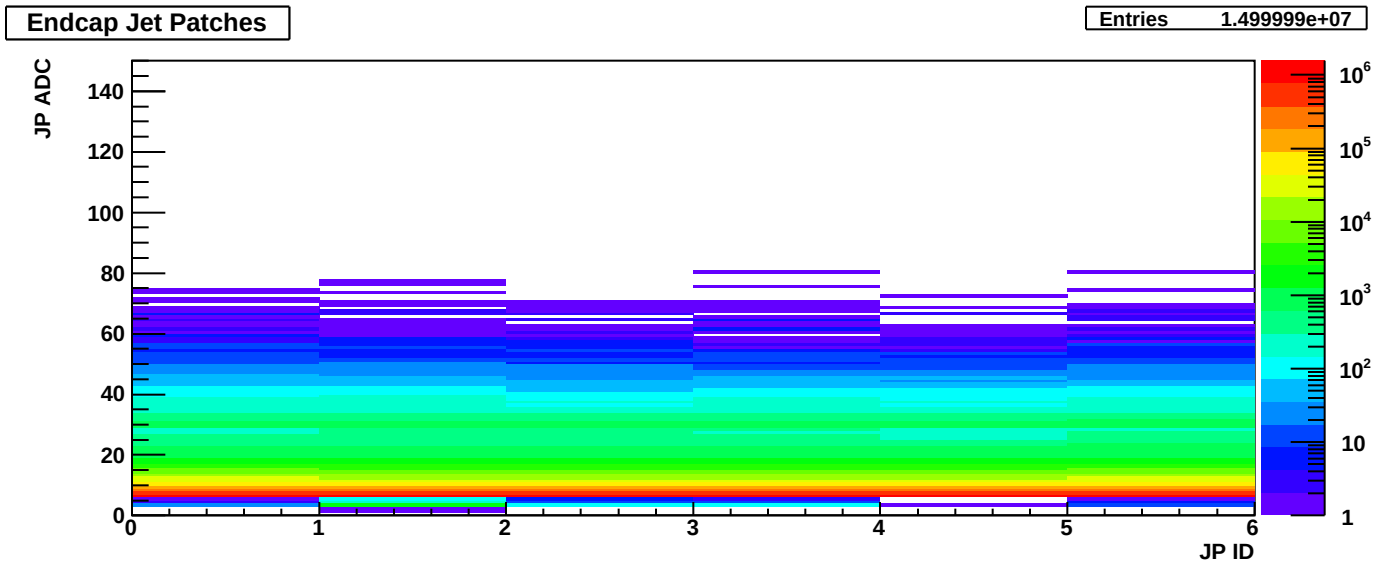

Barrel EMC L0 Input - High Tower
Entries 7.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

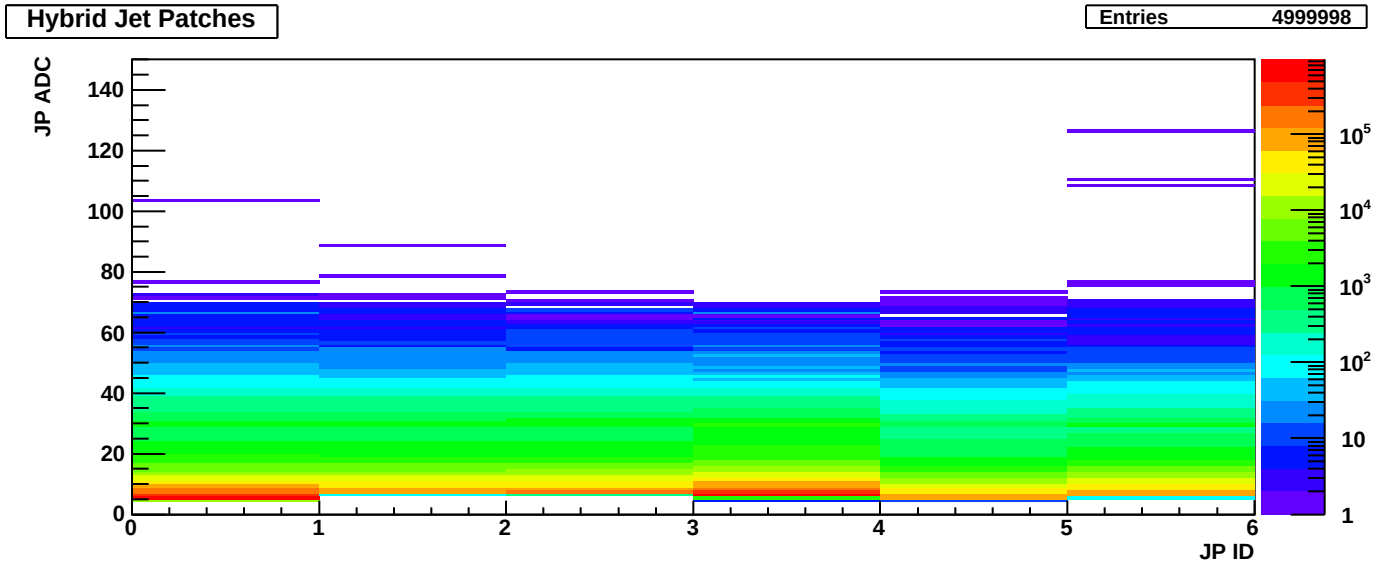

Barrel EMC L0 Input - Patch Sum
Entries 7.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

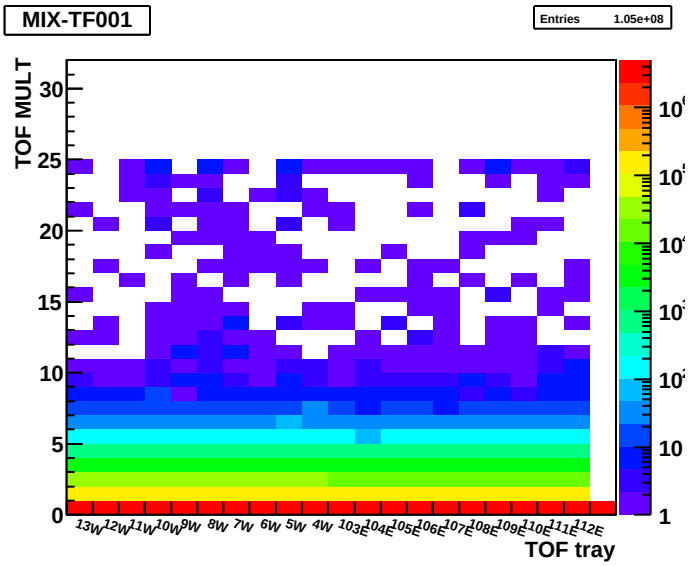

Endcap EMC L0 Input - High Tower
Entries 2.249999e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

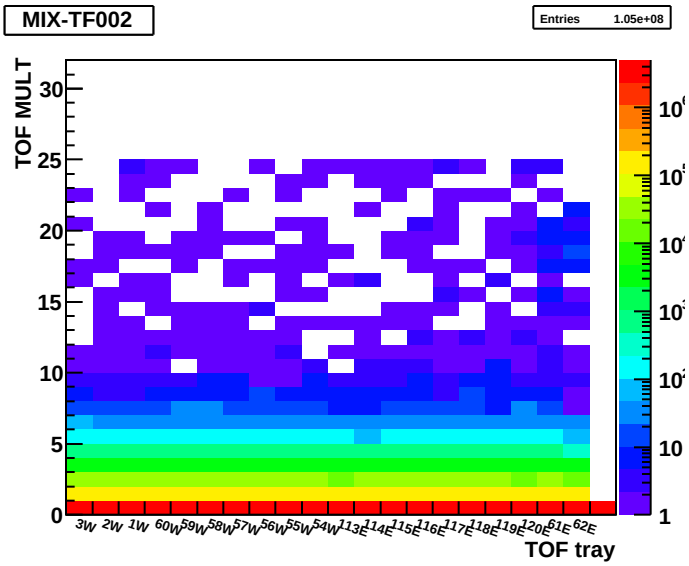

Endcap EMC L0 Input - Patch Sum
Entries 2.249999e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

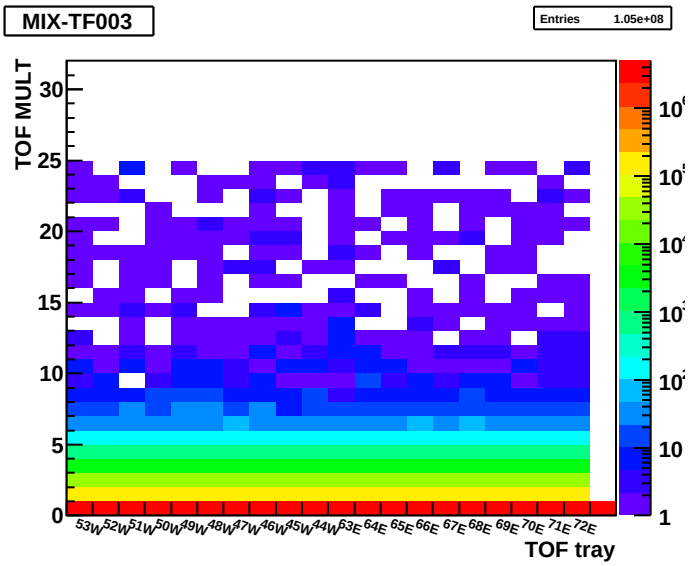

Barrel Jet Patches
Entries 4.499998e+07
JP ADC
JP ID

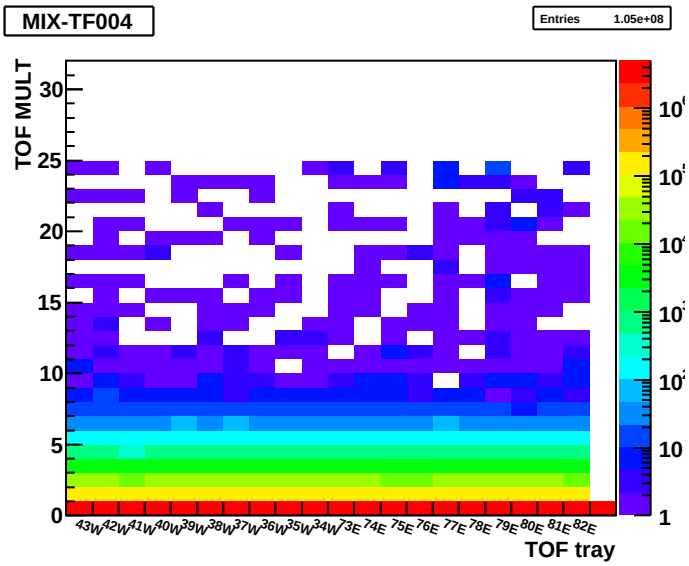

Endcap Jet Patches
Entries 1.499999e+07
JP ADC
JP ID

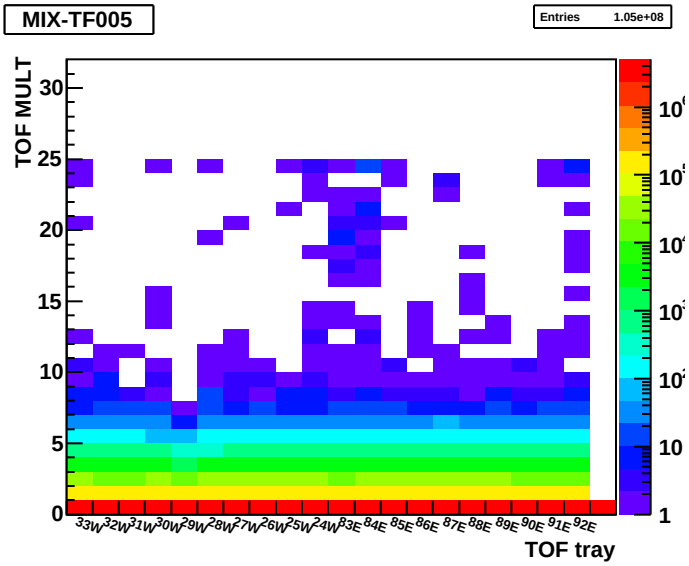

Hybrid Jet Patches
Entries 4999998
JP ADC
JP ID

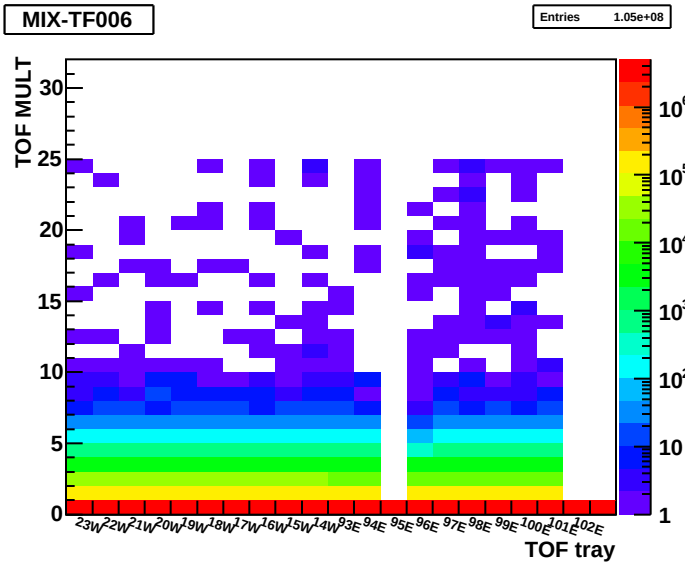

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

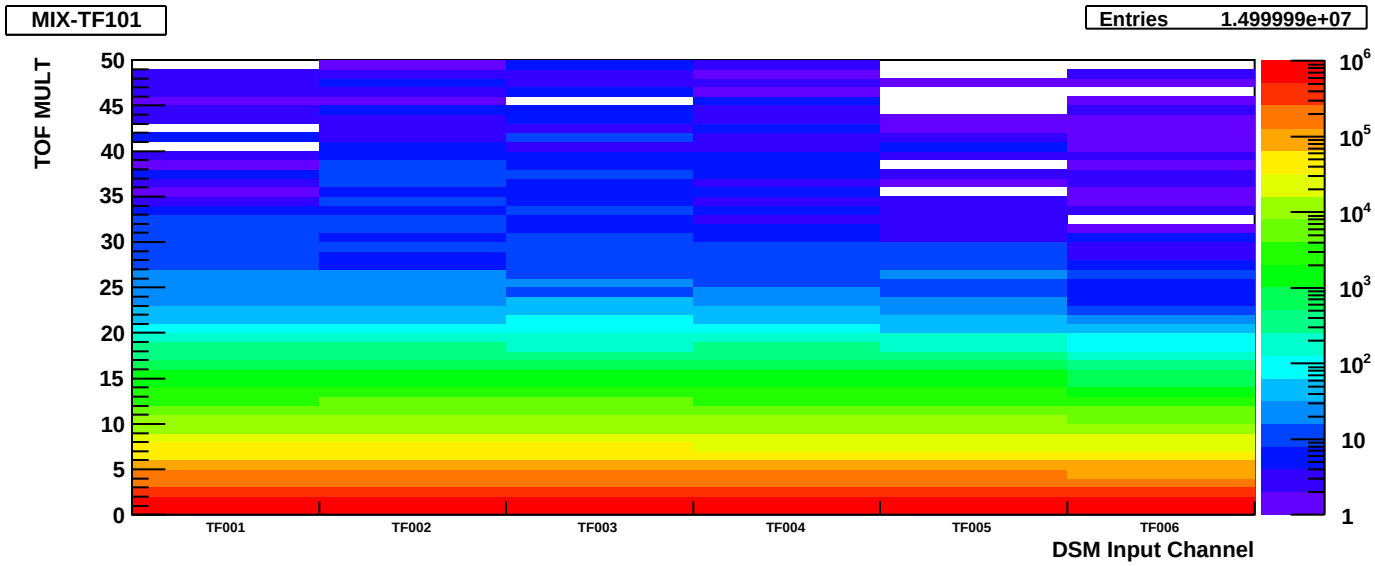

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

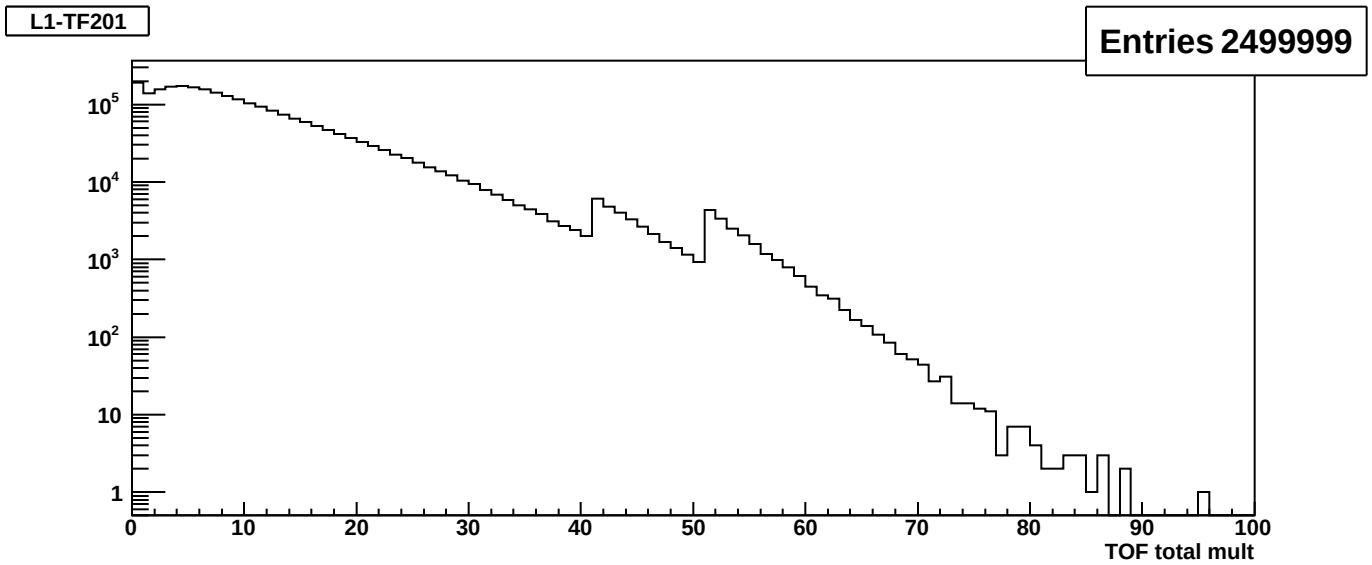

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

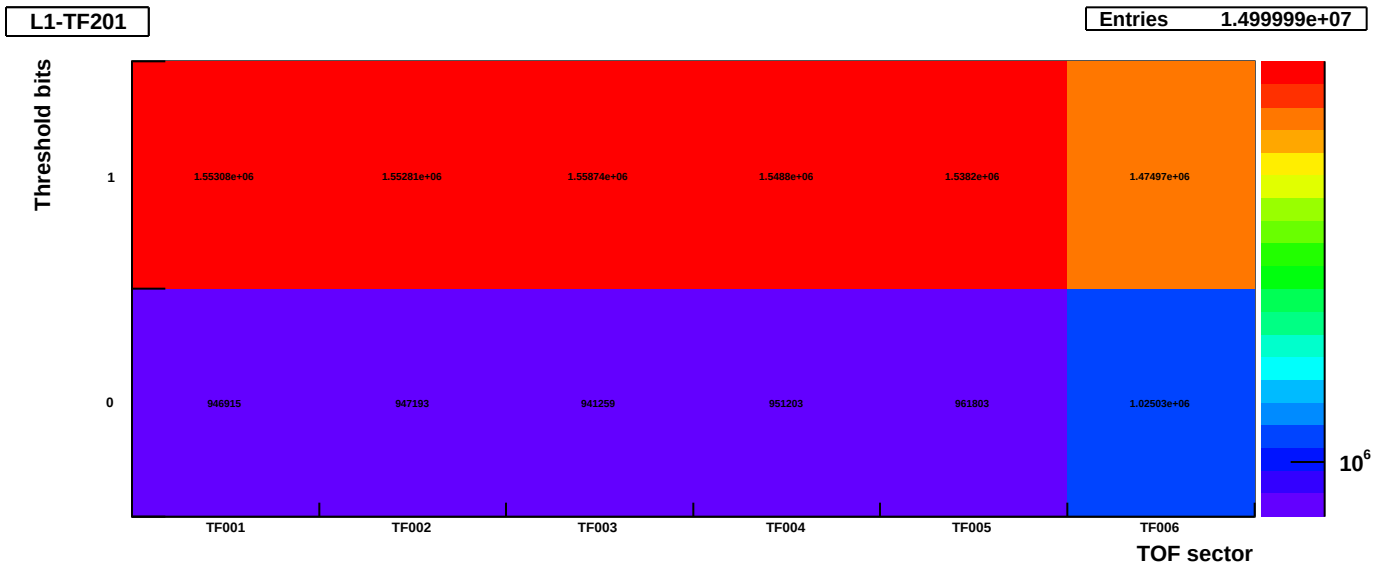

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

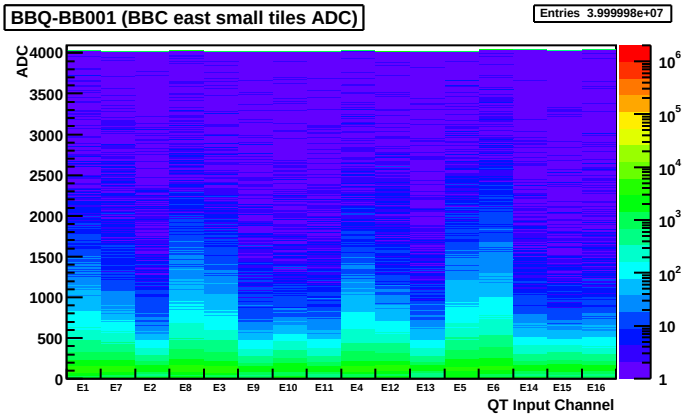

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

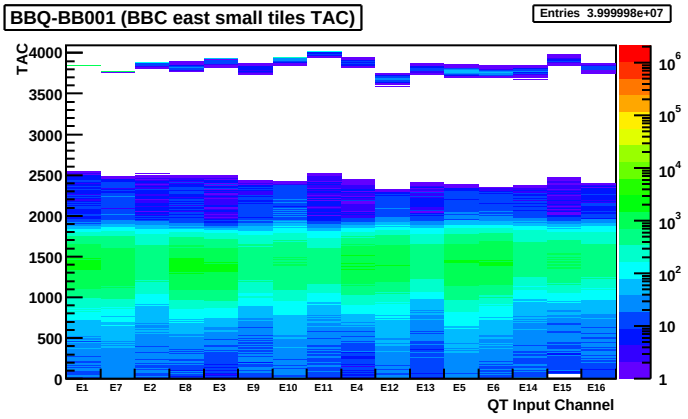

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

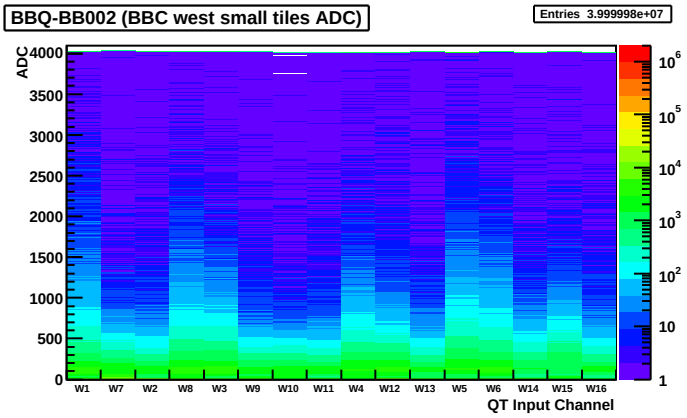

MIX-TF101
Entries 1.499999e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

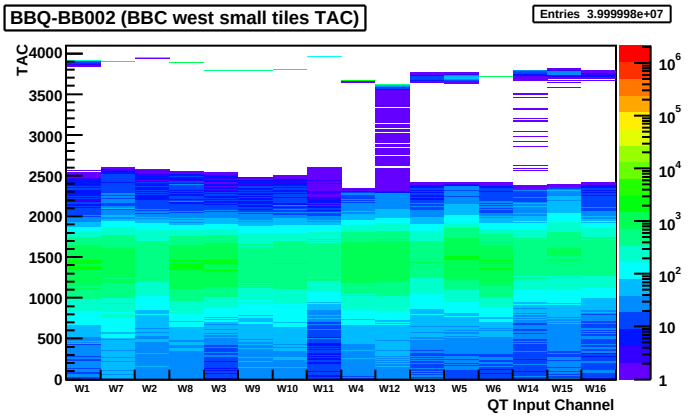

L1-TF201
Entries 2499999
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

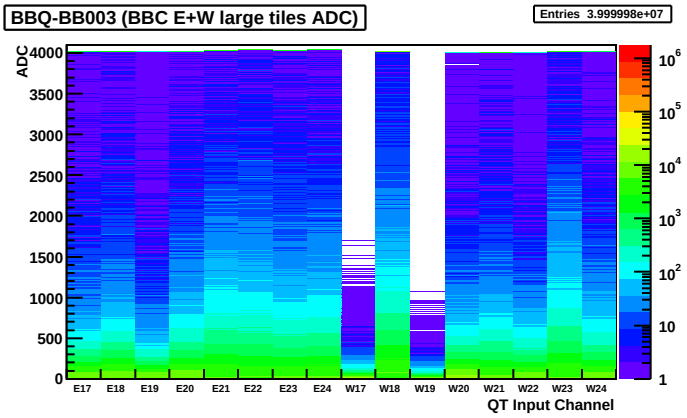

L1-TF201
Entries 1.499999e+07
Threshold bits
1
0
1.55308e+06
1.55281e+06
1.55874e+06
1.5488e+06
1.5382e+06
1.47497e+06
946915
947193
941259
951203
961803
1.02503e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

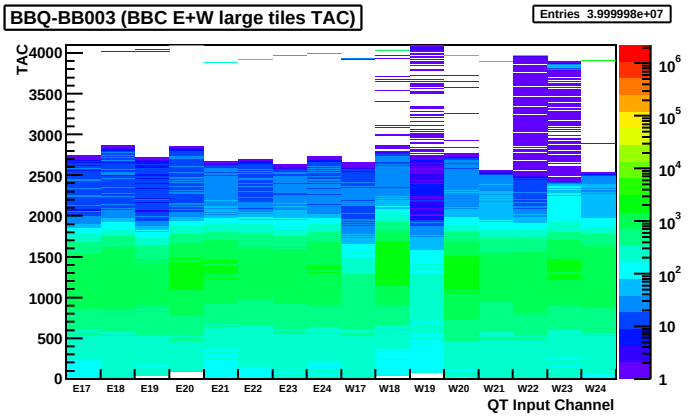

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

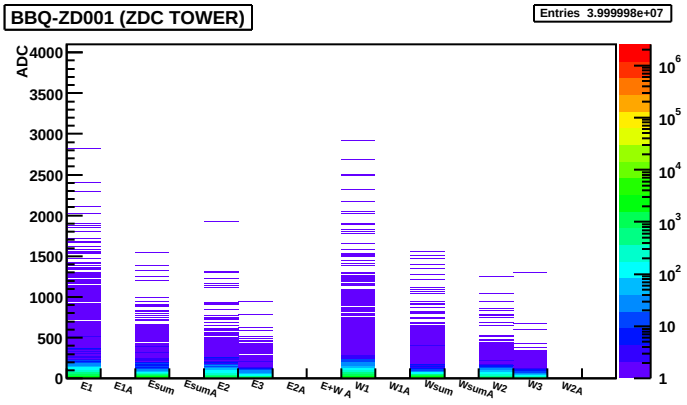

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

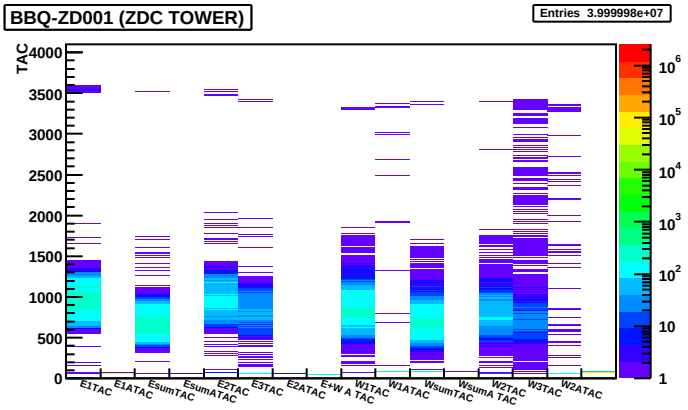

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

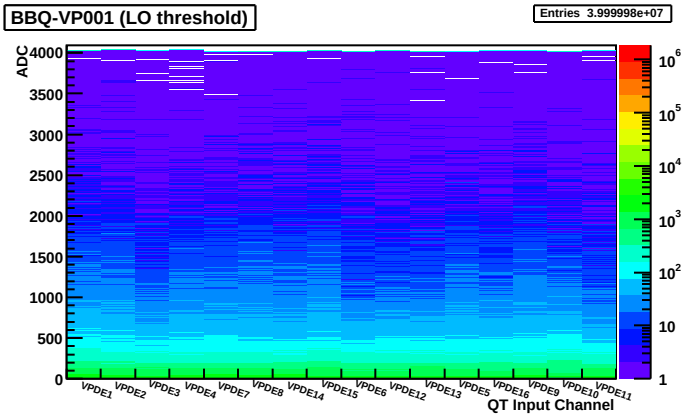

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

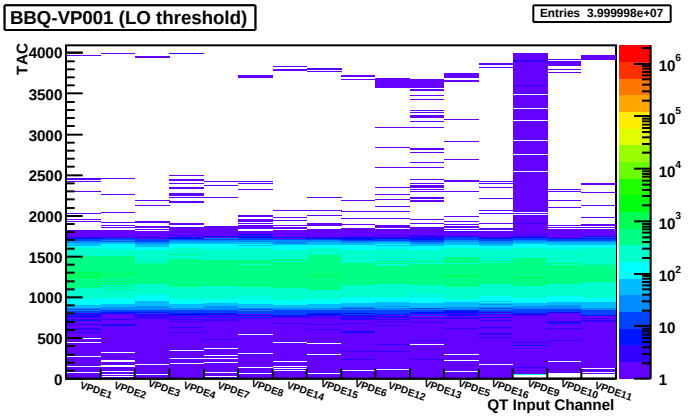

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.999998e+07
ADC
QT Input Channel

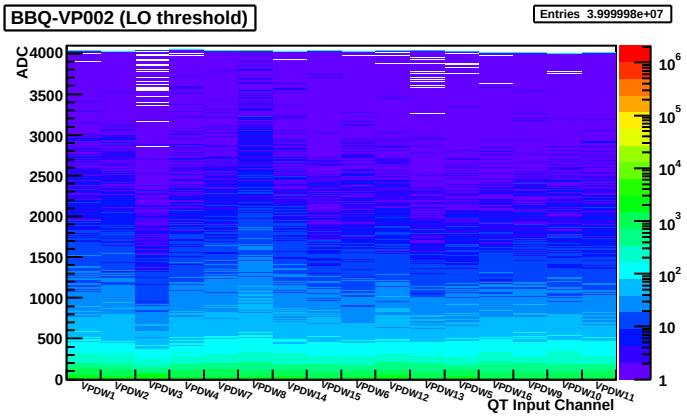

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.999998e+07
TAC
QT Input Channel

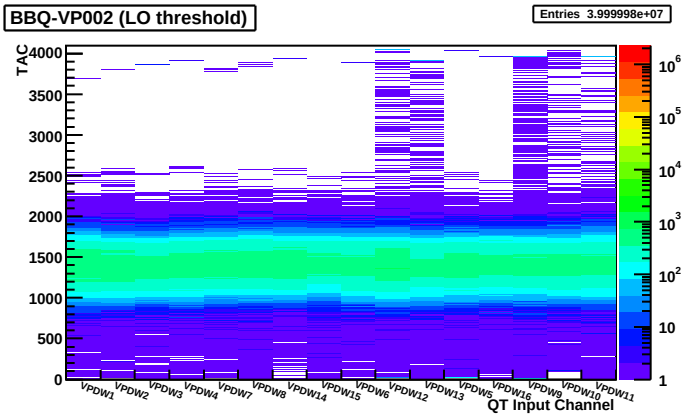

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
ADC

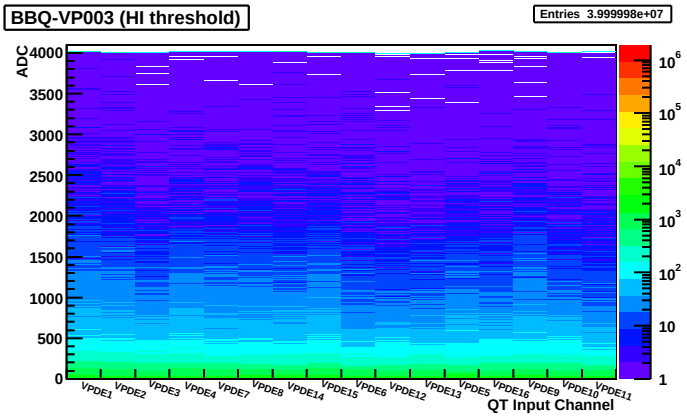

BBQ-ZD001 (ZDC TOWER)
Entries 3.999998e+07
TAC

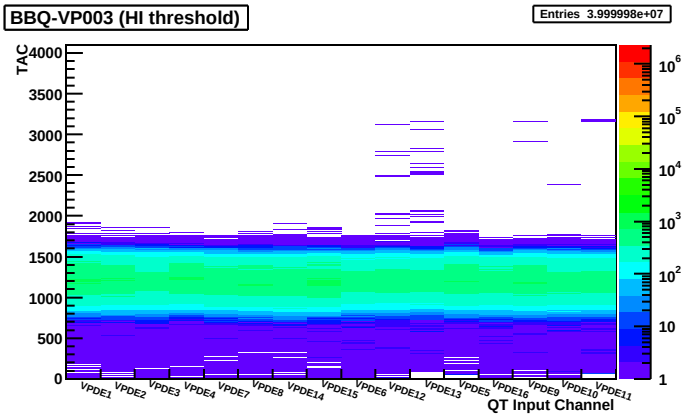

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

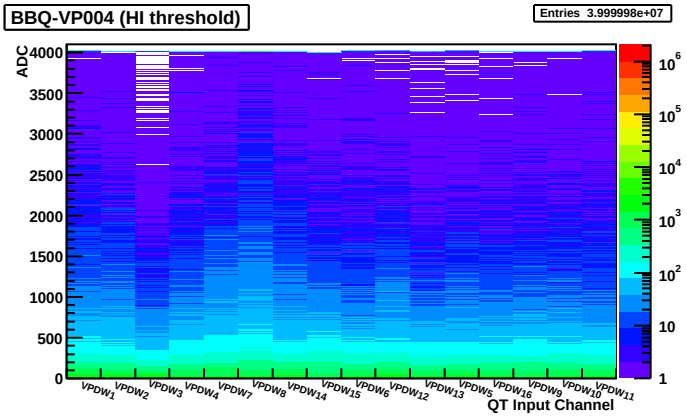

BBQ-VP001 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

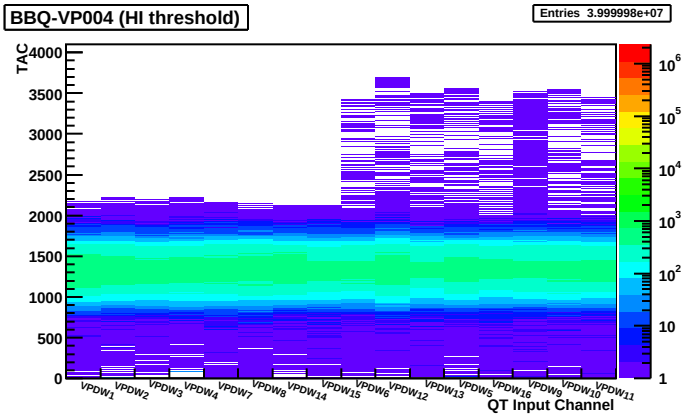

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
ADC
QT Input Channel

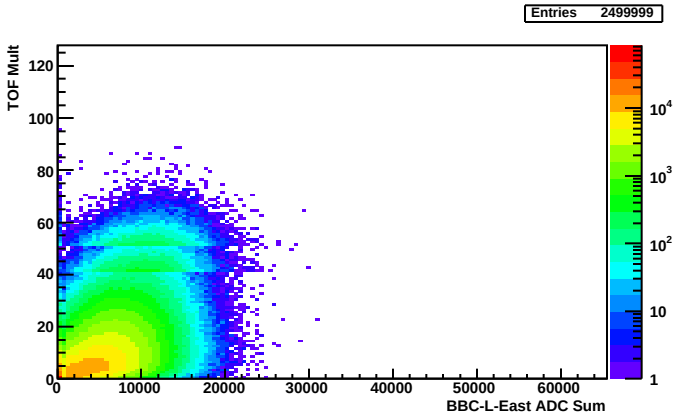

BBQ-VP002 (LO threshold)
Entries 3.999998e+07
TAC
QT Input Channel

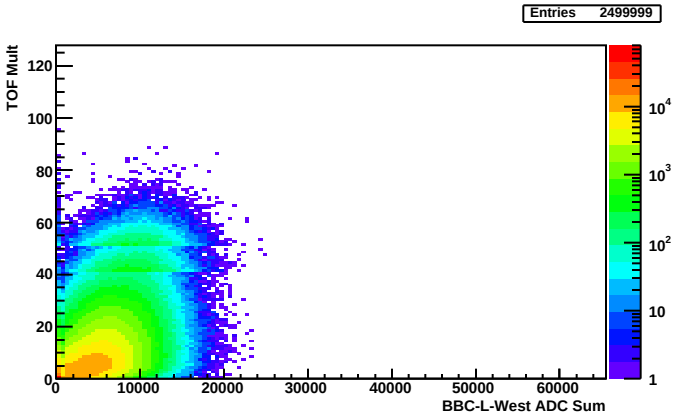

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

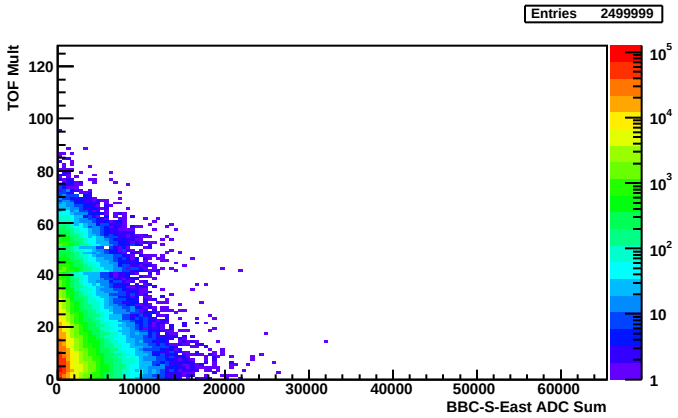

BBQ-VP003 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

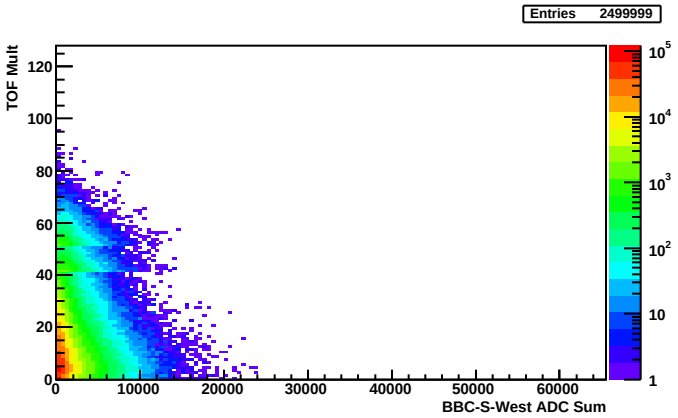

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
ADC
QT Input Channel

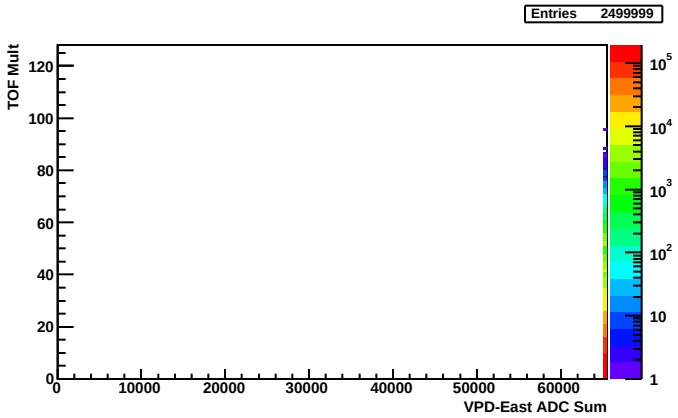

BBQ-VP004 (HI threshold)
Entries 3.999998e+07
TAC
QT Input Channel

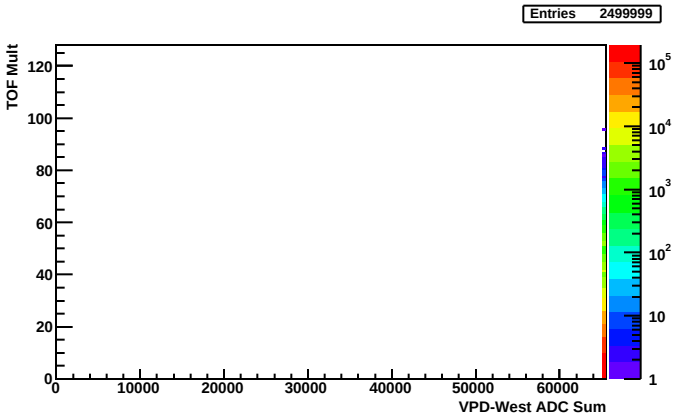

Entries 2499999
TOF Mult
BBC-L-East ADC Sum

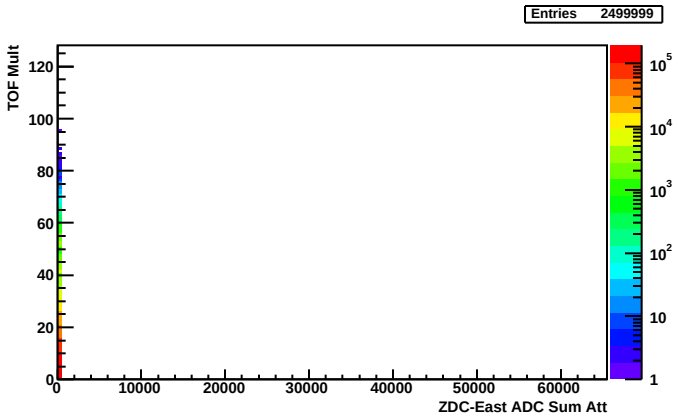

Entries 2499999
TOF Mult
BBC-L-West ADC Sum

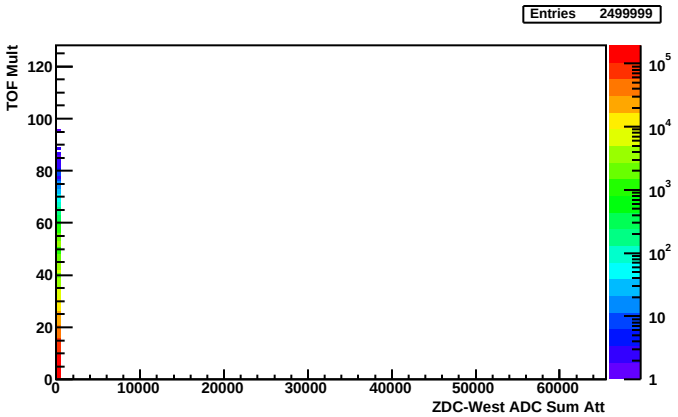

Entries 2499999
TOF Mult
BBC-S-East ADC Sum

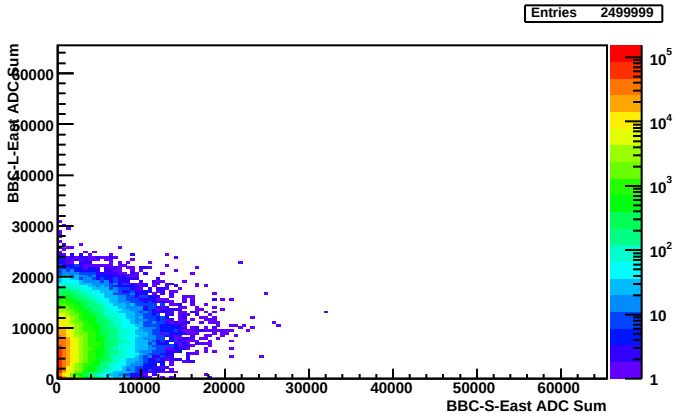

Entries 2499999
TOF Mult
BBC-S-West ADC Sum

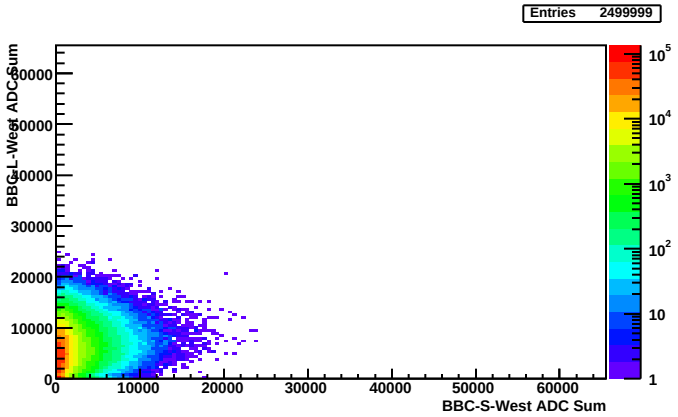

Entries 2499999
TOF Mult
VPD-East ADC Sum

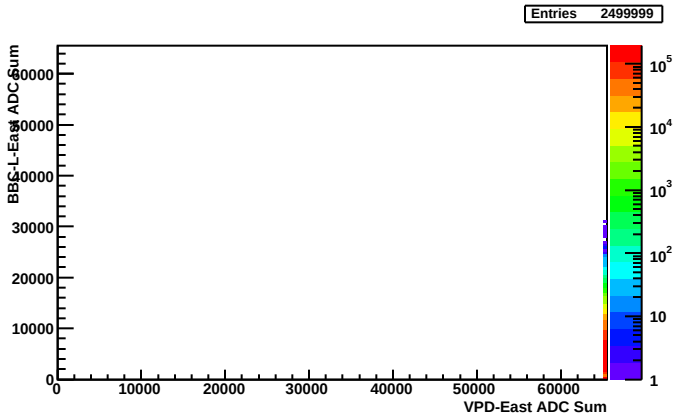

Entries 2499999
TOF Mult
VPD-West ADC Sum

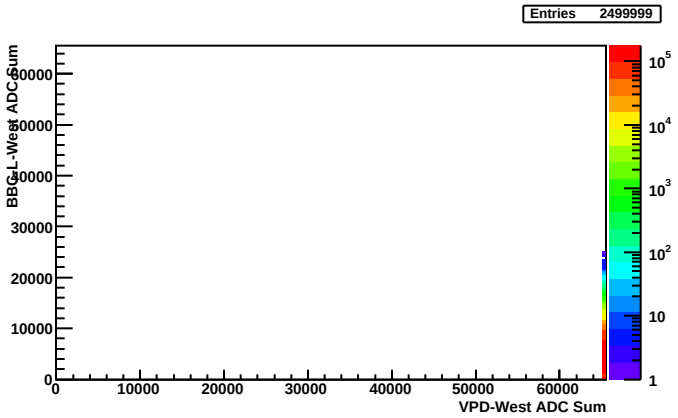

Entries 2499999
TOF Mult
ZDC-East ADC Sum Att

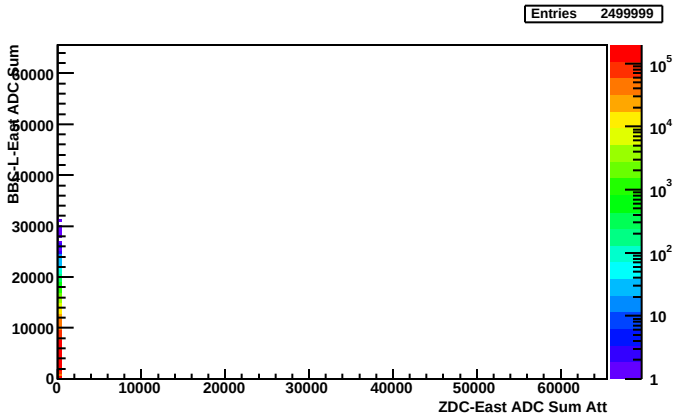

Entries 2499999
TOF Mult
ZDC-West ADC Sum Att


Entries 2499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2499999
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2499999
BBC-L-West ADC Sum
VPD-West ADC Sum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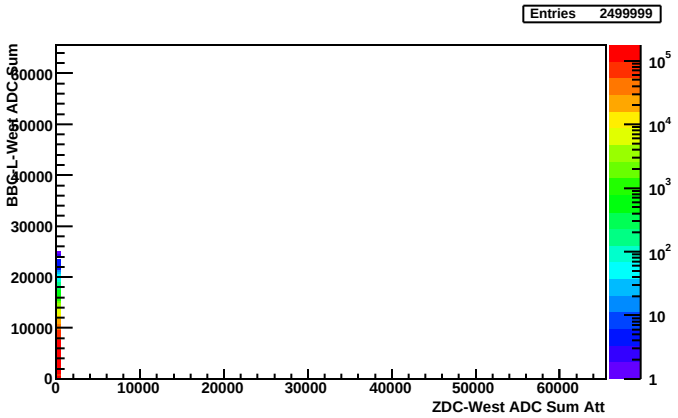
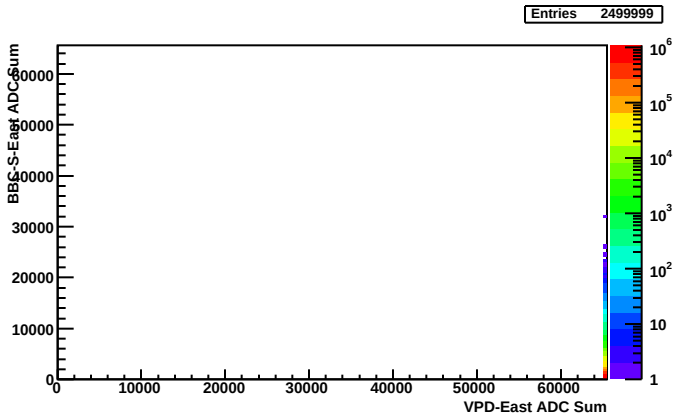
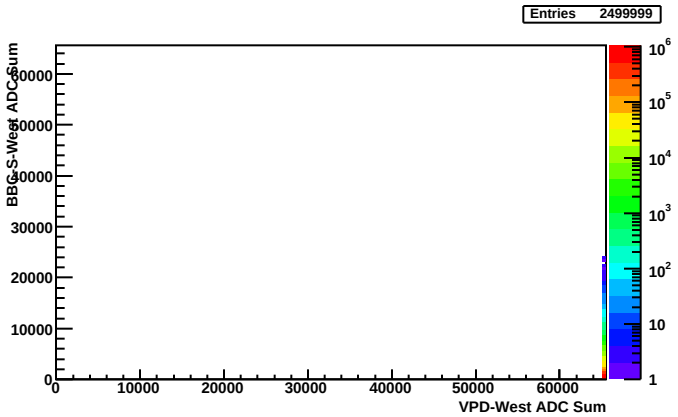
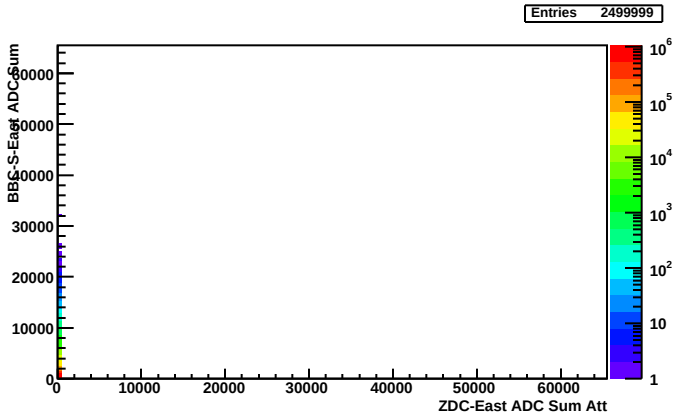
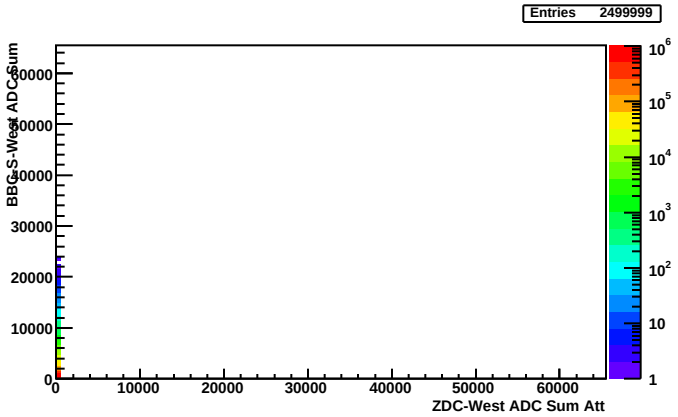
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

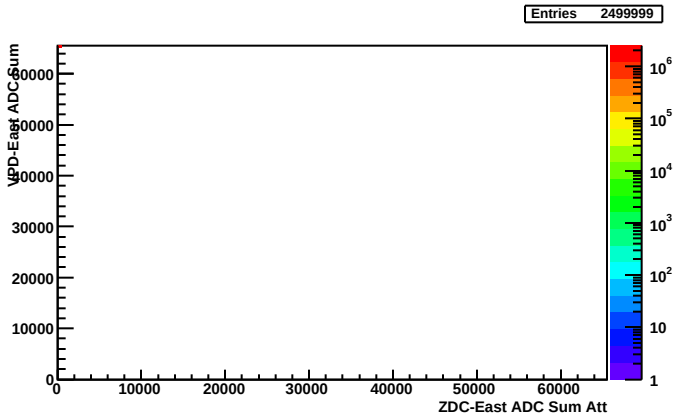

Entries 2499999
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

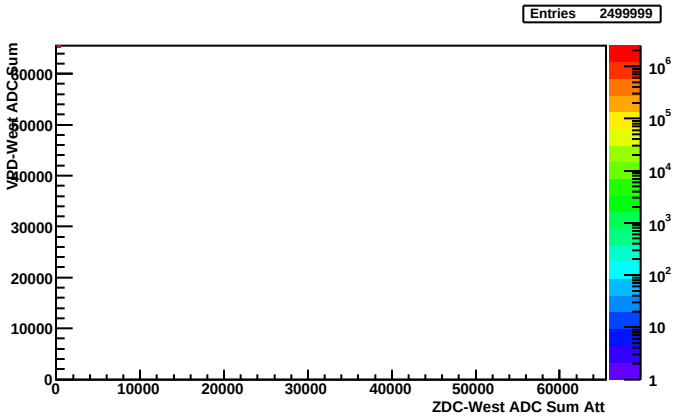

Entries 2499999
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2499999
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2499999
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2499999
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2499999
VPD-East ADC Sum
ZDC-East ADC Sum Att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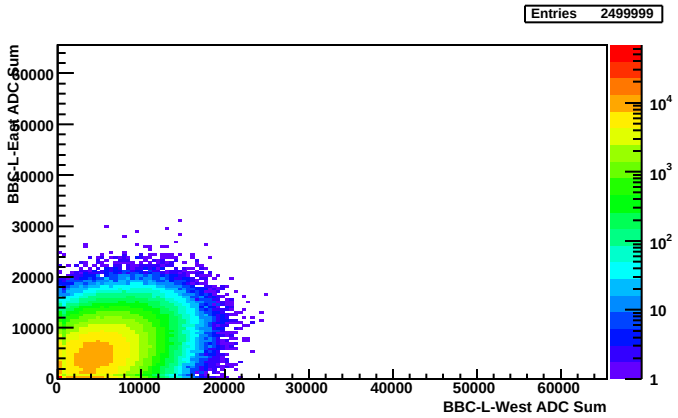
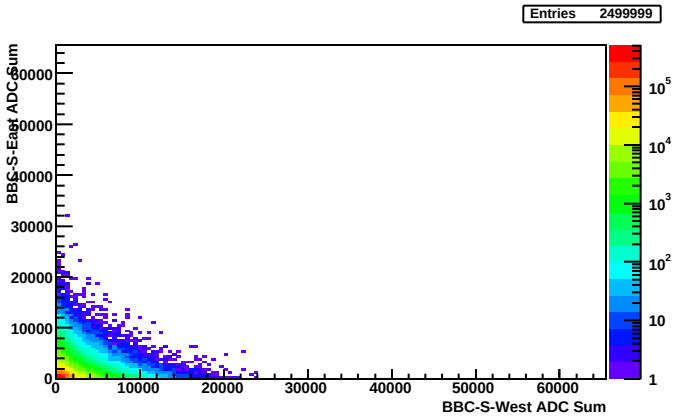
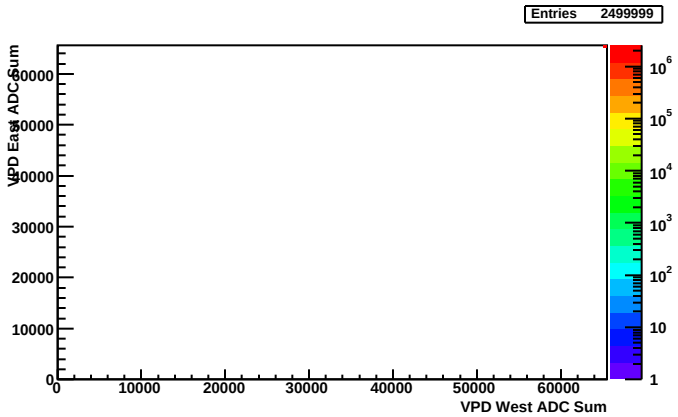
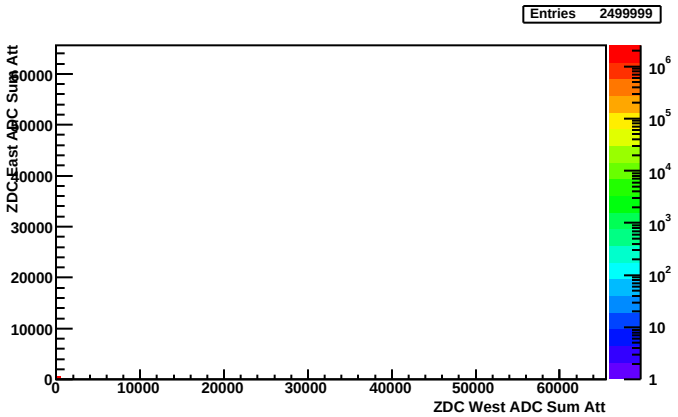
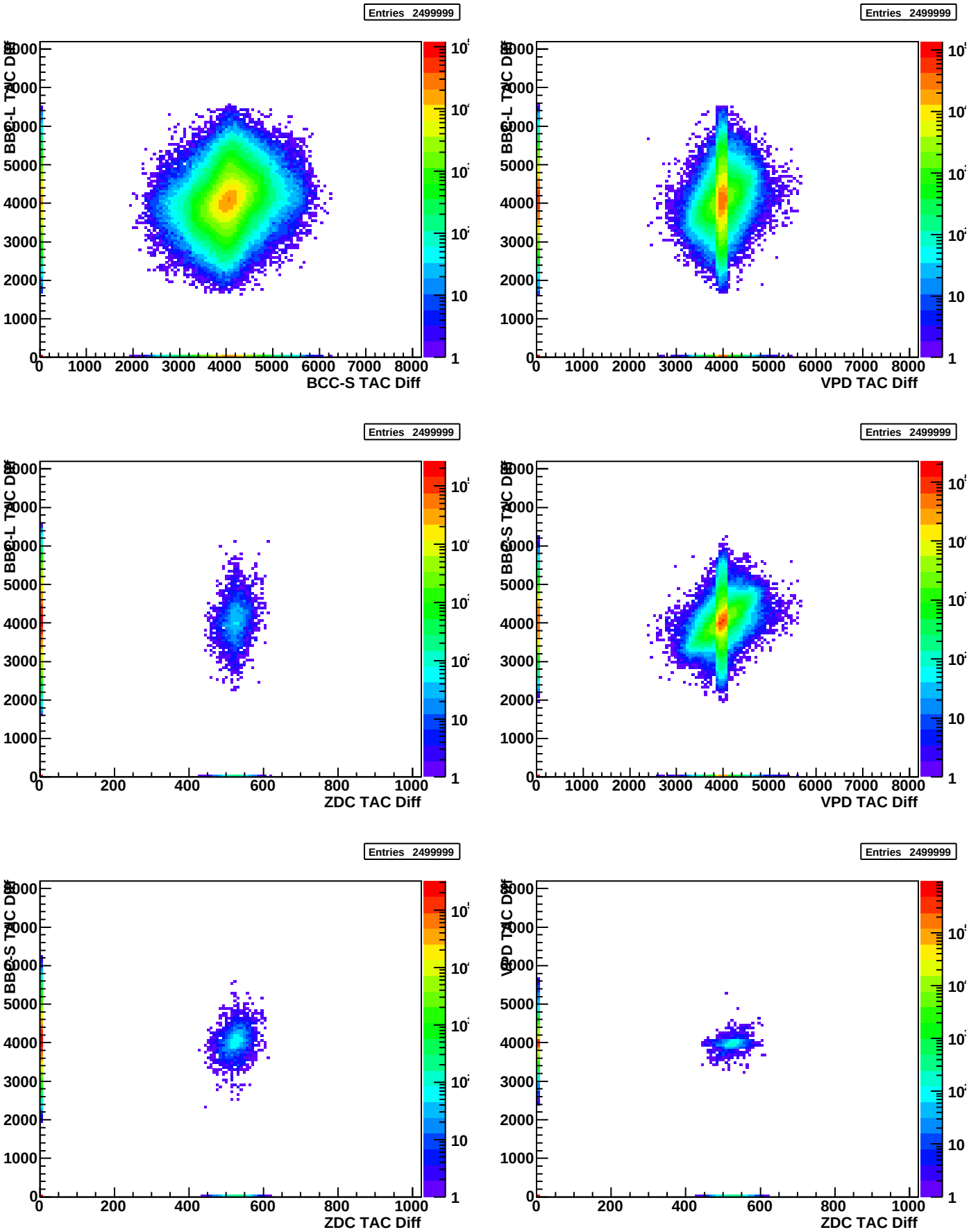
VPD-West ADC Sum
ZDC-West ADC Sum Att

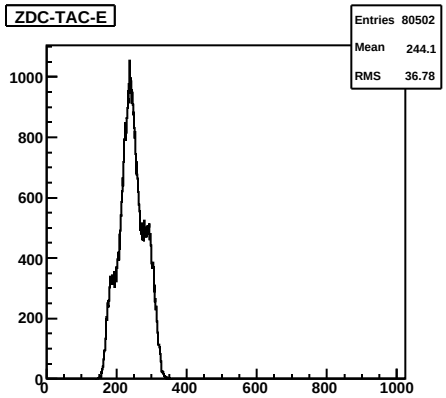

Entries 2499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

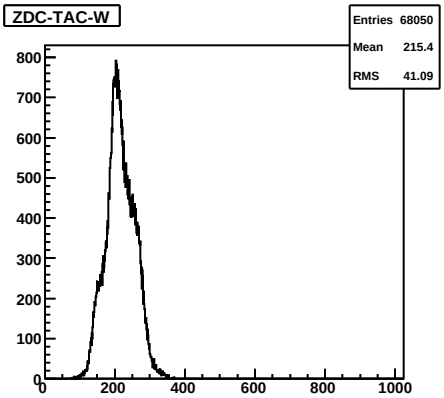

Entries 2499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

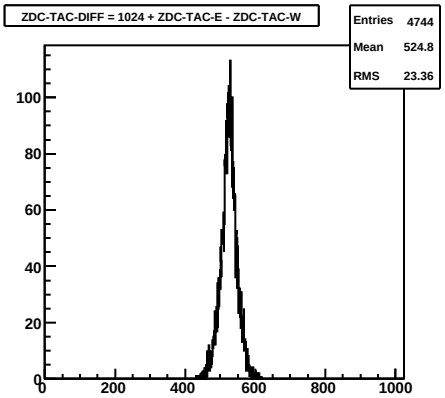

Entries 2499999
VPD East ADC Sum
VPD West ADC Sum

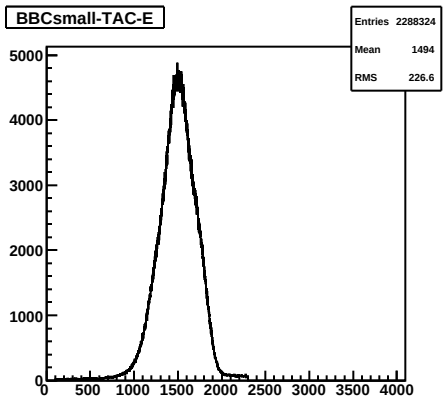

Entries 2499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

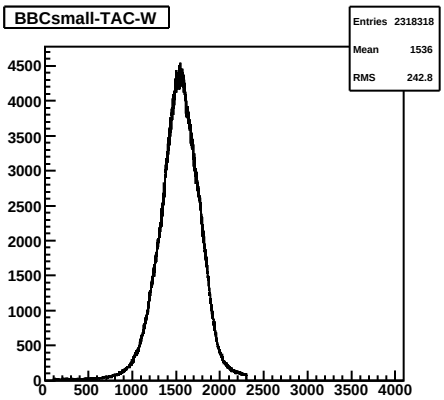

Entries 2499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2499999
BBC-L TAC Diff
VPD TAC Diff
Entries 2499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 2499999
BBC-S TAC Diff
VPD TAC Diff
Entries 2499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 2499999
VPD TAC Diff
ZDC TAC Diff

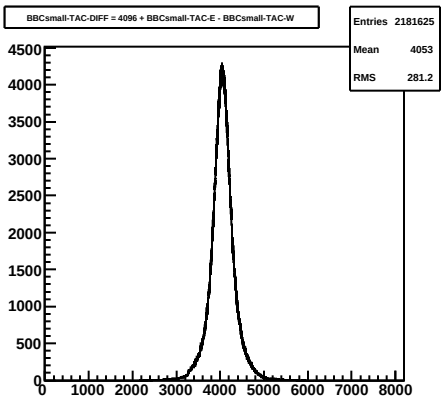

ZDC-TAC-E
Entries 80502
Mean 244.1
RMS 36.78

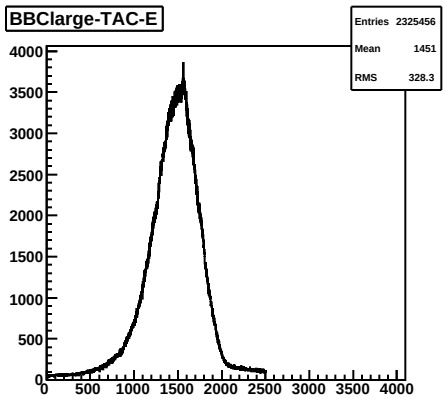

ZDC-TAC-W
Entries 68050
Mean 215.4
RMS 41.09

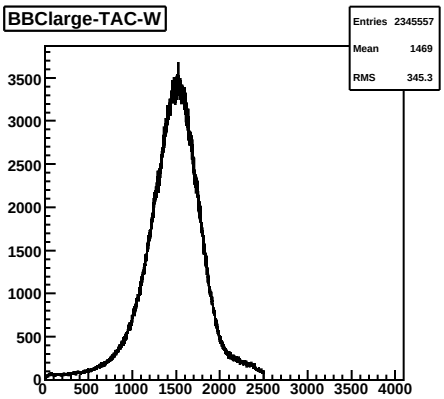

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4744
Mean 524.8
RMS 23.36

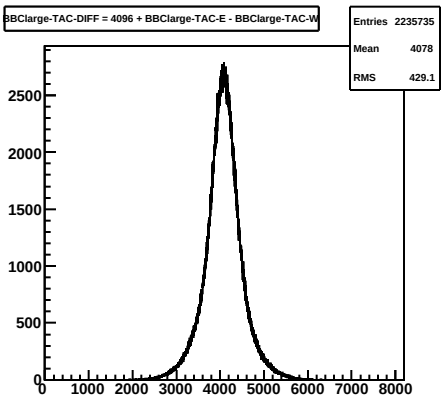

BBCsmall-TAC-E
Entries 2288324
Mean 1494
RMS 226.6

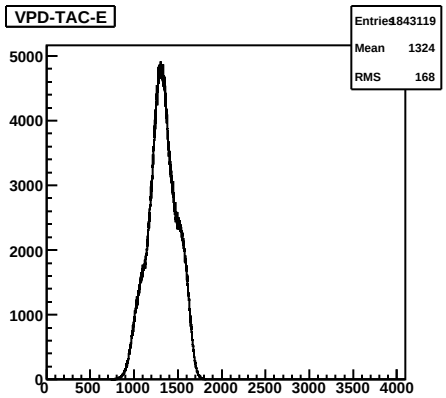

BBCsmall-TAC-W
Entries 2318318
Mean 1536
RMS 242.8

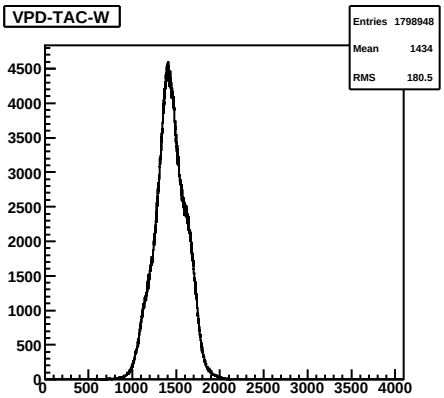

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2181625
Mean 4053
RMS 281.2

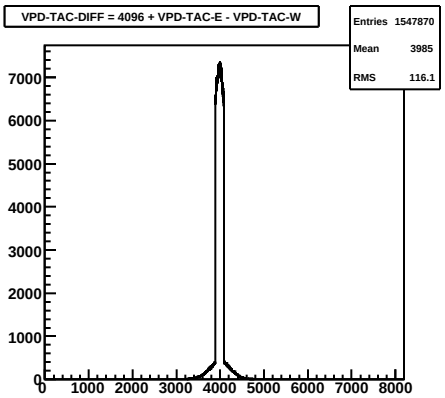

BBClarge-TAC-E
Entries 2325456
Mean 1451
RMS 328.3

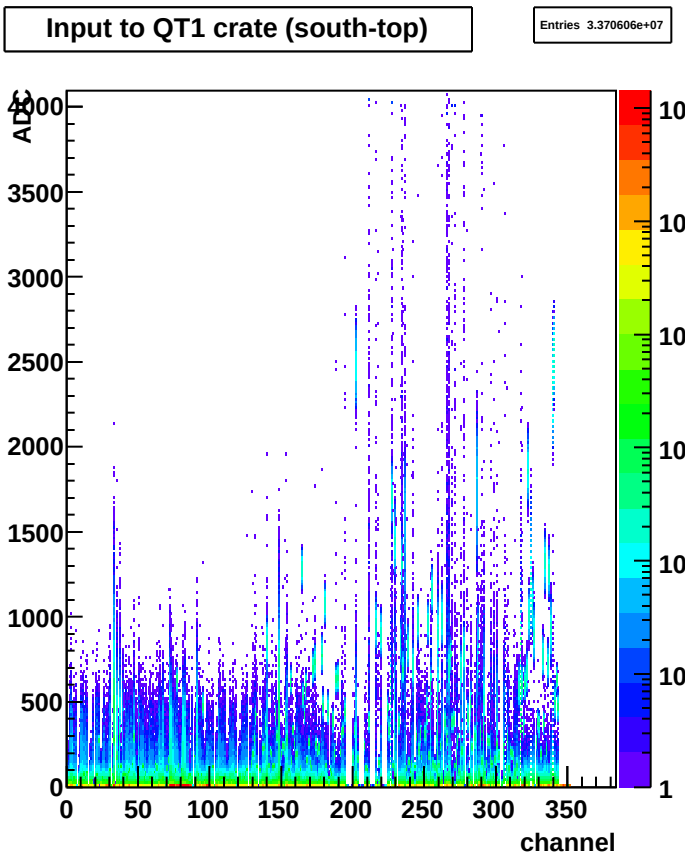

BBClarge-TAC-W
Entries 2345557
Mean 1469
RMS 345.3

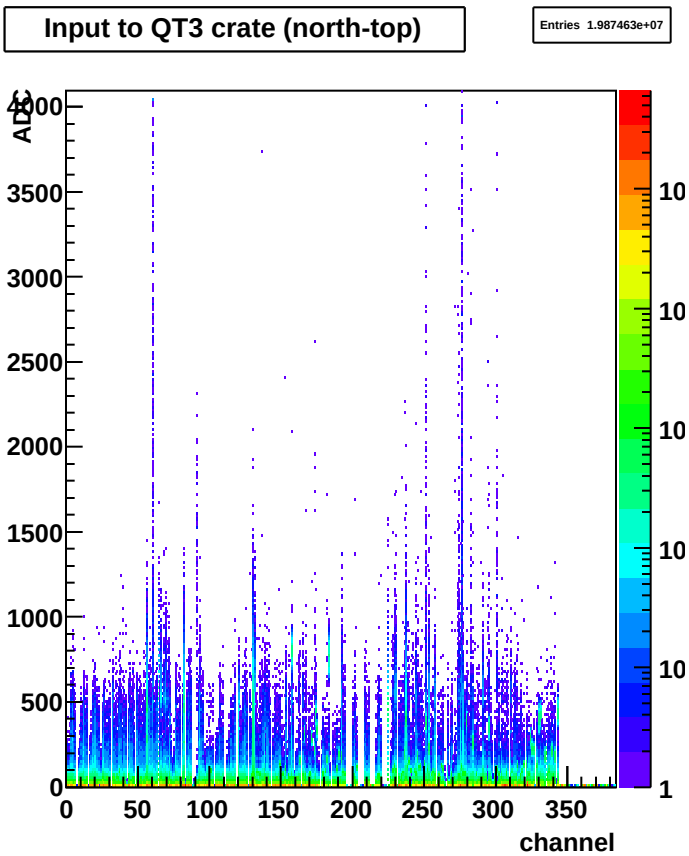

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2235735
Mean 4078
RMS 429.1

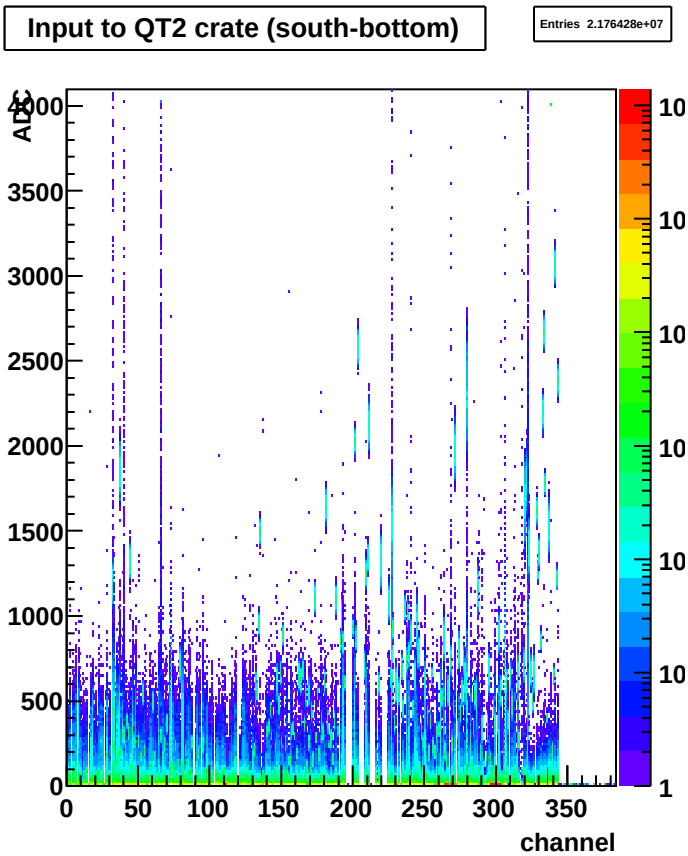

VPD-TAC-E
Entries 1843119
Mean 1324
RMS 168

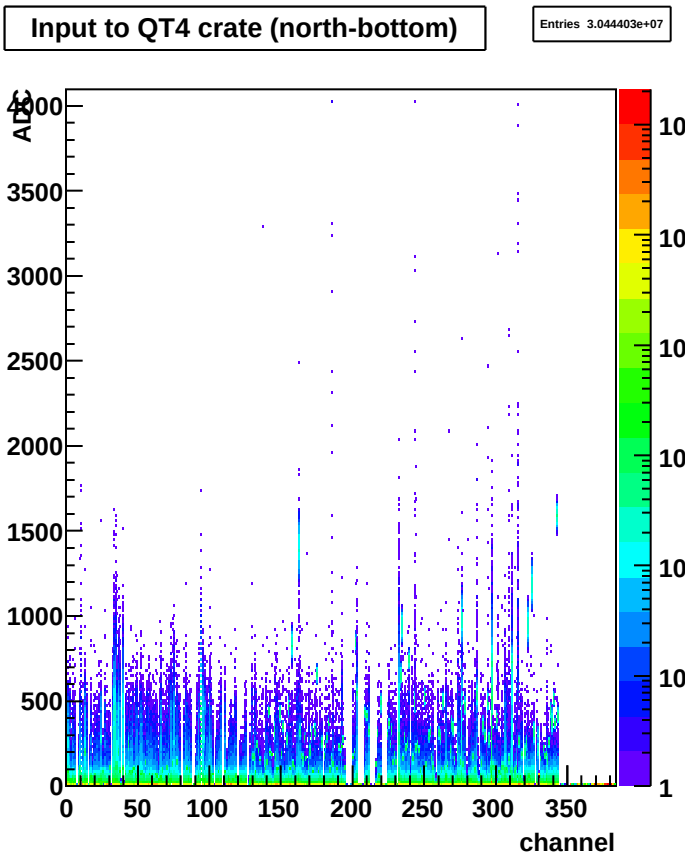

VPD-TAC-W
Entries 1798948
Mean 1434
RMS 180.5

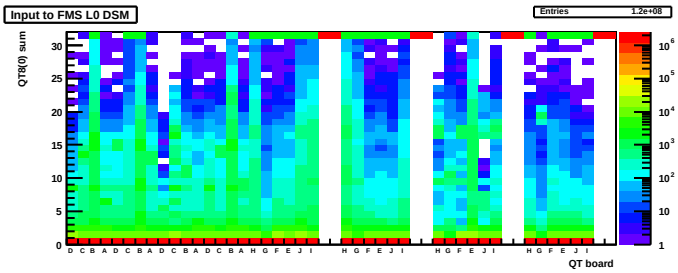

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1547870
Mean 3985
RMS 116.1

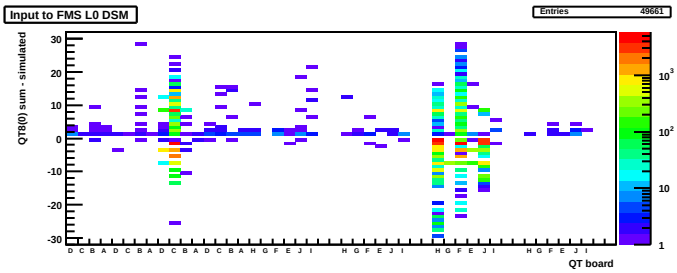

Input to QT1 crate (south-top)
Entries 3.370606e+07
ADC
channel

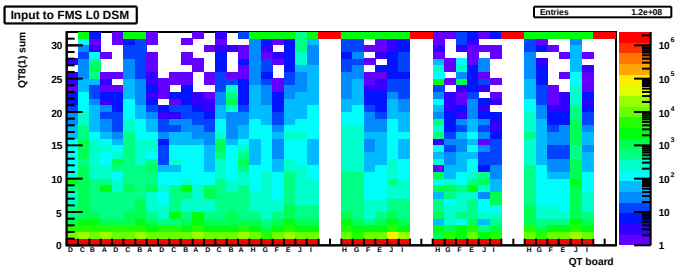

Input to QT3 crate (north-top)
Entries 1.987463e+07
ADC
channel

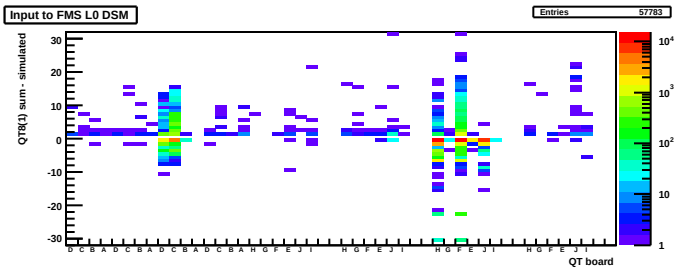

Input to QT2 crate (south-bottom)
Entries 2.176428e+07
ADC
channel

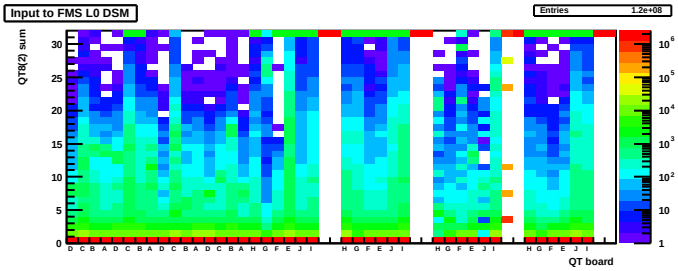

Input to QT4 crate (north-bottom)
Entries 3.044403e+07
ADC
channel

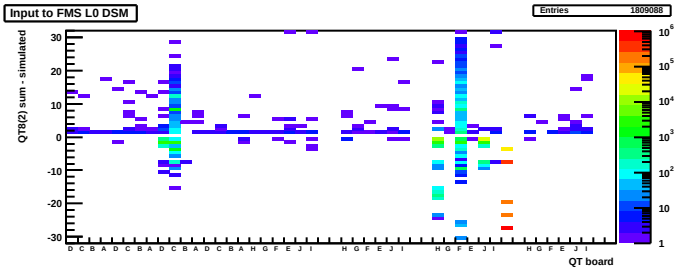

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

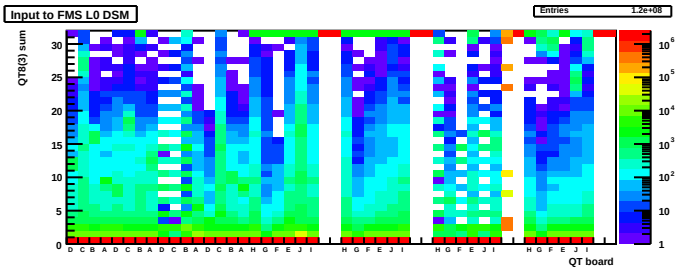

Input to FMS L0 DSM
Entries 49661
QT8(0) sum - simulated
QT board

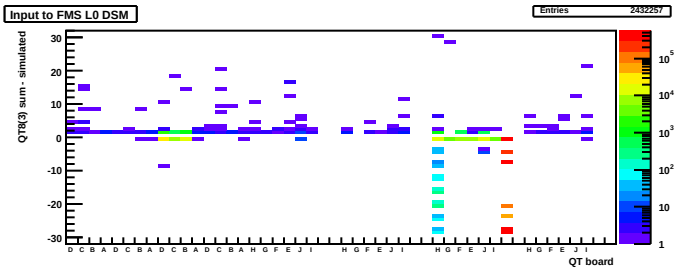

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

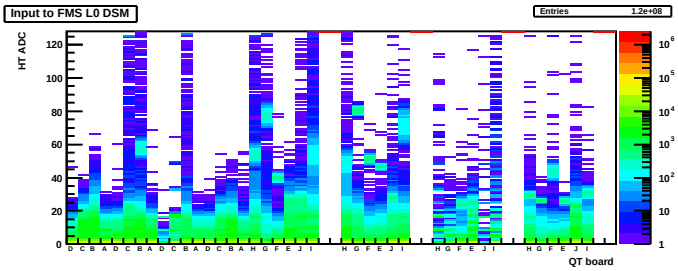

Input to FMS L0 DSM
Entries 57783
QT8(1) sum - simulated
QT board

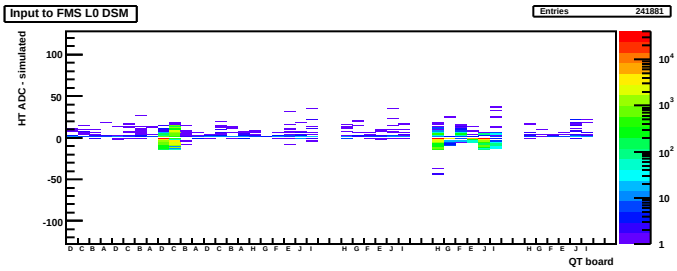

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

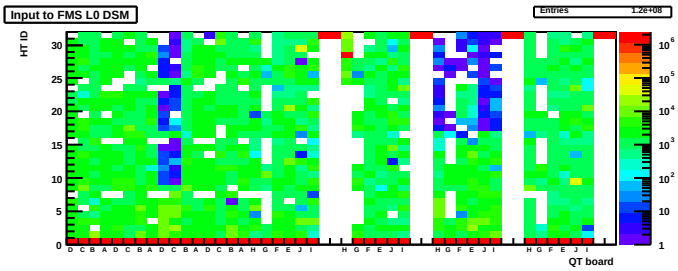

Input to FMS L0 DSM
Entries 1809088
QT8(2) sum - simulated
QT board

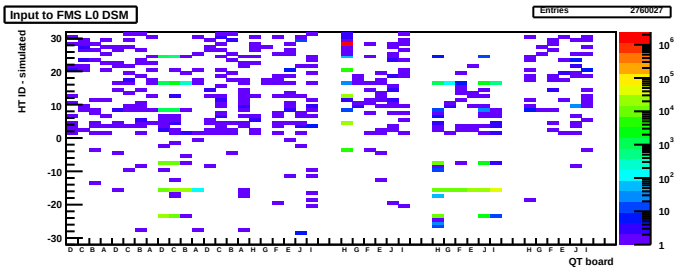

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

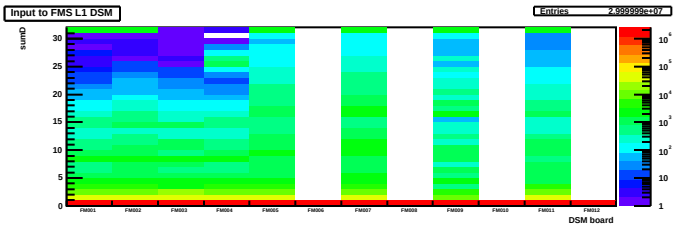

Input to FMS L0 DSM
Entries 2432257
QT8(3) sum - simulated
QT board

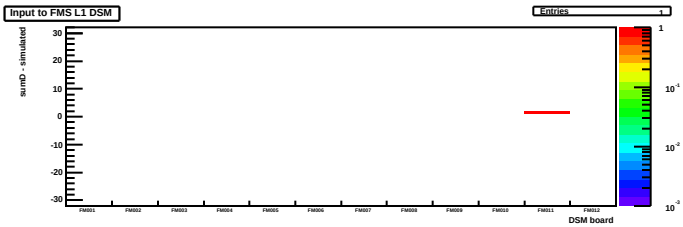

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

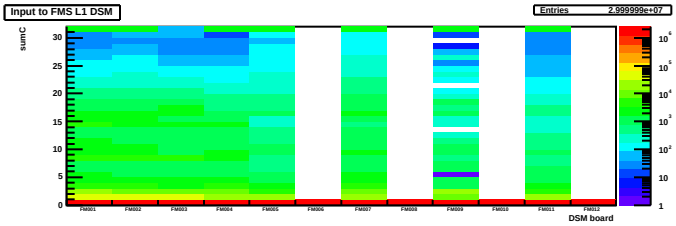

Input to FMS L0 DSM
Entries 241881
HT ADC - simulated
QT board

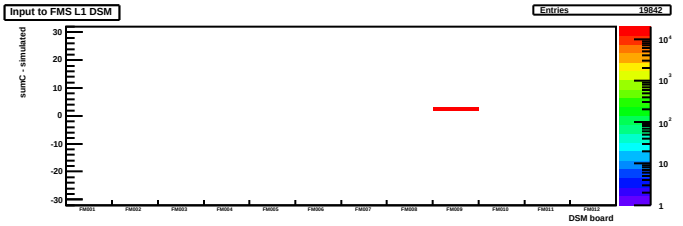

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

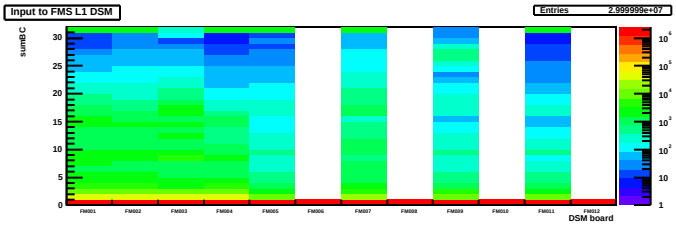

Input to FMS L0 DSM
Entries 2760027
HT ID - simulated
QT board

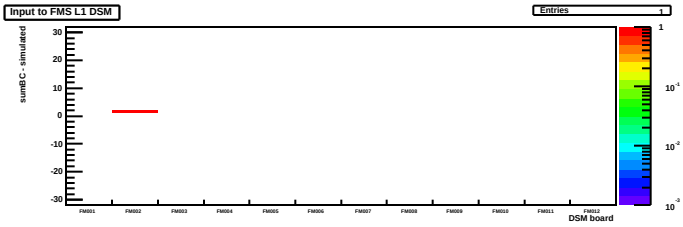

Input to FMS L1 DSM
Entries 2.999999e+07
sumD
DSM board

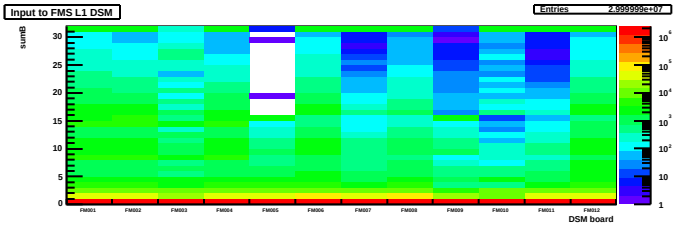

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

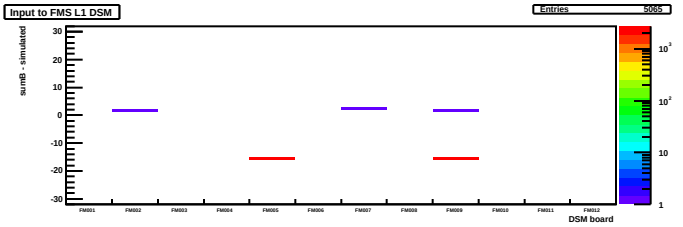

Input to FMS L1 DSM
Entries 2.999999e+07
sumC
DSM board

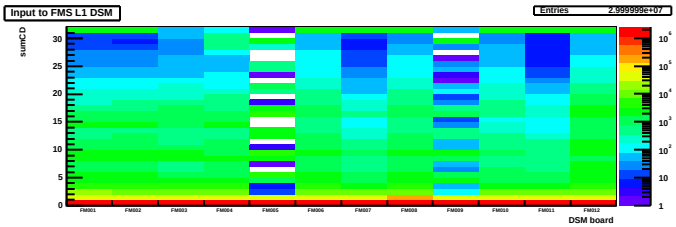

Input to FMS L1 DSM
Entries 19842
sumC - simulated
DSM board

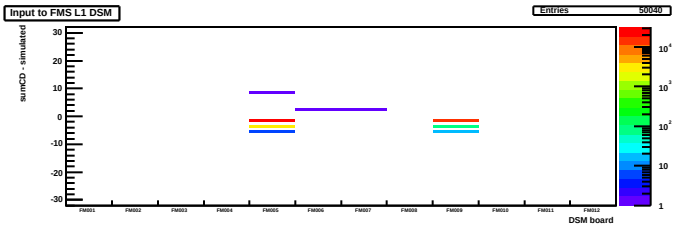

Input to FMS L1 DSM
Entries 2.999999e+07
sumBC
DSM board

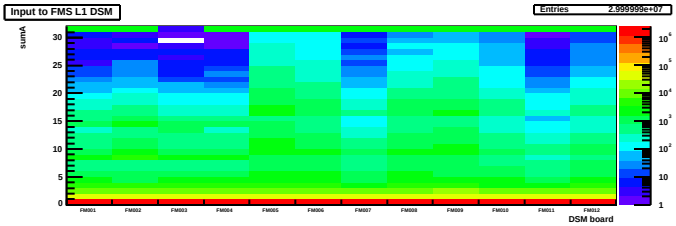

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

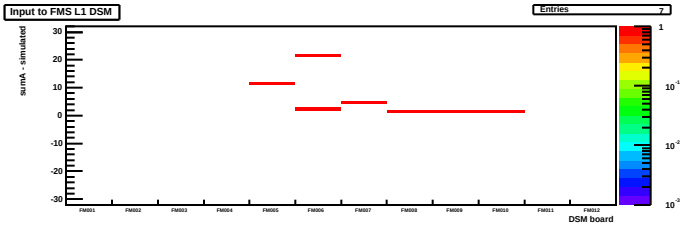

Input to FMS L1 DSM
Entries 2.999999e+07
sumB
DSM board

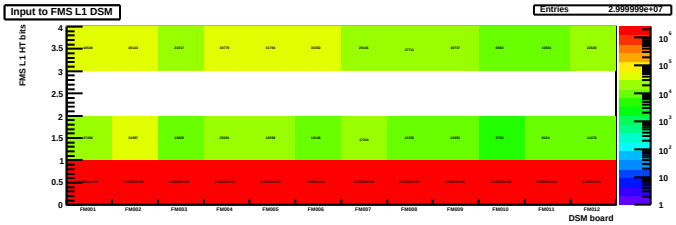

Input to FMS L1 DSM
Entries 5065
sumB - simulated
DSM board

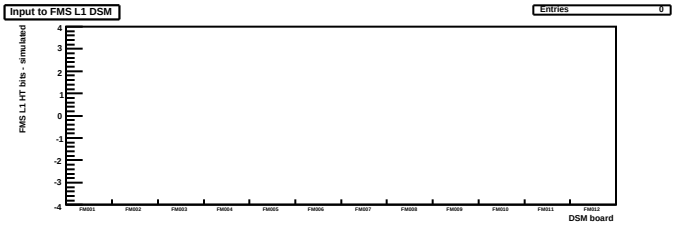

Input to FMS L1 DSM
Entries 2.999999e+07
sumCD
DSM board

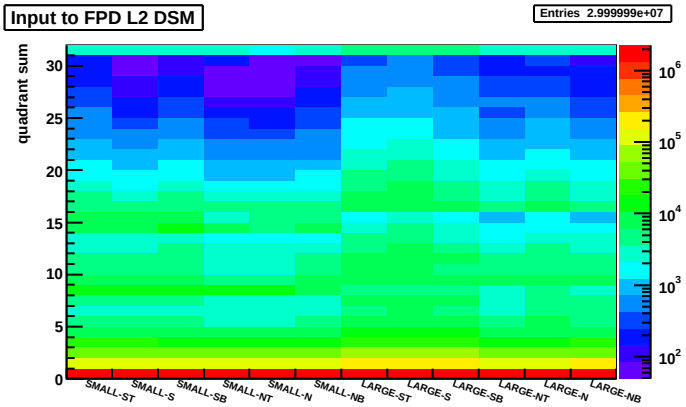

Input to FMS L1 DSM
Entries 50040
sumCD - simulated
DSM board

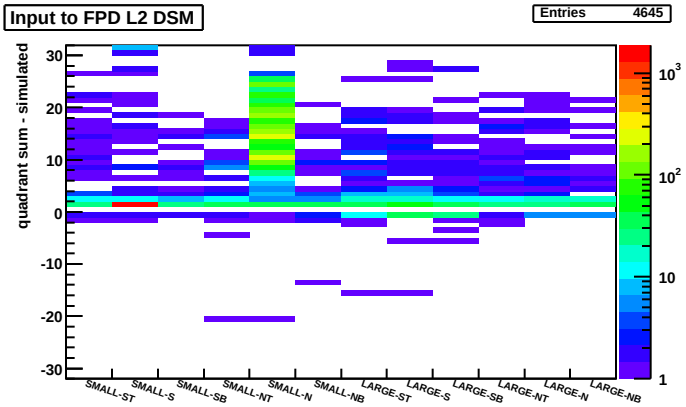

Input to FMS L1 DSM
Entries 2.999999e+07
sumA
DSM board

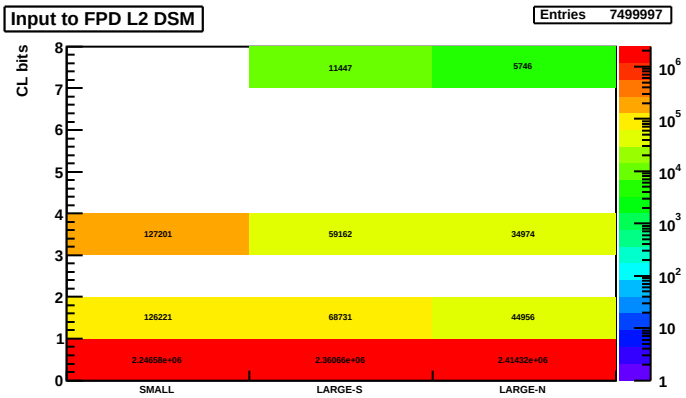

Input to FMS L1 DSM
Entries 7
sumA - simulated
DSM board

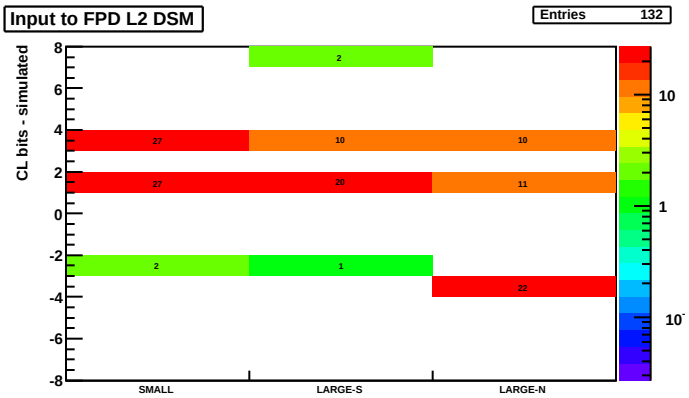

Input to FMS L1 DSM
Entries 2.999999e+07
FMS L1 HT bits
DSM board

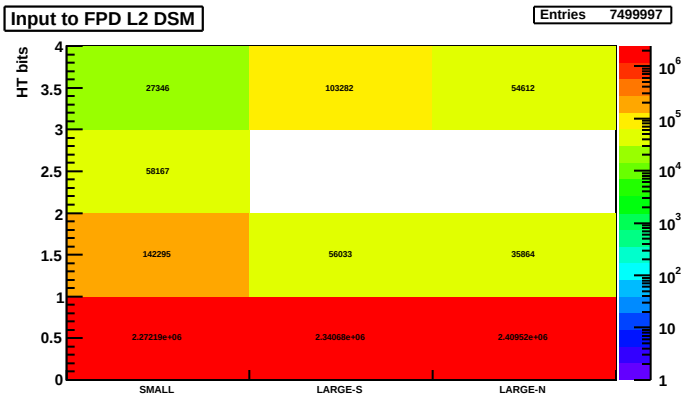

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

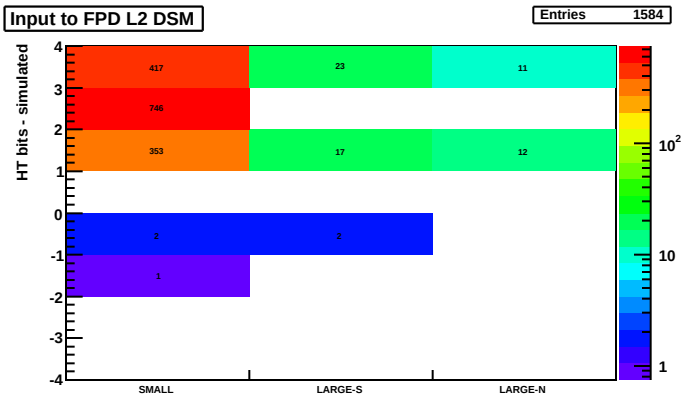

Input to FPD L2 DSM
Entries 2.999999e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

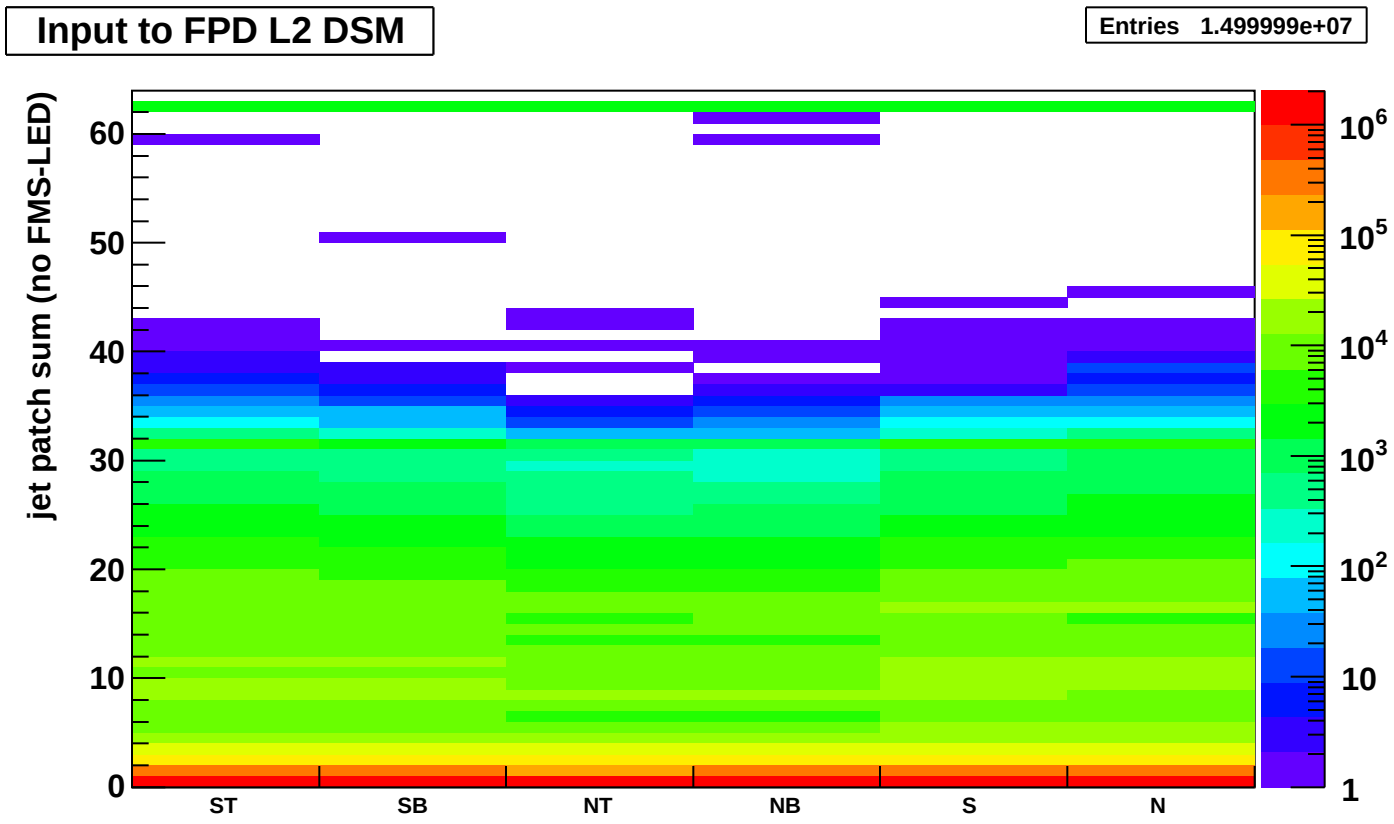

Input to FPD L2 DSM
Entries 4645
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

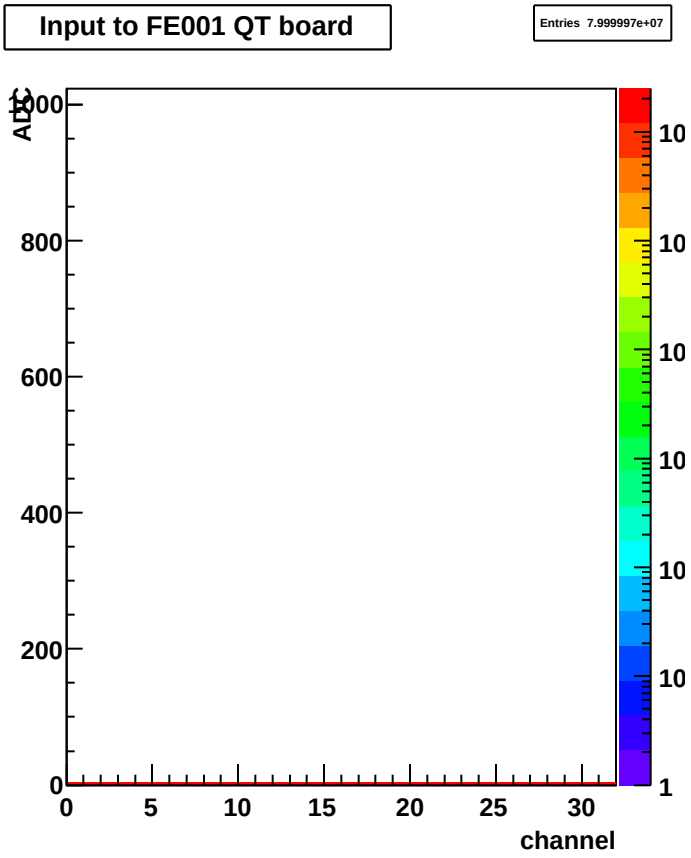

Input to FPD L2 DSM
Entries 7499997
CL bits
11447
5746
127201
59162
34974
126221
68731
44956
2.24658e+06
2.36066e+06
2.41432e+06
SMALL
LARGE-S
LARGE-N

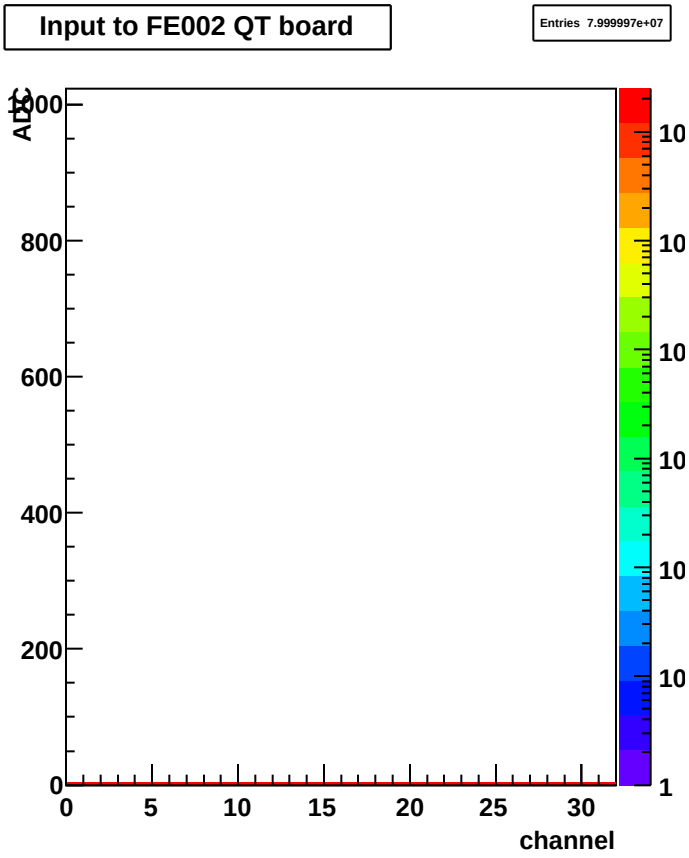

Input to FPD L2 DSM
Entries 132
CL bits - simulated
2
27
10
10
27
20
11
2
1
22
SMALL
LARGE-S
LARGE-N

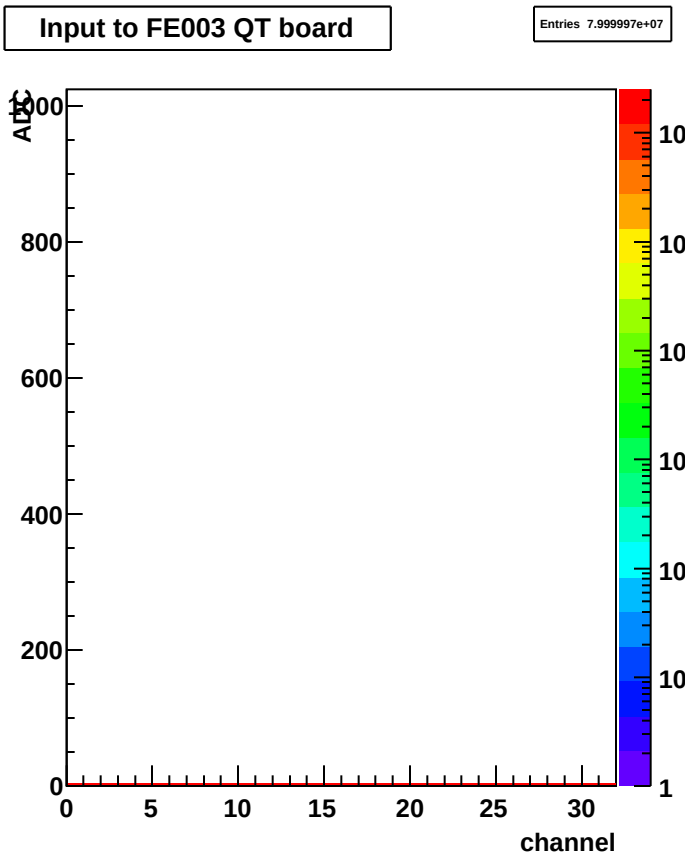

Input to FPD L2 DSM
Entries 7499997
HT bits
27346
103282
54612
58167
142295
56033
35864
2.27219e+06
2.34068e+06
2.40952e+06
SMALL
LARGE-S
LARGE-N

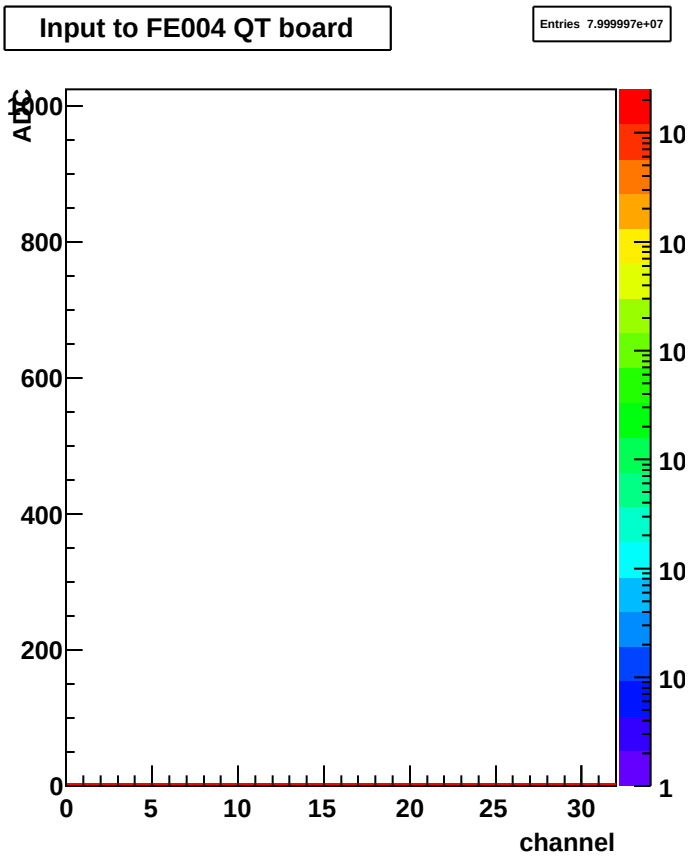

Input to FPD L2 DSM
Entries 1584
HT bits - simulated
417
23
11
746
353
17
12
2
2
1
SMALL
LARGE-S
LARGE-N

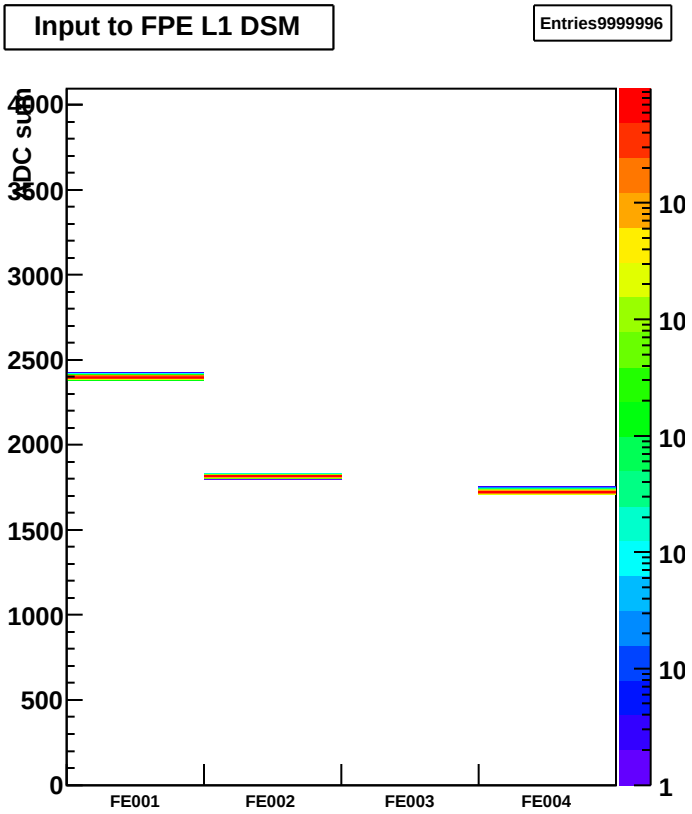

Input to FPD L2 DSM
Entries 1.499999e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 7.999997e+07
ADC
channel

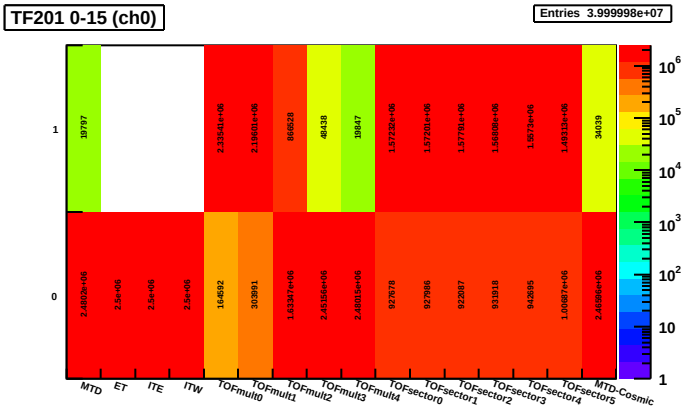

Input to FE002 QT board
Entries 7.999997e+07
ADC
channel


Input to FE003 QT board
Entries 7.999997e+07
ADC
channel

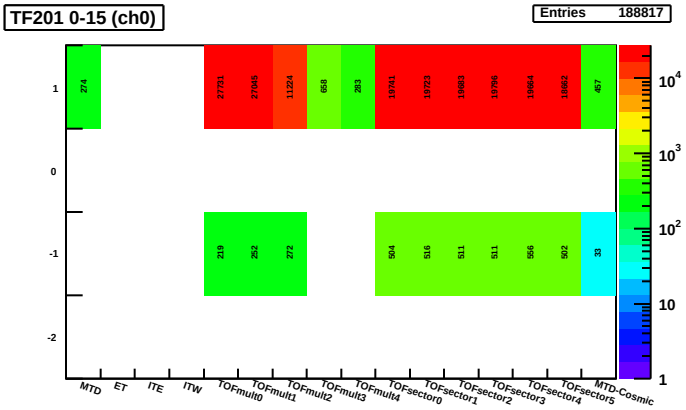

Input to FE004 QT board
Entries 7.999997e+07
ADC
channel


Input to FPE L1 DSM
Entries9999996
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

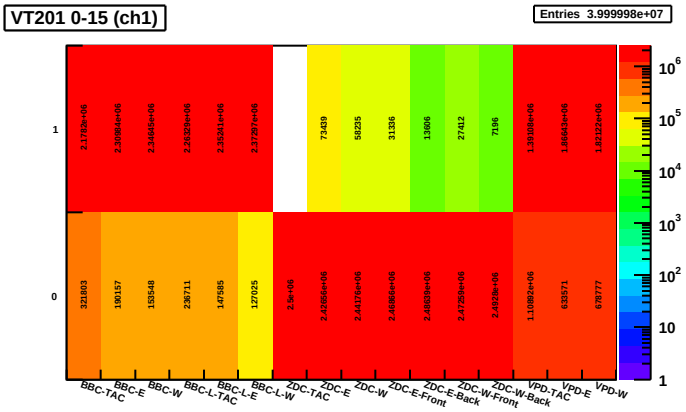
TF201 0-15 (ch0)



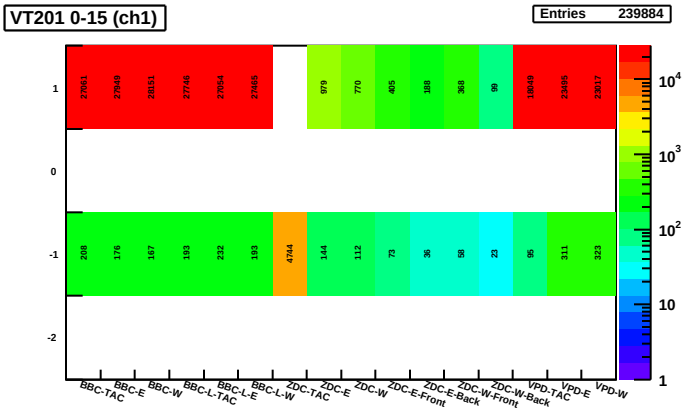
TF201 0-15 (ch0)



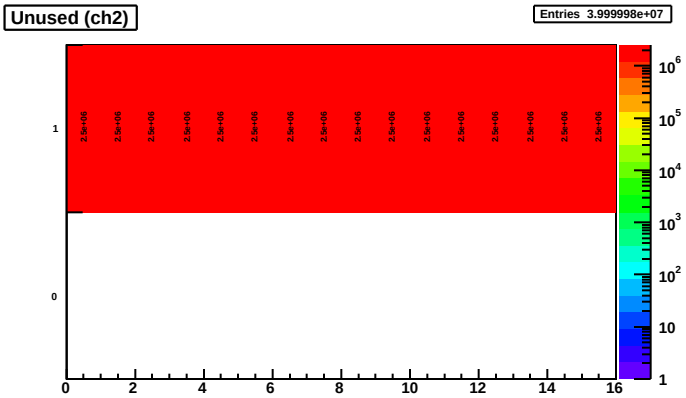
VT201 0-15 (ch1)

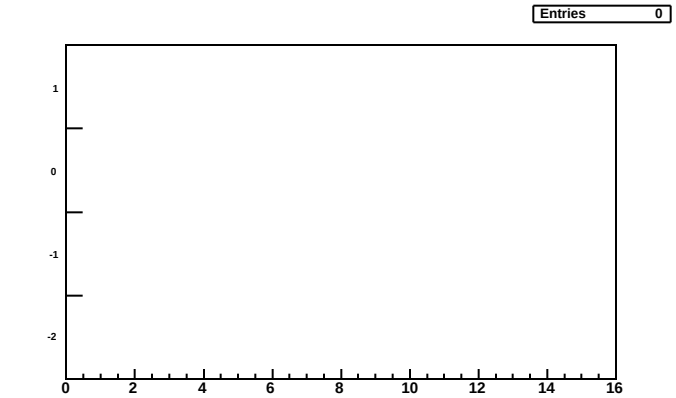


VT201 0-15 (ch1)



Unused (ch2)





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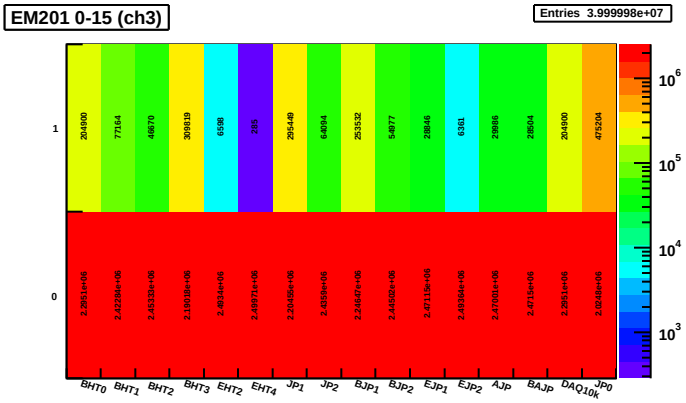

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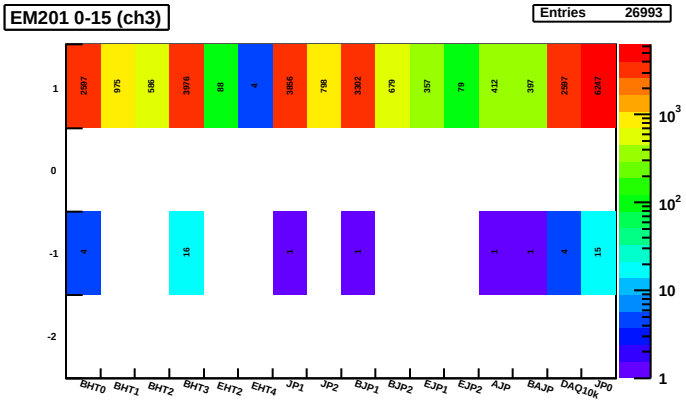
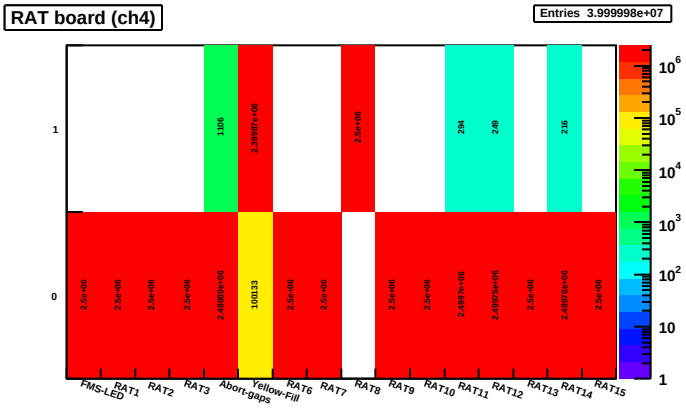
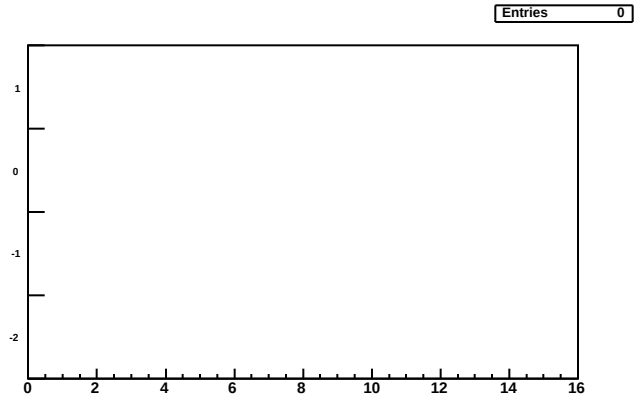


RAT board (ch4)





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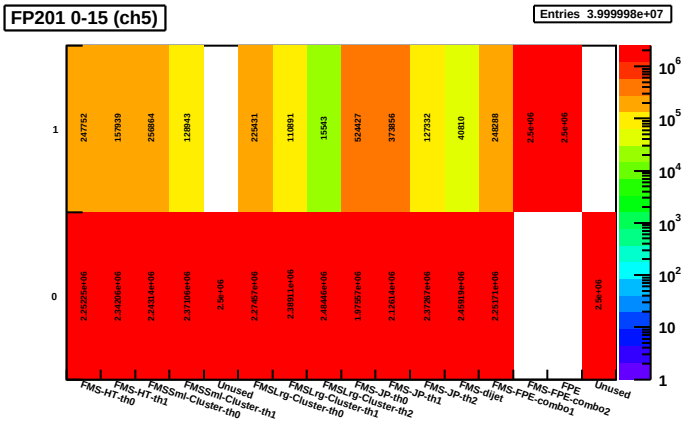

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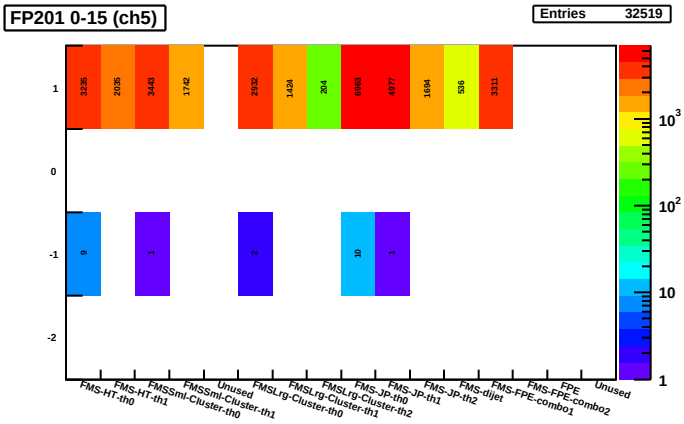

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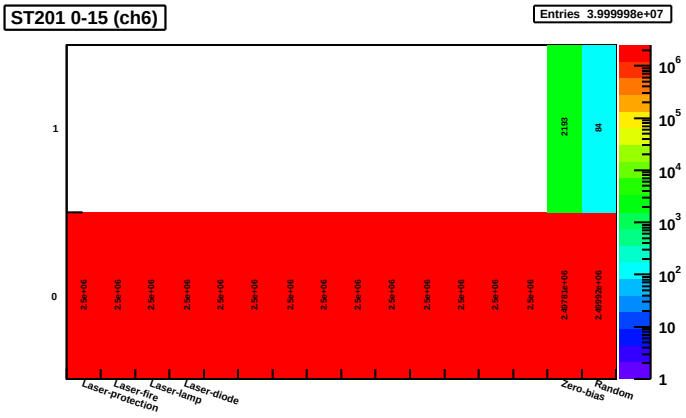

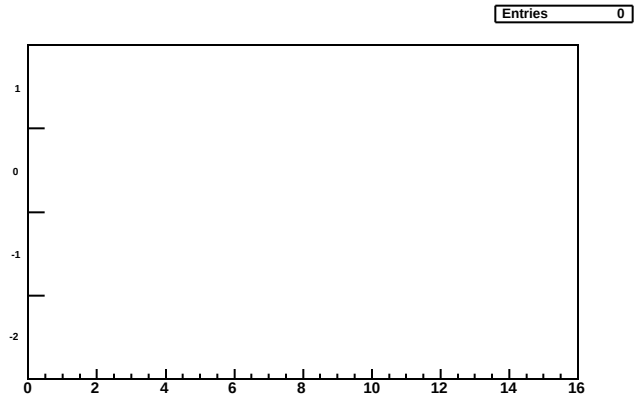
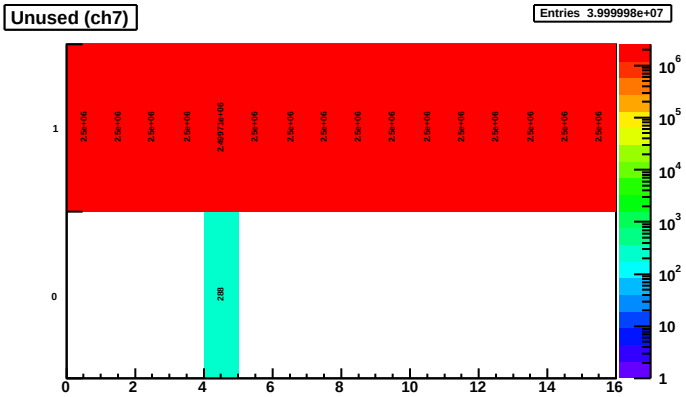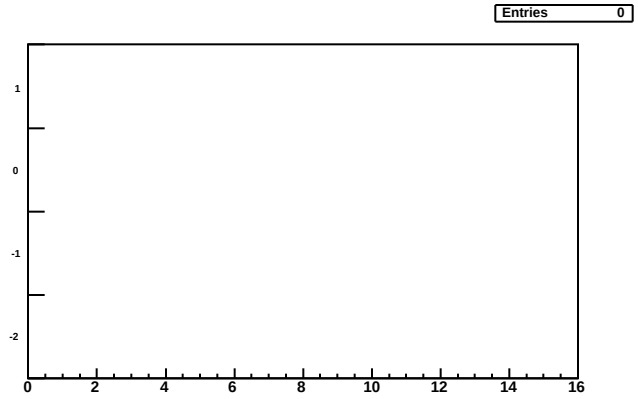
Unused (ch7)