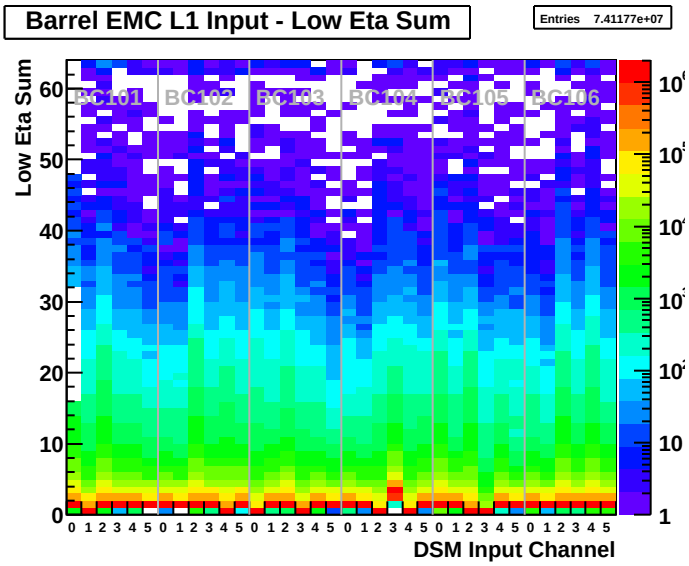
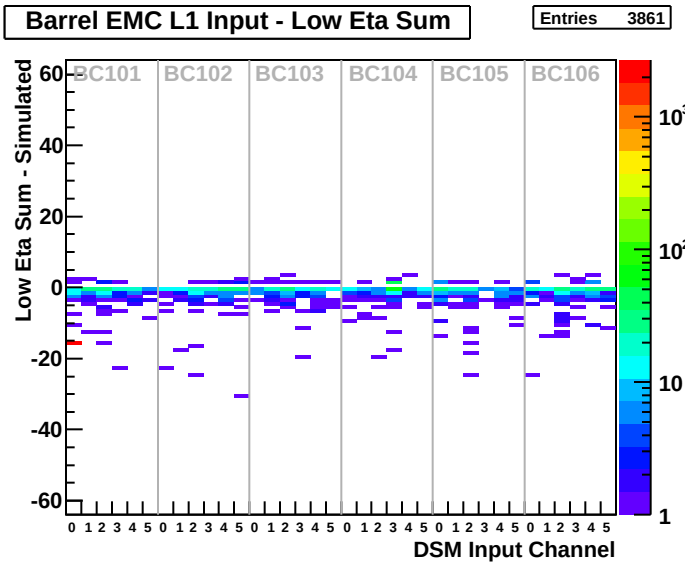
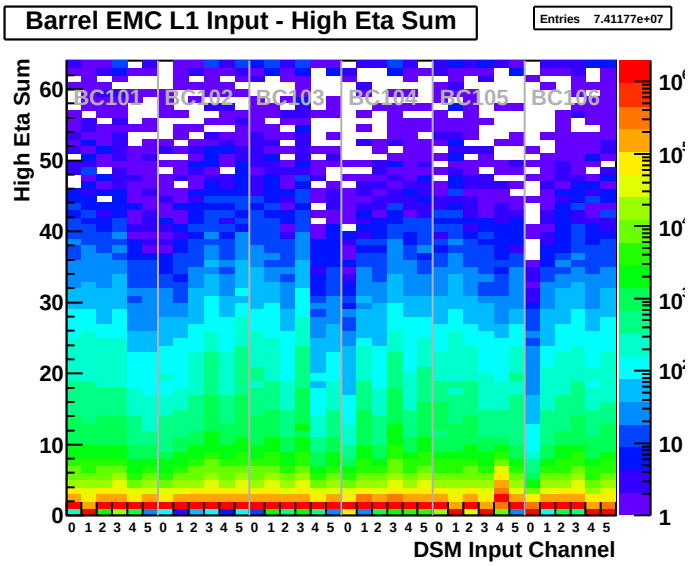
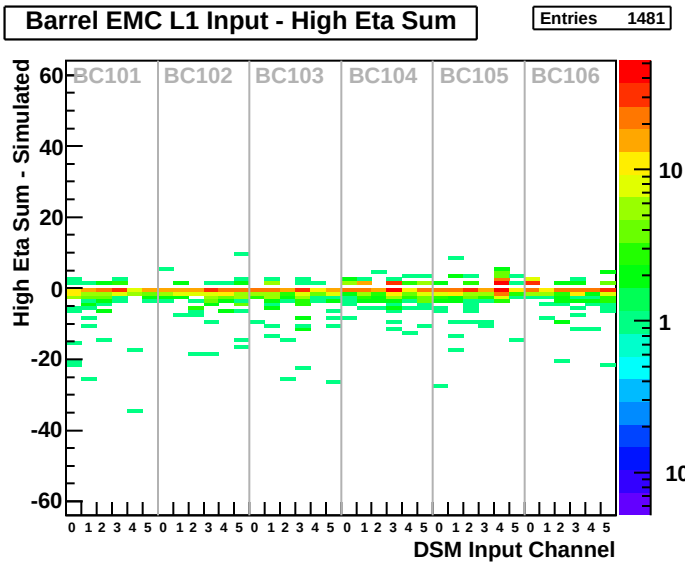
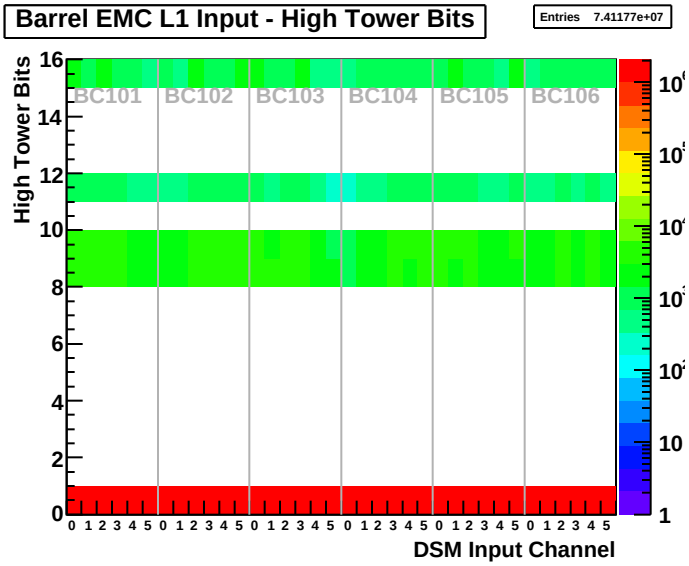
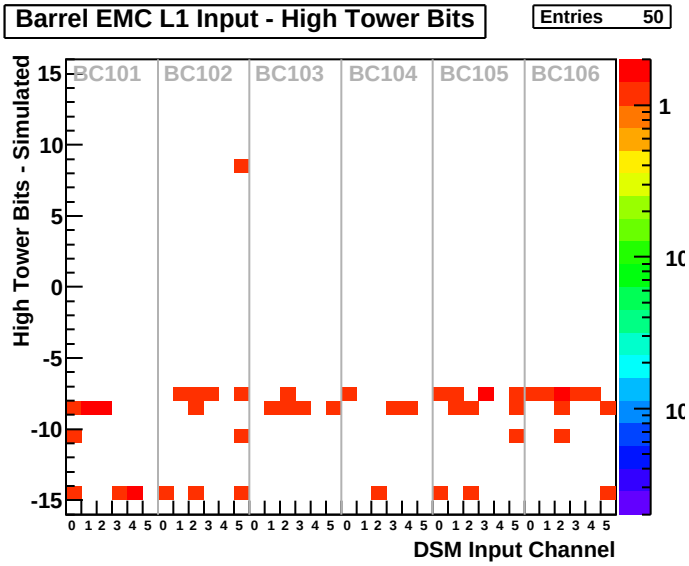

Barrel EMC L1 Input - Low Eta Sum
Entries 7.41177e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

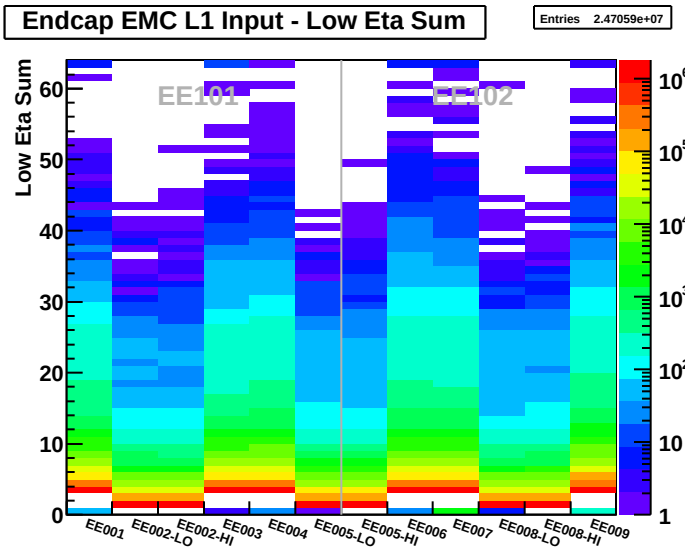

Barrel EMC L1 Input - Low Eta Sum
Entries 3861
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

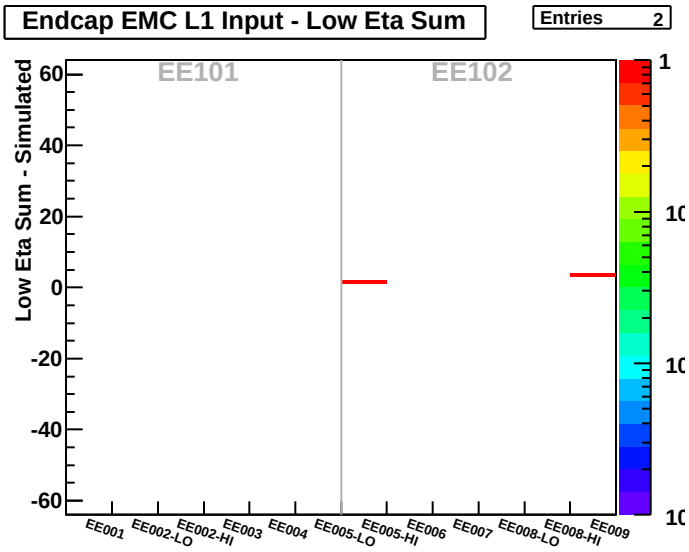

Barrel EMC L1 Input - High Eta Sum
Entries 7.41177e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

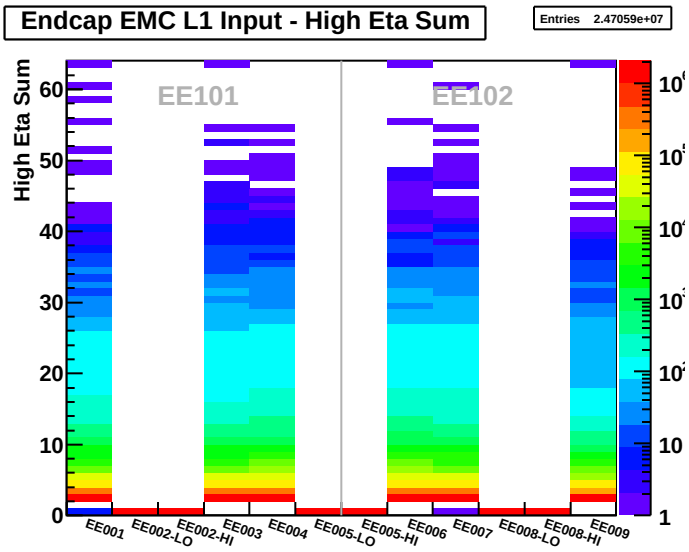

Barrel EMC L1 Input - High Eta Sum
Entries 1481
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

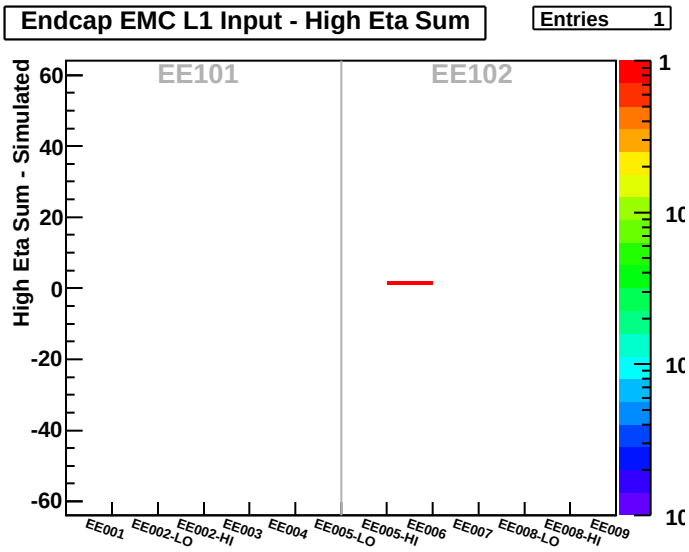

Barrel EMC L1 Input - High Tower Bits
Entries 7.41177e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

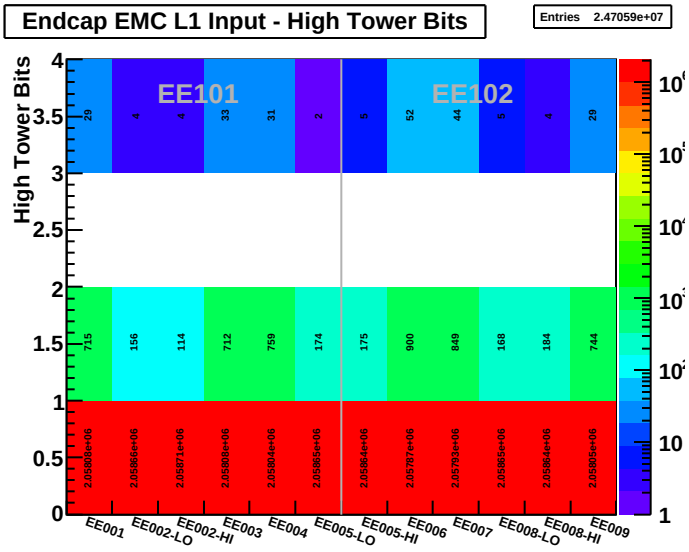

Barrel EMC L1 Input - High Tower Bits
Entries 50
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

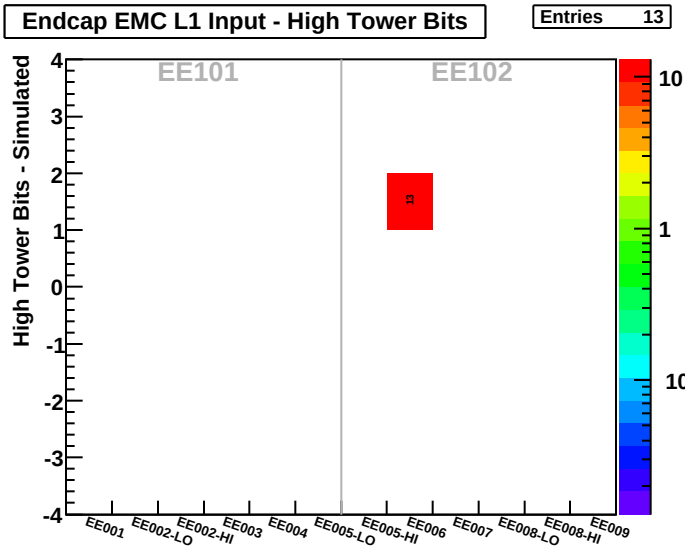

Endcap EMC L1 Input - Low Eta Sum
Entries 2.47059e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

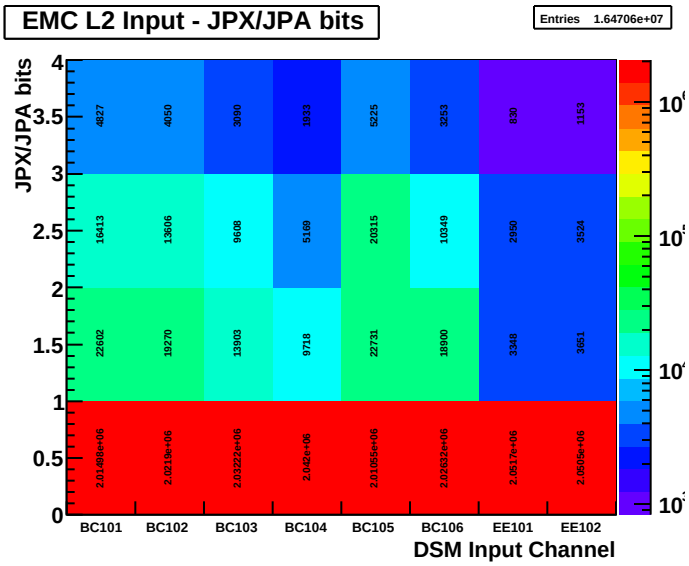

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

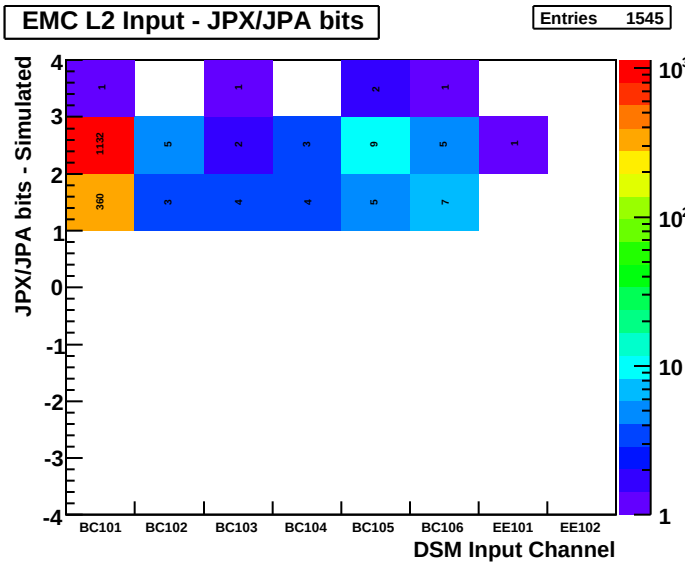

Endcap EMC L1 Input - High Eta Sum
Entries 2.47059e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

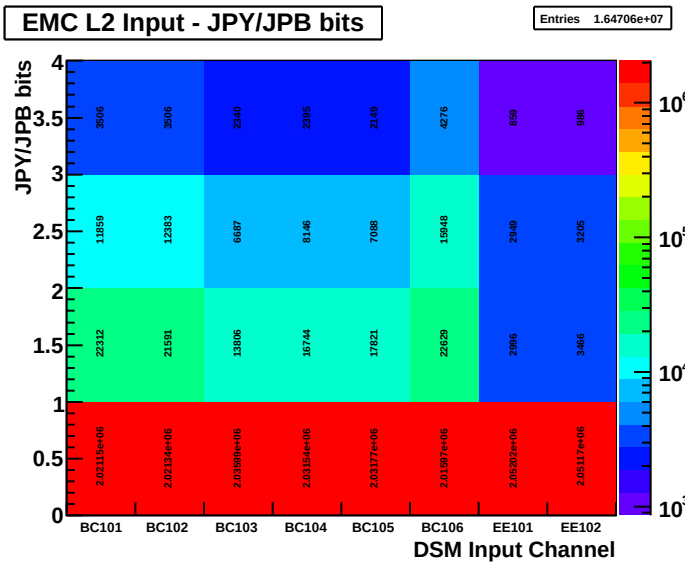

Endcap EMC L1 Input - High Eta Sum
Entries 1
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

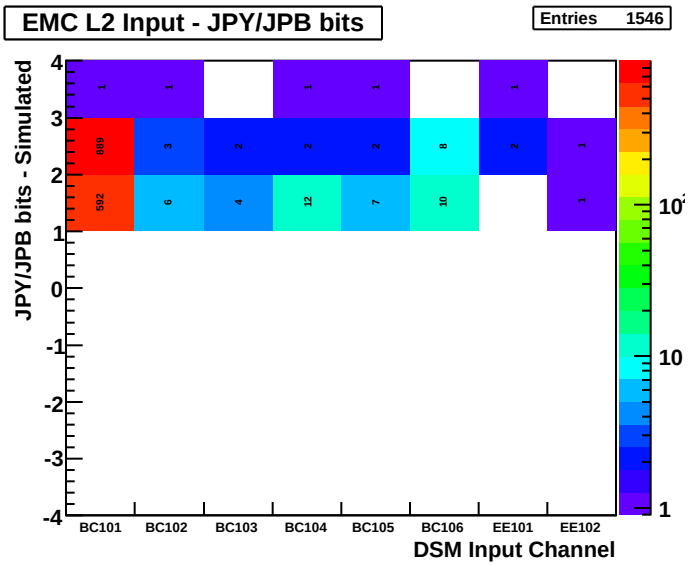

Endcap EMC L1 Input - High Tower Bits
Entries 2.47059e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

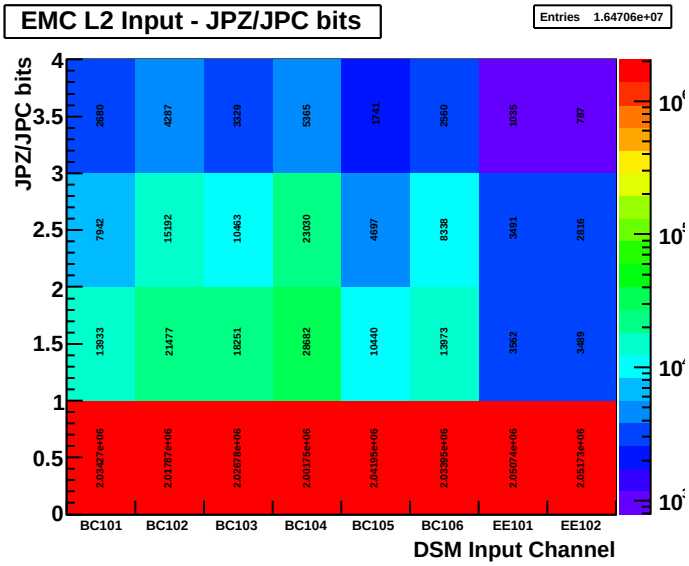

Endcap EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

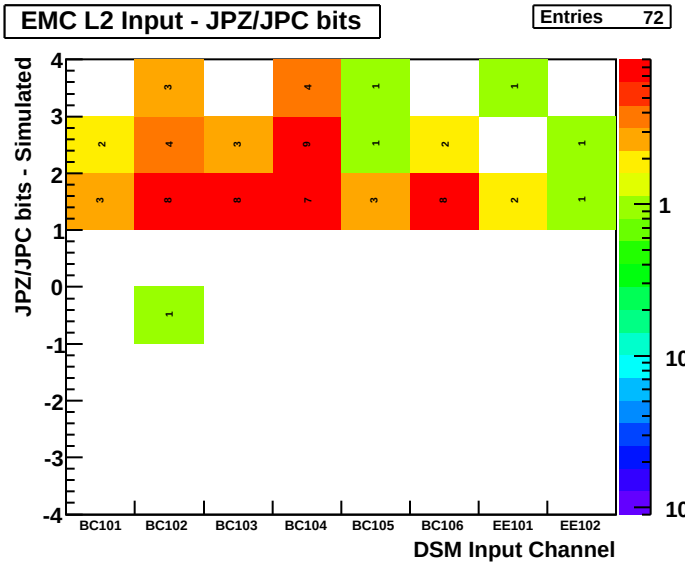

EMC L2 Input - JPX/JPA bits
Entries 1.64706e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

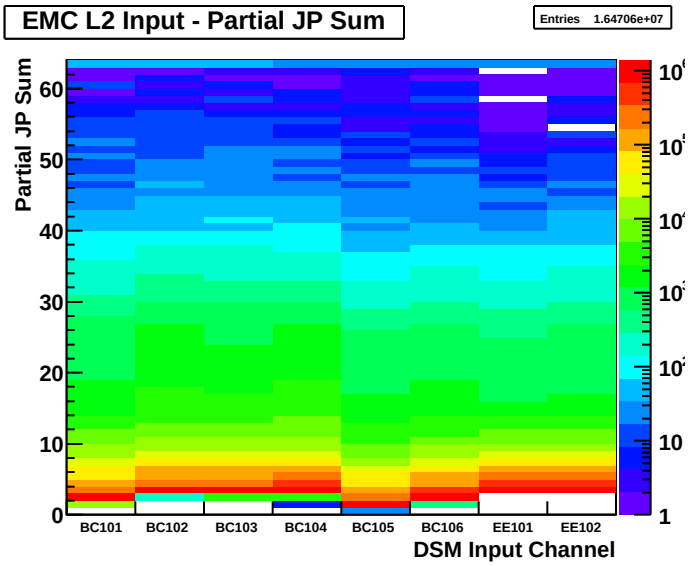

EMC L2 Input - JPX/JPA bits
Entries 1545
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

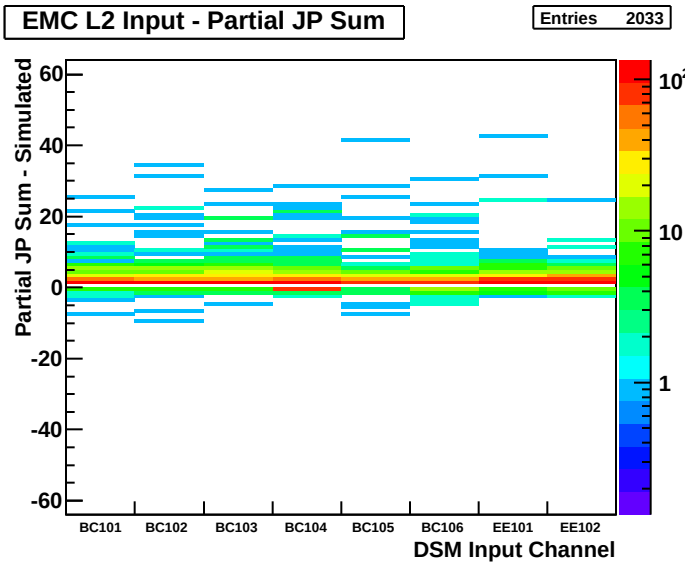

EMC L2 Input - JPY/JPB bits
Entries 1.64706e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

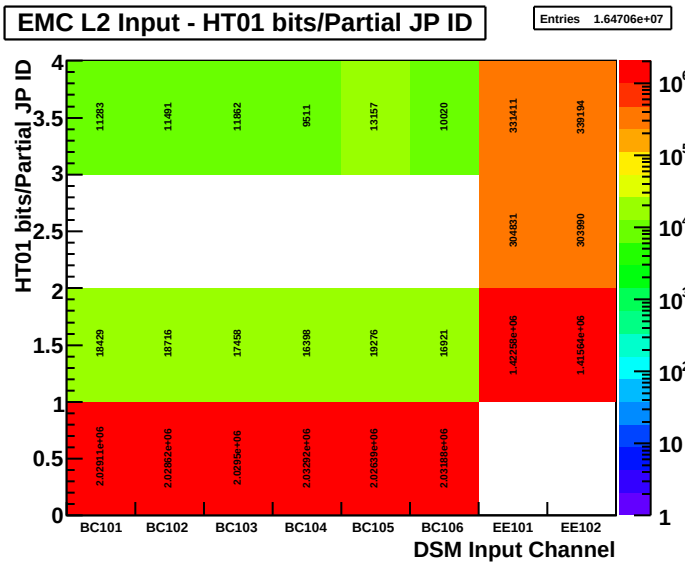

EMC L2 Input - JPY/JPB bits
Entries 1546
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

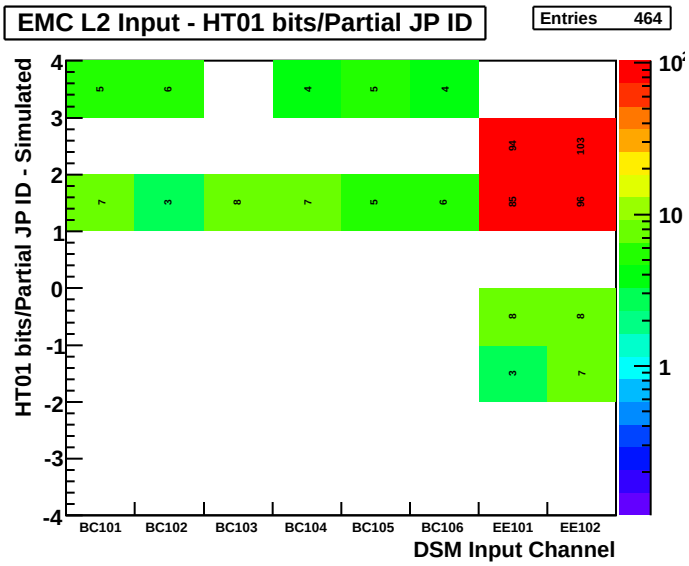

EMC L2 Input - JPZ/JPC bits
Entries 1.64706e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

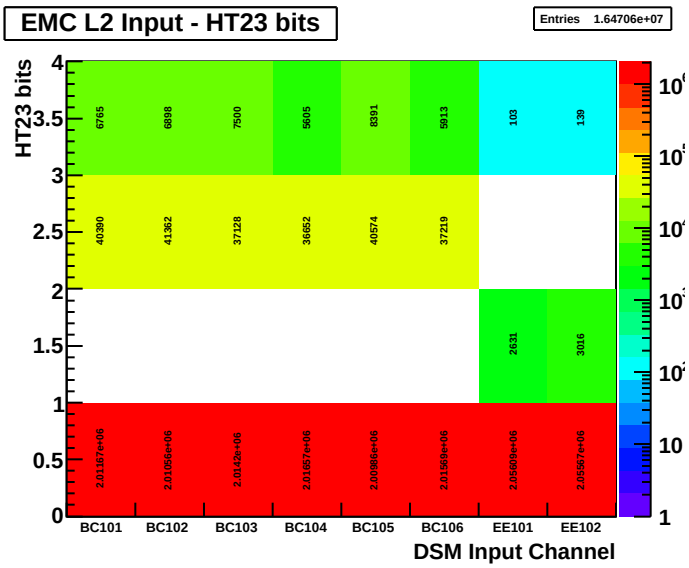

EMC L2 Input - JPZ/JPC bits
Entries 72
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

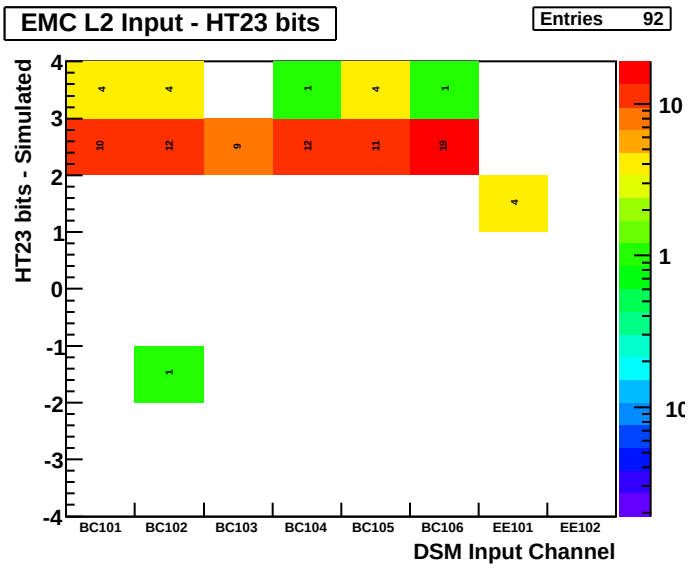

EMC L2 Input - Partial JP Sum
Entries 1.64706e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

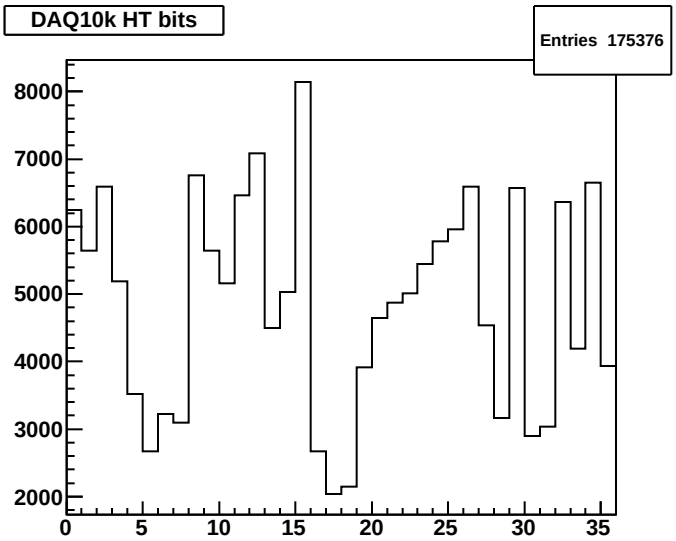

EMC L2 Input - Partial JP Sum
Entries 2033
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

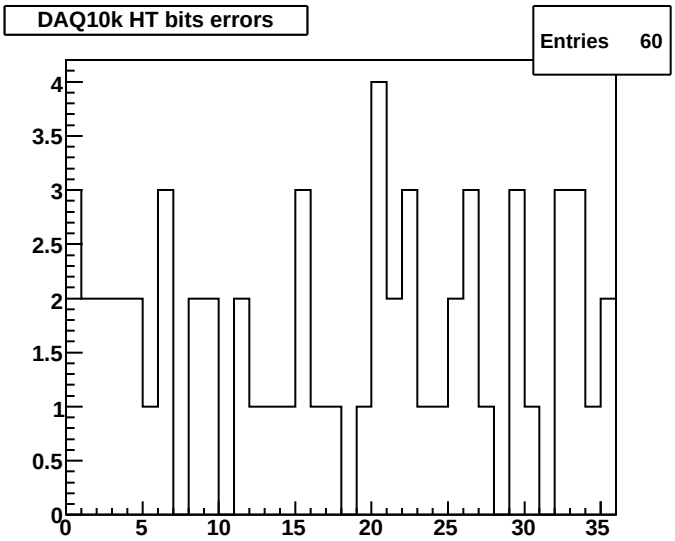

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.64706e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

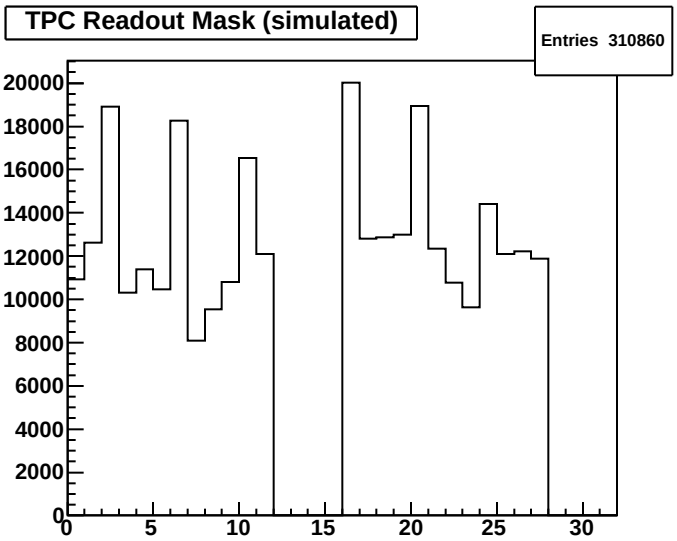

EMC L2 Input - HT01 bits/Partial JP ID
Entries 464
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

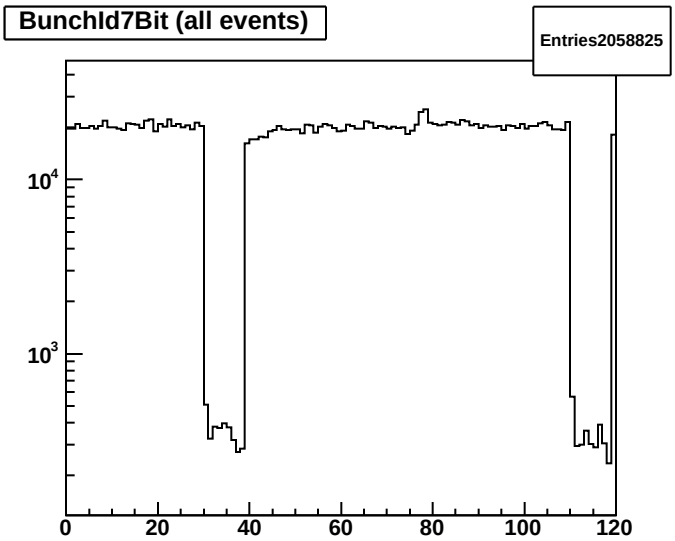

EMC L2 Input - HT23 bits
Entries 1.64706e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

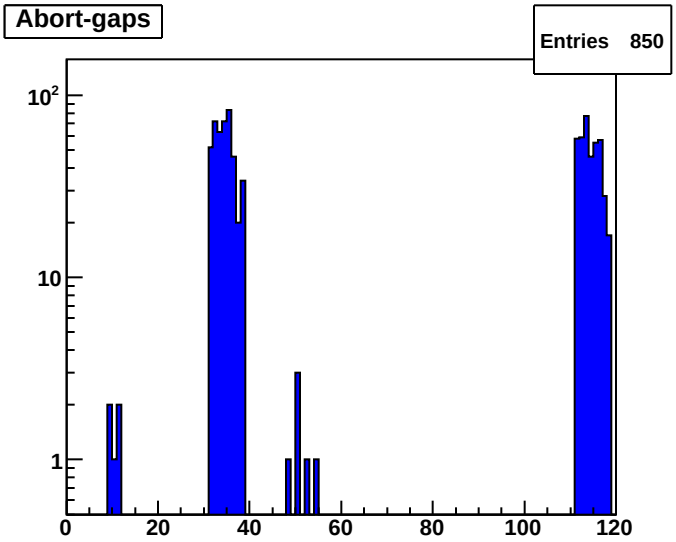

EMC L2 Input - HT23 bits
Entries 92
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

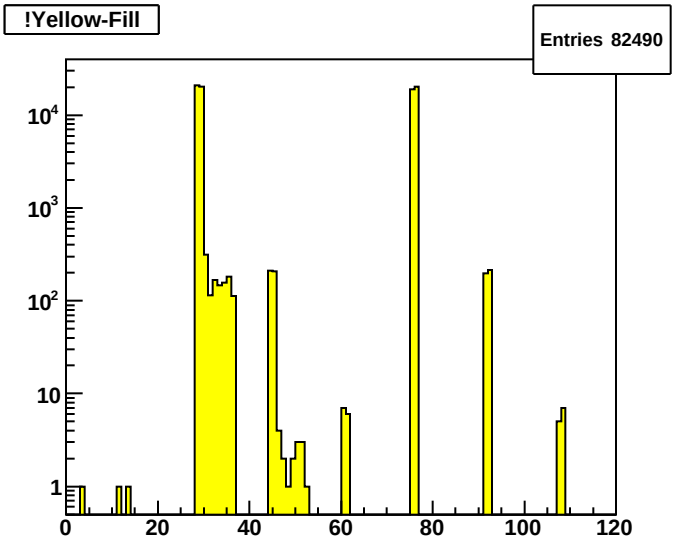

DAQ10k HT bits
Entries 175376

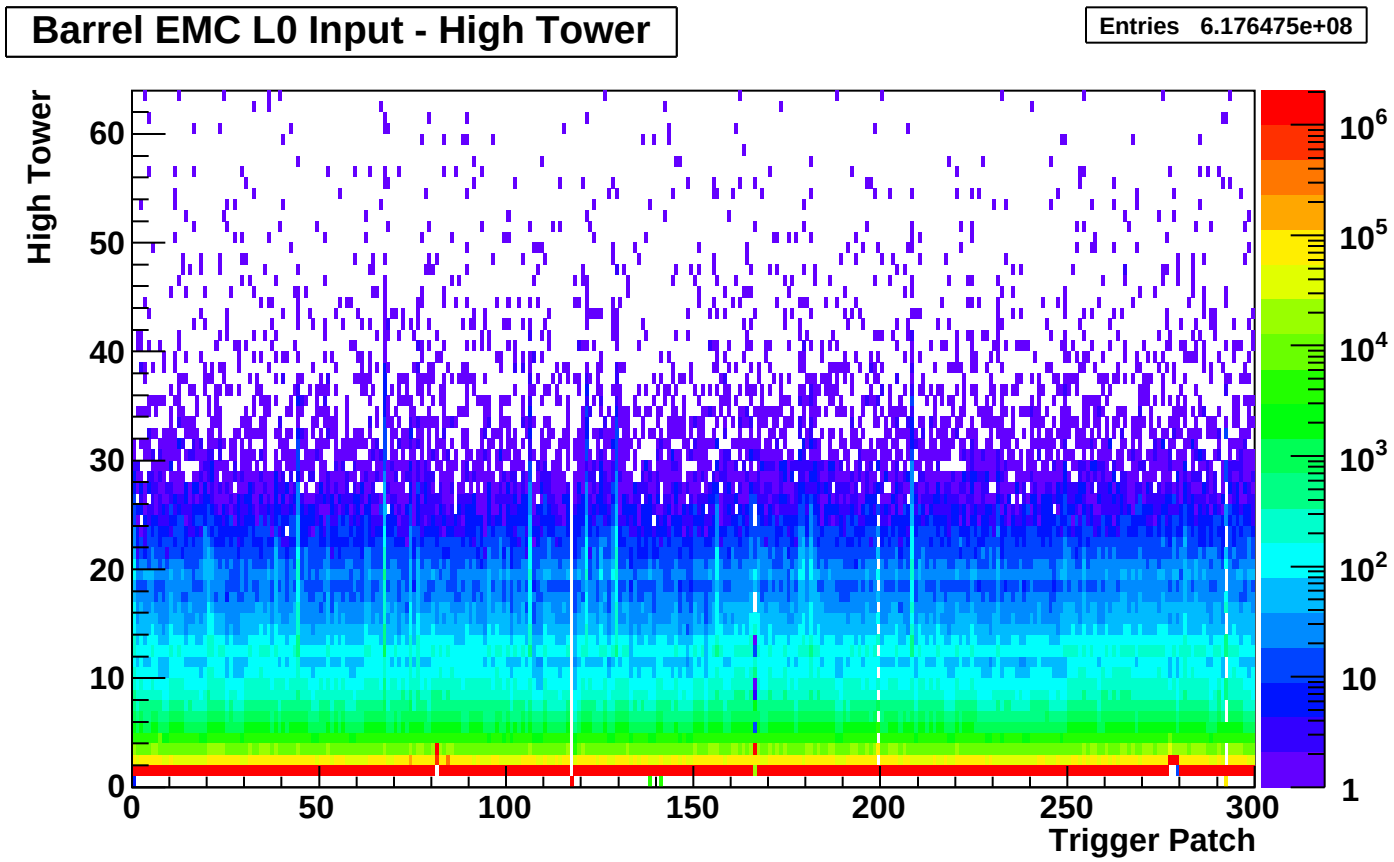

DAQ10k HT bits errors
Entries 60

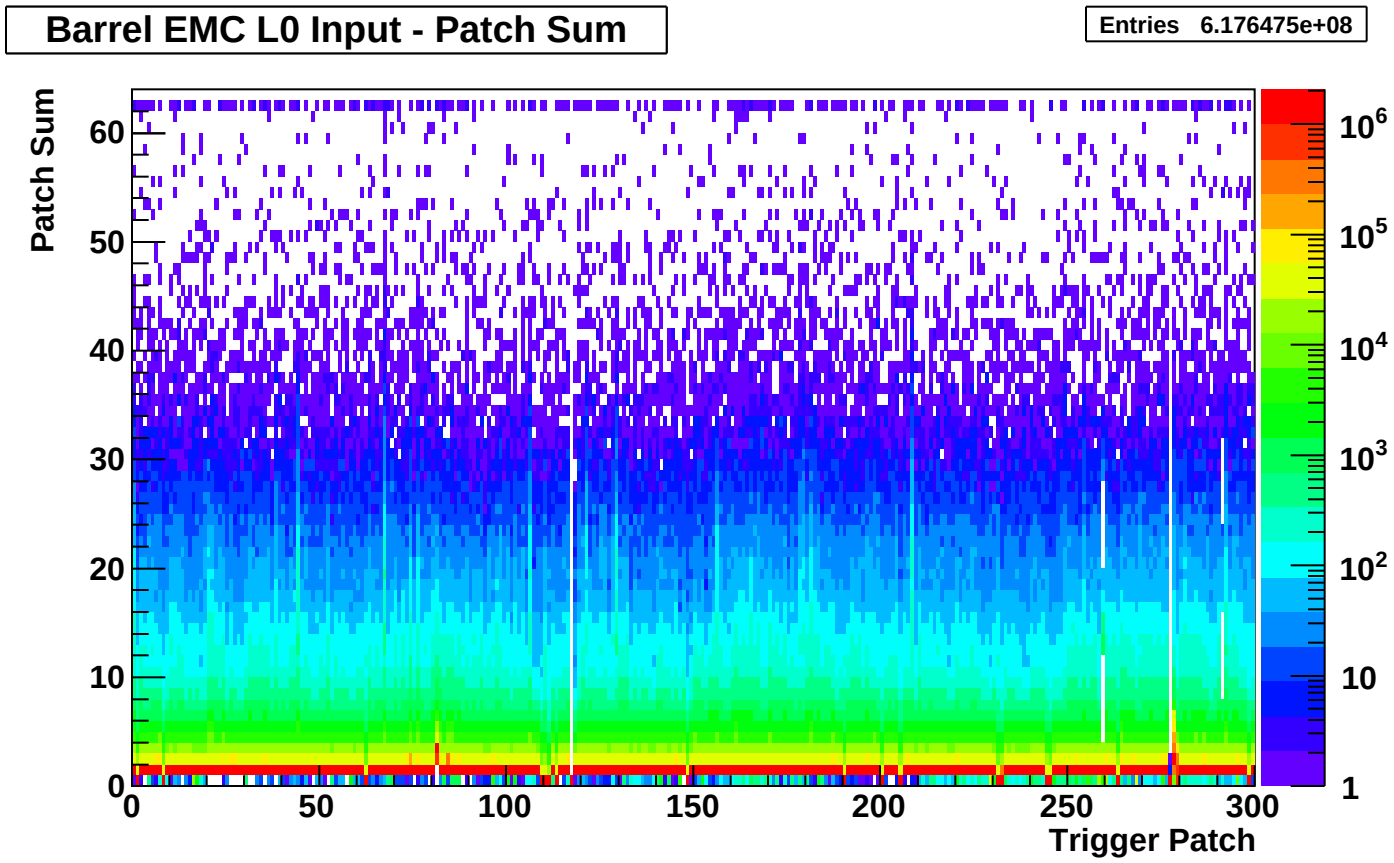

TPC Readout Mask (simulated)
Entries 310860

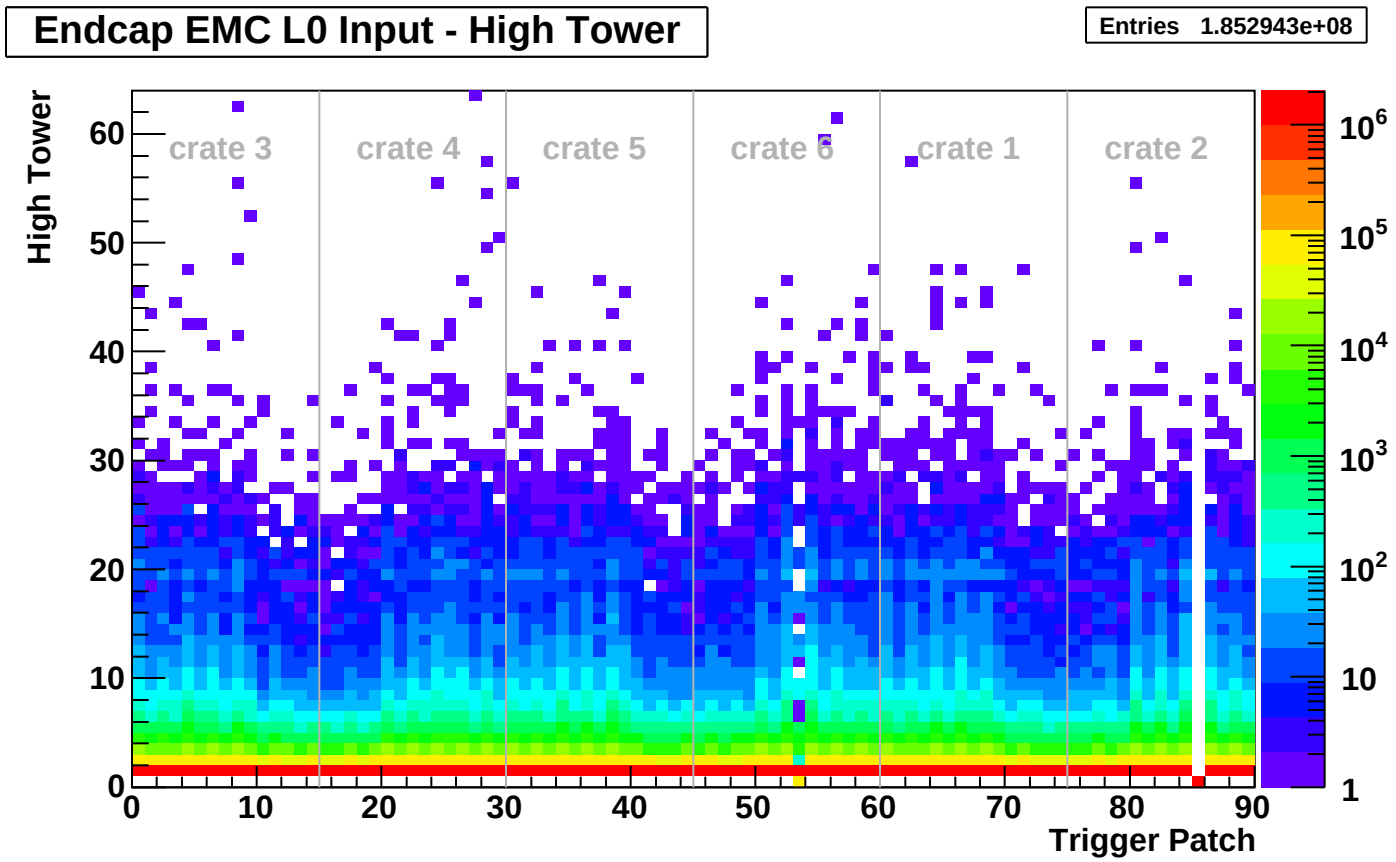

BunchId7Bit (all events)
Entries2058825

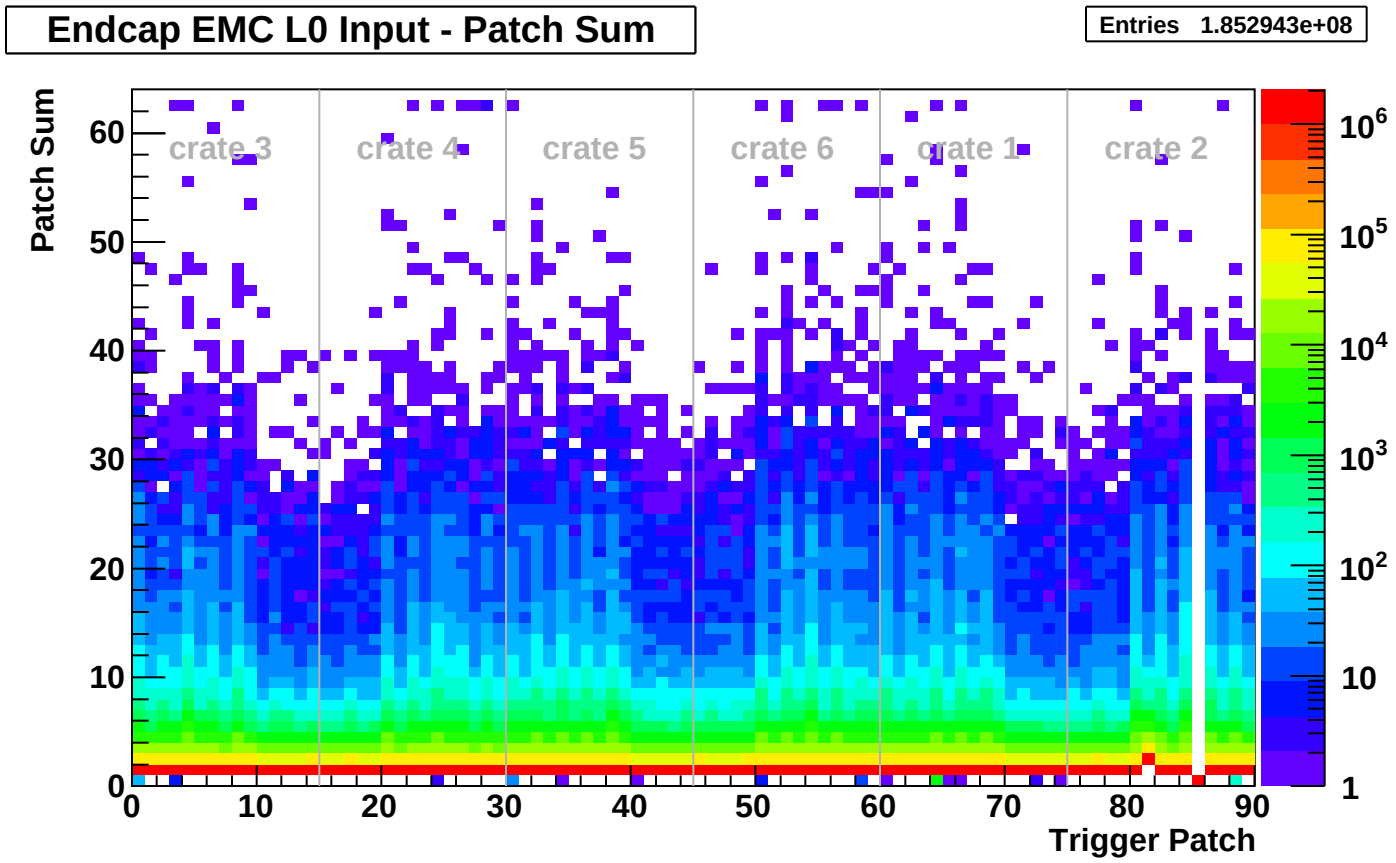

Abort-gaps
Entries 850

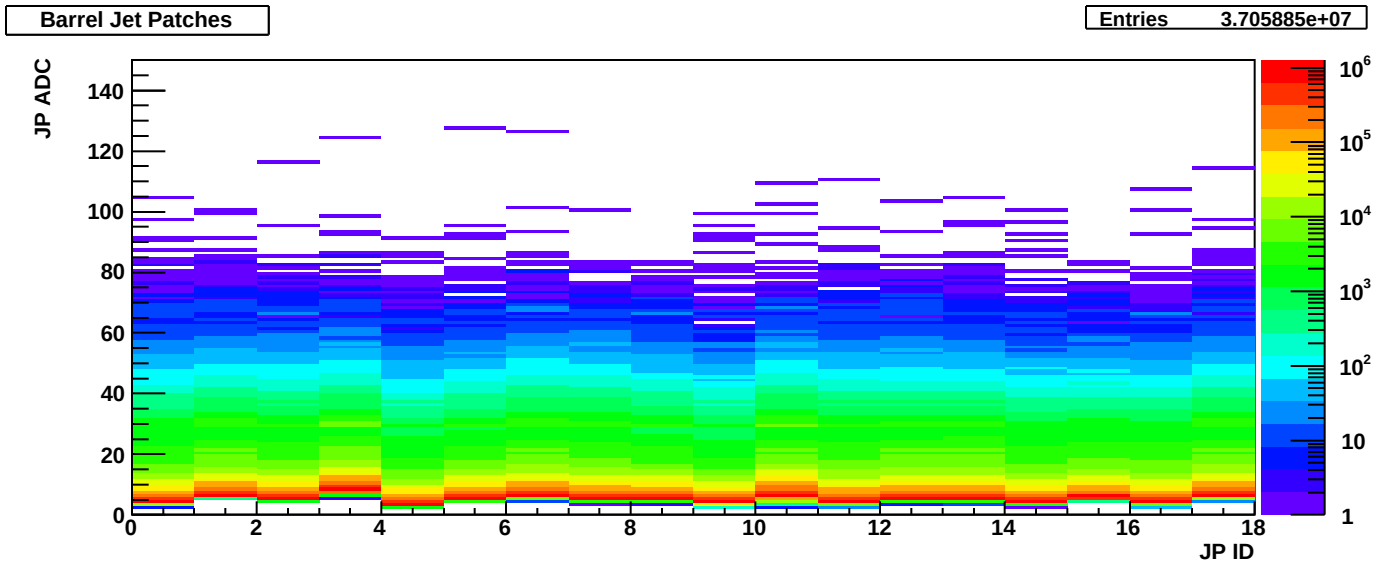

!Yellow-Fill
Entries 82490

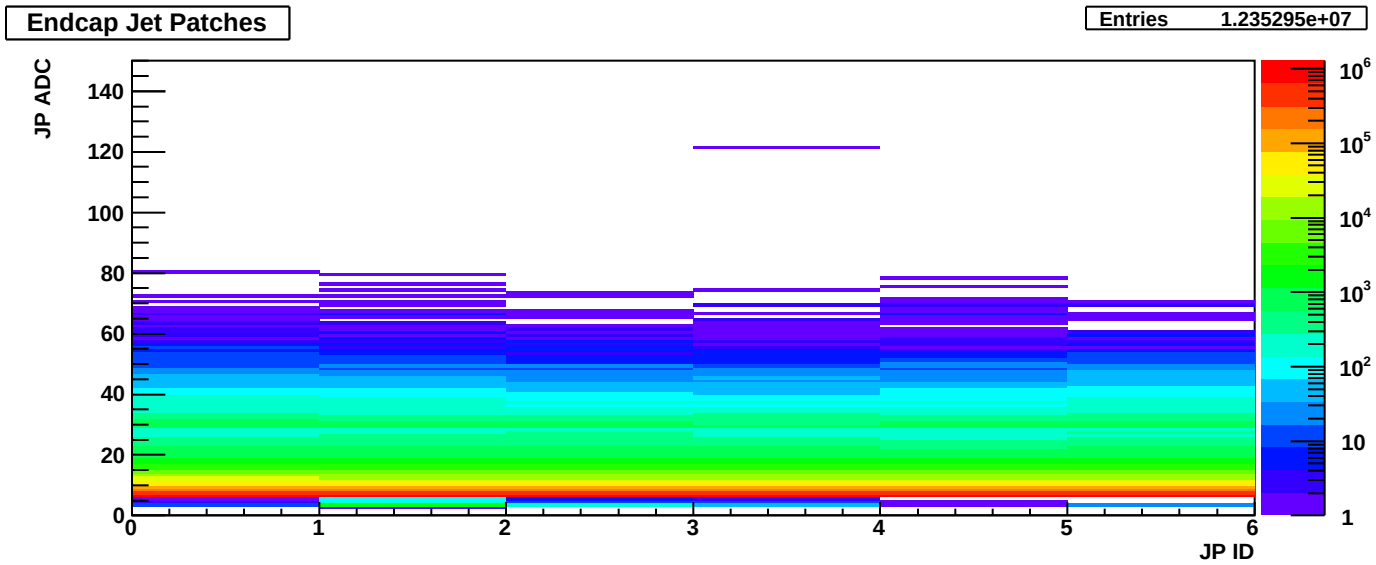

Barrel EMC L0 Input - High Tower
Entries 6.176475e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

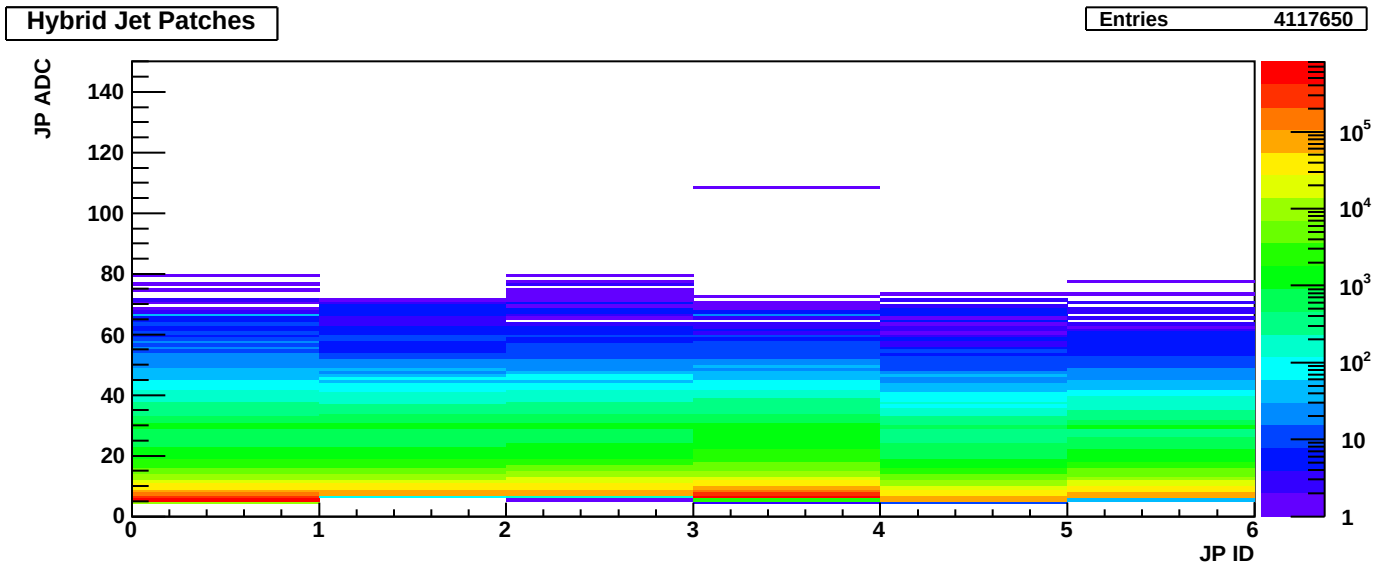

Barrel EMC L0 Input - Patch Sum
Entries 6.176475e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

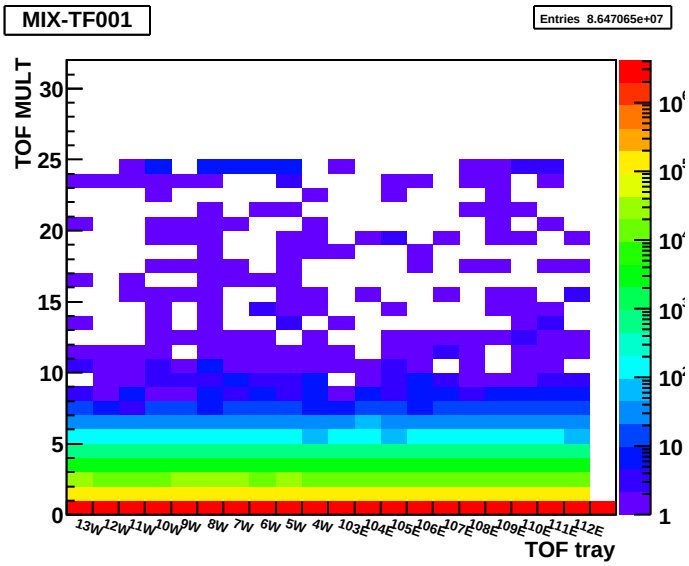

Endcap EMC L0 Input - High Tower
Entries 1.852943e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

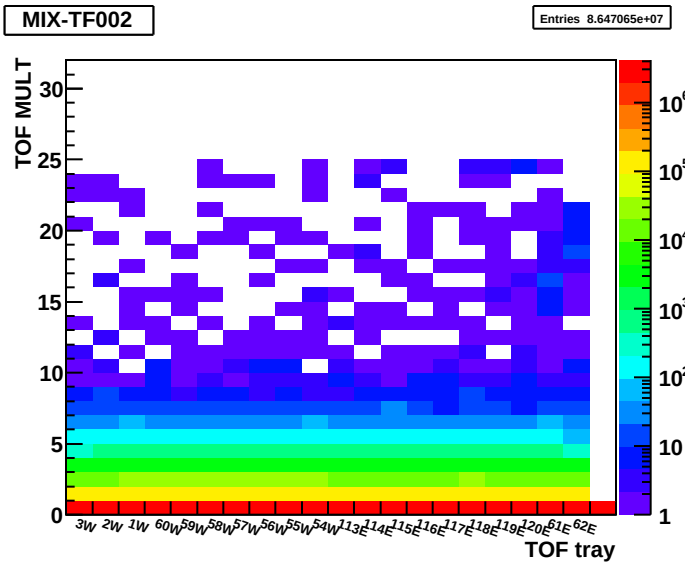

Endcap EMC L0 Input - Patch Sum
Entries 1.852943e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

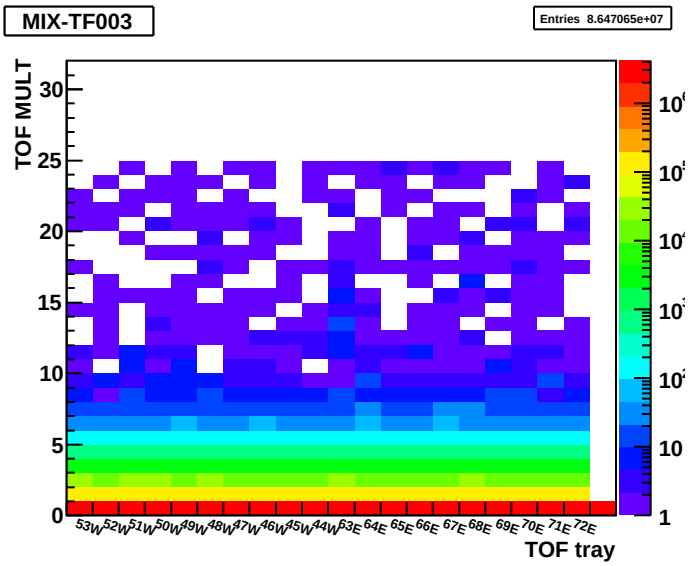

Barrel Jet Patches
Entries 3.705885e+07
JP ADC
JP ID

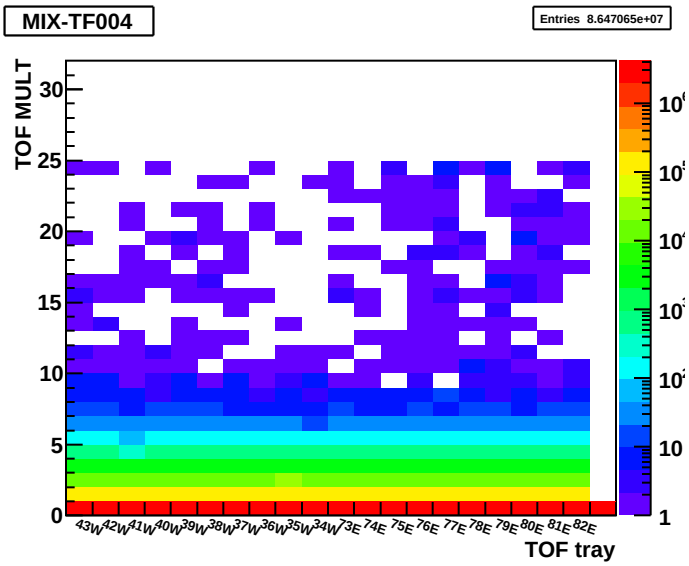

Endcap Jet Patches
Entries 1.235295e+07
JP ADC
JP ID

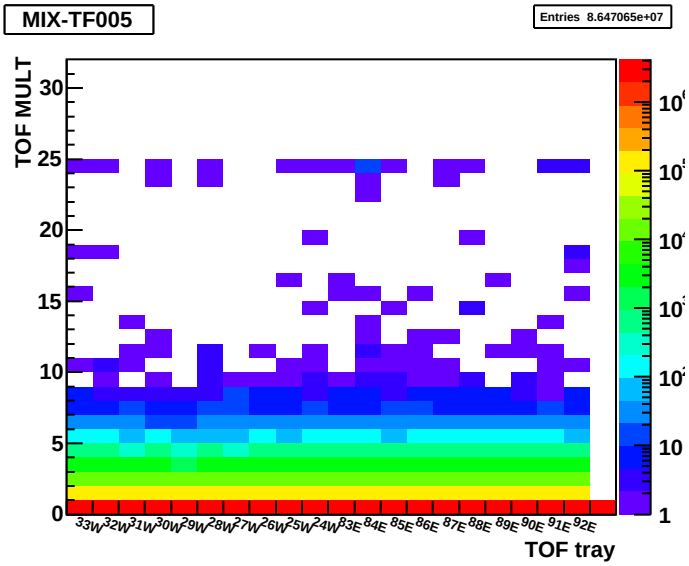

Hybrid Jet Patches
Entries 4117650
JP ADC
JP ID

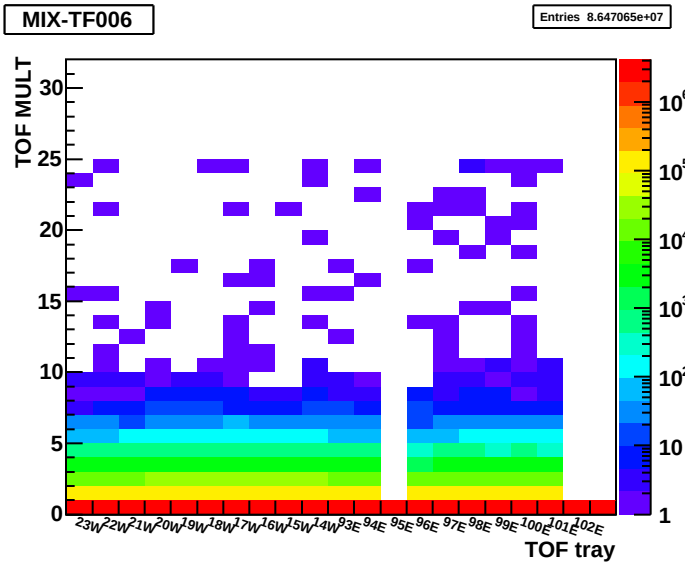

MIX-TF001
Entries 8.647065e+07
TOF MULT
TOF tray

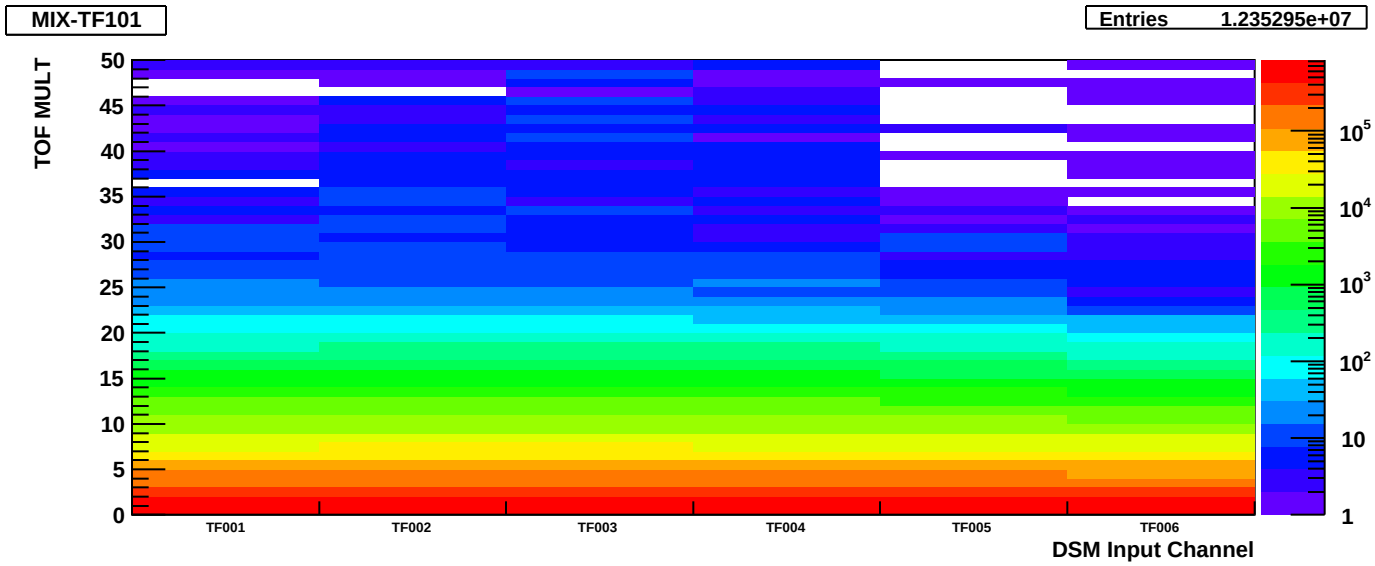

MIX-TF002
Entries 8.647065e+07
TOF MULT
TOF tray

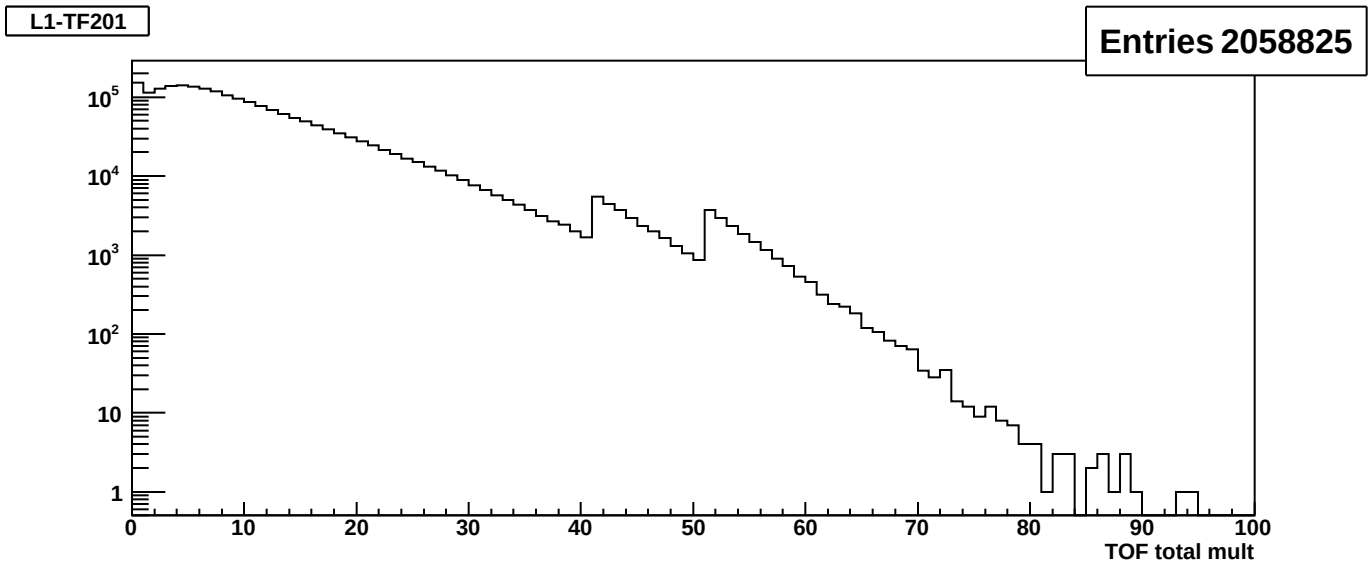

MIX-TF003
Entries 8.647065e+07
TOF MULT
TOF tray

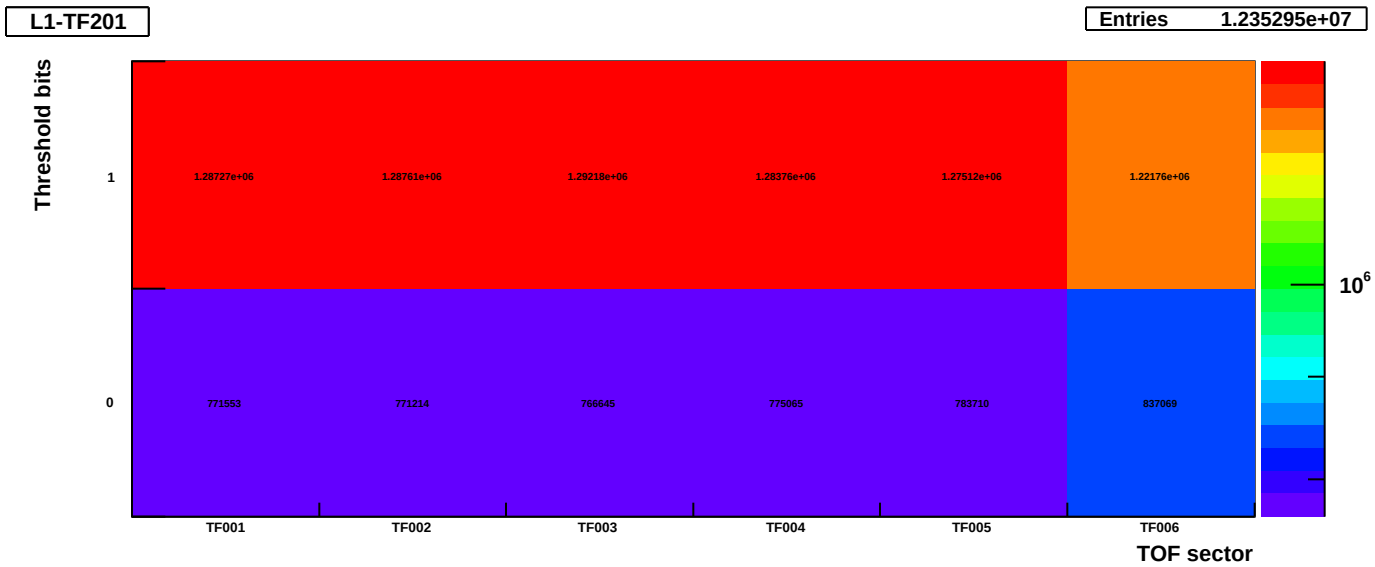

MIX-TF004
Entries 8.647065e+07
TOF MULT
TOF tray

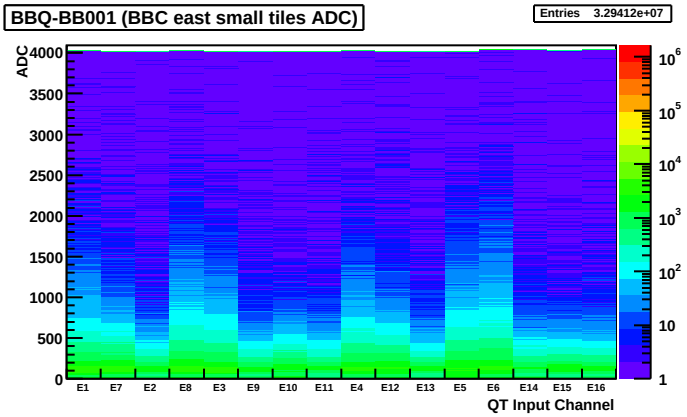

MIX-TF005
Entries 8.647065e+07
TOF MULT
TOF tray

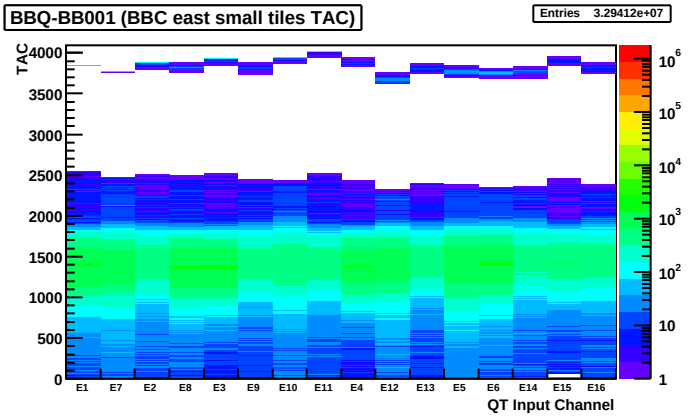

MIX-TF006
Entries 8.647065e+07
TOF MULT
TOF tray

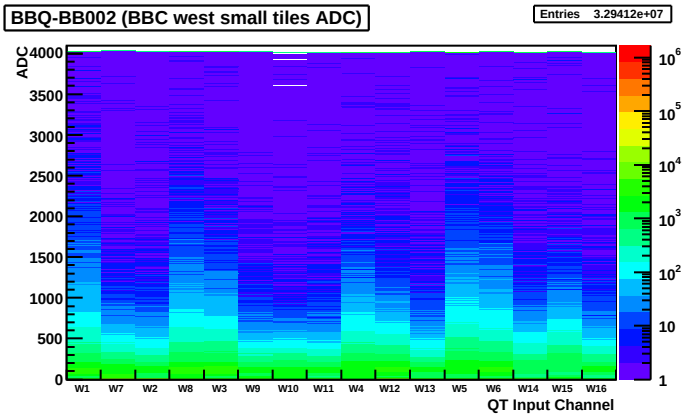

MIX-TF101
Entries 1.235295e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

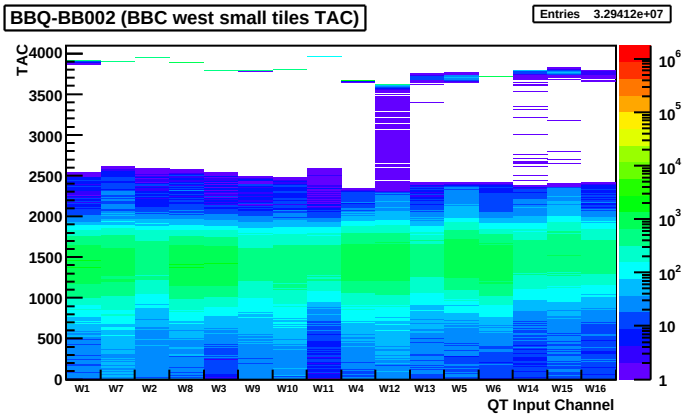

L1-TF201
Entries 2058825
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

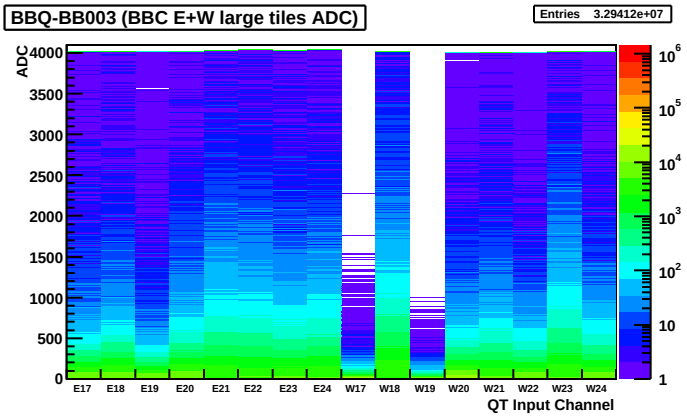

L1-TF201
Entries 1.235295e+07
Threshold bits
1
0
1.28727e+06
1.28761e+06
1.29218e+06
1.28376e+06
1.27512e+06
1.22176e+06
771553
771214
766645
775065
783710
837069
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

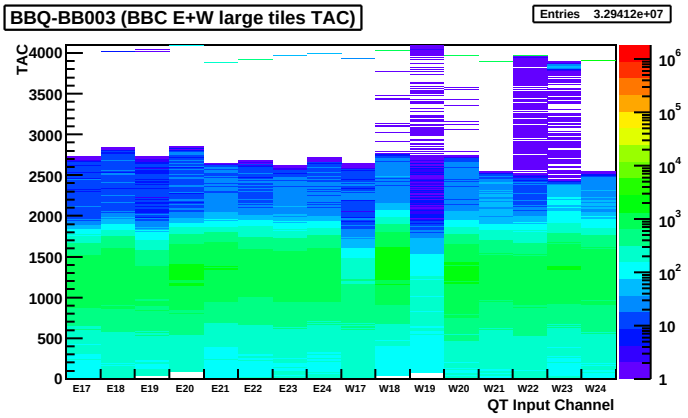

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.29412e+07
ADC
QT Input Channel

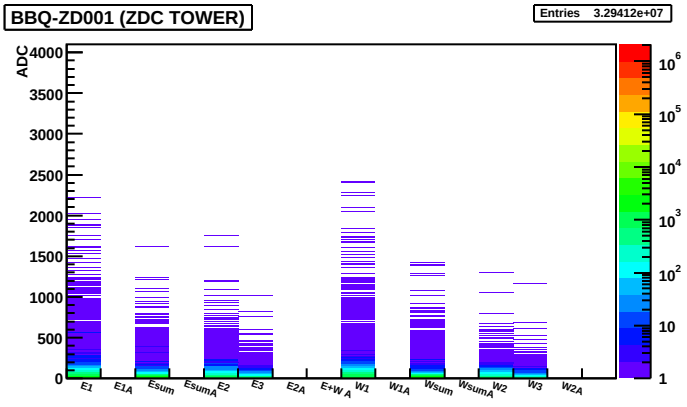

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.29412e+07
TAC
QT Input Channel

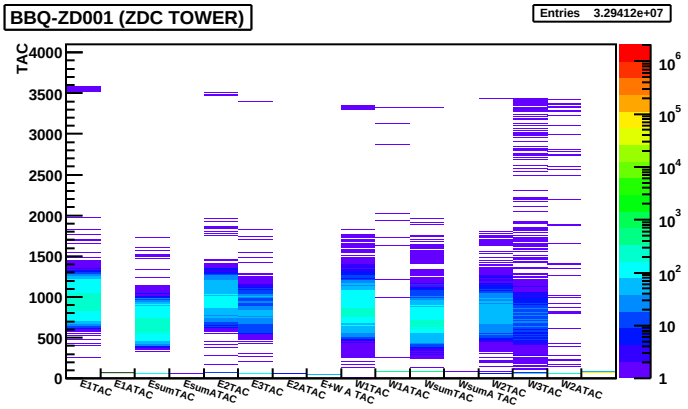

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.29412e+07
ADC
QT Input Channel

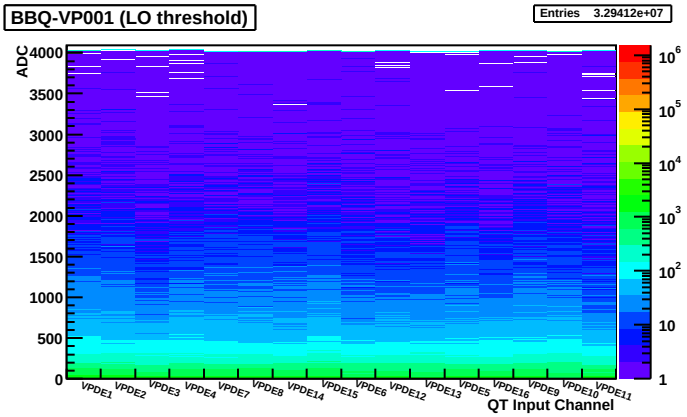

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.29412e+07
TAC
QT Input Channel

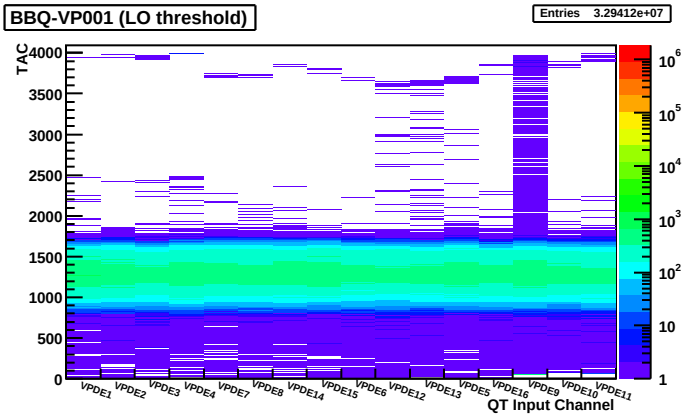

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.29412e+07
ADC
QT Input Channel

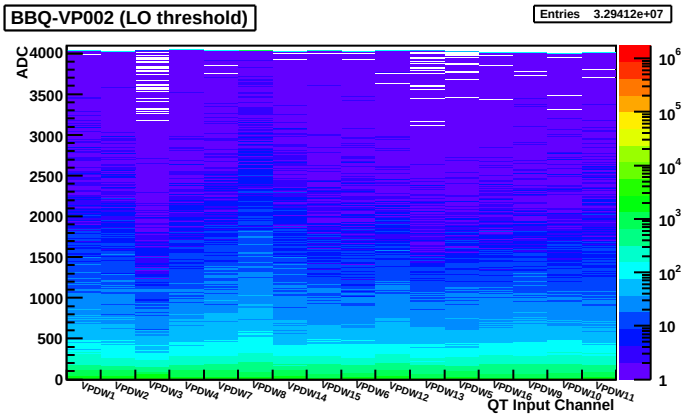

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.29412e+07
TAC
QT Input Channel

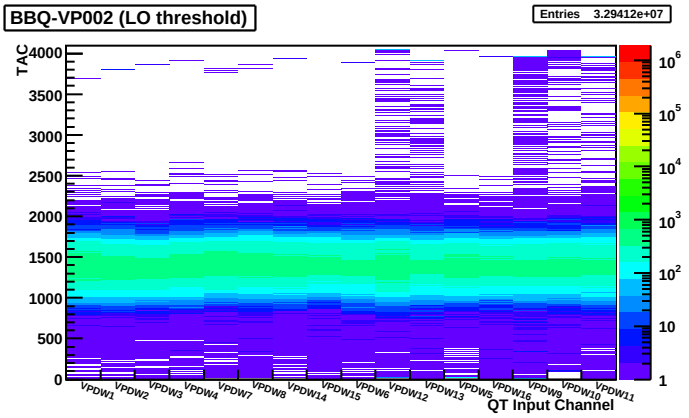

BBQ-ZD001 (ZDC TOWER)
Entries 3.29412e+07
ADC

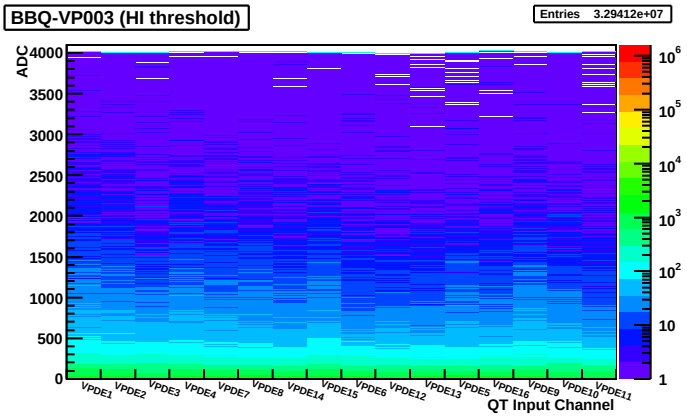

BBQ-ZD001 (ZDC TOWER)
Entries 3.29412e+07
TAC

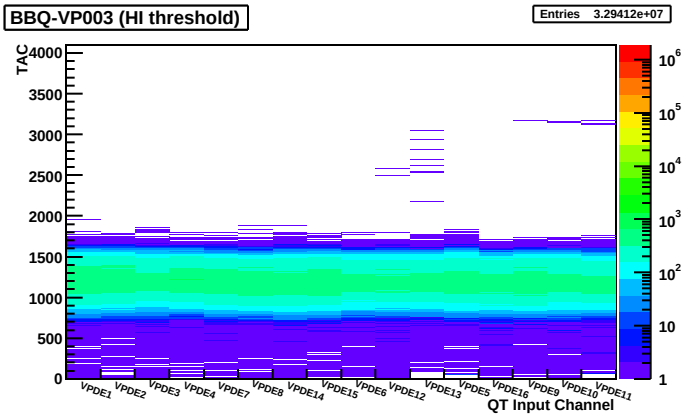

BBQ-VP001 (LO threshold)
Entries 3.29412e+07
ADC
QT Input Channel

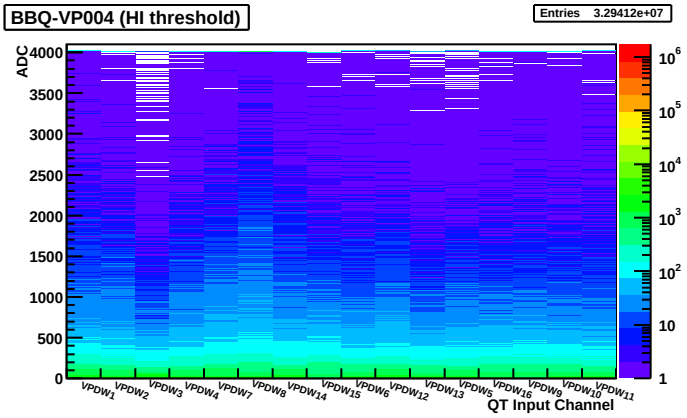

BBQ-VP001 (LO threshold)
Entries 3.29412e+07
TAC
QT Input Channel

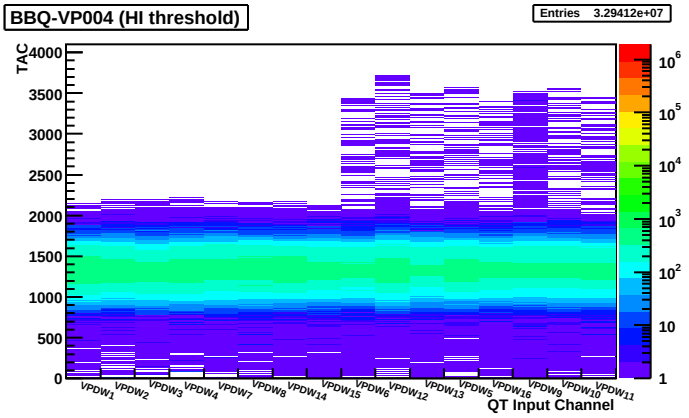

BBQ-VP002 (LO threshold)
Entries 3.29412e+07
ADC
QT Input Channel

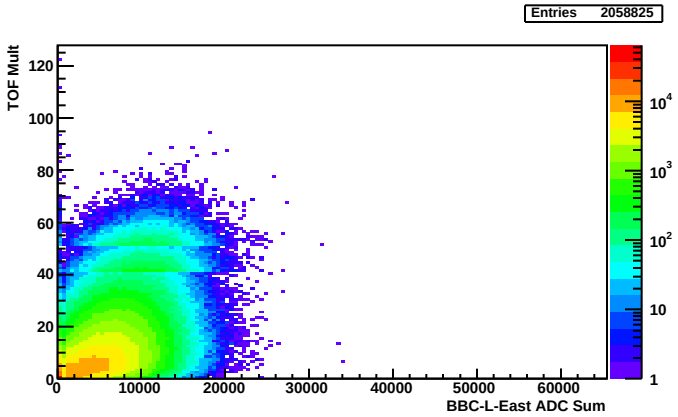

BBQ-VP002 (LO threshold)
Entries 3.29412e+07
TAC
QT Input Channel

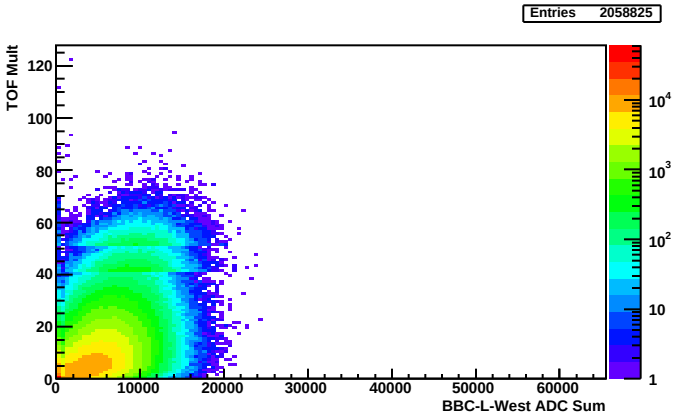

BBQ-VP003 (HI threshold)
Entries 3.29412e+07
ADC
QT Input Channel

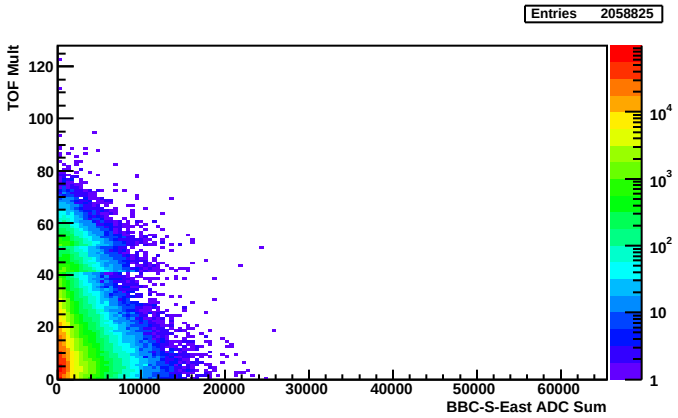

BBQ-VP003 (HI threshold)
Entries 3.29412e+07
TAC
QT Input Channel

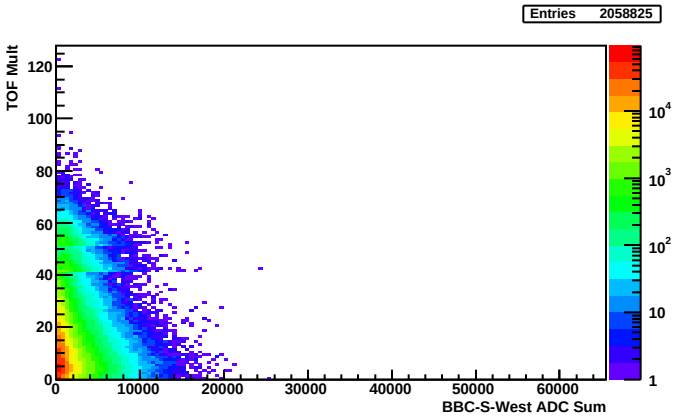

BBQ-VP004 (HI threshold)
Entries 3.29412e+07
ADC
QT Input Channel

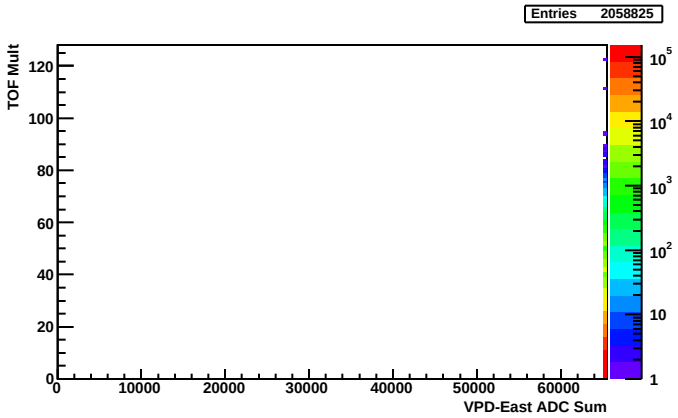

BBQ-VP004 (HI threshold)
Entries 3.29412e+07
TAC
QT Input Channel

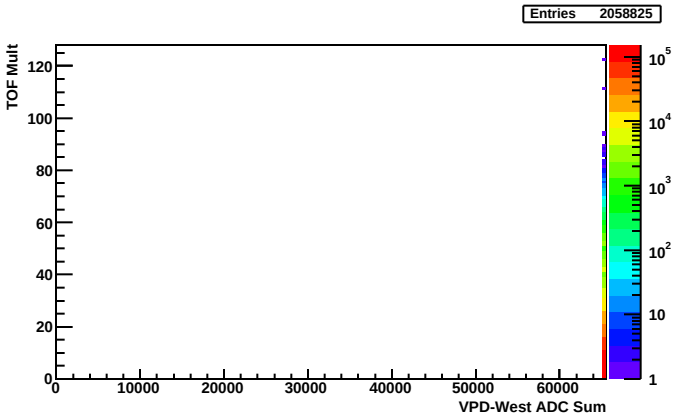

Entries 2058825
TOF Mult
BBC-L-East ADC Sum

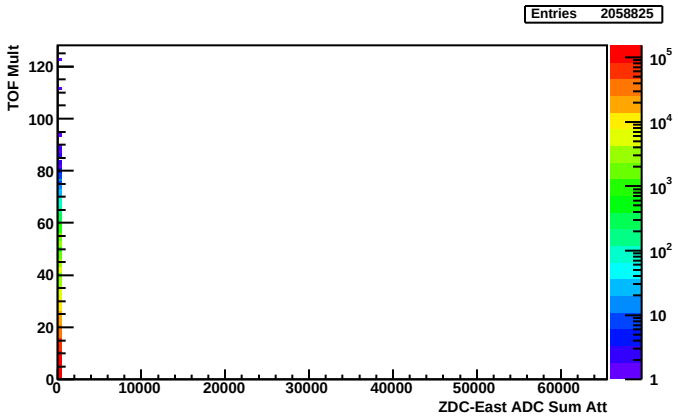

Entries 2058825
TOF Mult
BBC-L-West ADC Sum

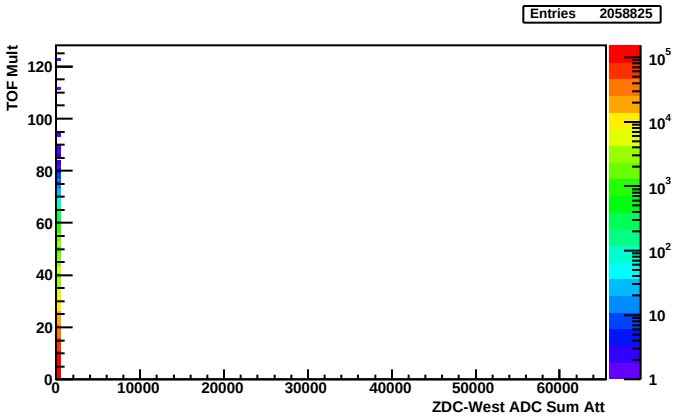

Entries 2058825
TOF Mult
BBC-S-East ADC Sum

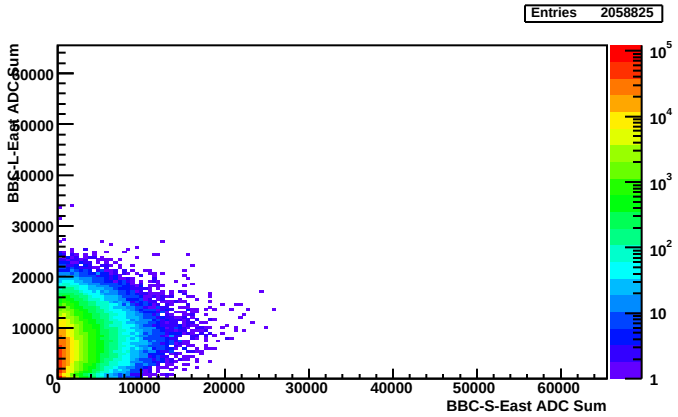

Entries 2058825
TOF Mult
BBC-S-West ADC Sum

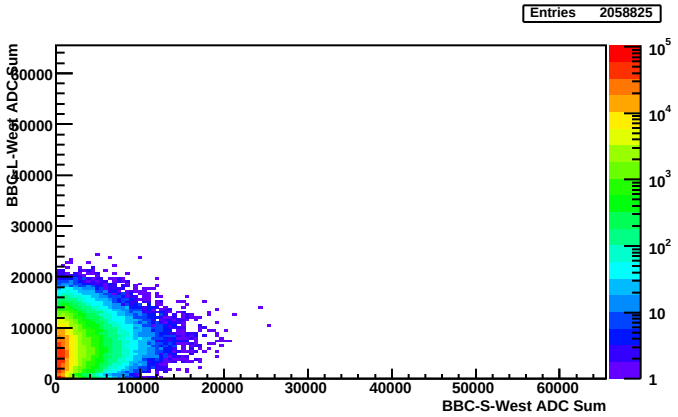

Entries 2058825
TOF Mult
VPD-East ADC Sum

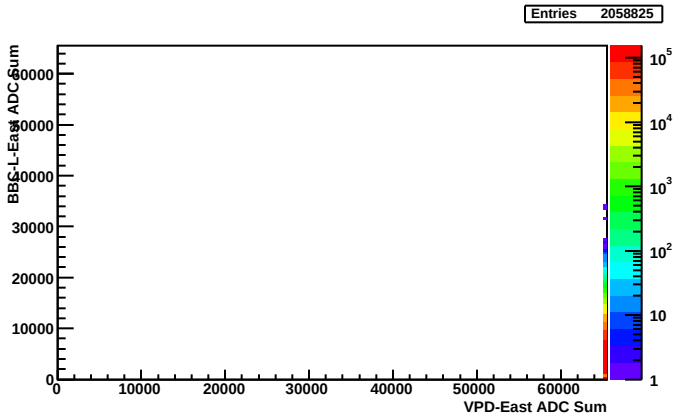

Entries 2058825
TOF Mult
VPD-West ADC Sum

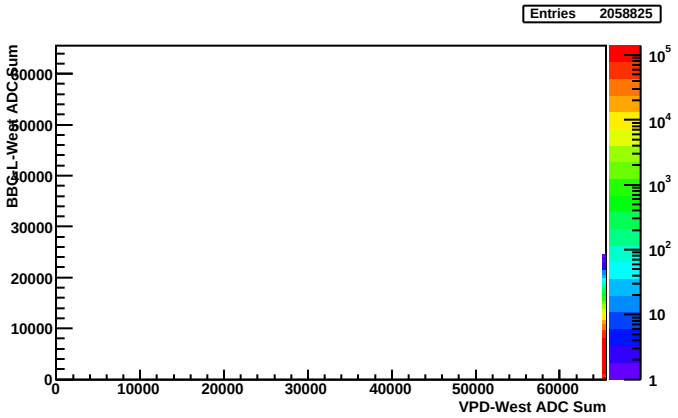

Entries 2058825
TOF Mult
ZDC-East ADC Sum Att

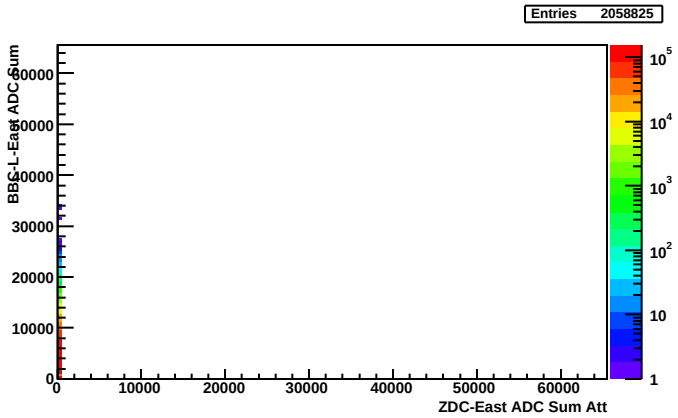

Entries 2058825
TOF Mult
ZDC-West ADC Sum Att


Entries 2058825
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 2058825
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 2058825
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 2058825
BBC-L-West ADC Sum
VPD-West ADC Sum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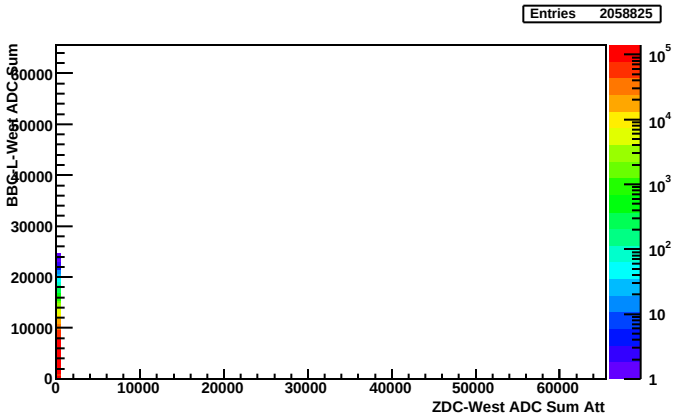
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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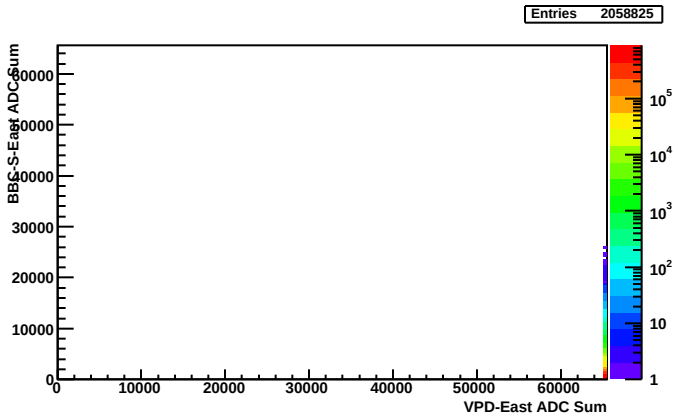
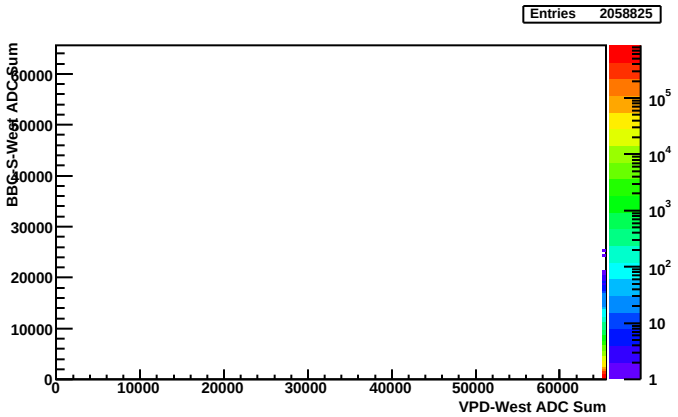
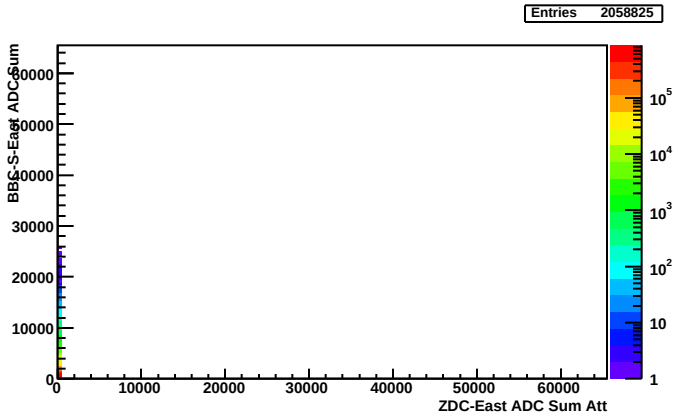
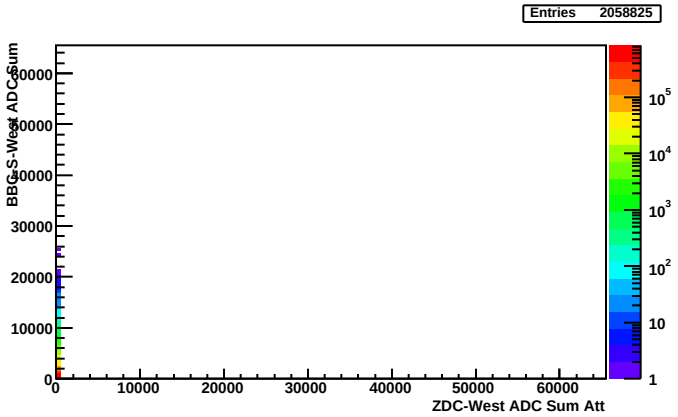
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2058825
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2058825
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2058825
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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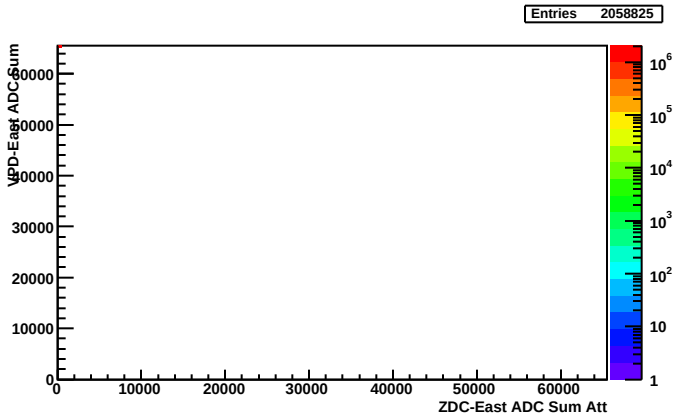
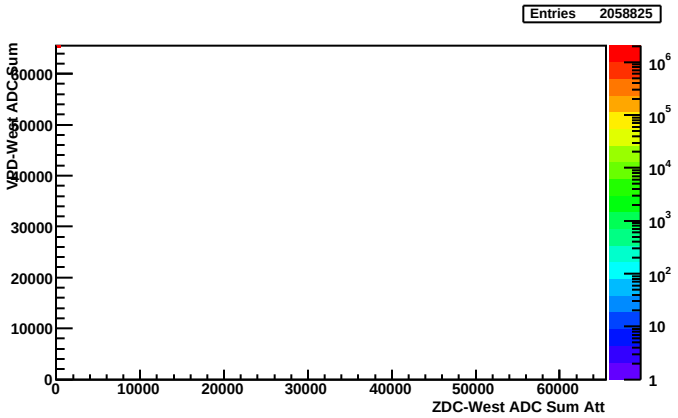
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2058825
VPD-East ADC Sum
ZDC-East ADC Sum Att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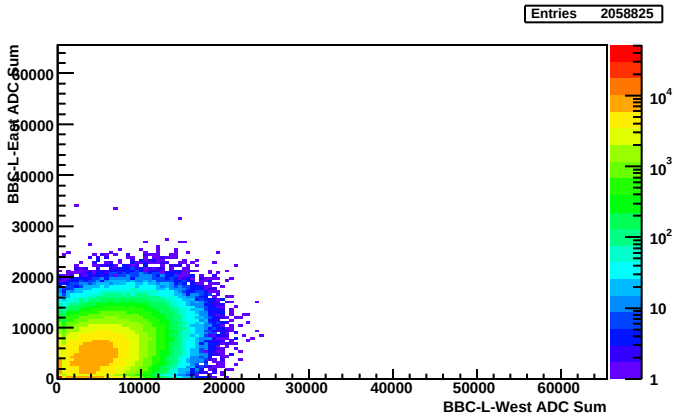
VPD-West ADC Sum
ZDC-West ADC Sum Att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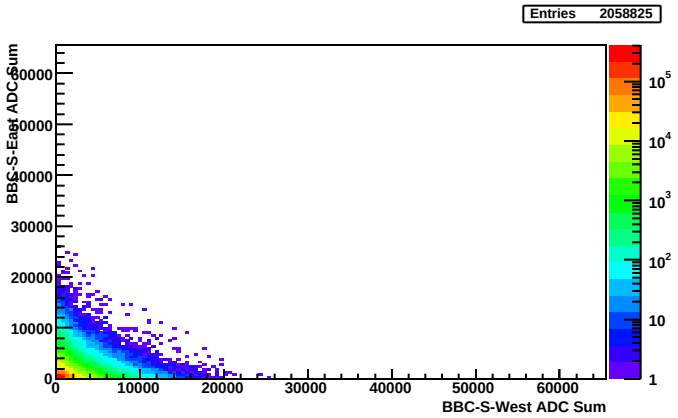
BBC-L-East ADC Sum
BBC-L-West ADC Sum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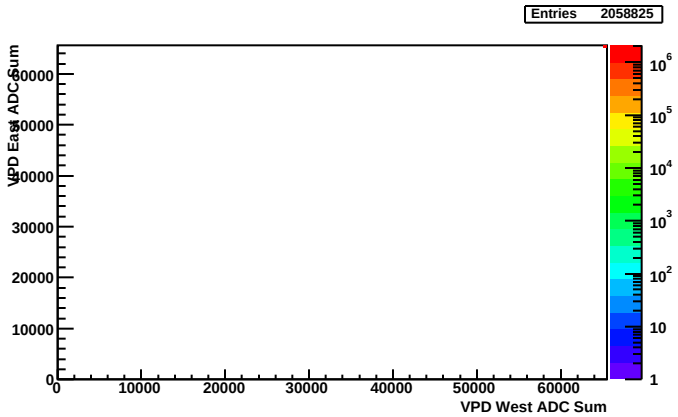
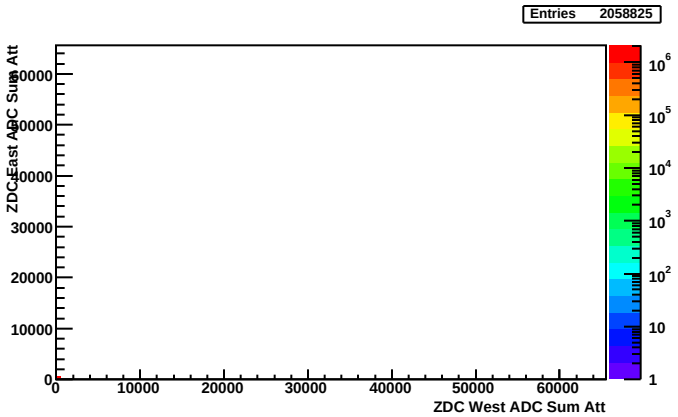
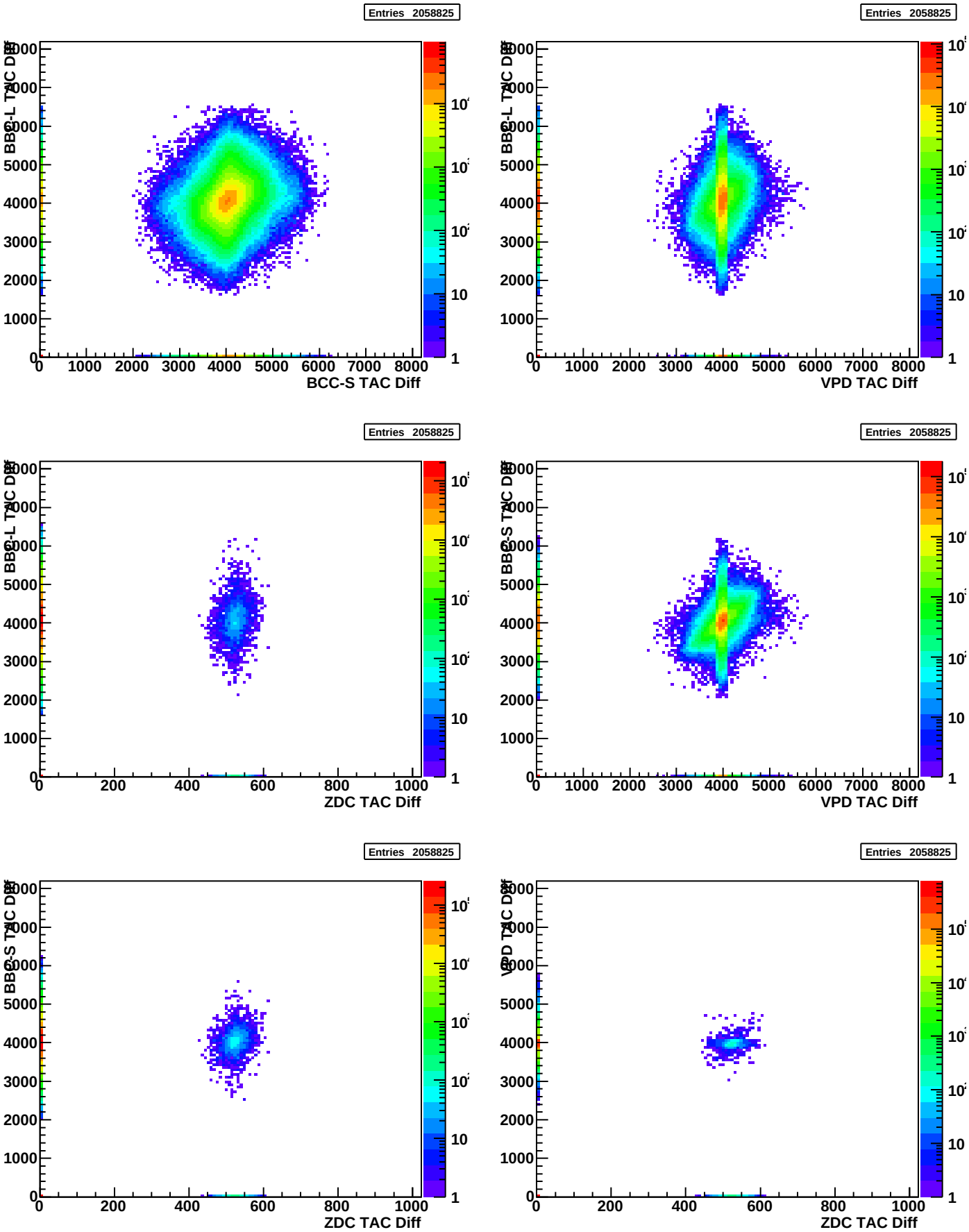
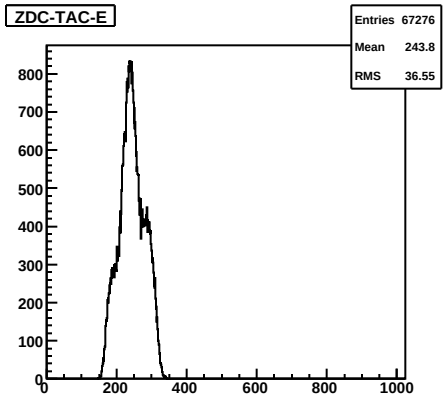
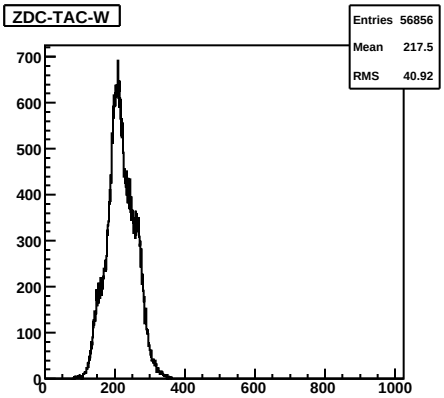
BBC-S-East ADC Sum
BBC-S-West ADC Sum

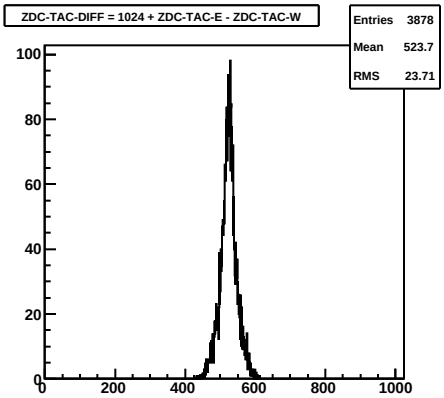

Entries 2058825
VPD East ADC Sum
VPD West ADC Sum

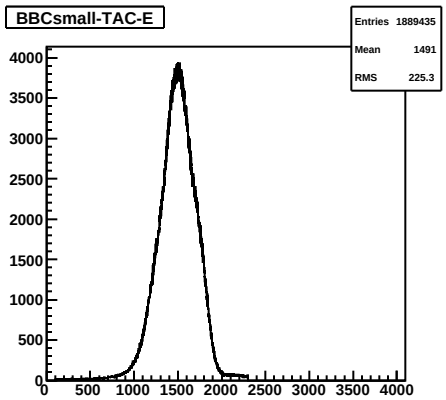

Entries 2058825
ZDC East ADC Sum Att
ZDC West ADC Sum Att

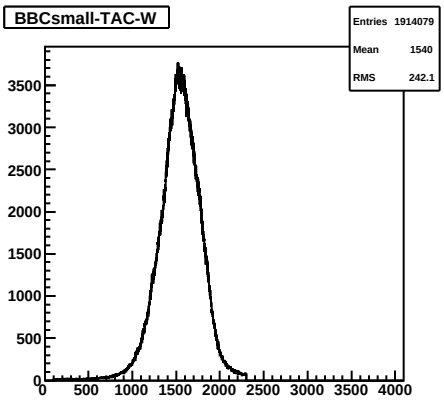

Entries 2058825
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2058825
BBC-L TAC Diff
VPD TAC Diff
Entries 2058825
BBC-L TAC Diff
ZDC TAC Diff
Entries 2058825
BBC-S TAC Diff
VPD TAC Diff
Entries 2058825
BBC-S TAC Diff
ZDC TAC Diff
Entries 2058825
VPD TAC Diff
ZDC TAC Diff

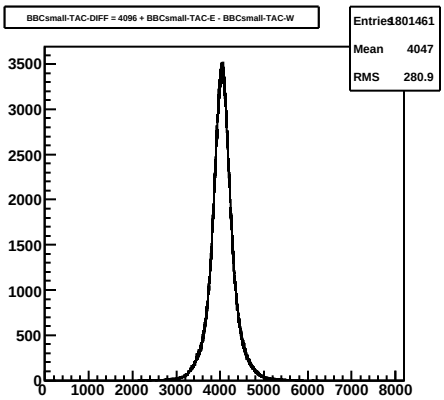

ZDC-TAC-E
Entries 67276
Mean 243.8
RMS 36.55

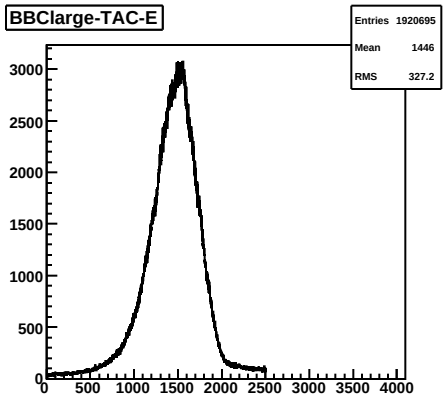

ZDC-TAC-W
Entries 56856
Mean 217.5
RMS 40.92

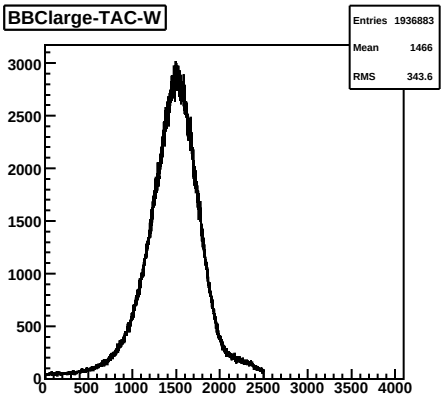

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3878
Mean 523.7
RMS 23.71

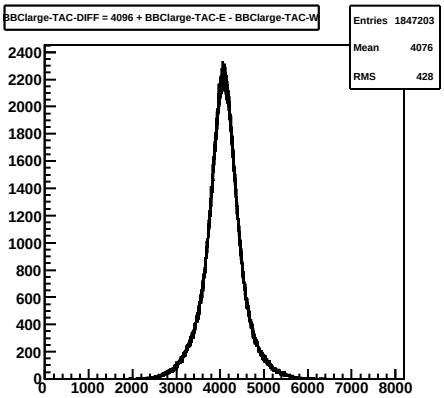

BBCsmall-TAC-E
Entries 1889435
Mean 1491
RMS 225.3

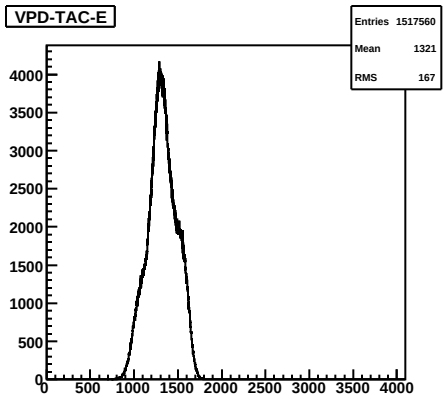

BBCsmall-TAC-W
Entries 1914079
Mean 1540
RMS 242.1

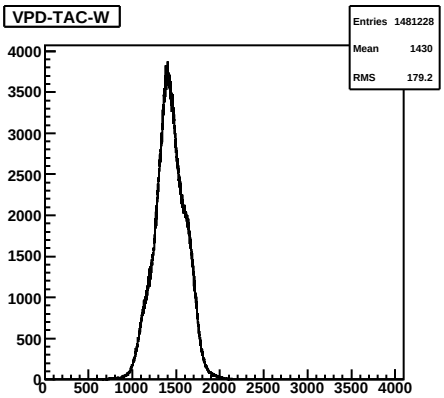

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1801461
Mean 4047
RMS 280.9

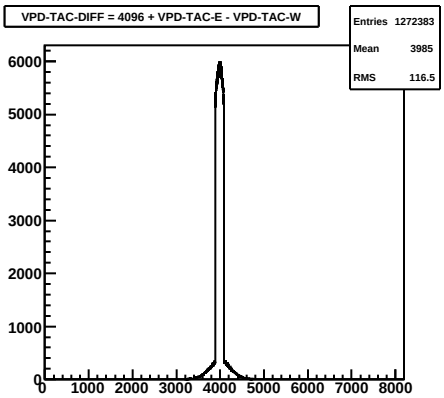

BBClarge-TAC-E
Entries 1920695
Mean 1446
RMS 327.2

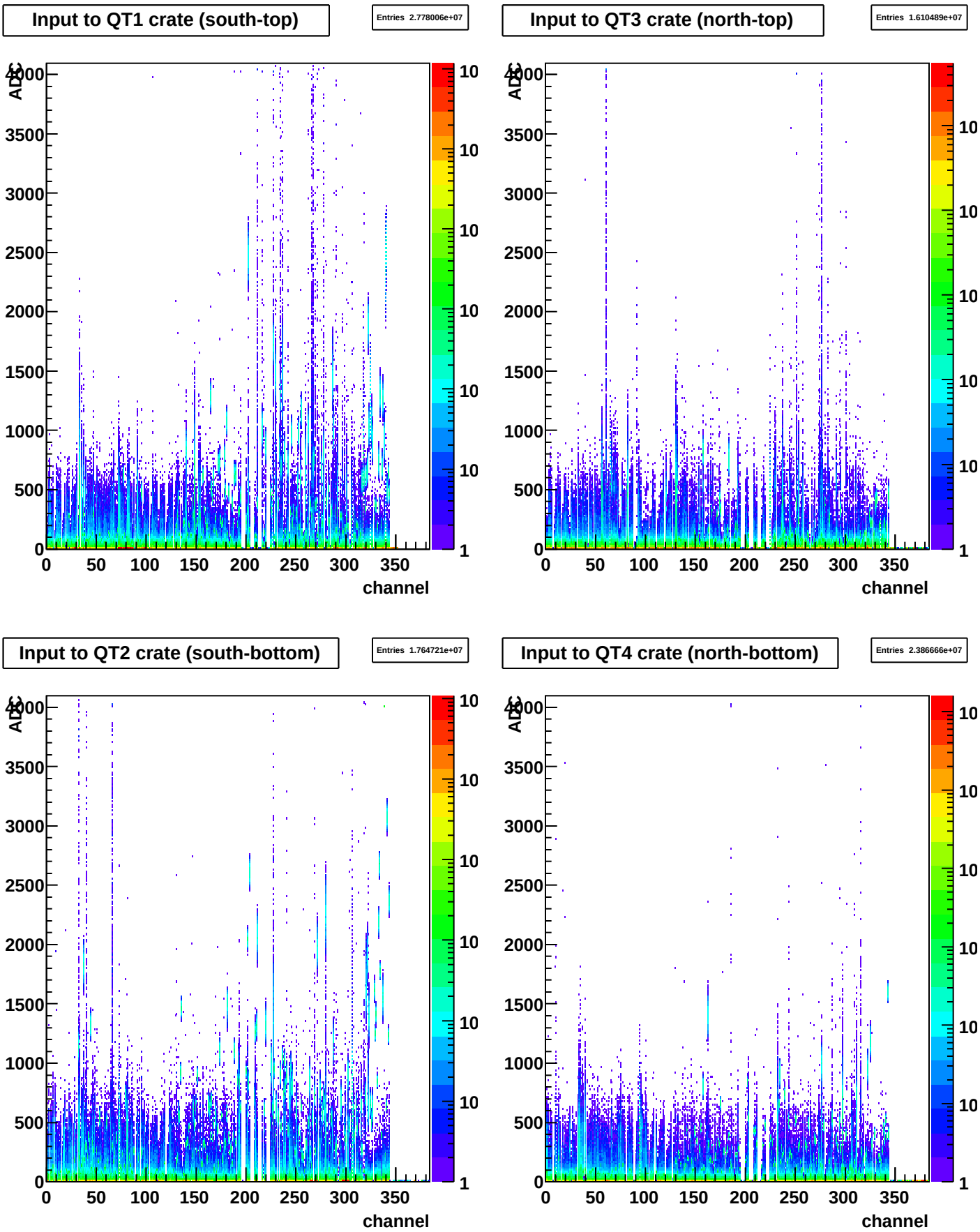

BBClarge-TAC-W
Entries 1936883
Mean 1466
RMS 343.6

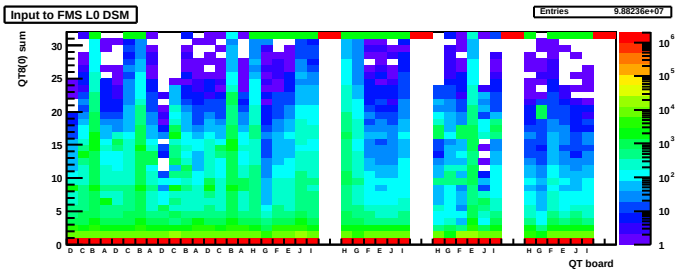

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1847203
Mean 4076
RMS 428

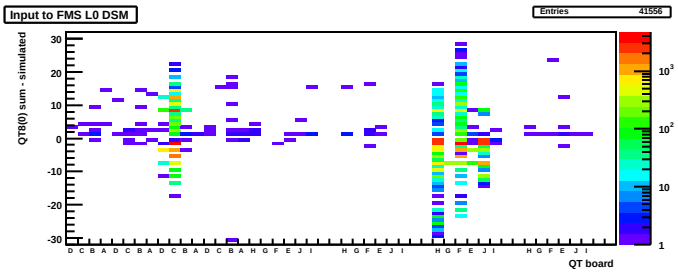

VPD-TAC-E
Entries 1517560
Mean 1321
RMS 167

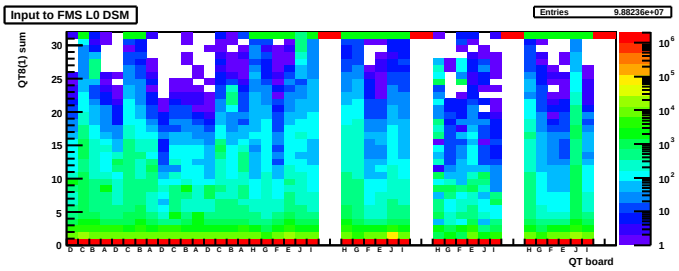

VPD-TAC-W
Entries 1481228
Mean 1430
RMS 179.2

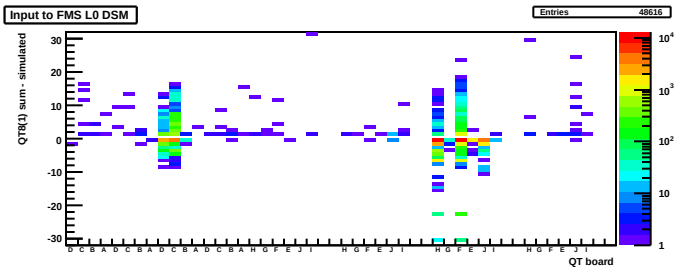

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1272383
Mean 3985
RMS 116.5

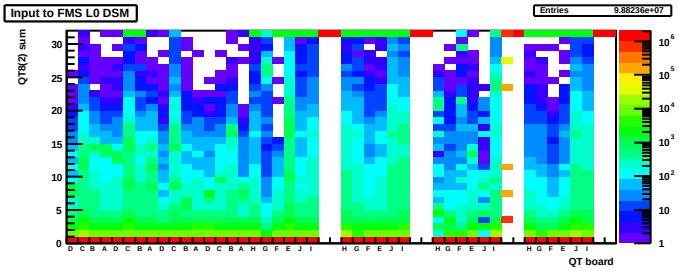

Input to QT1 crate (south-top)
Entries 2.778006e+07
Input to QT3 crate (north-top)
Entries 1.610489e+07
Input to QT2 crate (south-bottom)
Entries 1.764721e+07
Input to QT4 crate (north-bottom)
Entries 2.386666e+07
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

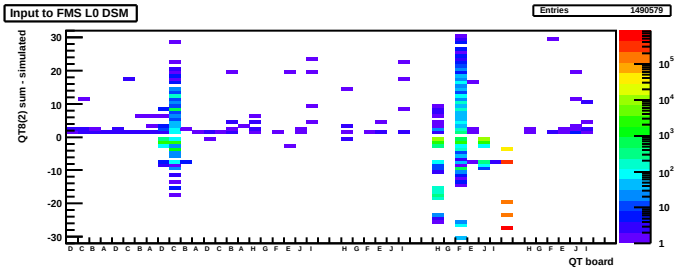

Input to FMS L0 DSM
Entries 9.88236e+07
QT8(0) sum
QT board

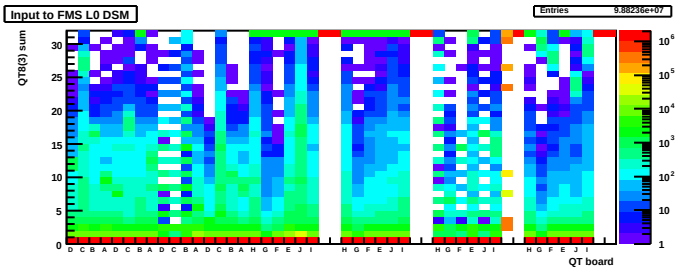

Input to FMS L0 DSM
Entries 41556
QT8(0) sum - simulated
QT board

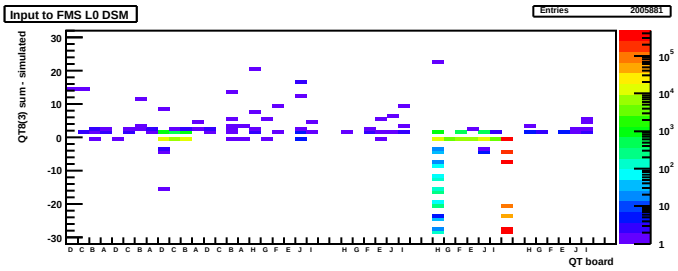

Input to FMS L0 DSM
Entries 9.88236e+07
QT8(1) sum
QT board

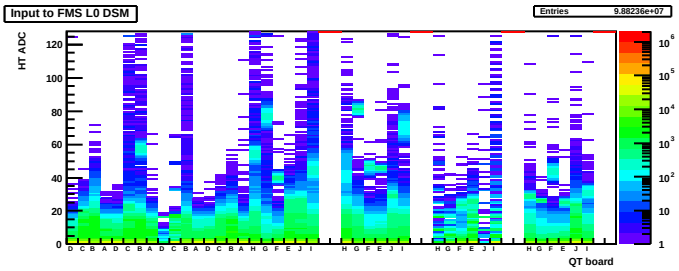

Input to FMS L0 DSM
Entries 48616
QT8(1) sum - simulated
QT board

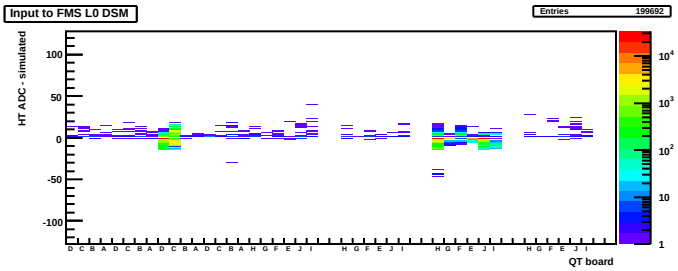

Input to FMS L0 DSM
Entries 9.88236e+07
QT8(2) sum
QT board

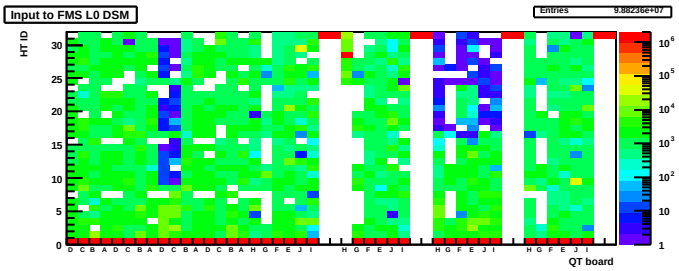

Input to FMS L0 DSM
Entries 1490579
QT8(2) sum - simulated
QT board

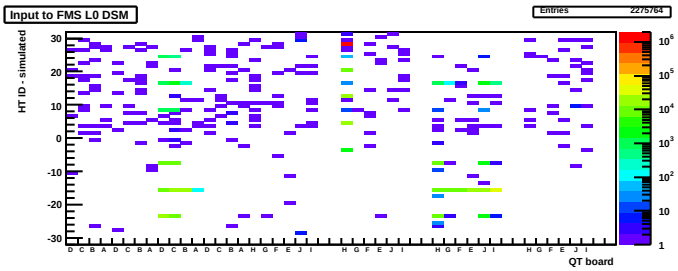

Input to FMS L0 DSM
Entries 9.88236e+07
QT8(3) sum
QT board

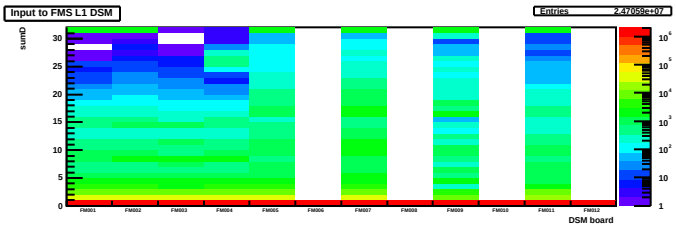

Input to FMS L0 DSM
Entries 2005881
QT8(3) sum - simulated
QT board

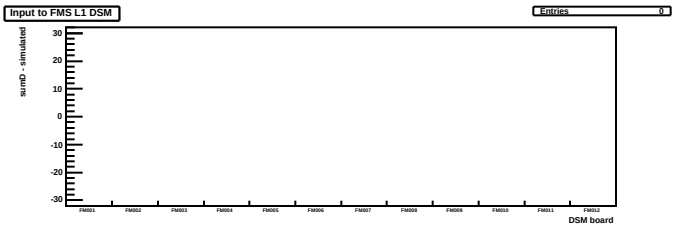

Input to FMS L0 DSM
Entries 9.88236e+07
HT ADC
QT board

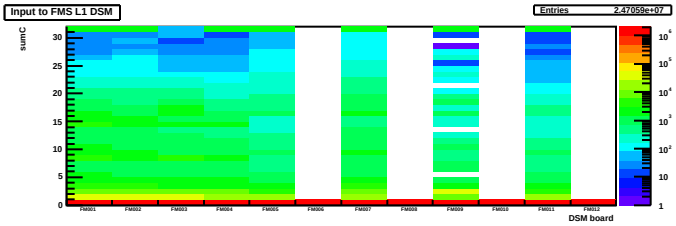

Input to FMS L0 DSM
Entries 199692
HT ADC - simulated
QT board

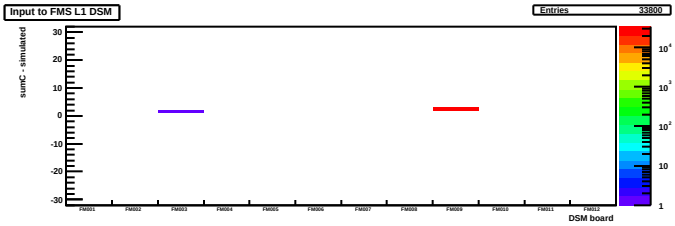

Input to FMS L0 DSM
Entries 9.88236e+07
HT ID
QT board

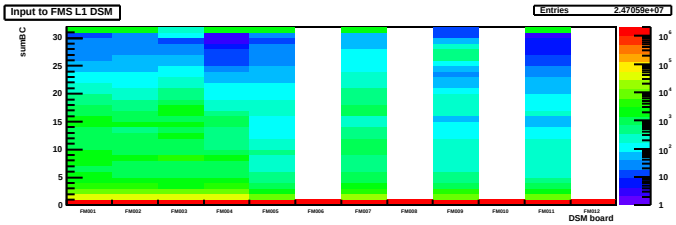

Input to FMS L0 DSM
Entries 2275764
HT ID - simulated
QT board

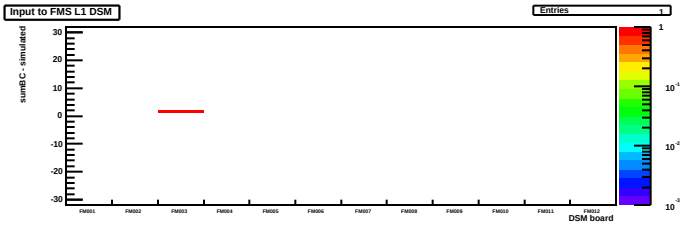

Input to FMS L1 DSM
Entries 2.47059e+07
sumD
DSM board

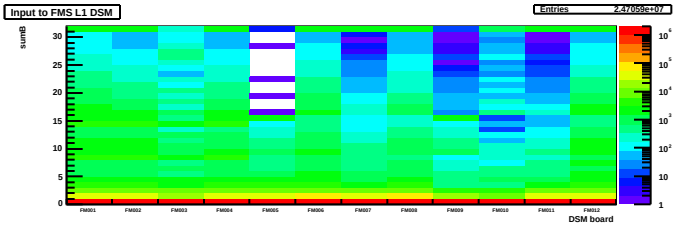

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

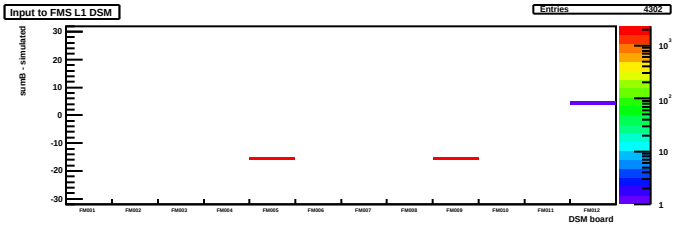

Input to FMS L1 DSM
Entries 2.47059e+07
sumC
DSM board

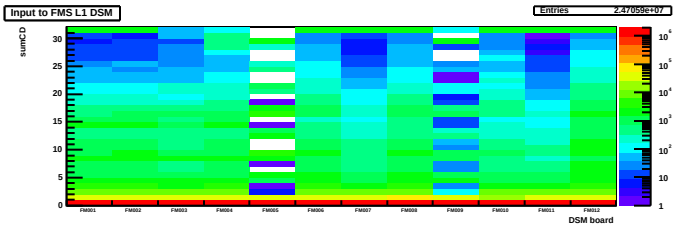

Input to FMS L1 DSM
Entries 33800
sumC - simulated
DSM board

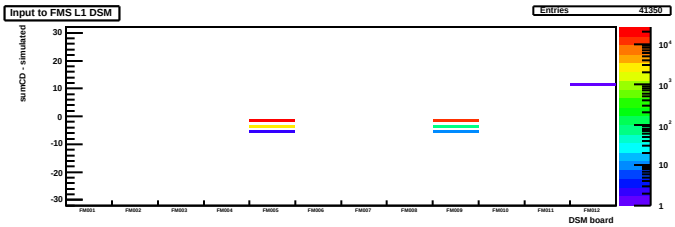

Input to FMS L1 DSM
Entries 2.47059e+07
sumBC
DSM board

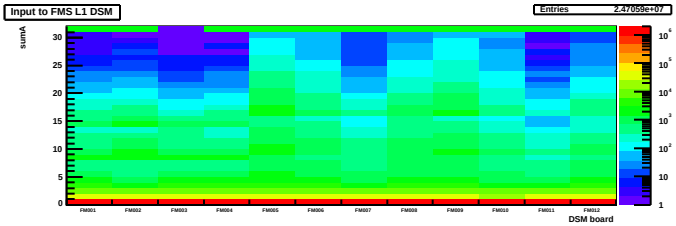

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

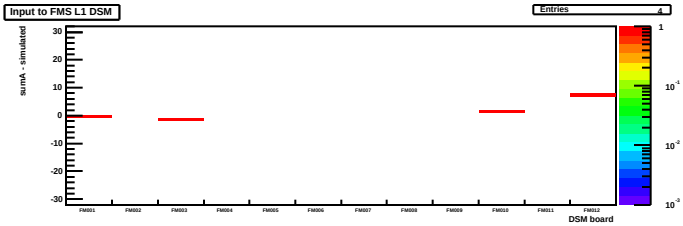

Input to FMS L1 DSM
Entries 2.47059e+07
sumB
DSM board

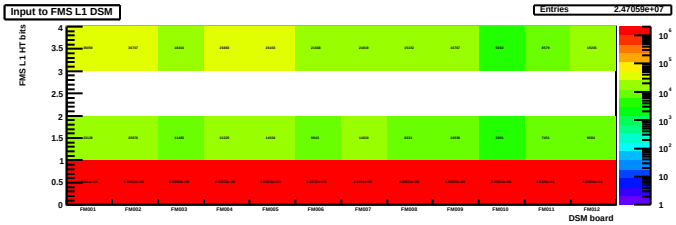

Input to FMS L1 DSM
Entries 4302
sumB - simulated
DSM board

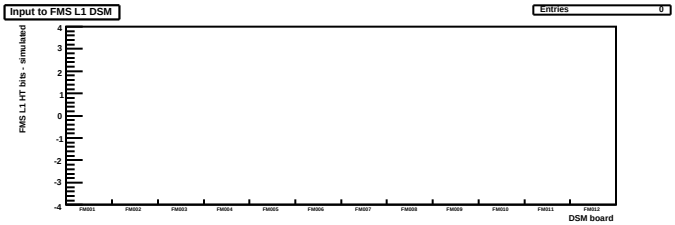

Input to FMS L1 DSM
Entries 2.47059e+07
sumCD
DSM board

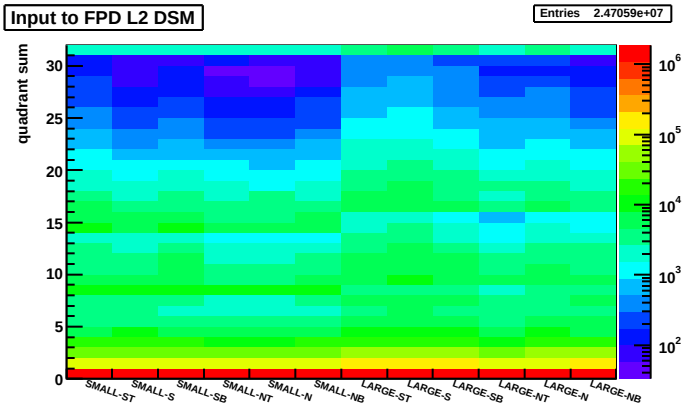

Input to FMS L1 DSM
Entries 41350
sumCD - simulated
DSM board

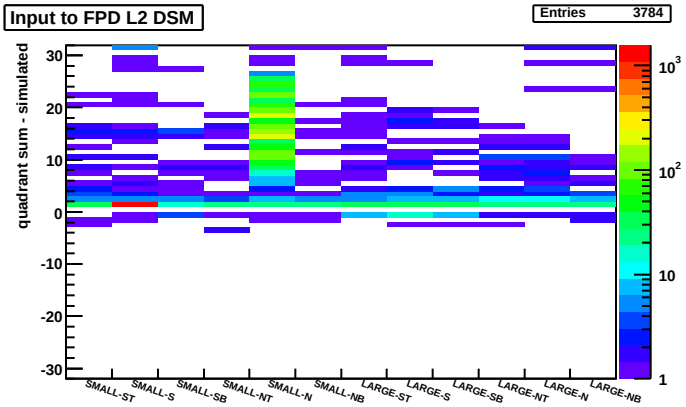

Input to FMS L1 DSM
Entries 2.47059e+07
sumA
DSM board

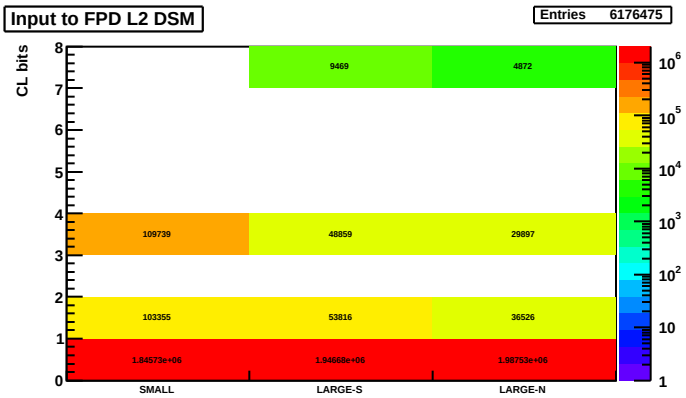

Input to FMS L1 DSM
Entries 4
sumA - simulated
DSM board

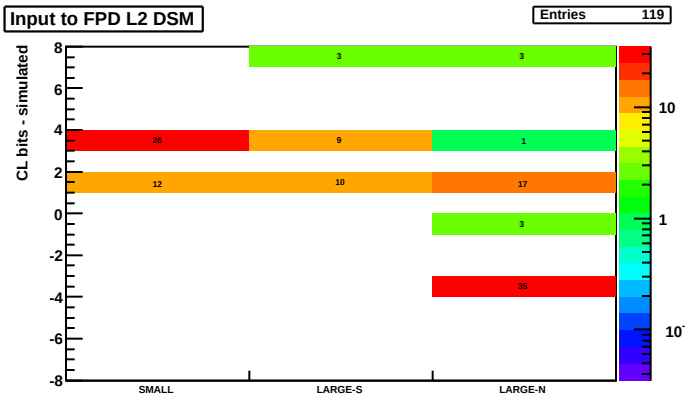

Input to FMS L1 DSM
Entries 2.47059e+07
FMS L1 HT bits
DSM board

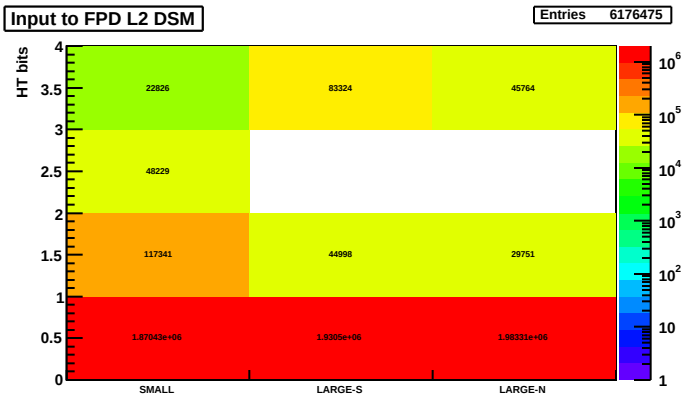

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

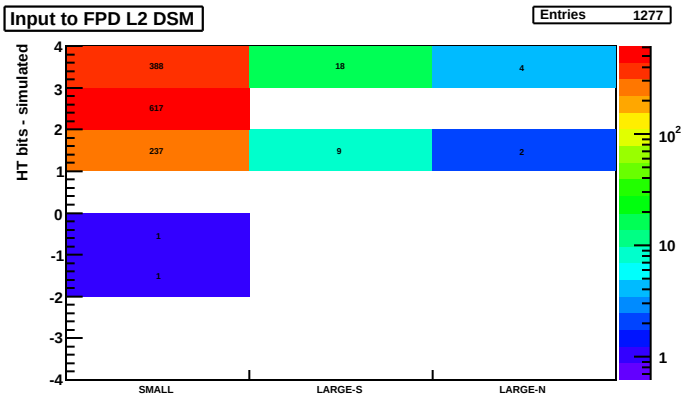

Input to FPD L2 DSM
Entries 2.47059e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

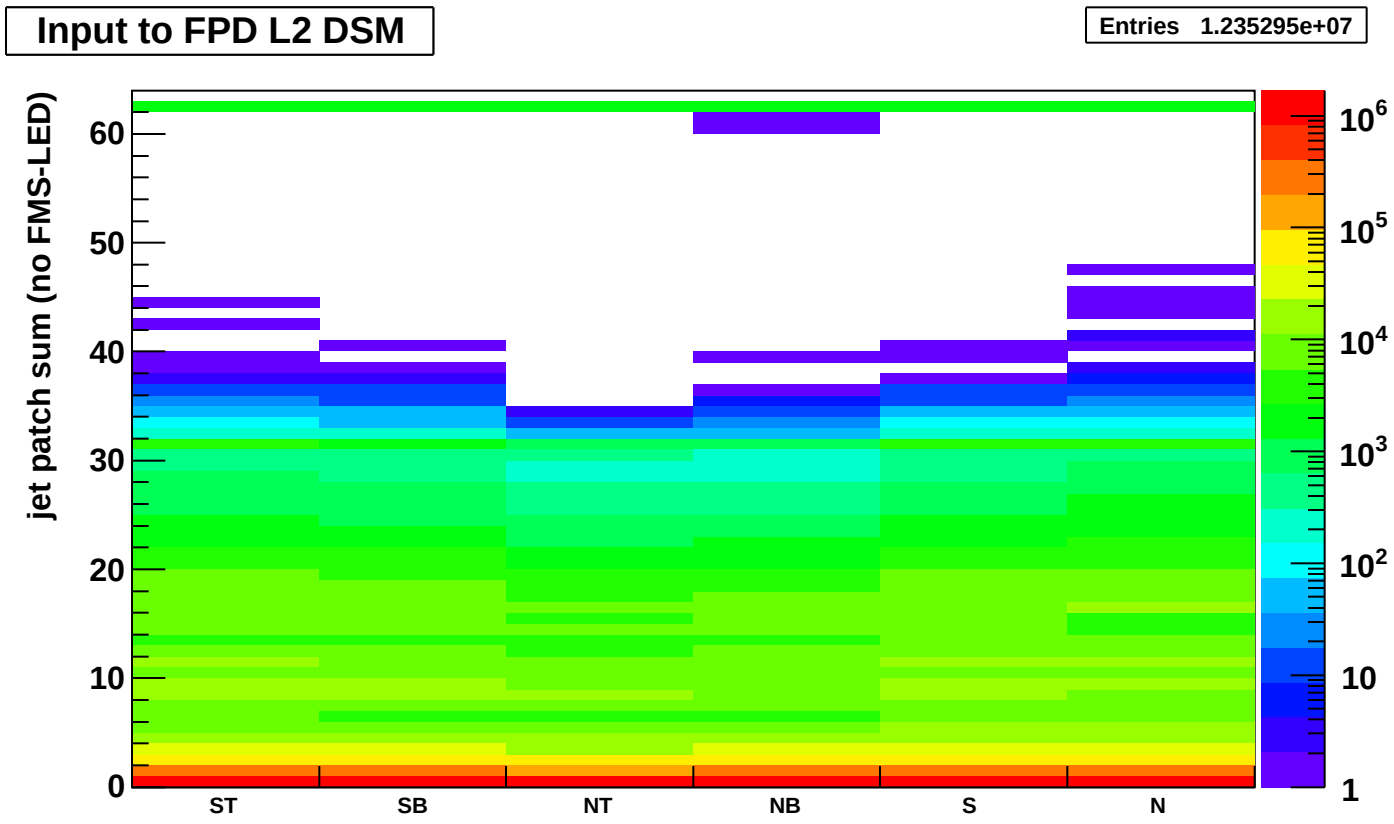

Input to FPD L2 DSM
Entries 3784
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

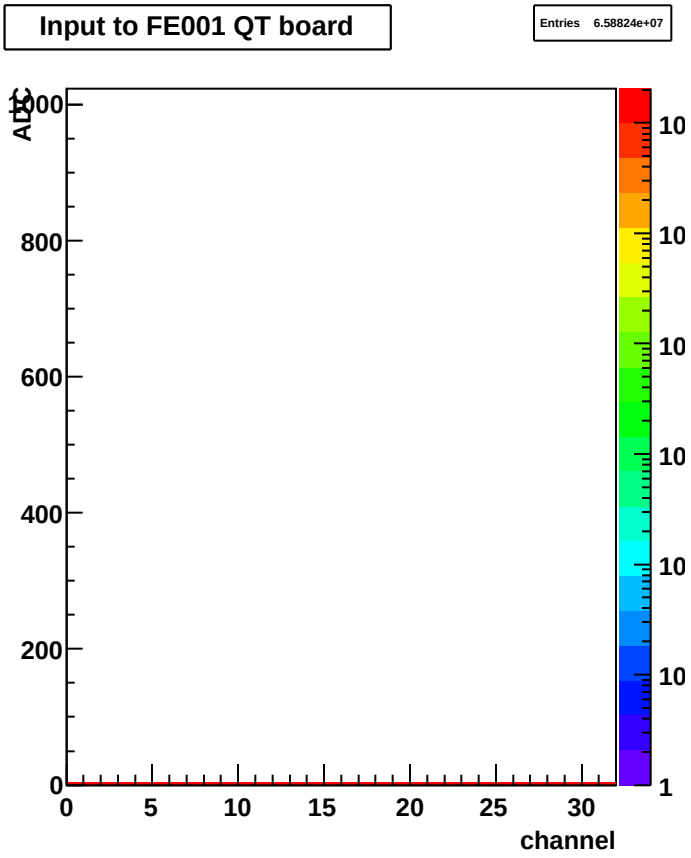

Input to FPD L2 DSM
Entries 6176475
CL bits
9469
4872
109739
48859
29897
103355
53816
36526
1.84573e+06
1.94668e+06
1.98753e+06
SMALL
LARGE-S
LARGE-N

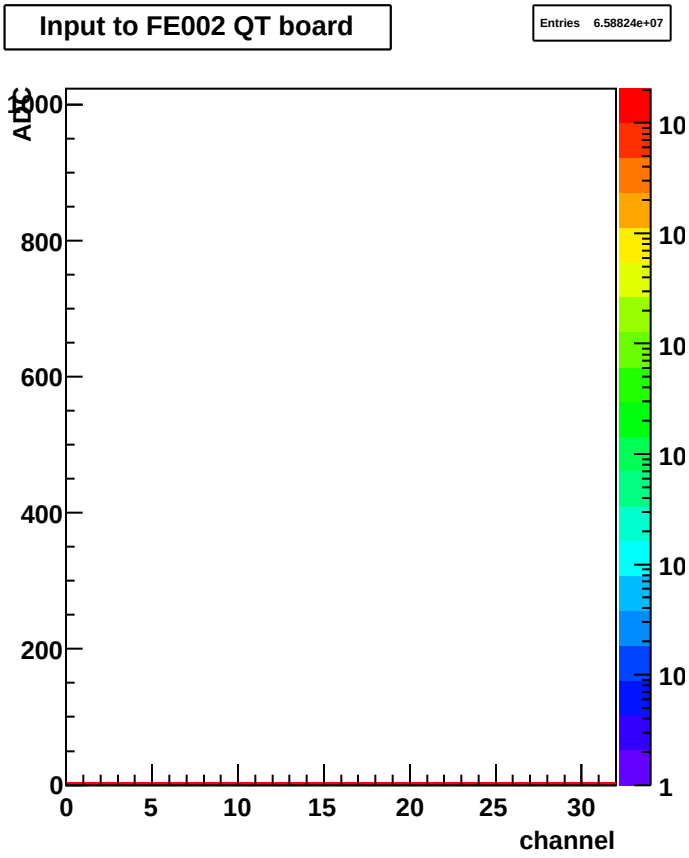

Input to FPD L2 DSM
Entries 119
CL bits - simulated
3
3
26
9
1
12
10
17
3
35
SMALL
LARGE-S
LARGE-N

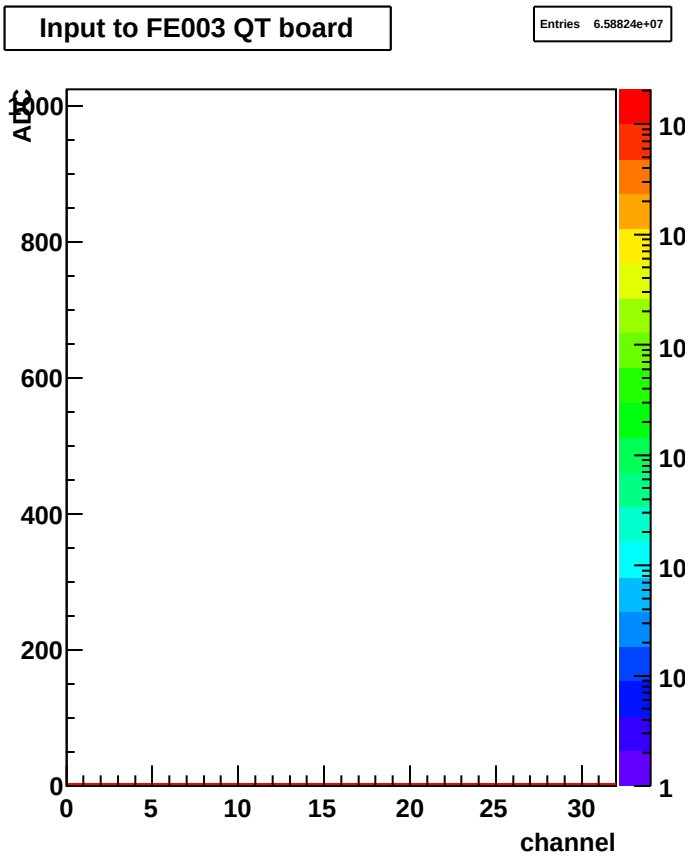

Input to FPD L2 DSM
Entries 6176475
HT bits
22826
83324
45764
48229
117341
44998
29751
1.87043e+06
1.9305e+06
1.98331e+06
SMALL
LARGE-S
LARGE-N

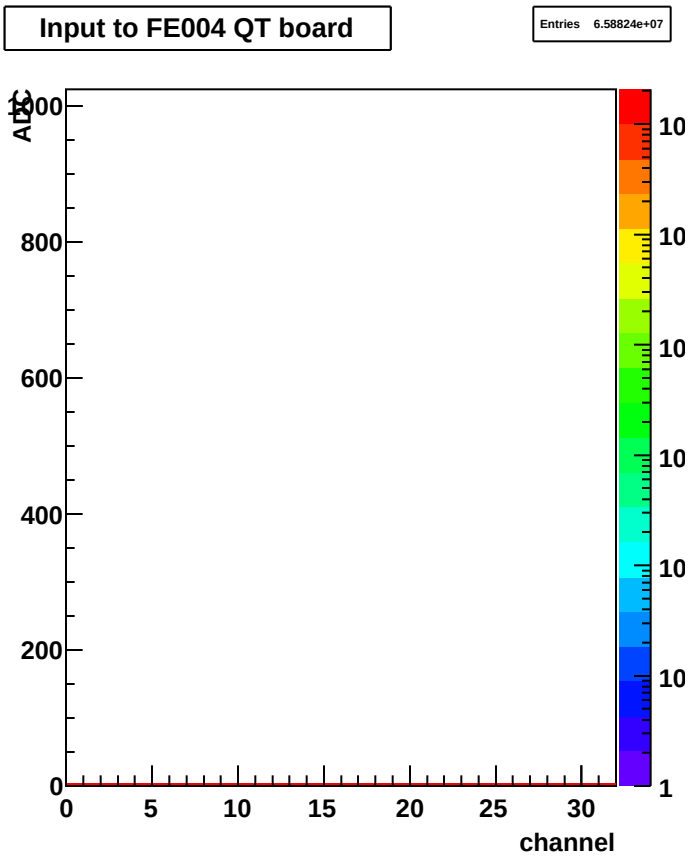

Input to FPD L2 DSM
Entries 1277
HT bits - simulated
388
18
4
617
237
9
2
1
1
SMALL
LARGE-S
LARGE-N

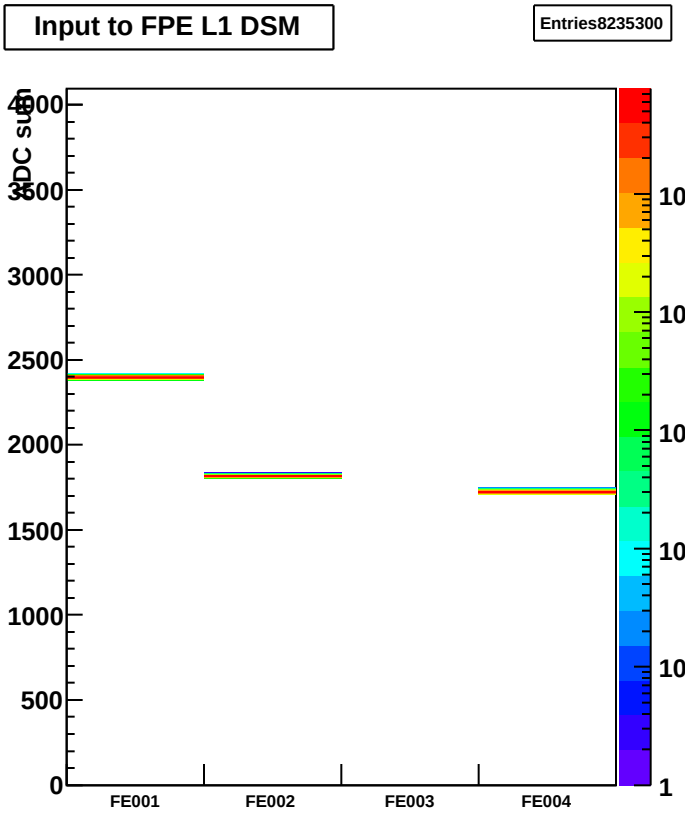

Input to FPD L2 DSM
Entries 1.235295e+07
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 6.58824e+07
ADC
channel

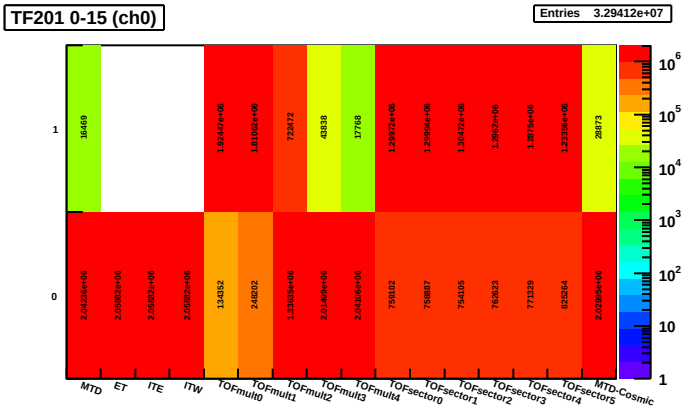

Input to FE002 QT board
Entries 6.58824e+07
ADC
channel


Input to FE003 QT board
Entries 6.58824e+07
ADC
channel

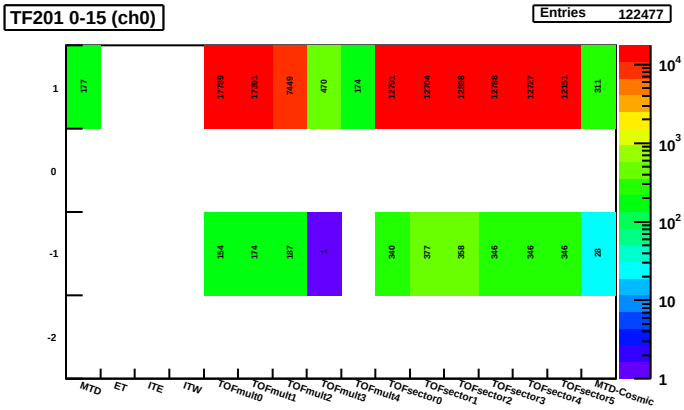

Input to FE004 QT board
Entries 6.58824e+07
ADC
channel


Input to FPE L1 DSM
Entries8235300
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

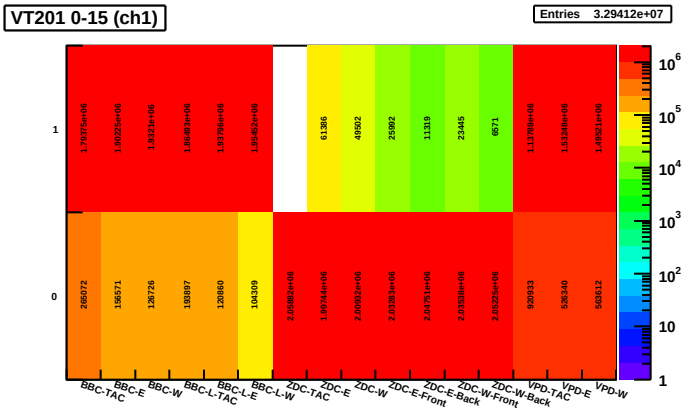
TF201 0-15 (ch0)



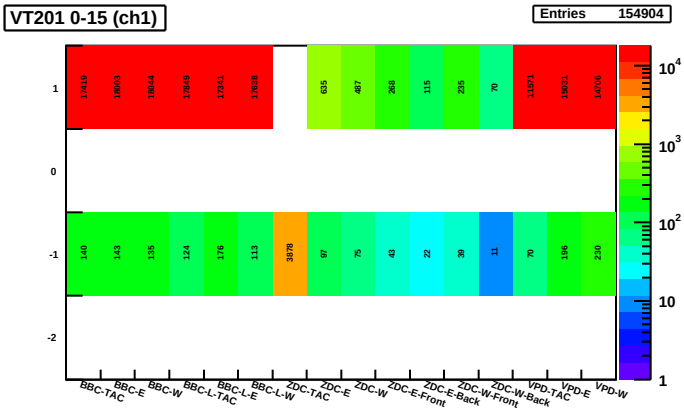
TF201 0-15 (ch0)



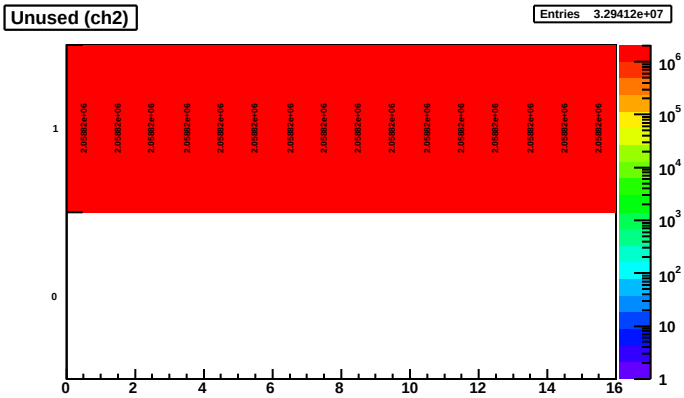
VT201 0-15 (ch1)

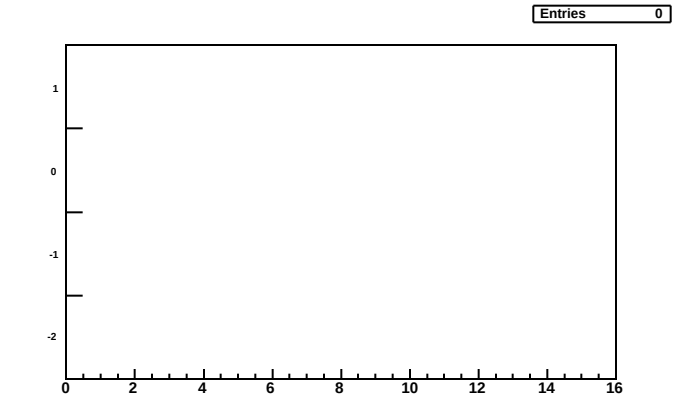


VT201 0-15 (ch1)

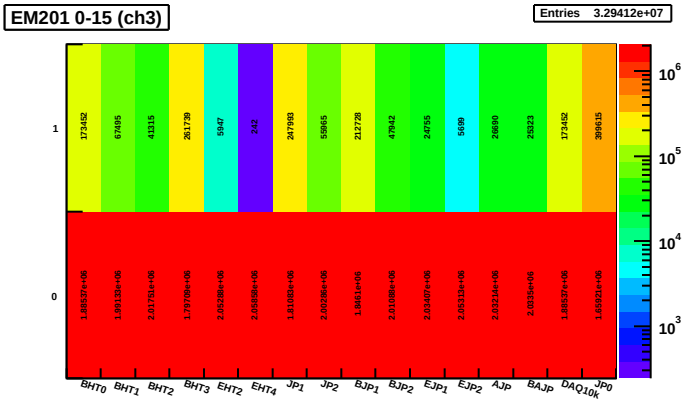


Unused (ch2)

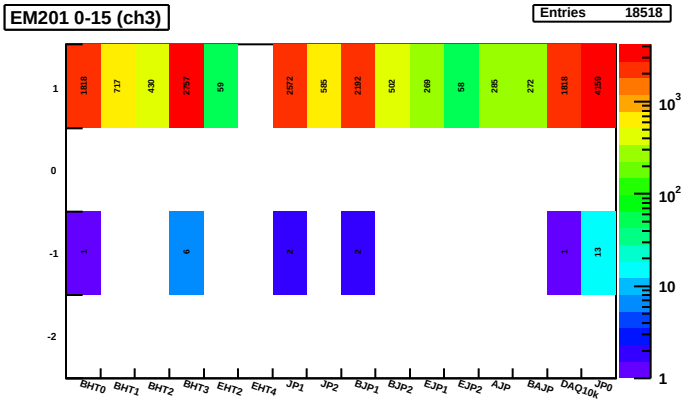




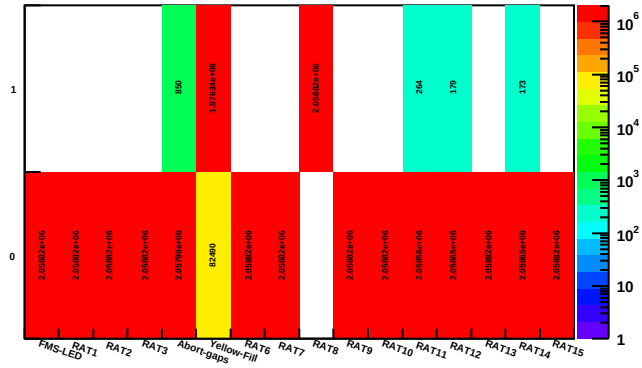
EM201 0-15 (ch3)

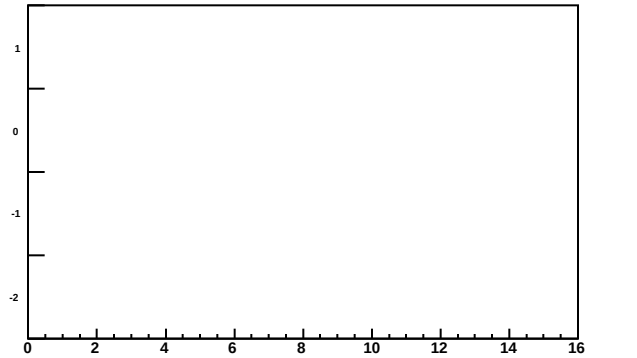


EM201 0-15 (ch3)

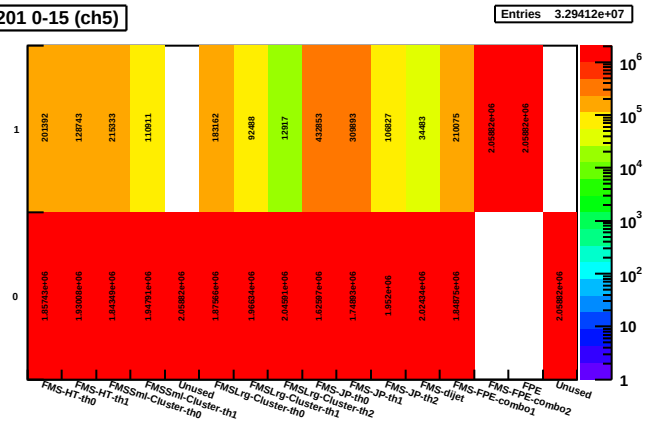


RAT board (ch4)





FP201 0-15 (ch5)



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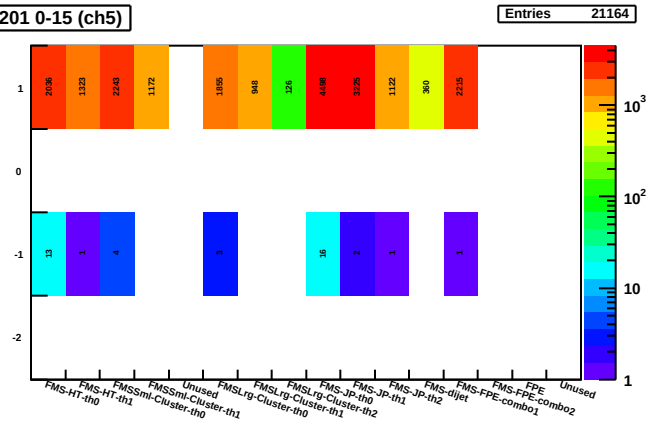

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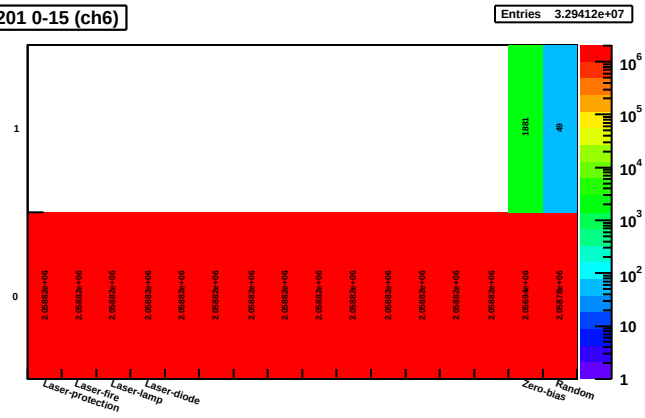

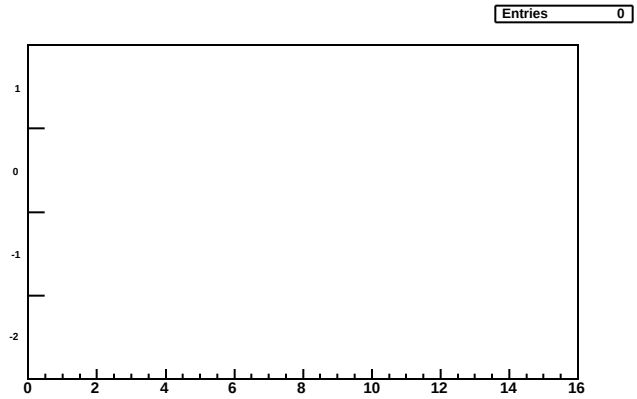
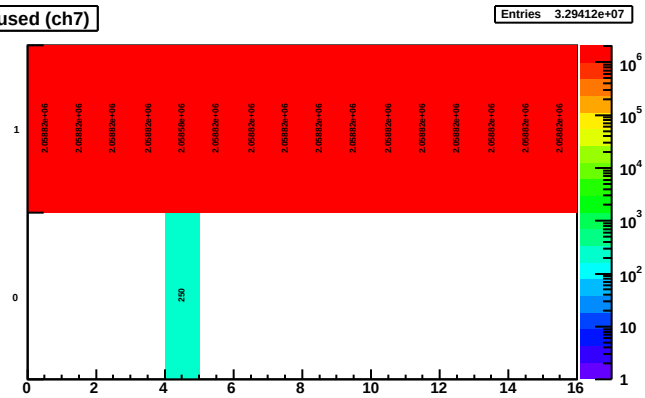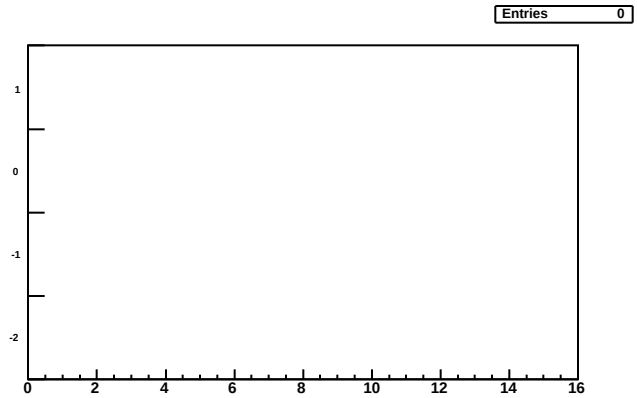
Unused (ch7)