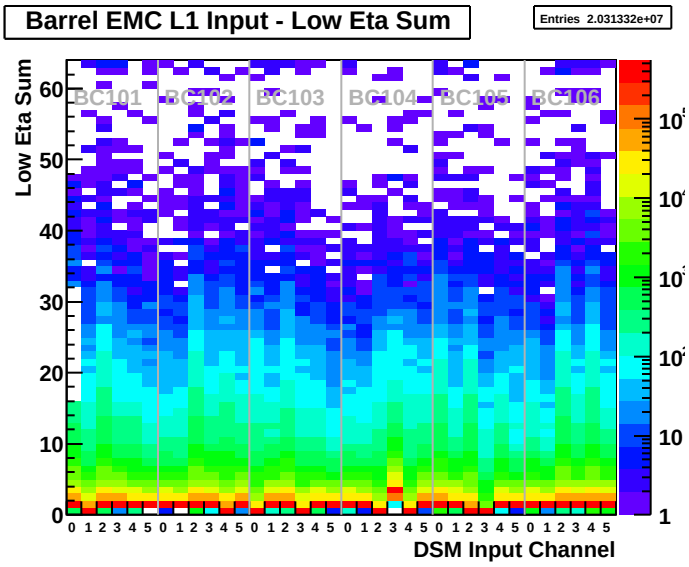
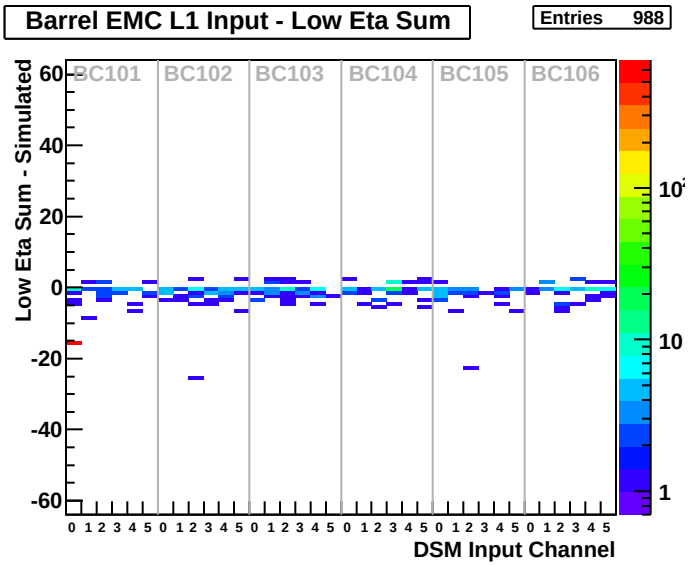
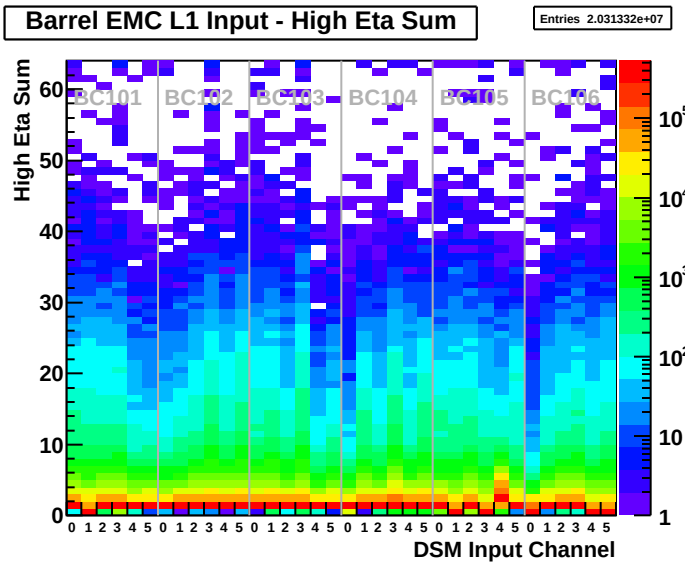
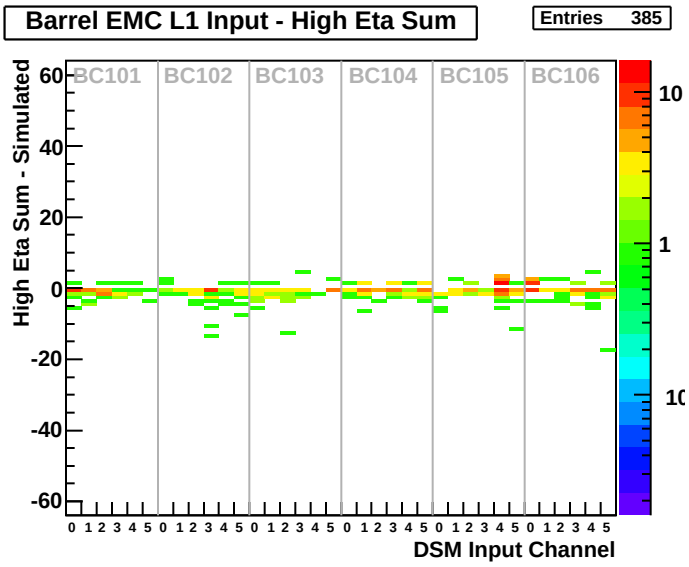
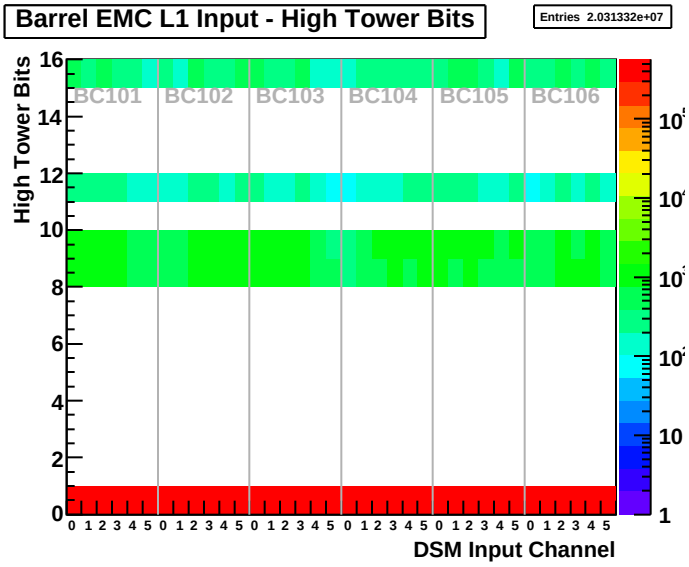
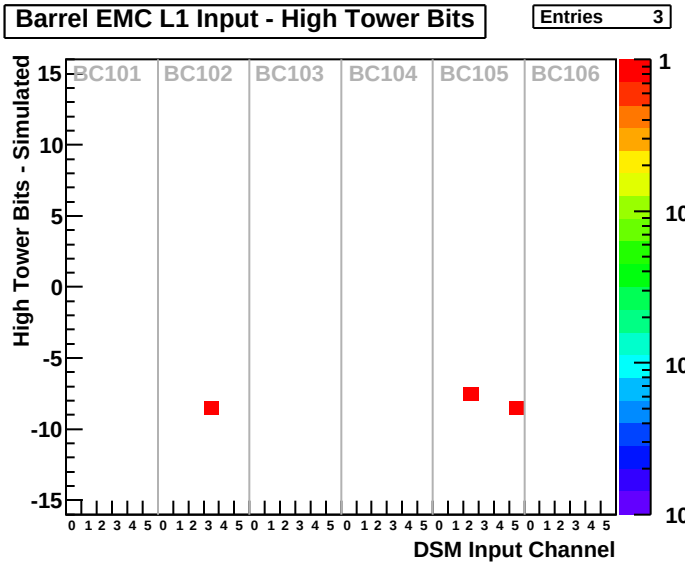

Barrel EMC L1 Input - Low Eta Sum
Entries 2.031332e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

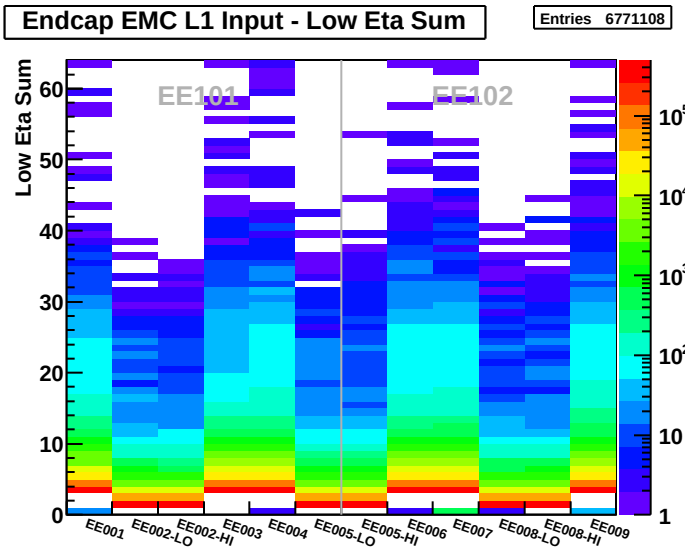

Barrel EMC L1 Input - Low Eta Sum
Entries 988
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

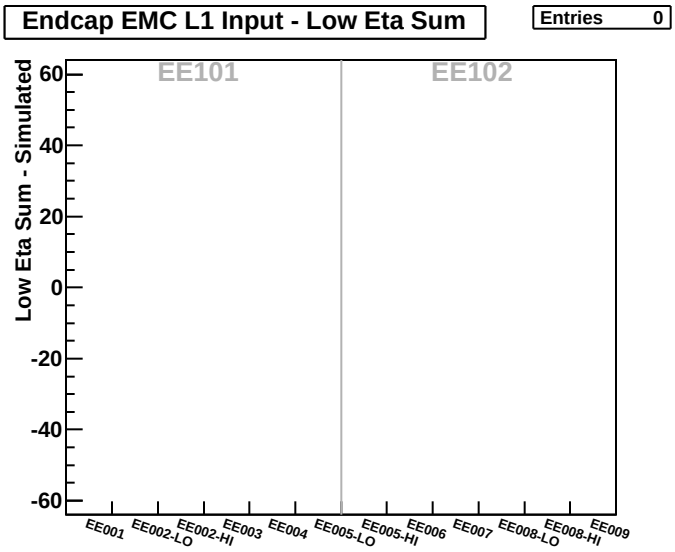

Barrel EMC L1 Input - High Eta Sum
Entries 2.031332e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

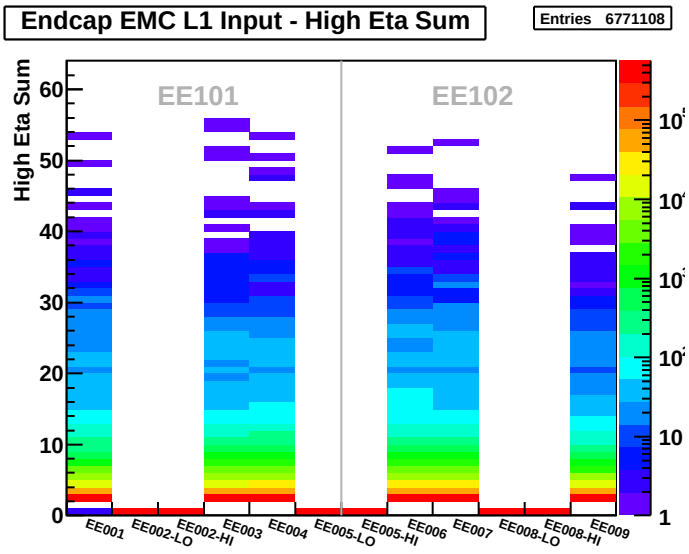

Barrel EMC L1 Input - High Eta Sum
Entries 385
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

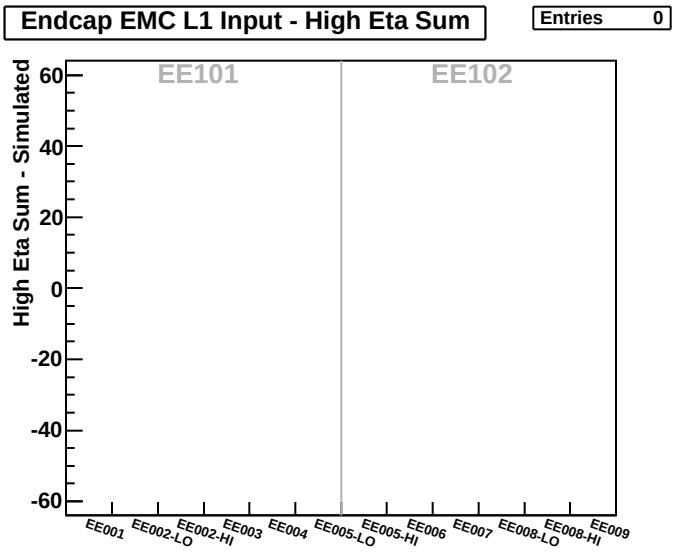

Barrel EMC L1 Input - High Tower Bits
Entries 2.031332e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

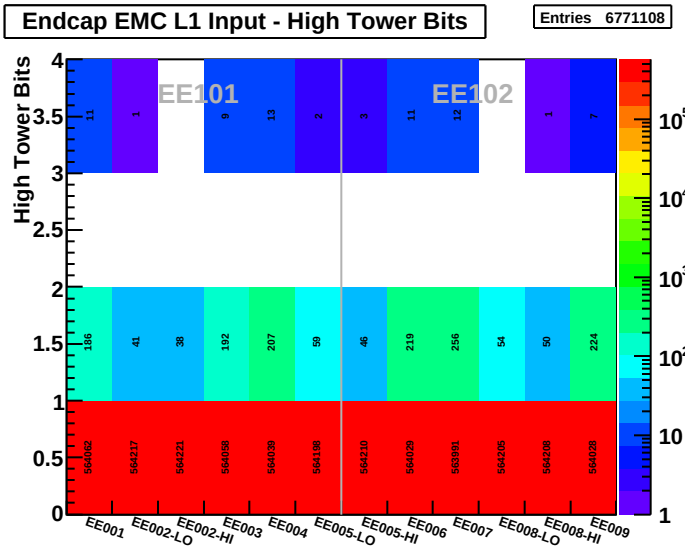

Barrel EMC L1 Input - High Tower Bits
Entries 3
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

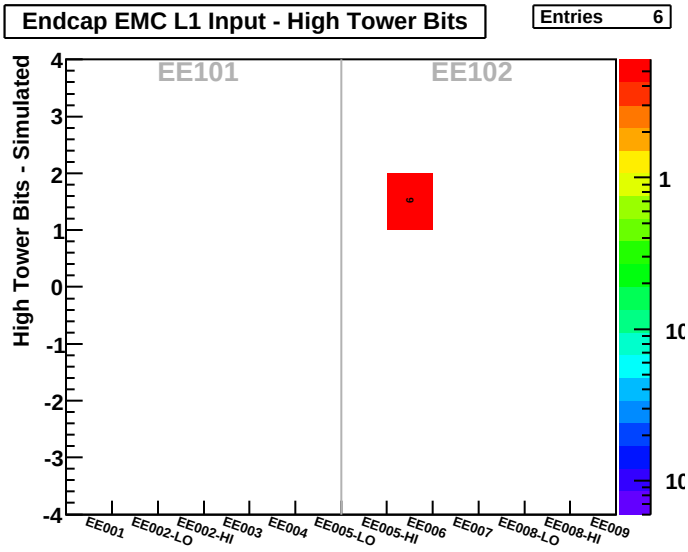

Endcap EMC L1 Input - Low Eta Sum
Entries 6771108
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

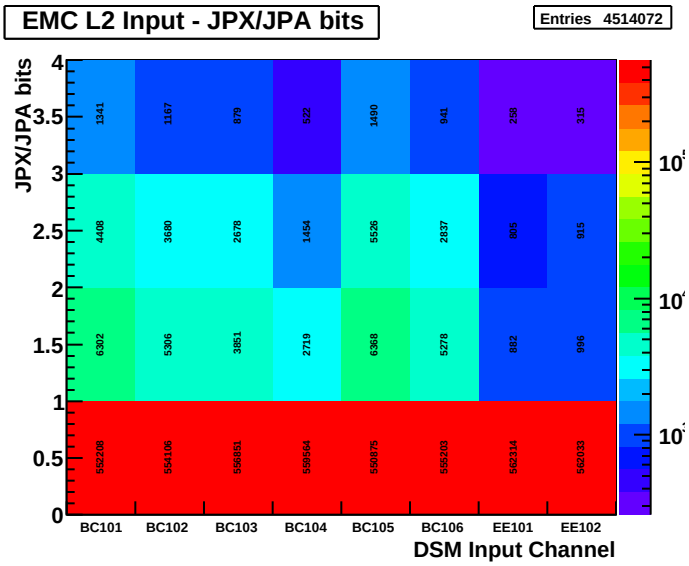

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

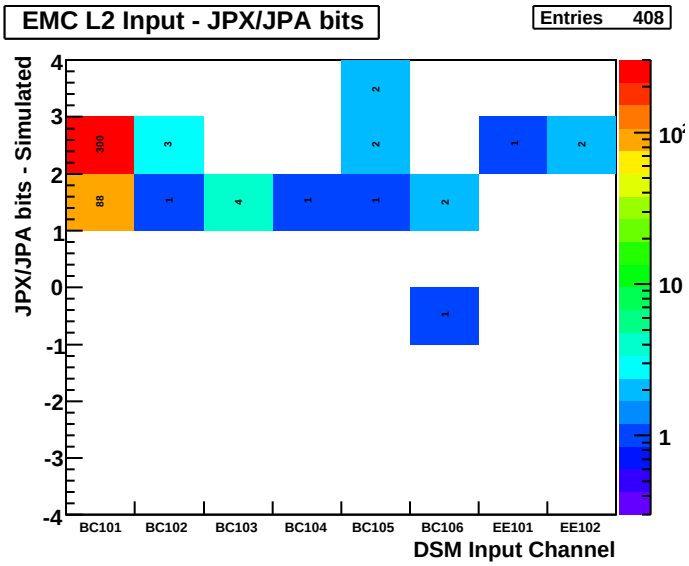

Endcap EMC L1 Input - High Eta Sum
Entries 6771108
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

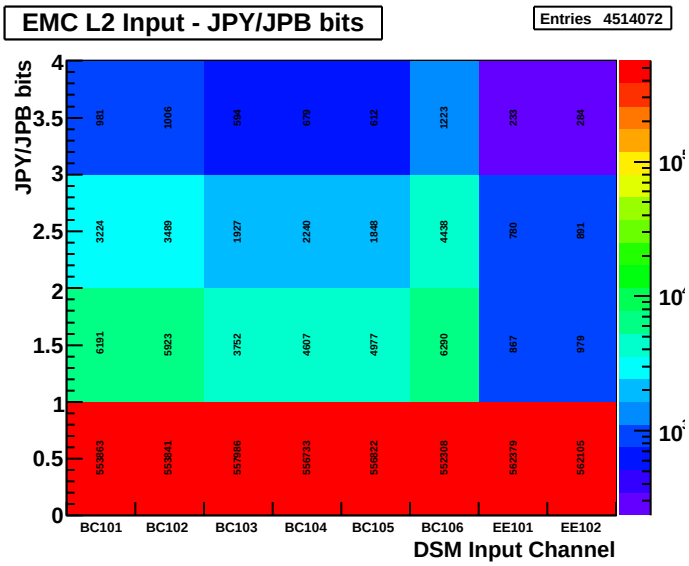

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

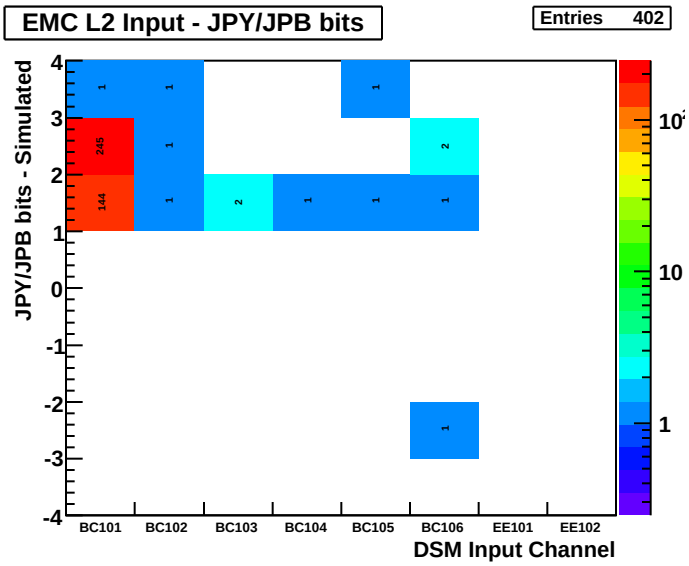

Endcap EMC L1 Input - High Tower Bits
Entries 6771108
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

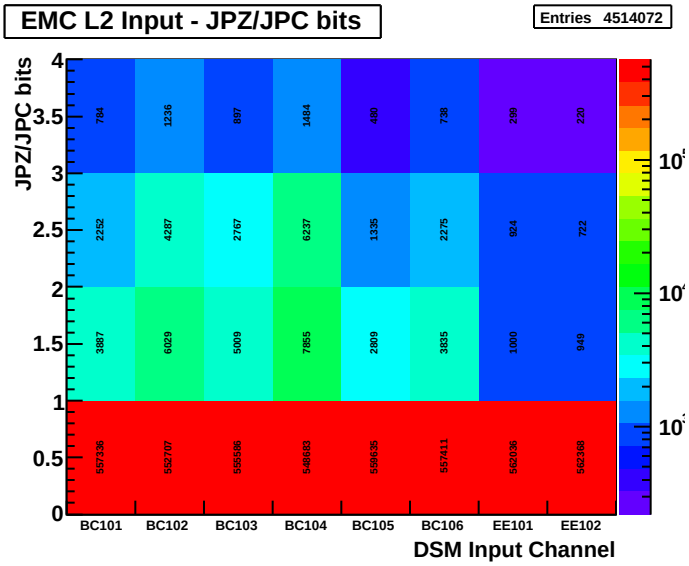

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

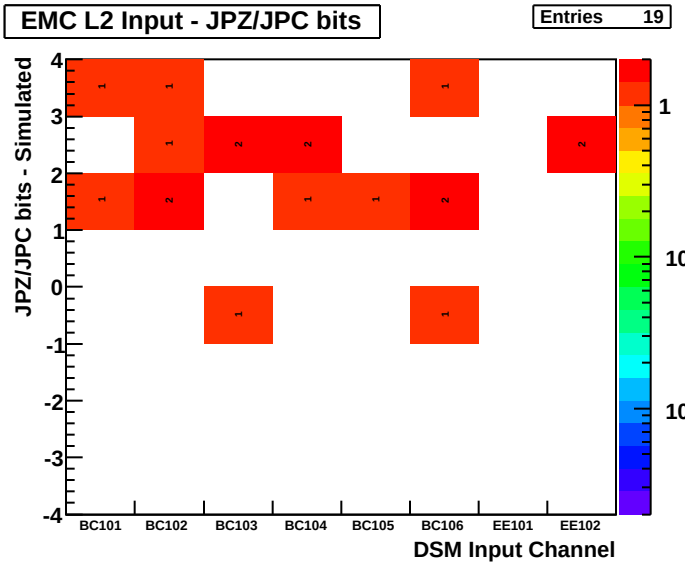

EMC L2 Input - JPX/JPA bits
Entries 4514072
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1341 1167 879 522 1490 941 258 315
4408 3680 2678 1454 5526 2837 805 915
6302 5306 3851 2719 6368 5278 882 996
562208 554106 558051 559564 559875 559203 562314 562033

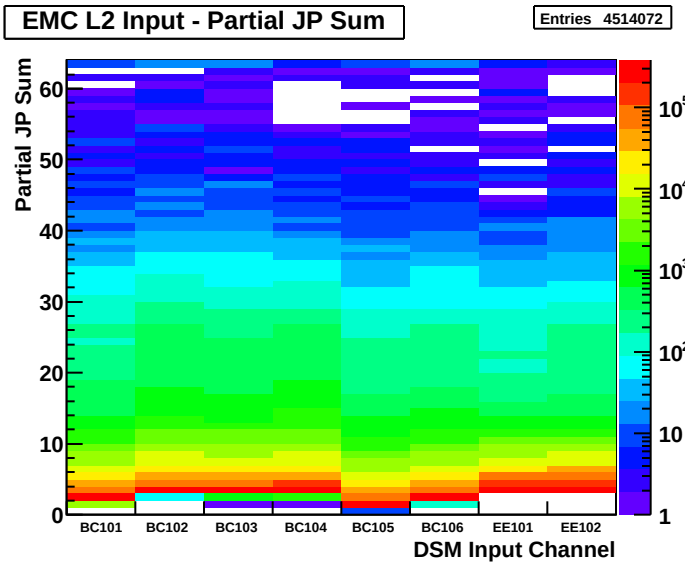

EMC L2 Input - JPX/JPA bits
Entries 408
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
300 3 2 2 1 2
88 1 4 1 1 2
1

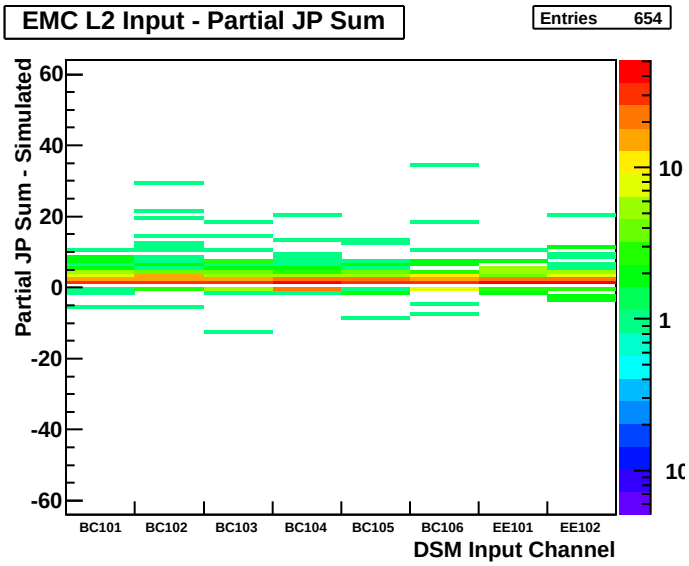

EMC L2 Input - JPY/JPB bits
Entries 4514072
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
981 1006 594 676 612 1223 233 284
3224 3489 1927 2240 1848 4438 780 891
6191 5823 3752 4907 4977 6290 867 979
555863 553841 557986 556733 556822 552308 562379 562105

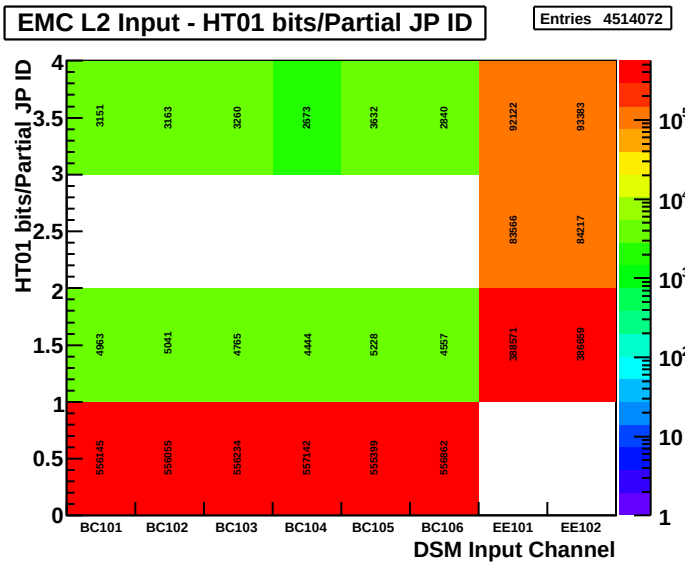

EMC L2 Input - JPY/JPB bits
Entries 402
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 2
245 1
144 1 2 1 1 1
1

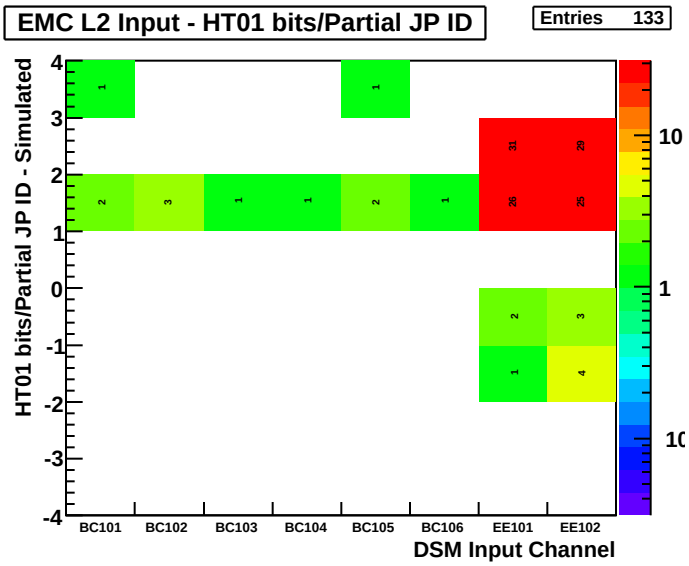

EMC L2 Input - JPZ/JPC bits
Entries 4514072
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
784 1236 897 1484 480 738 239 220
2252 4287 2767 6237 1335 2275 924 722
3887 6029 5009 7865 2809 3835 1000 949
557336 552707 555586 549683 559635 557411 562036 562368

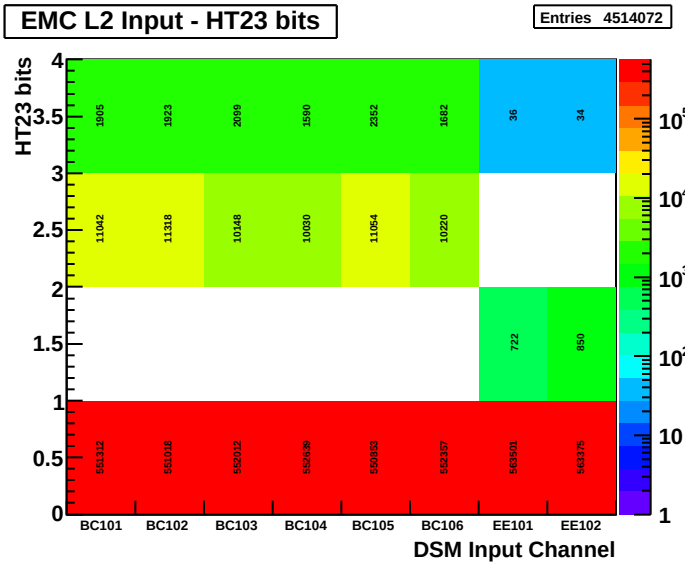

EMC L2 Input - JPZ/JPC bits
Entries 19
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
1 2 2 2
1 2 1 1 2
1 1

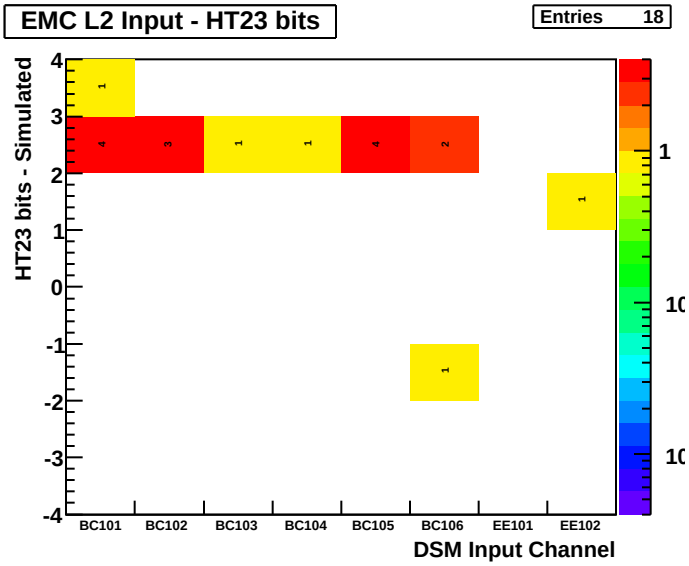

EMC L2 Input - Partial JP Sum
Entries 4514072
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

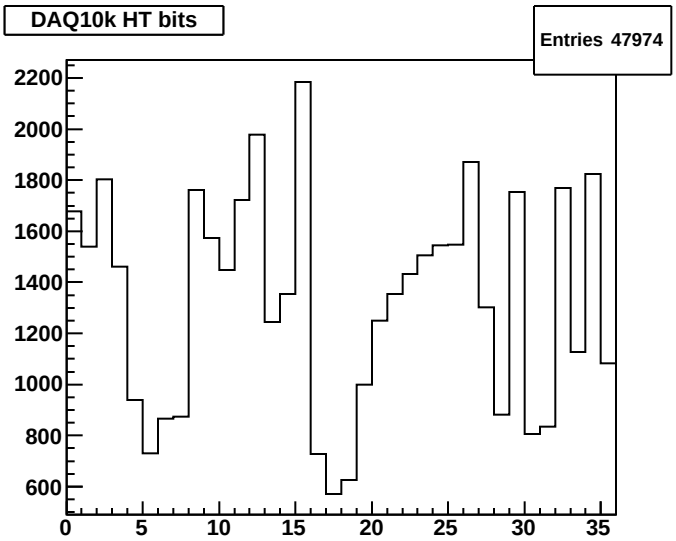

EMC L2 Input - Partial JP Sum
Entries 654
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

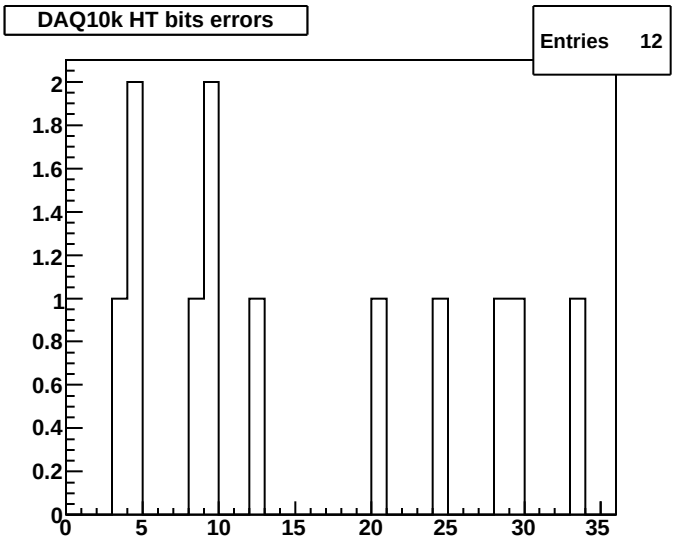

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4514072
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

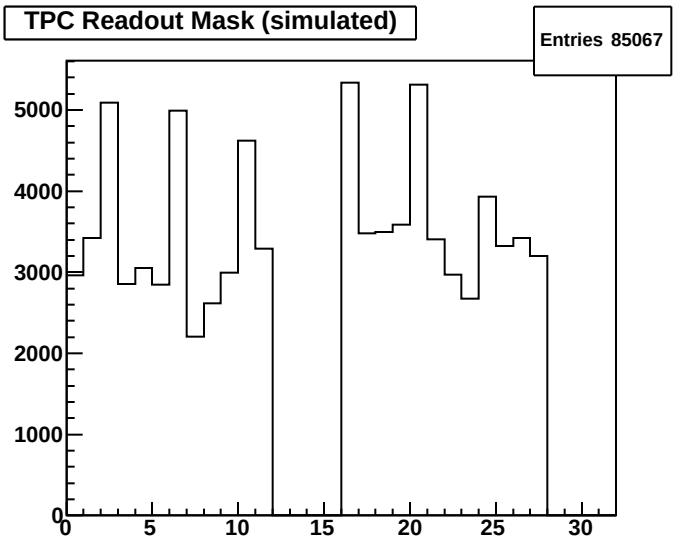

EMC L2 Input - HT01 bits/Partial JP ID
Entries 133
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

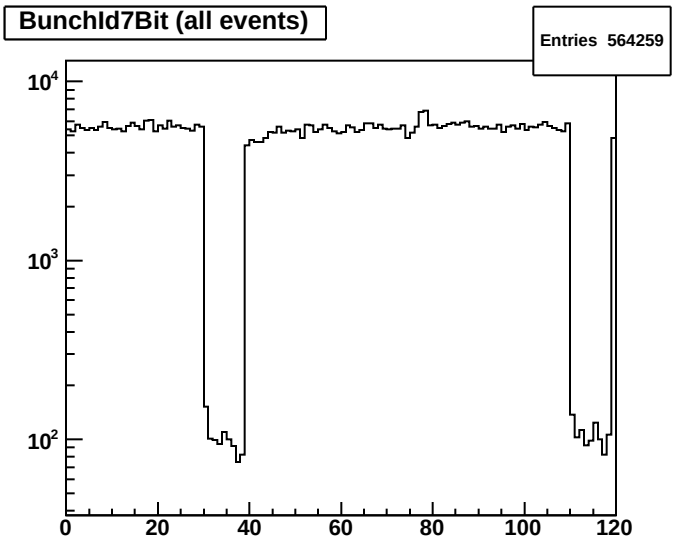

EMC L2 Input - HT23 bits
Entries 4514072
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

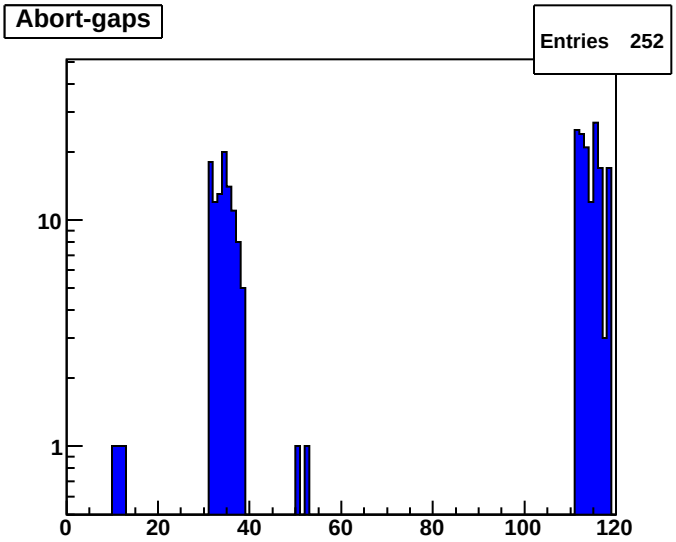

EMC L2 Input - HT23 bits
Entries 18
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

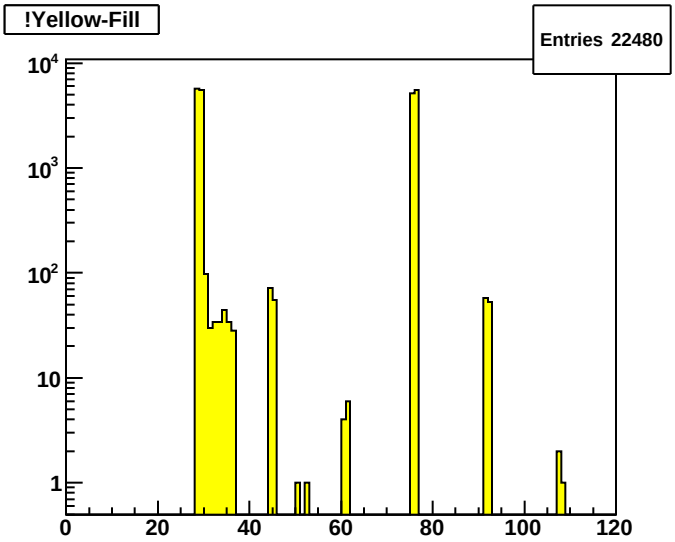

DAQ10k HT bits
Entries 47974

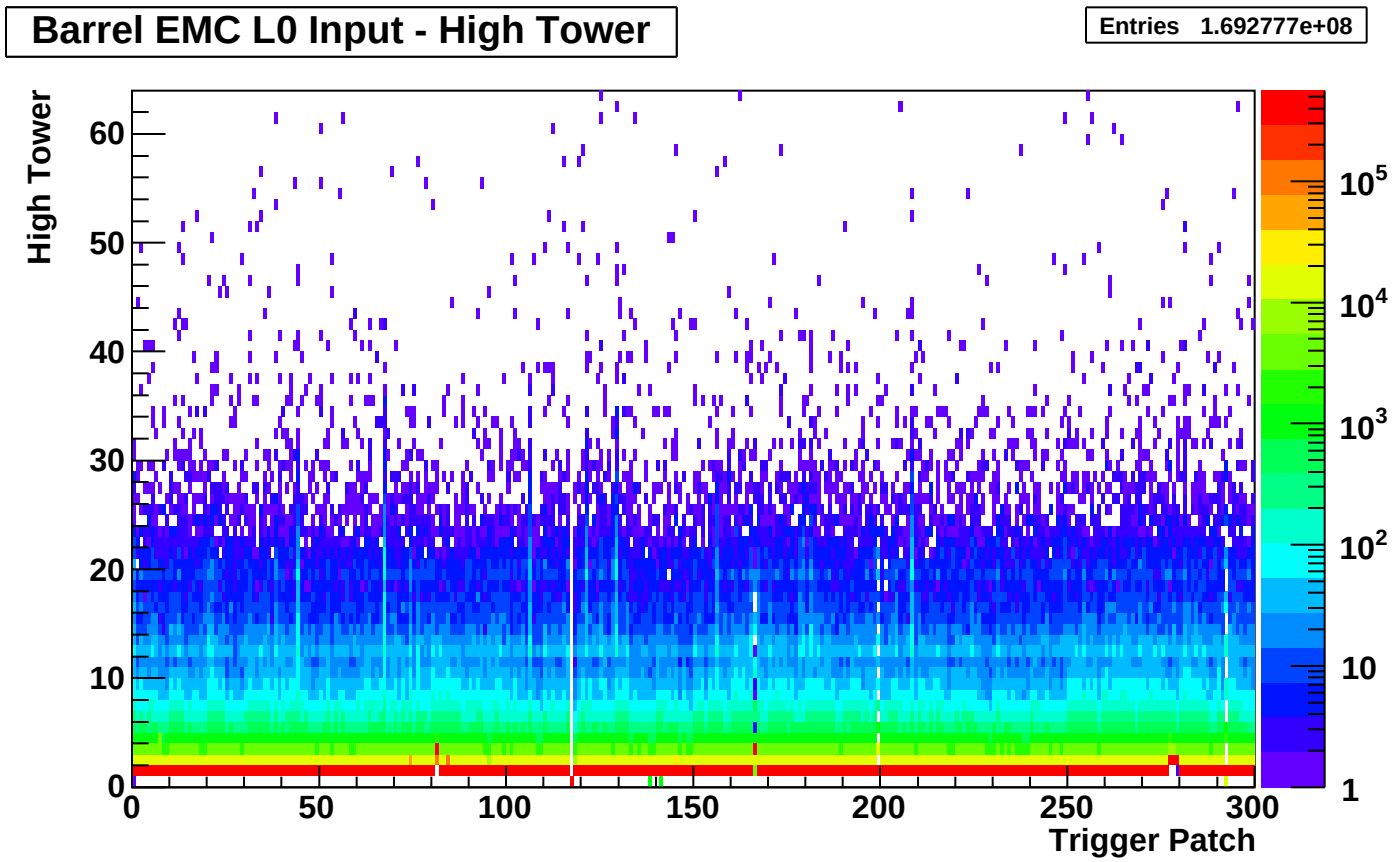

DAQ10k HT bits errors
Entries 12

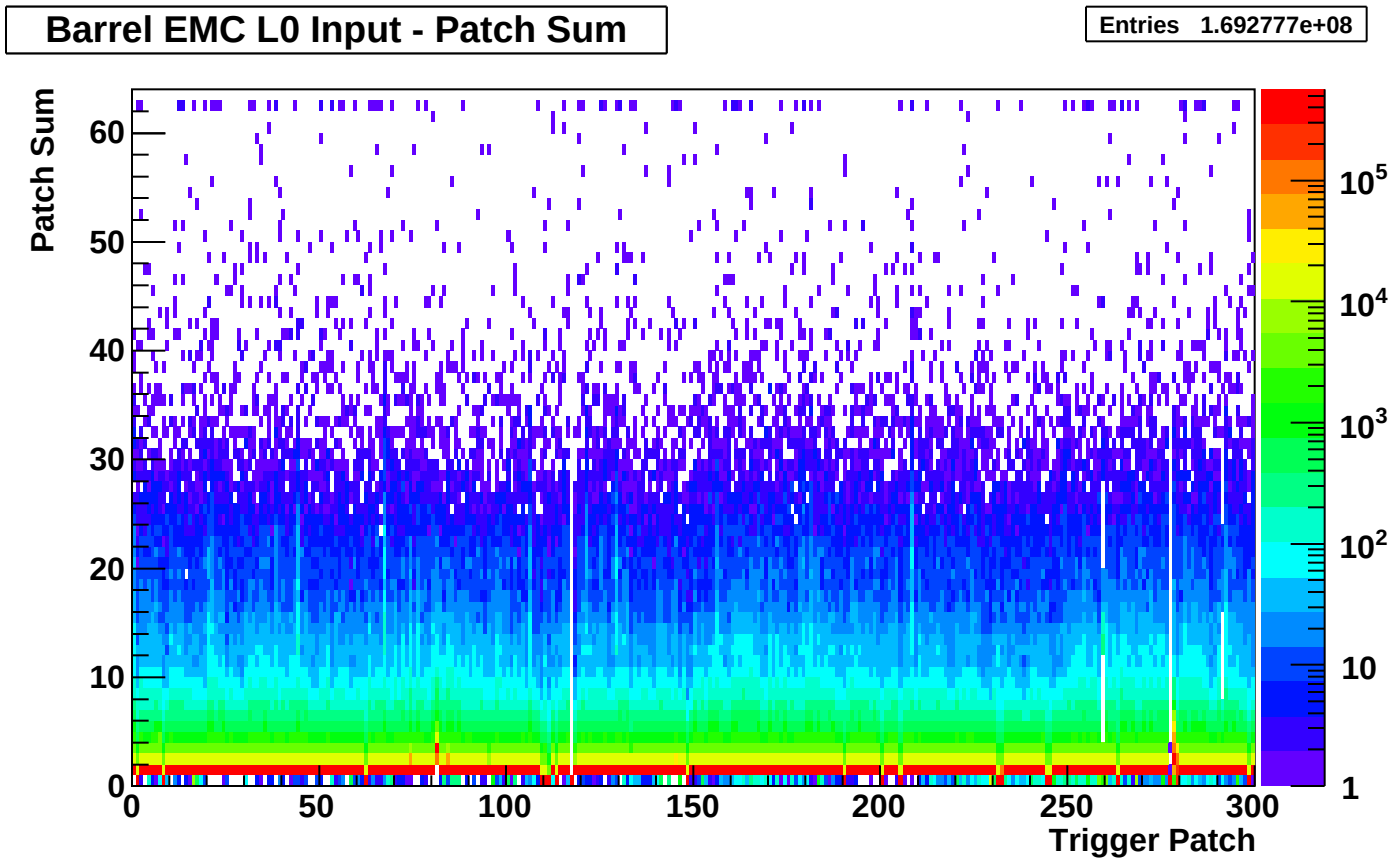

TPC Readout Mask (simulated)
Entries 85067

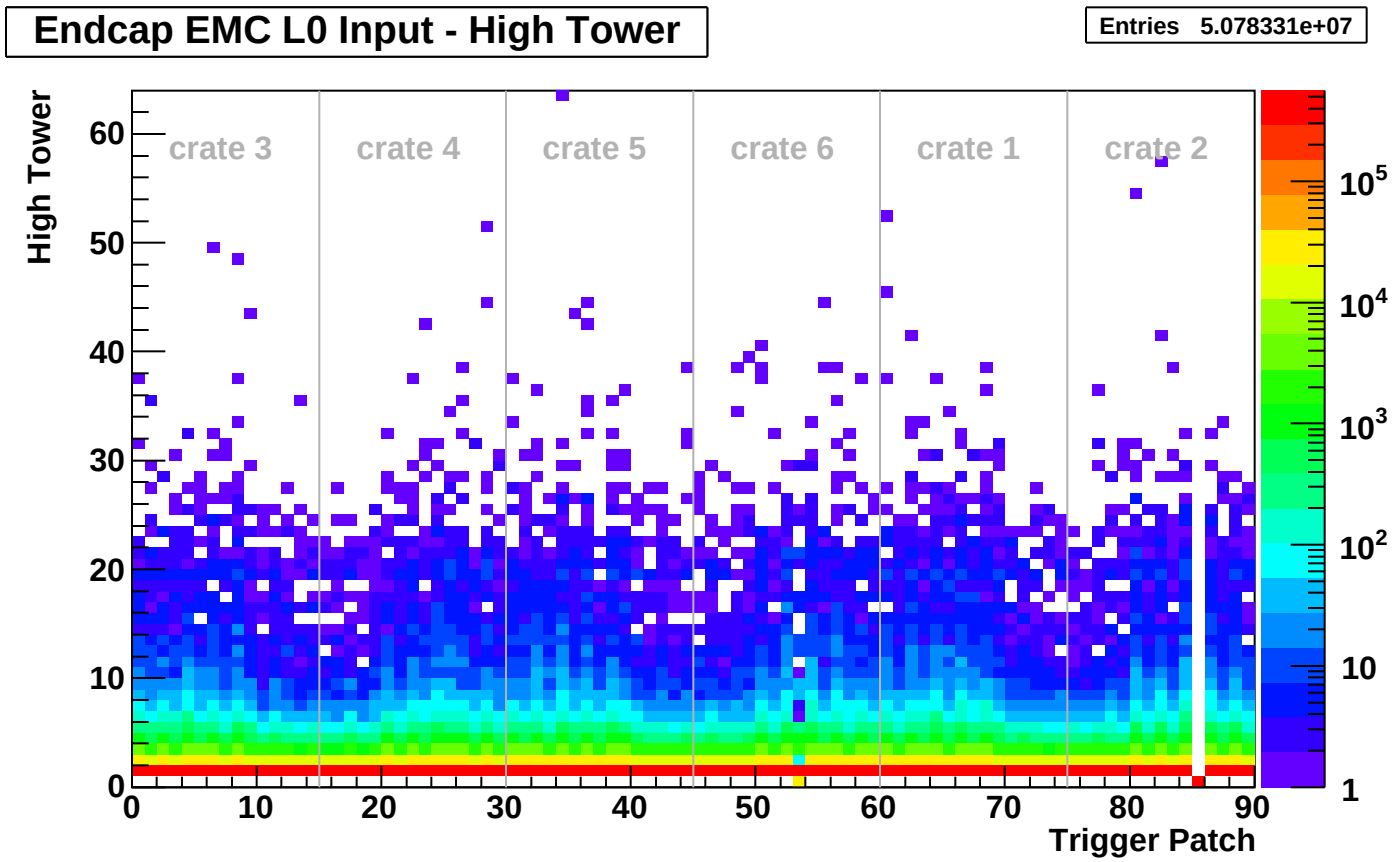

BunchId7Bit (all events)
Entries 564259

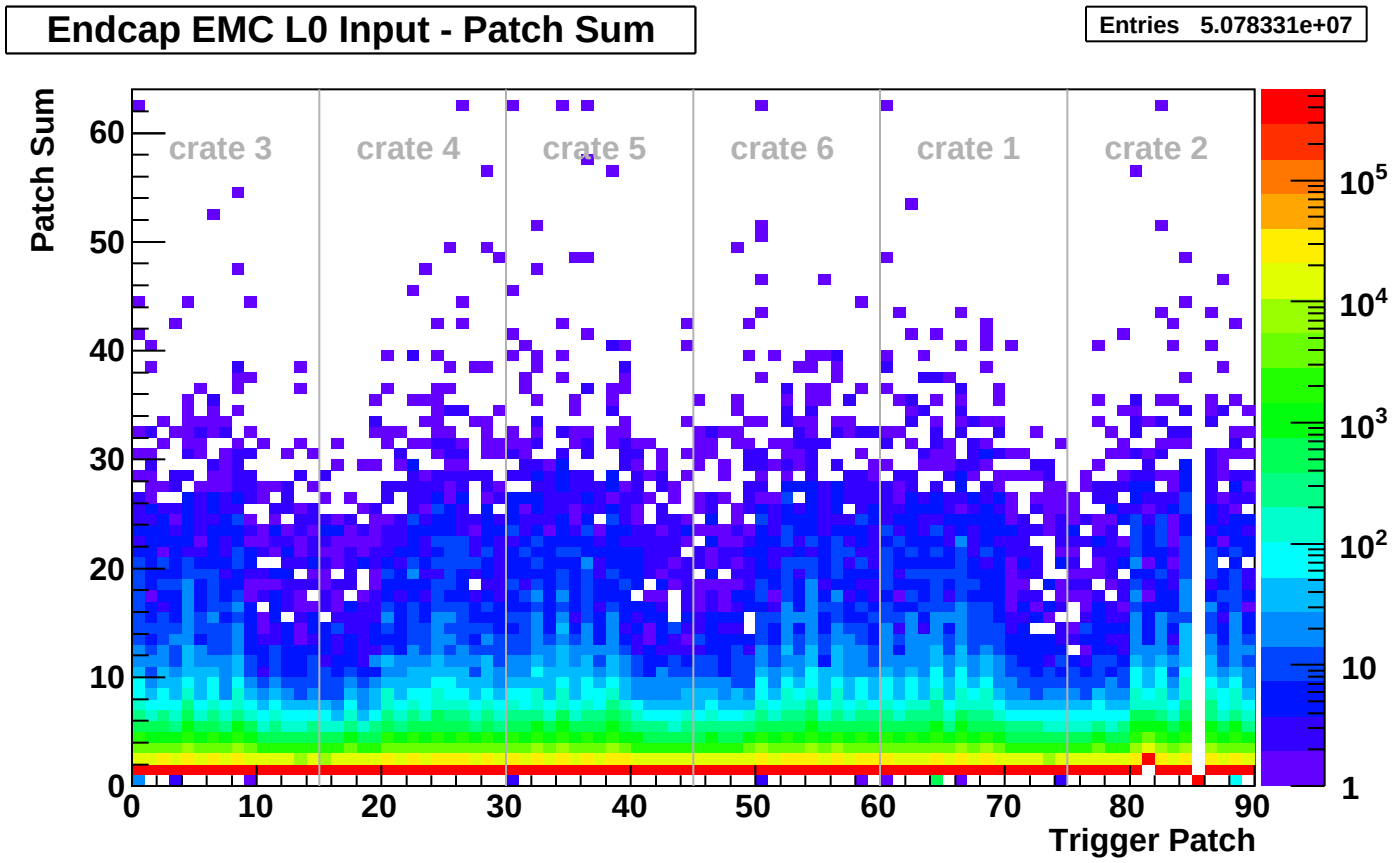

Abort-gaps
Entries 252

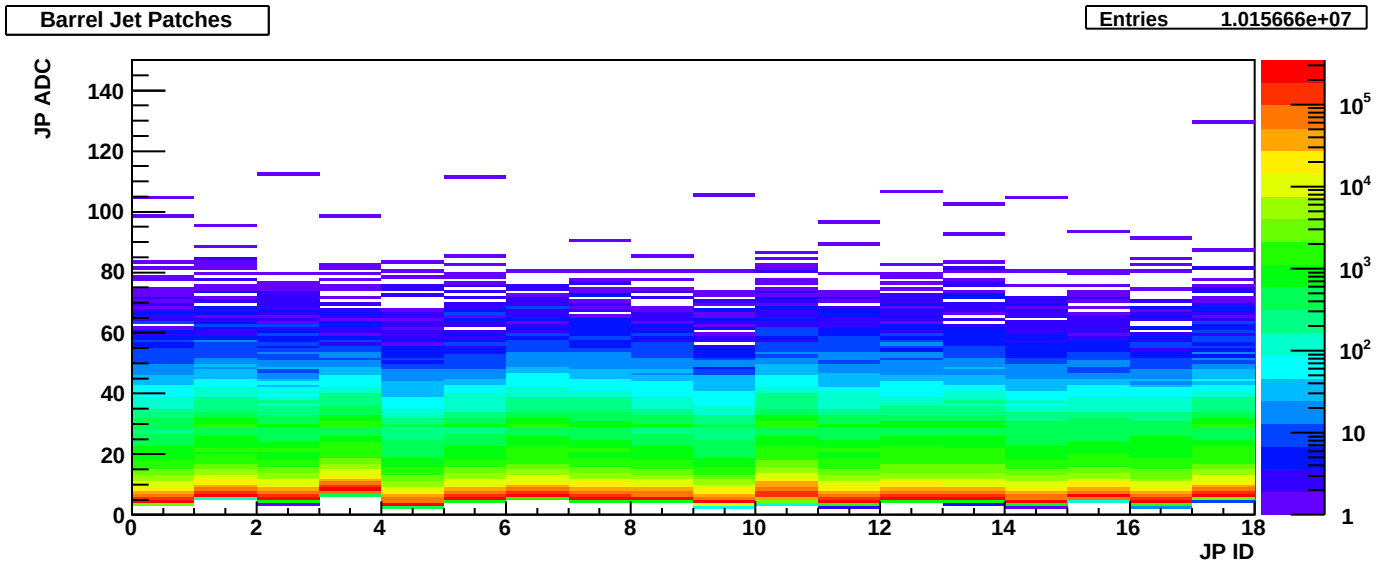

!Yellow-Fill
Entries 22480

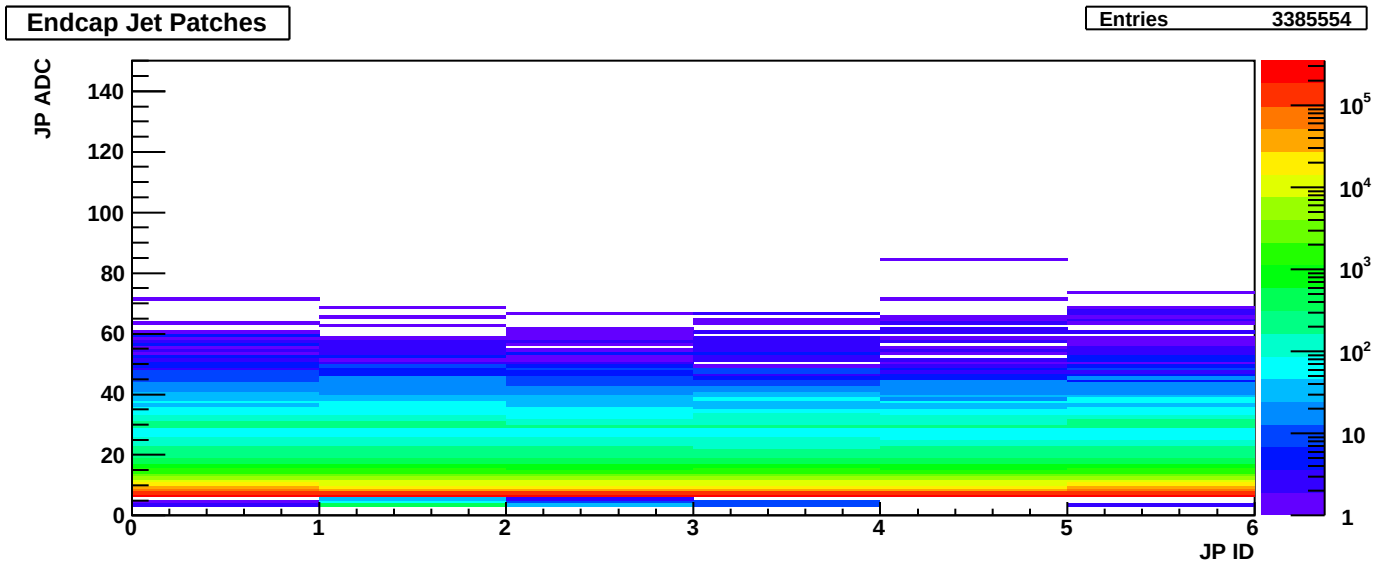

Barrel EMC L0 Input - High Tower
Entries 1.692777e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

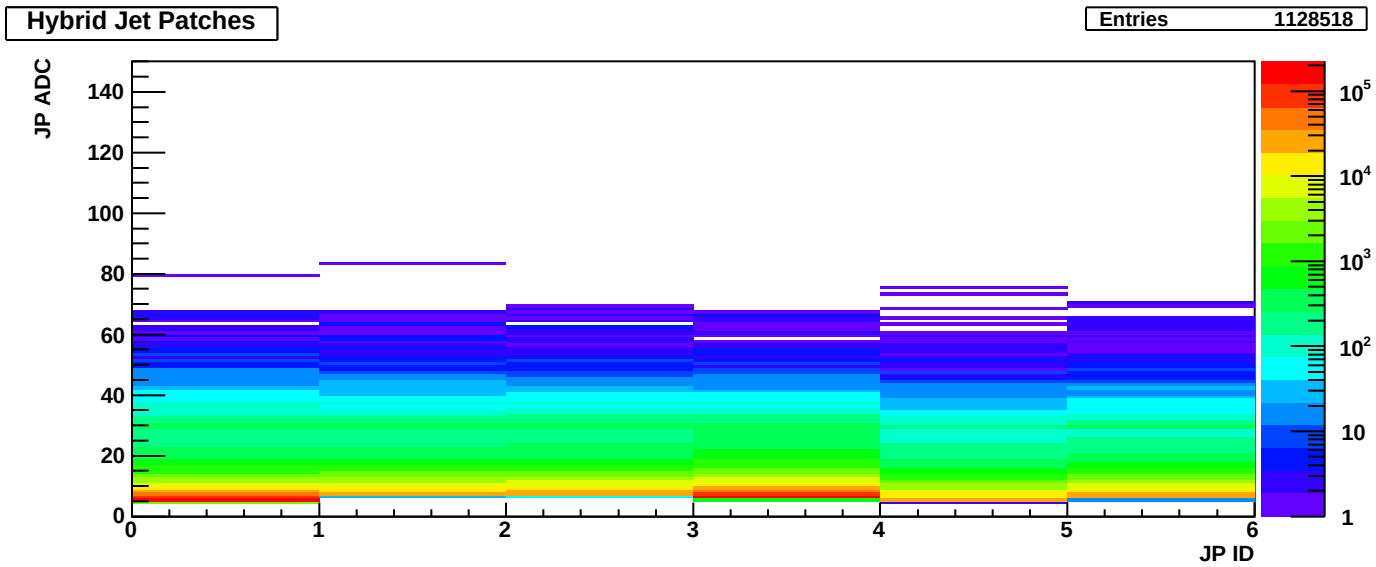

Barrel EMC L0 Input - Patch Sum
Entries 1.692777e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

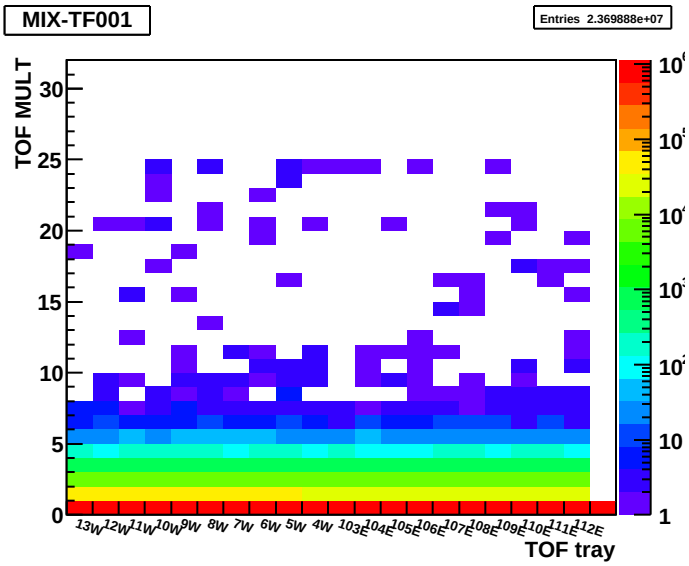

Endcap EMC L0 Input - High Tower
Entries 5.078331e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

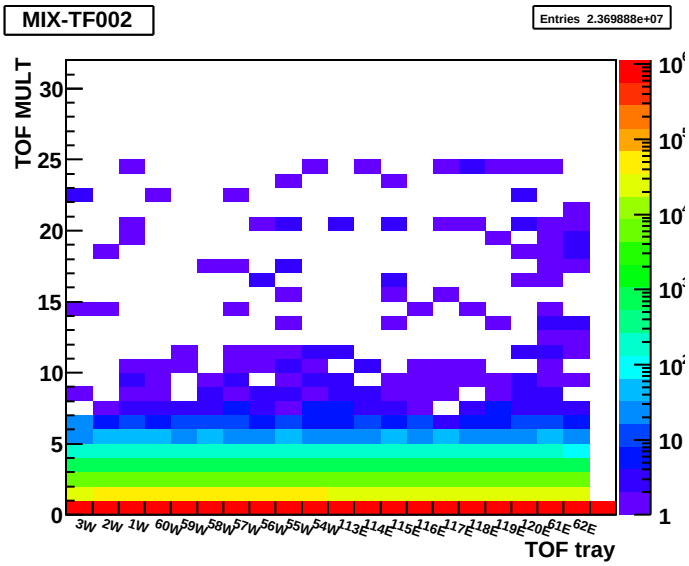

Endcap EMC L0 Input - Patch Sum
Entries 5.078331e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

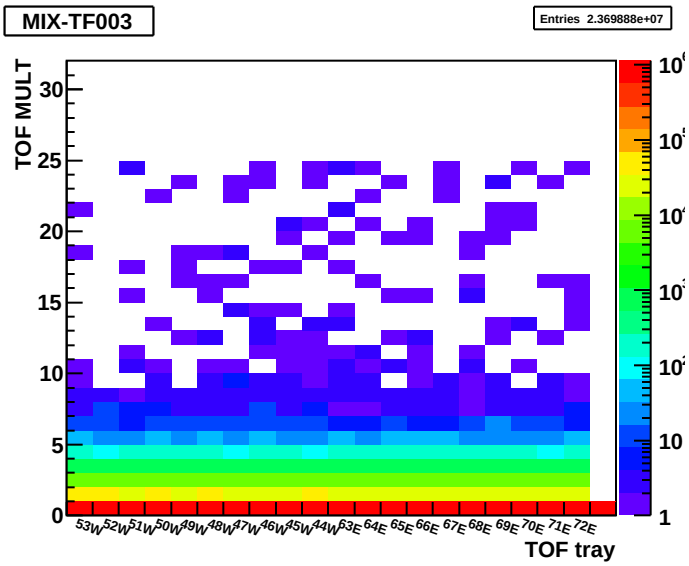

Barrel Jet Patches
Entries 1.015666e+07
JP ADC
JP ID

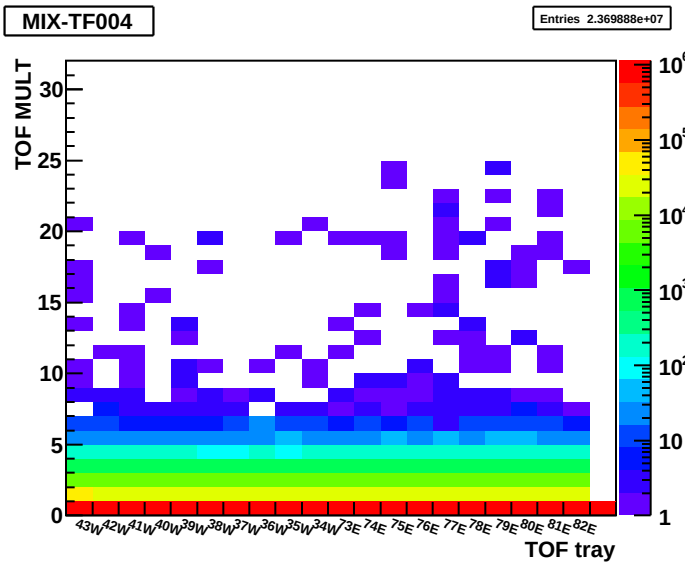

Endcap Jet Patches
Entries 3385554
JP ADC
JP ID

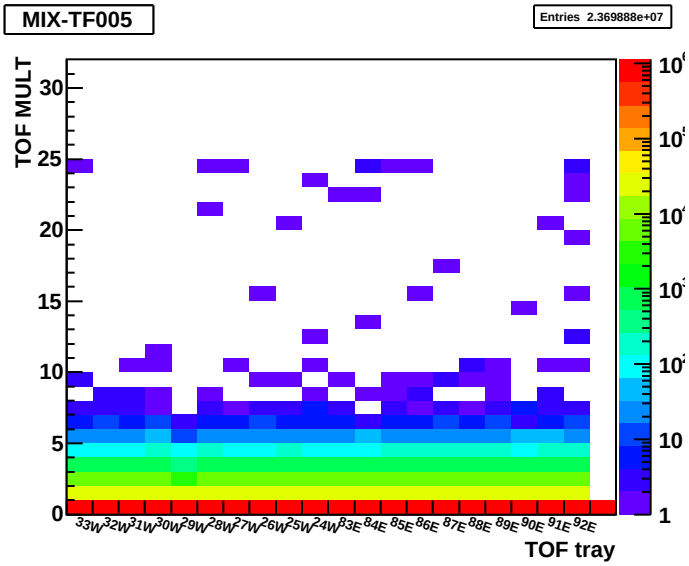

Hybrid Jet Patches
Entries 1128518
JP ADC
JP ID

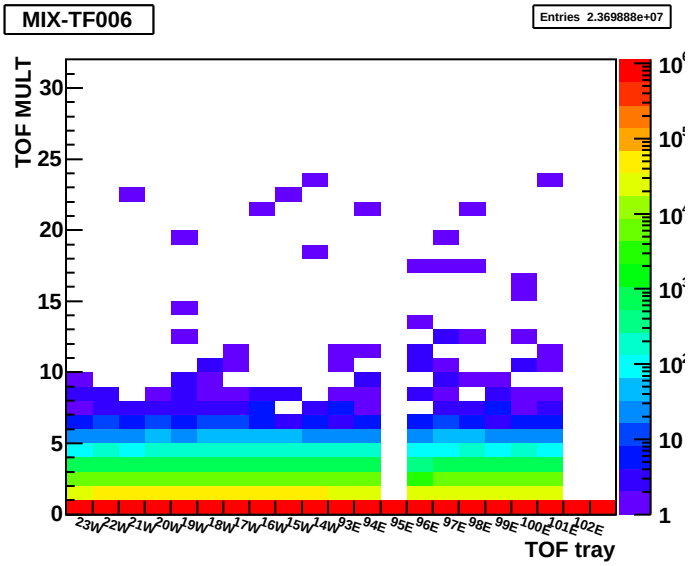

MIX-TF001
Entries 2.369888e+07
TOF MULT
TOF tray

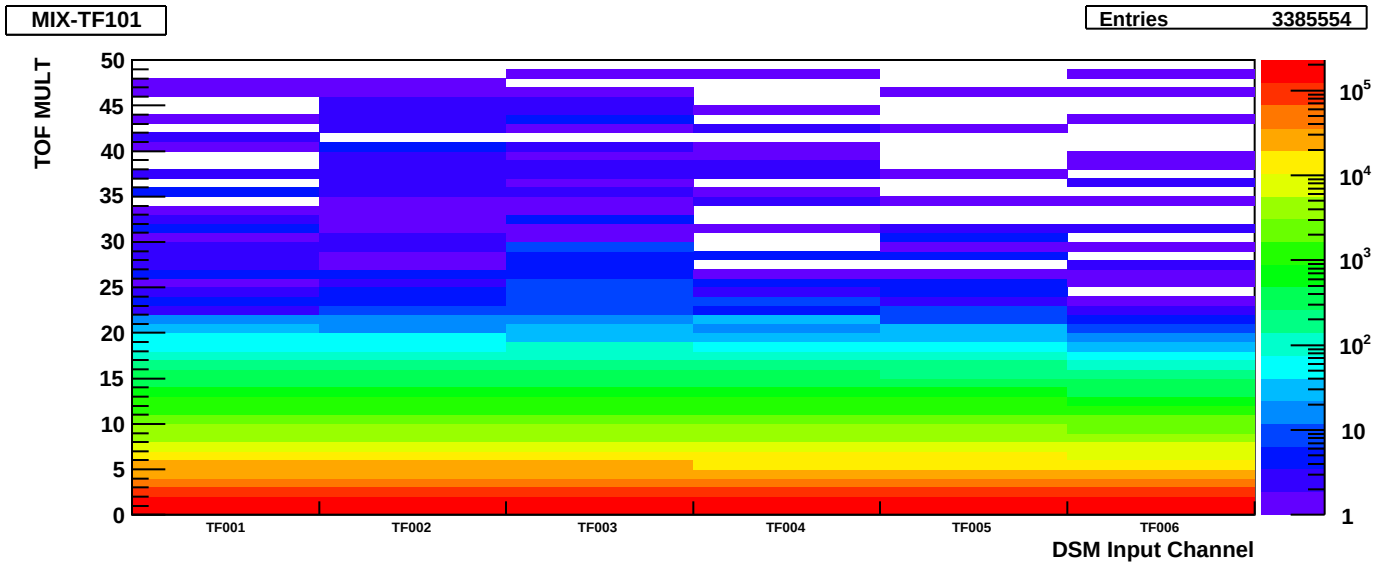

MIX-TF002
Entries 2.369888e+07
TOF MULT
TOF tray

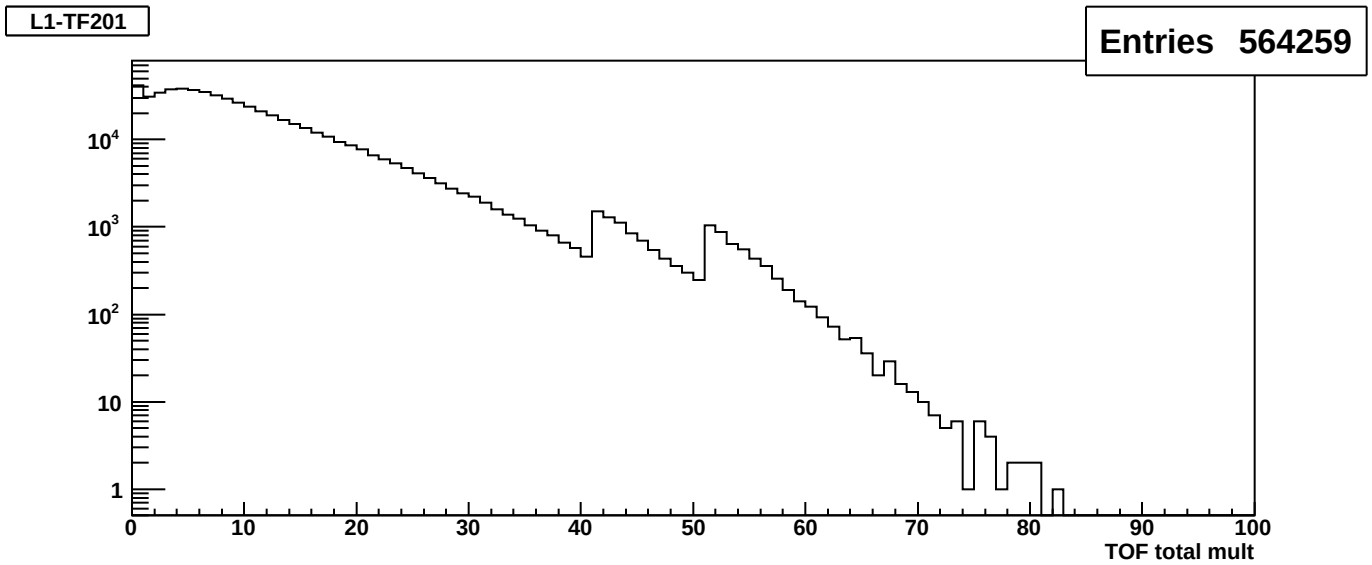

MIX-TF003
Entries 2.369888e+07
TOF MULT
TOF tray

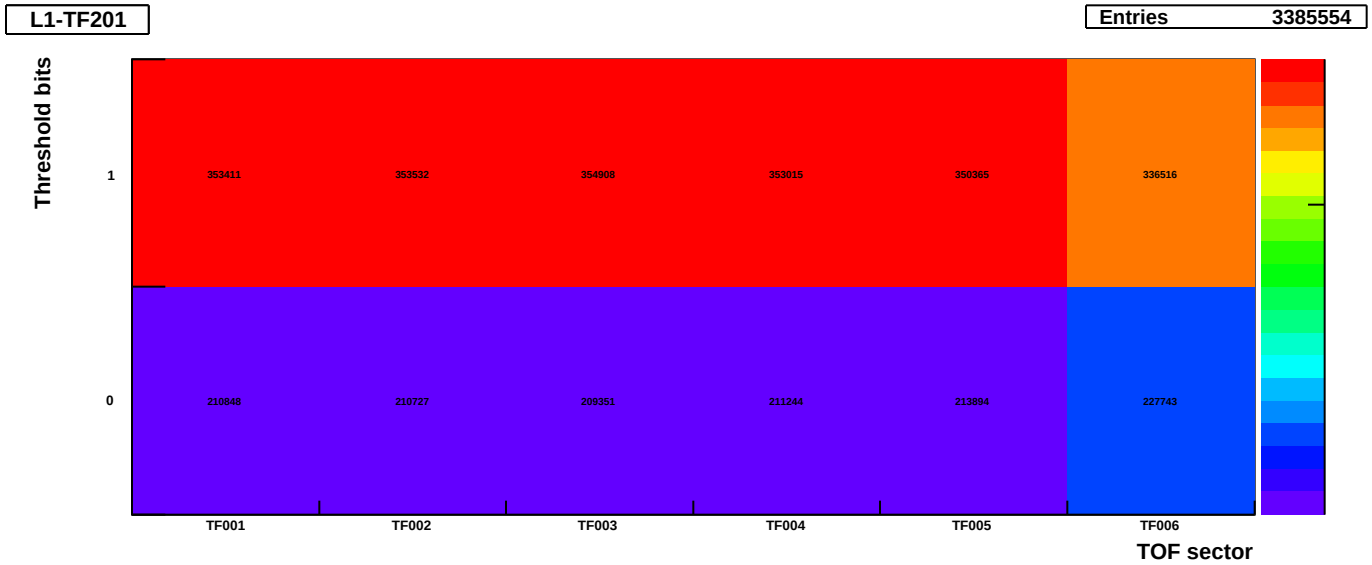

MIX-TF004
Entries 2.369888e+07
TOF MULT
TOF tray

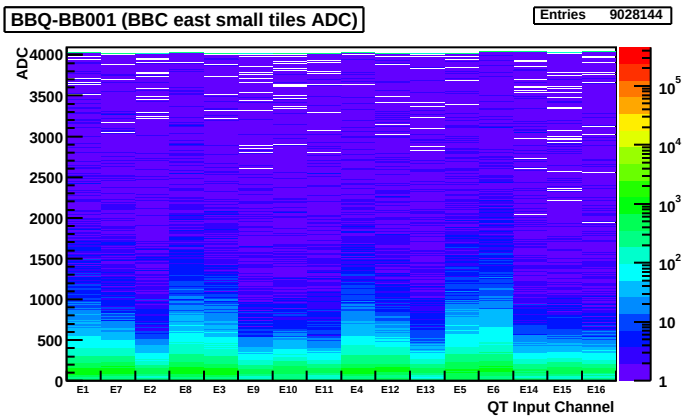

MIX-TF005
Entries 2.369888e+07
TOF MULT
TOF tray

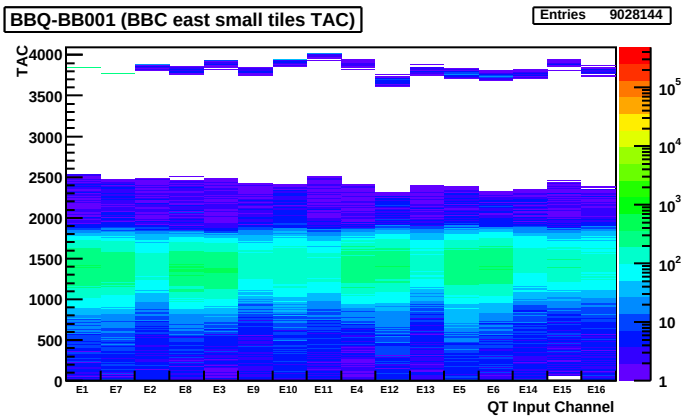

MIX-TF006
Entries 2.369888e+07
TOF MULT
TOF tray

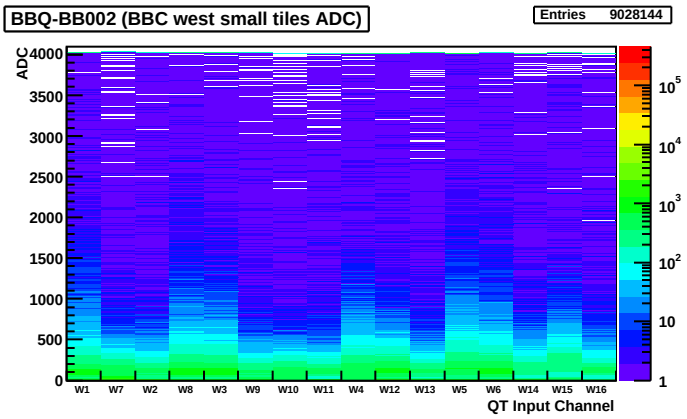

MIX-TF101
Entries 3385554
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

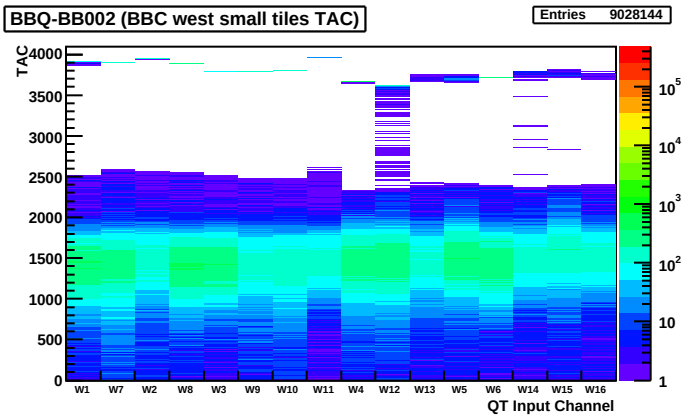

L1-TF201
Entries 564259
TOF total mult

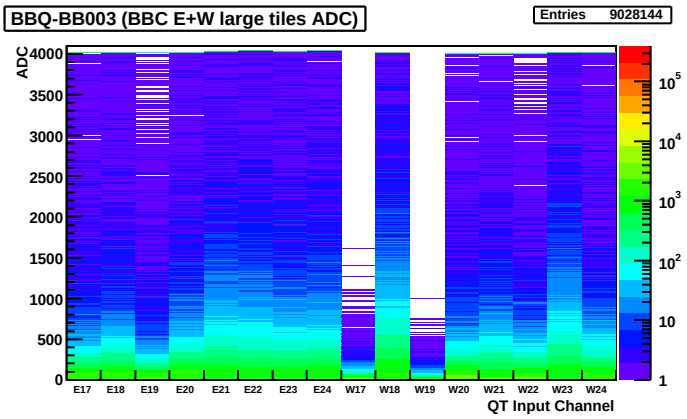

L1-TF201
Entries 3385554
Threshold bits
353411
353532
354908
353015
350365
336516
210848
210727
209351
211244
213894
227743
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

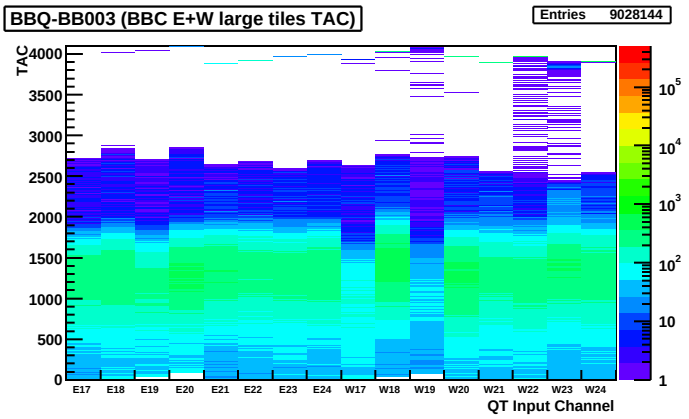

BBQ-BB001 (BBC east small tiles ADC)
Entries 9028144
ADC
QT Input Channel

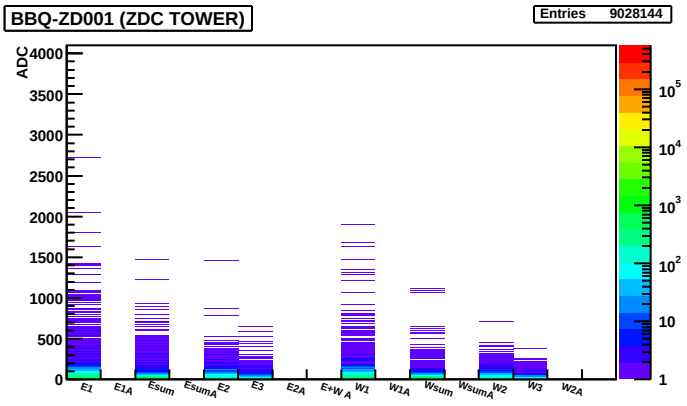

BBQ-BB001 (BBC east small tiles TAC)
Entries 9028144
TAC
QT Input Channel

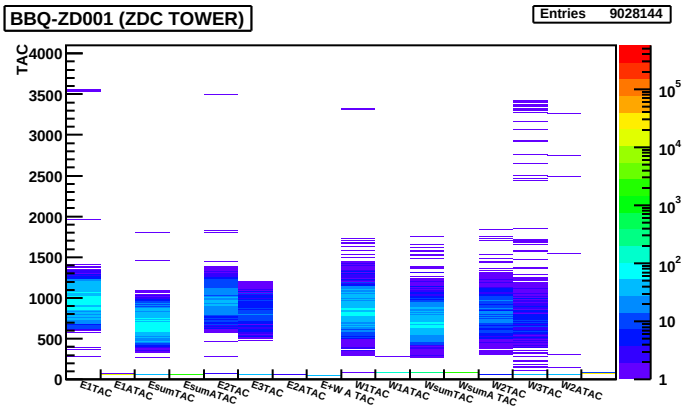

BBQ-BB002 (BBC west small tiles ADC)
Entries 9028144
ADC
QT Input Channel

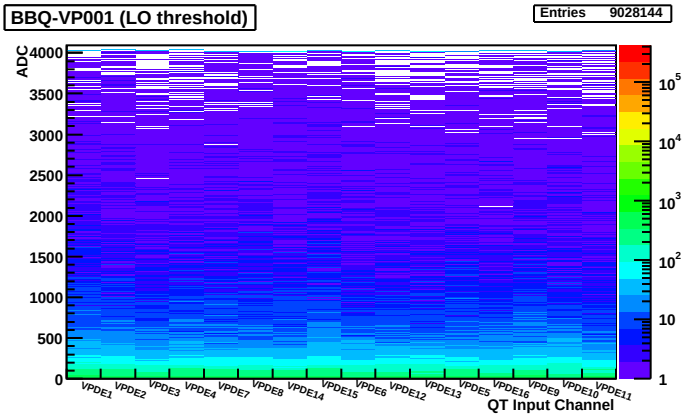

BBQ-BB002 (BBC west small tiles TAC)
Entries 9028144
TAC
QT Input Channel

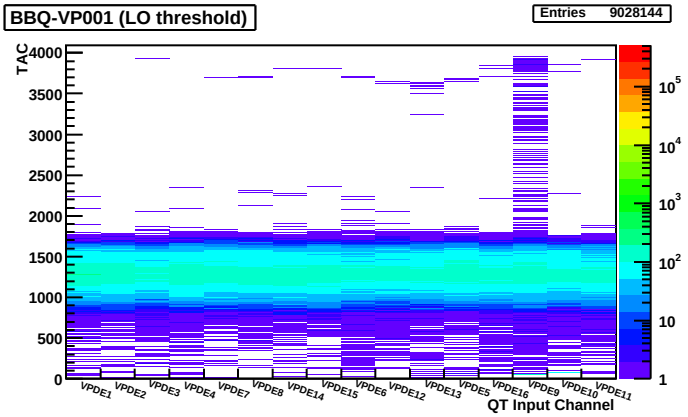

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9028144
ADC
QT Input Channel

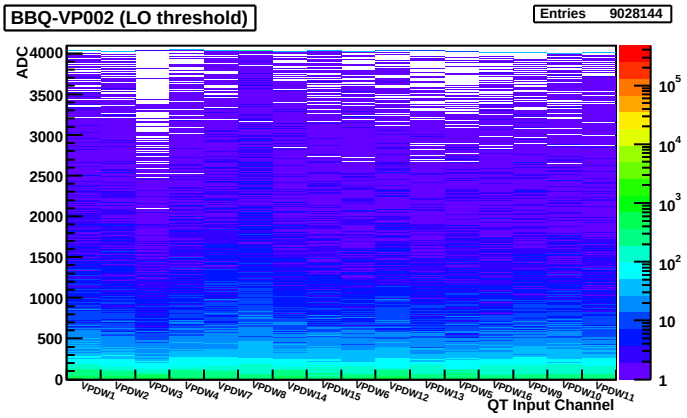

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9028144
TAC
QT Input Channel

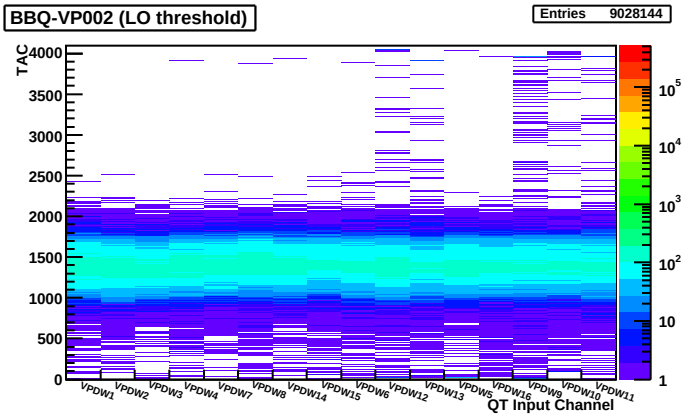

BBQ-ZD001 (ZDC TOWER)
Entries 9028144
ADC

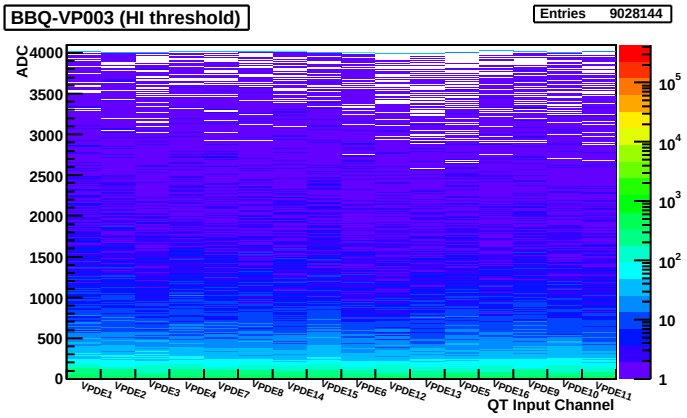

BBQ-ZD001 (ZDC TOWER)
Entries 9028144
TAC

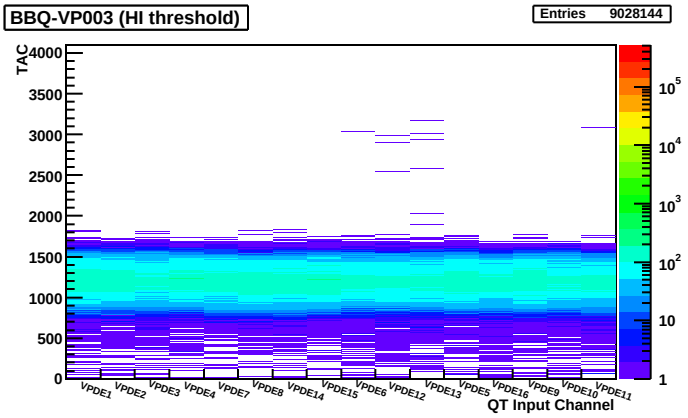

BBQ-VP001 (LO threshold)
Entries 9028144
ADC
QT Input Channel

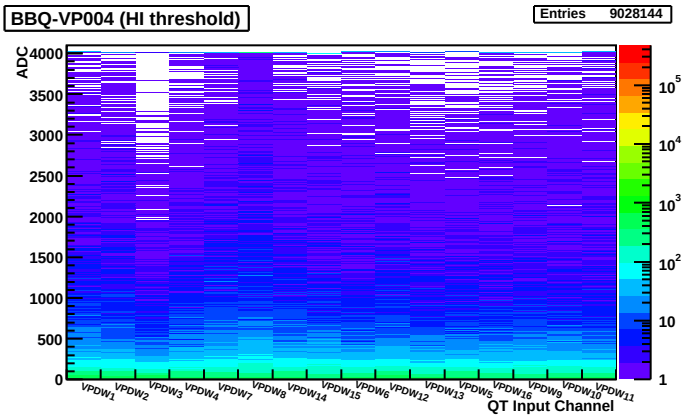

BBQ-VP001 (LO threshold)
Entries 9028144
TAC
QT Input Channel

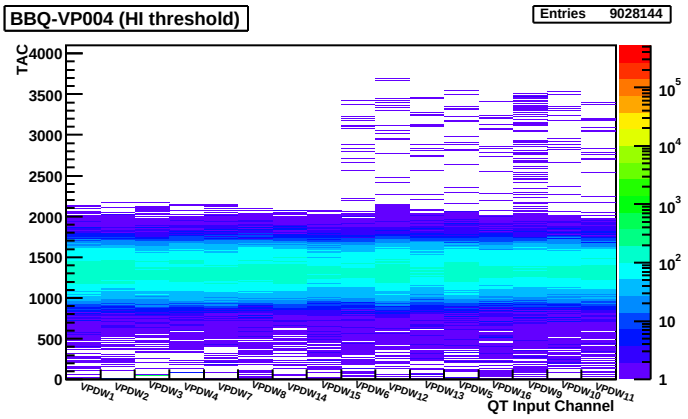

BBQ-VP002 (LO threshold)
Entries 9028144
ADC
QT Input Channel

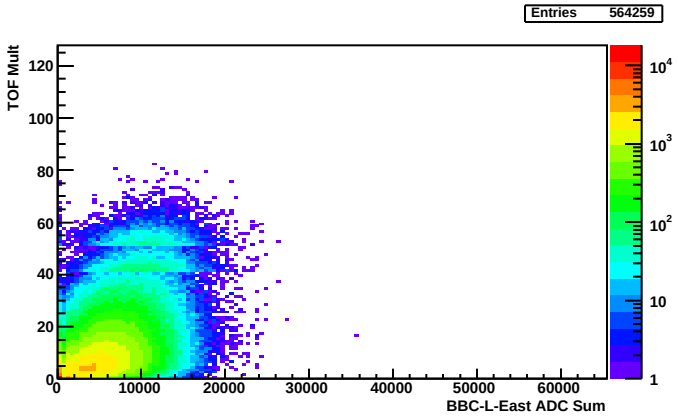

BBQ-VP002 (LO threshold)
Entries 9028144
TAC
QT Input Channel

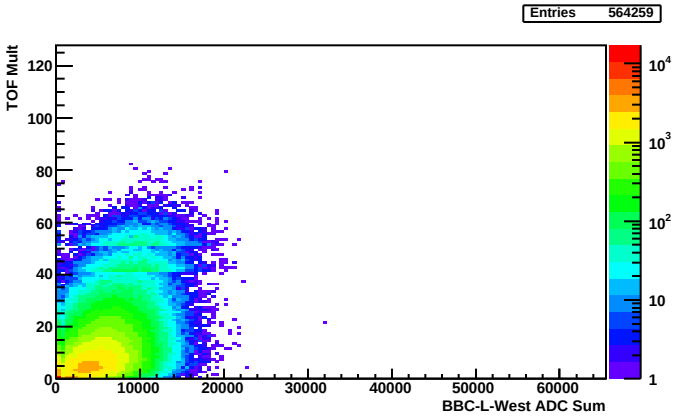

BBQ-VP003 (HI threshold)
Entries 9028144
ADC
QT Input Channel

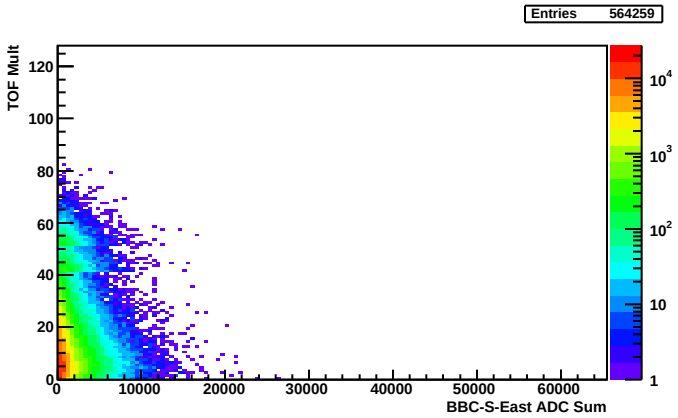

BBQ-VP003 (HI threshold)
Entries 9028144
TAC
QT Input Channel

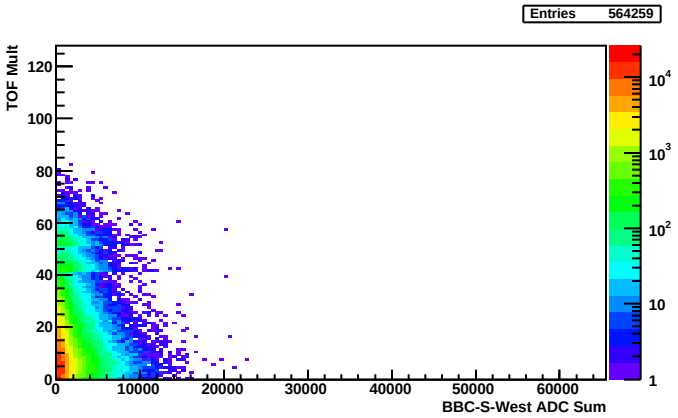

BBQ-VP004 (HI threshold)
Entries 9028144
ADC
QT Input Channel

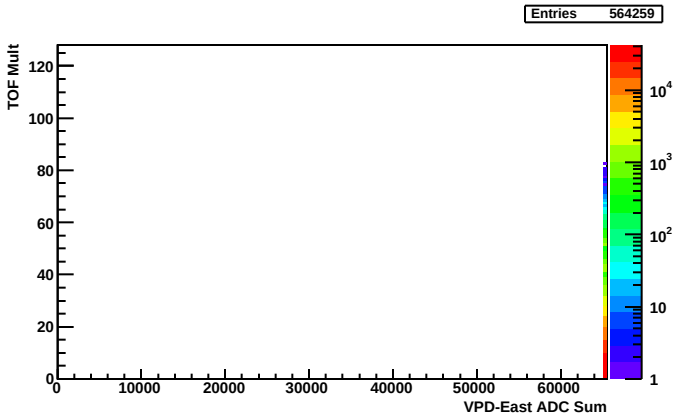

BBQ-VP004 (HI threshold)
Entries 9028144
TAC
QT Input Channel

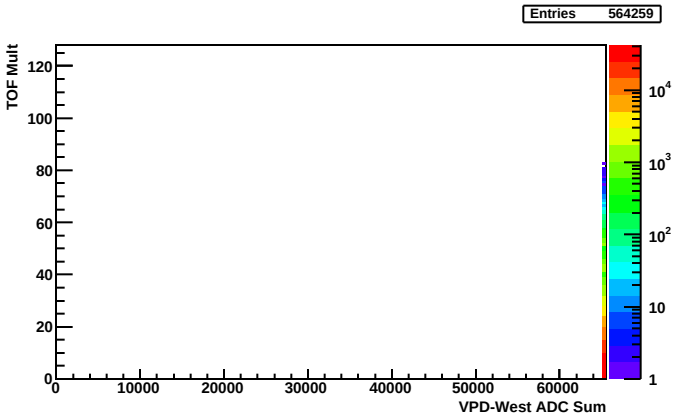

Entries 564259
TOF Mult
BBC-L-East ADC Sum

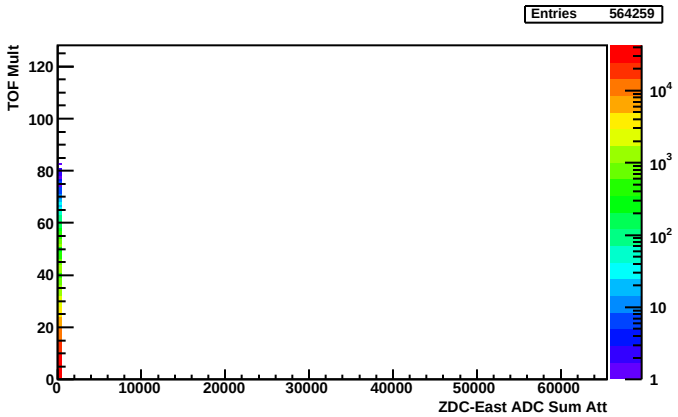

Entries 564259
TOF Mult
BBC-L-West ADC Sum

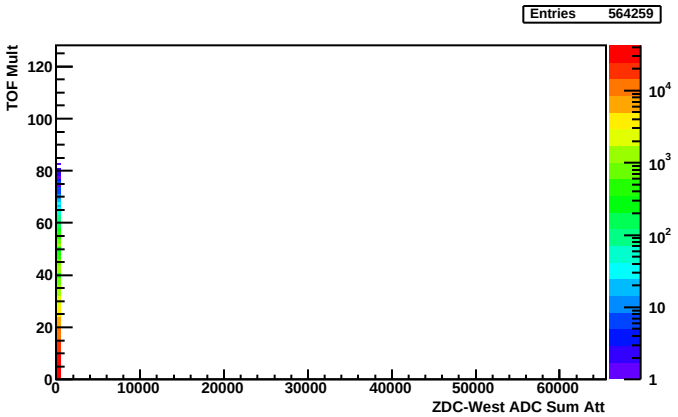

Entries 564259
TOF Mult
BBC-S-East ADC Sum

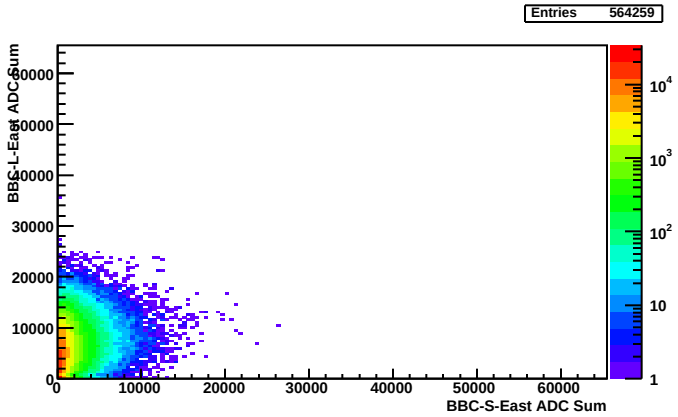

Entries 564259
TOF Mult
BBC-S-West ADC Sum

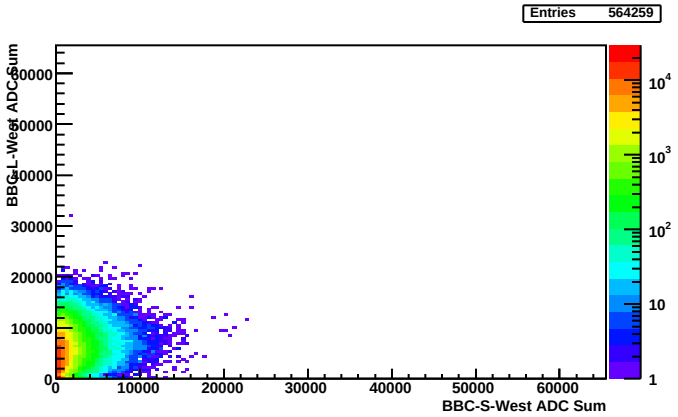

Entries 564259
TOF Mult
VPD-East ADC Sum

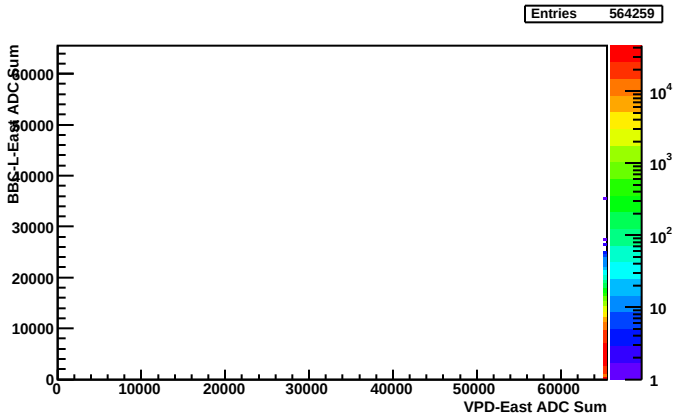

Entries 564259
TOF Mult
VPD-West ADC Sum

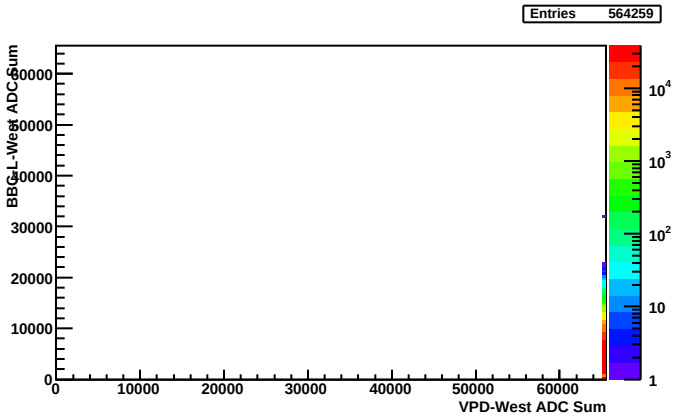

Entries 564259
TOF Mult
ZDC-East ADC Sum Att

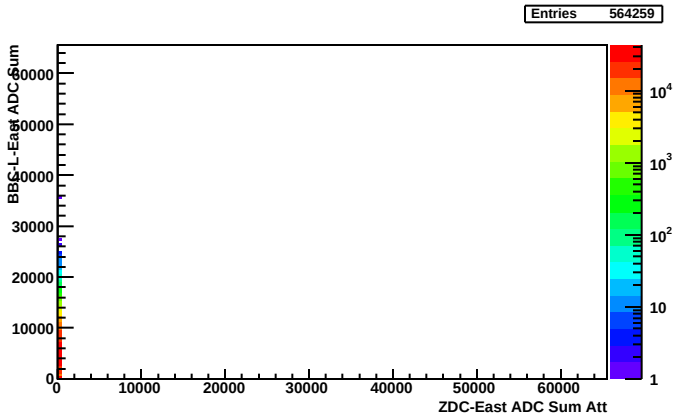

Entries 564259
TOF Mult
ZDC-West ADC Sum Att

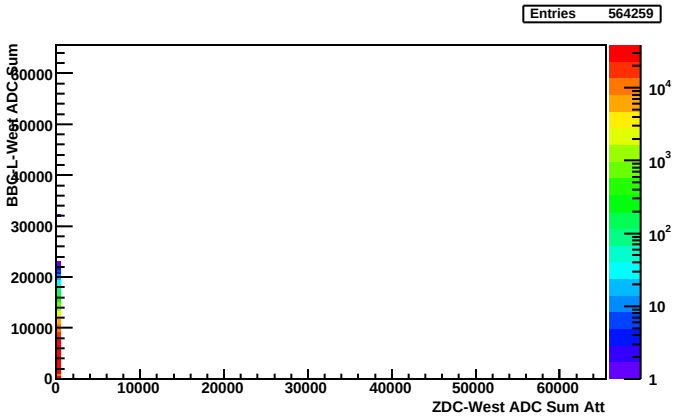

Entries 564259
BBC-L-East ADC Sum
BBC-S-East ADC Sum

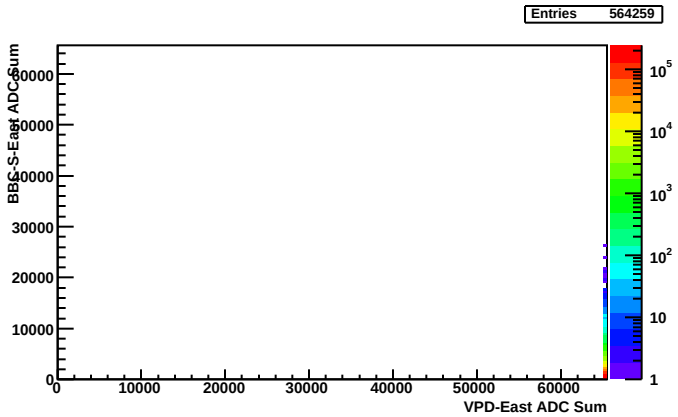

Entries 564259
BBC-L-West ADC Sum
BBC-S-West ADC Sum

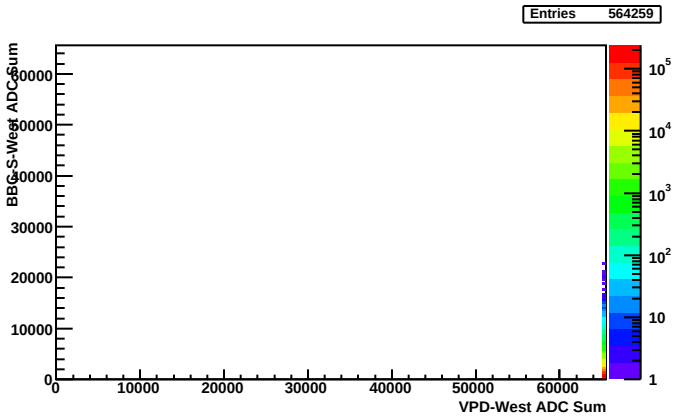

Entries 564259
BBC-L-East ADC Sum
VPD-East ADC Sum

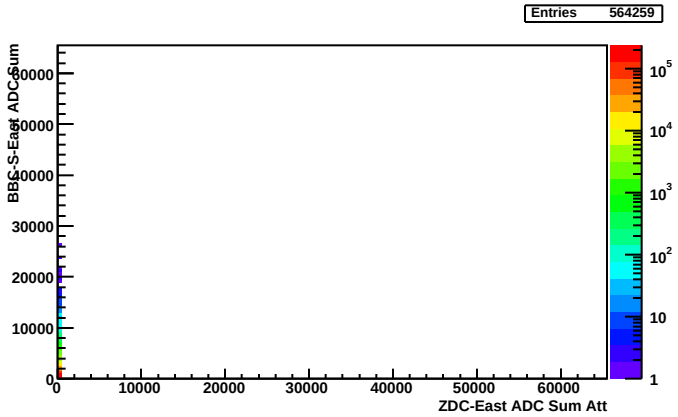

Entries 564259
BBC-L-West ADC Sum
VPD-West ADC Sum

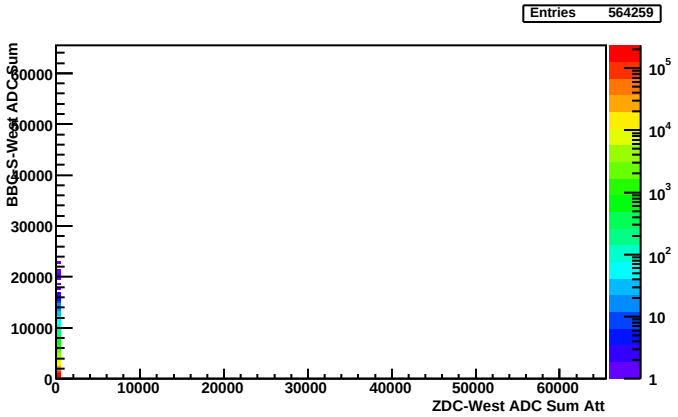

Entries 564259
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

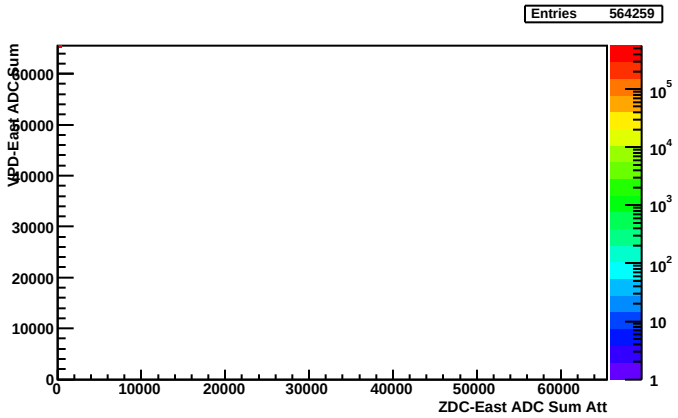

Entries 564259
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

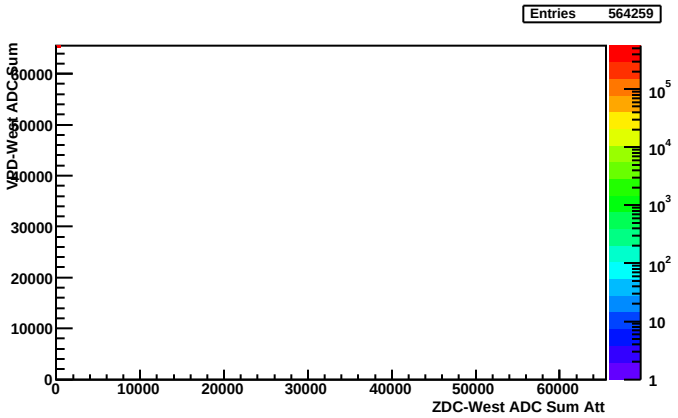

Entries 564259
BBC-S-East ADC Sum
VPD-East ADC Sum

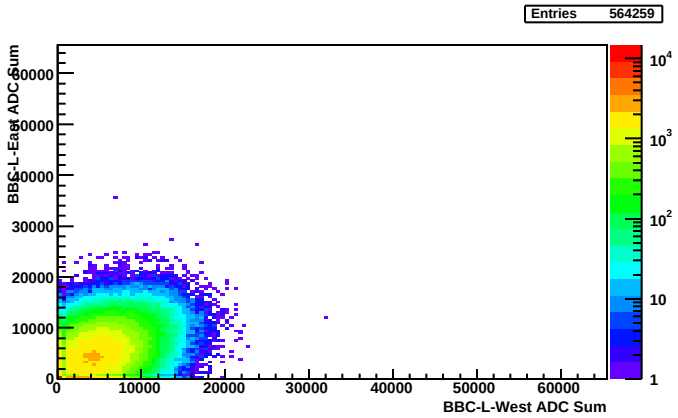

Entries 564259
BBC-S-West ADC Sum
VPD-West ADC Sum

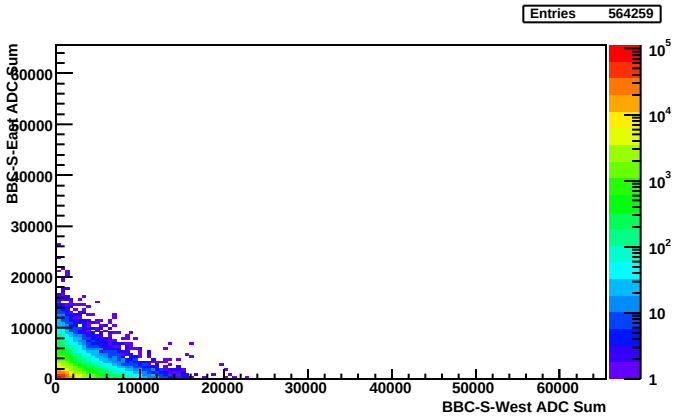

Entries 564259
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

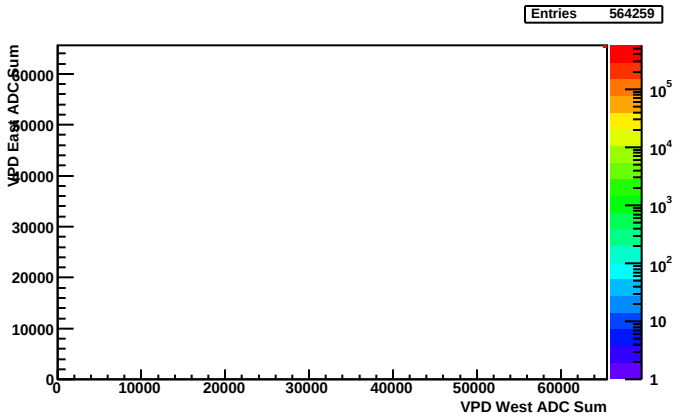

Entries 564259
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

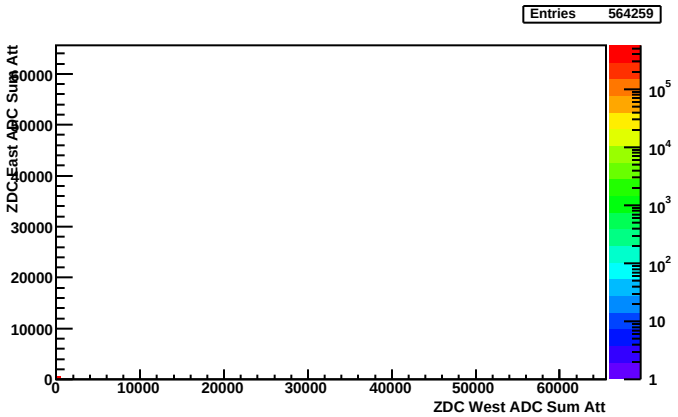

Entries 564259
VPD-East ADC Sum
ZDC-East ADC Sum Att

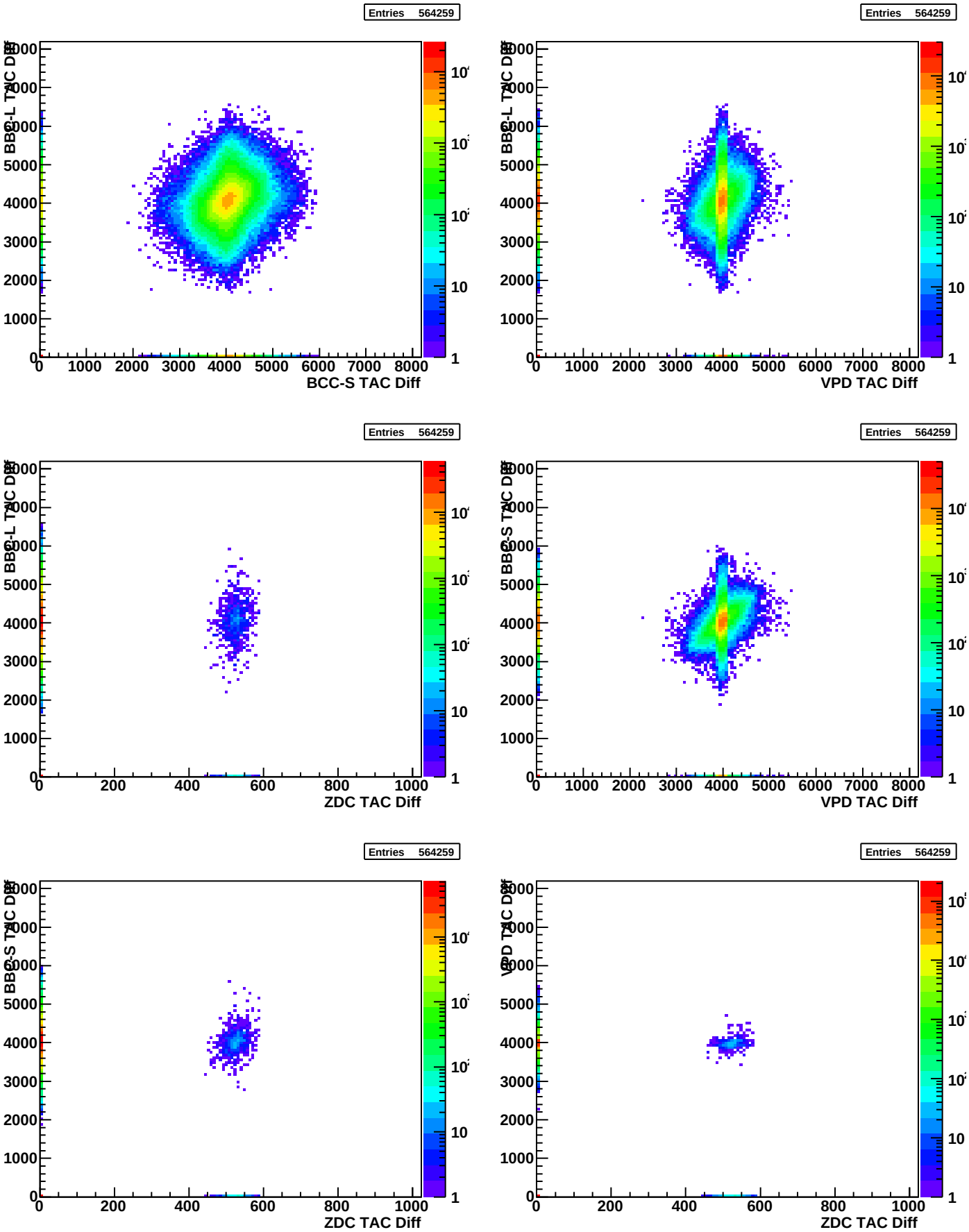

Entries 564259
VPD-West ADC Sum
ZDC-West ADC Sum Att

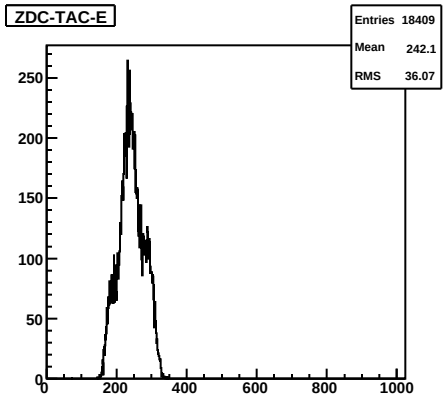

Entries 564259
BBC-L-East ADC Sum
BBC-L-West ADC Sum

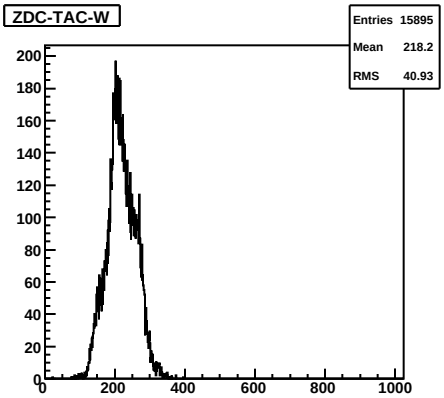

Entries 564259
BBC-S-East ADC Sum
BBC-S-West ADC Sum

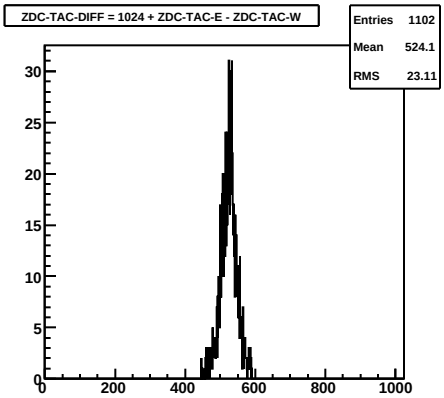

Entries 564259
VPD East ADC Sum
VPD West ADC Sum

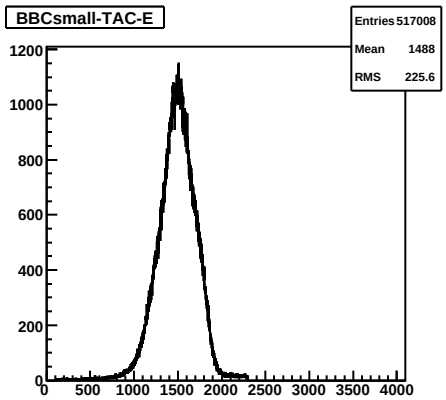

Entries 564259
ZDC East ADC Sum Att
ZDC West ADC Sum Att

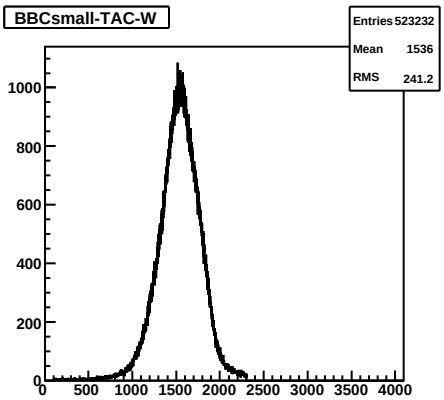

Entries 564259
BBC-L TAC Diff
BCC-S TAC Diff
Entries 564259
BBC-L TAC Diff
VPD TAC Diff
Entries 564259
BBC-L TAC Diff
ZDC TAC Diff
Entries 564259
BBC-S TAC Diff
VPD TAC Diff
Entries 564259
BBC-S TAC Diff
ZDC TAC Diff
Entries 564259
VPD TAC Diff
ZDC TAC Diff

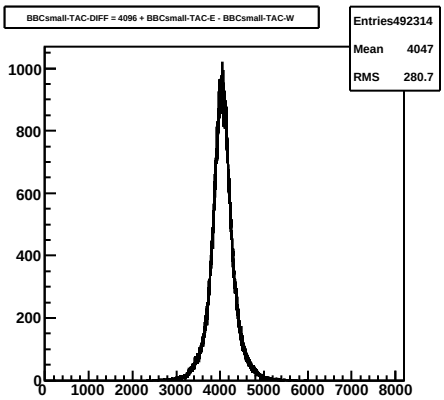

ZDC-TAC-E
Entries 18409
Mean 242.1
RMS 36.07

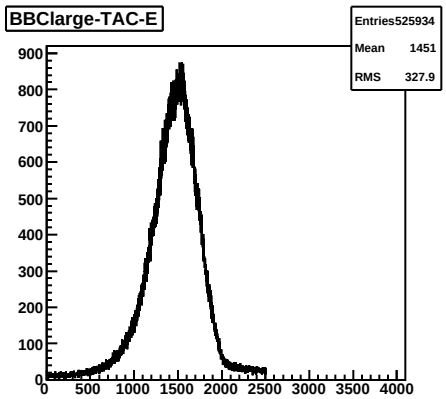

ZDC-TAC-W
Entries 15895
Mean 218.2
RMS 40.93

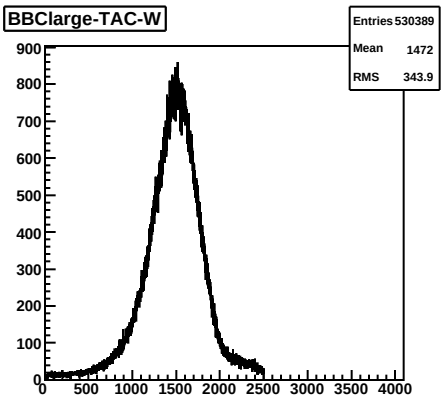

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1102
Mean 524.1
RMS 23.11

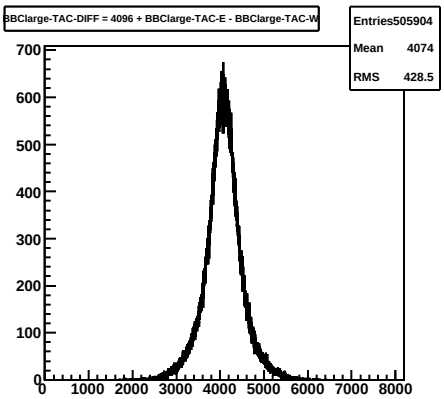

BBCsmall-TAC-E
Entries 517008
Mean 1488
RMS 225.6

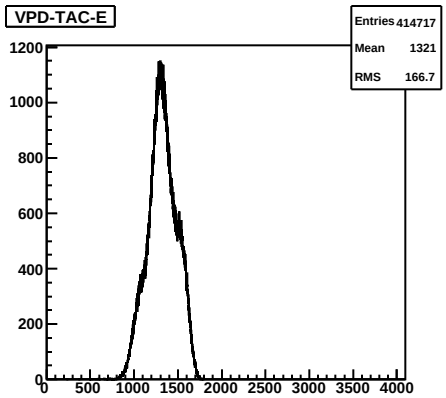

BBCsmall-TAC-W
Entries 523232
Mean 1536
RMS 241.2

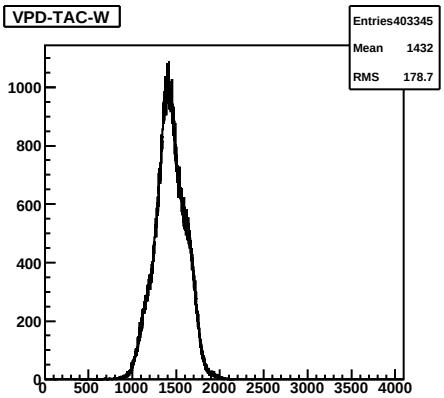

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 492314
Mean 4047
RMS 280.7

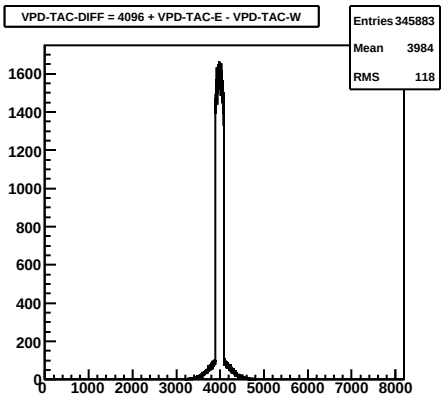

BBClarge-TAC-E
Entries 525934
Mean 1451
RMS 327.9

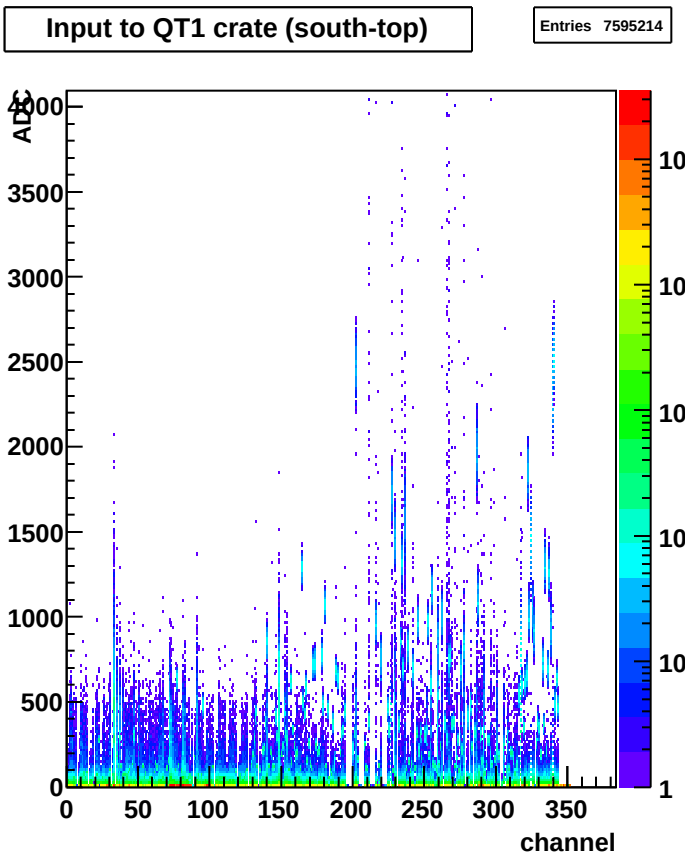

BBClarge-TAC-W
Entries 530389
Mean 1472
RMS 343.9

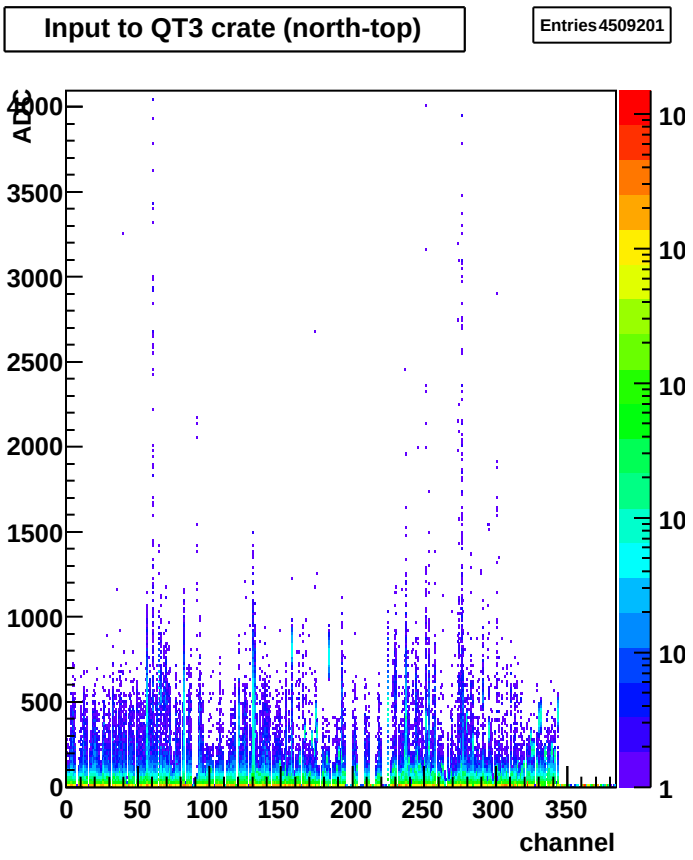

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 505904
Mean 4074
RMS 428.5

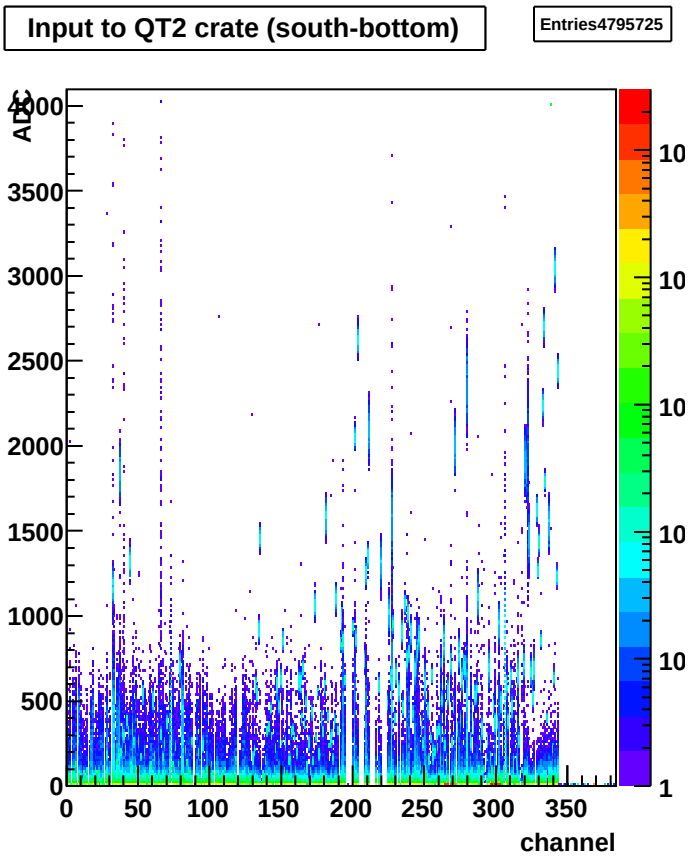

VPD-TAC-E
Entries 414717
Mean 1321
RMS 166.7

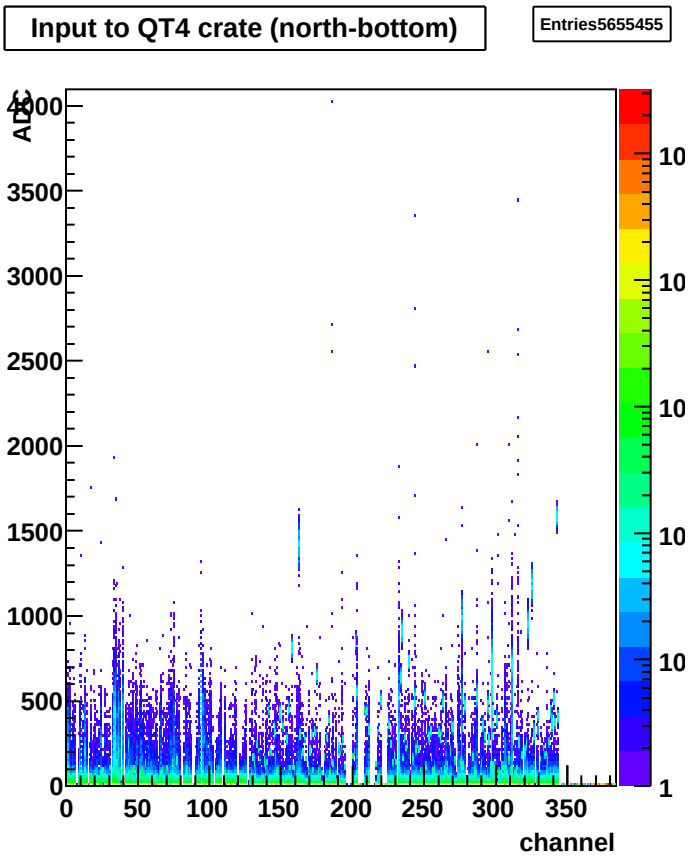

VPD-TAC-W
Entries 403345
Mean 1432
RMS 178.7

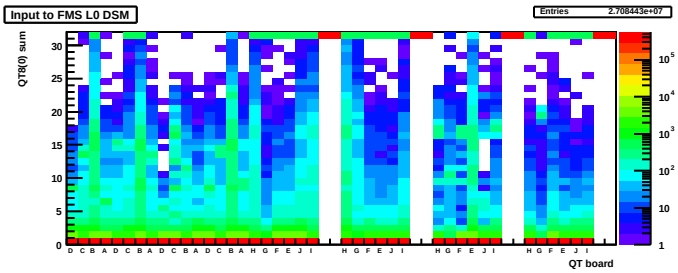

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 345883
Mean 3984
RMS 118

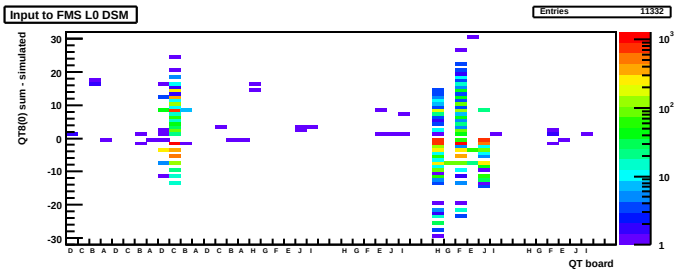

Input to QT1 crate (south-top)
Entries 7595214
ADC
channel

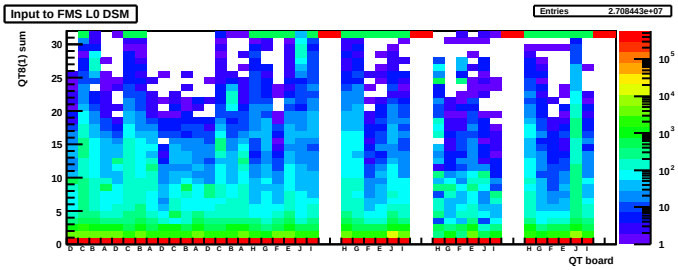

Input to QT3 crate (north-top)
Entries 4509201
ADC
channel

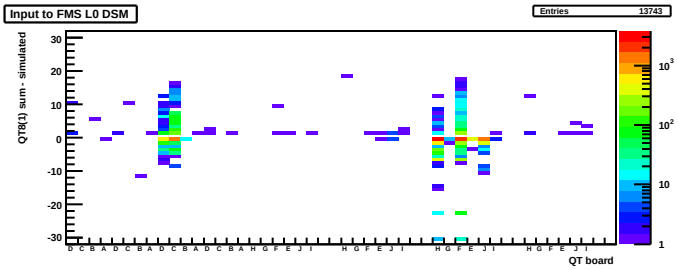

Input to QT2 crate (south-bottom)
Entries 4795725
ADC
channel

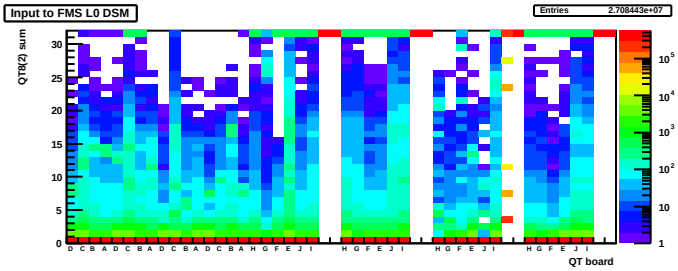

Input to QT4 crate (north-bottom)
Entries 5655455
ADC
channel

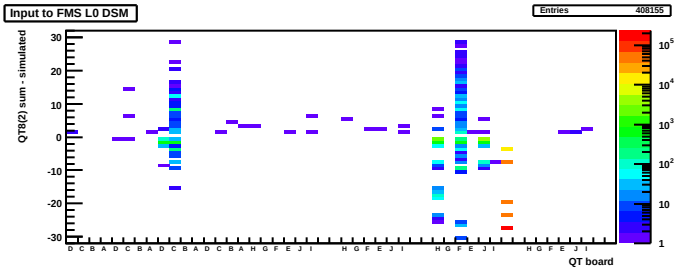

Input to FMS L0 DSM
Entries 2.708443e+07
QT8(0) sum
QT board

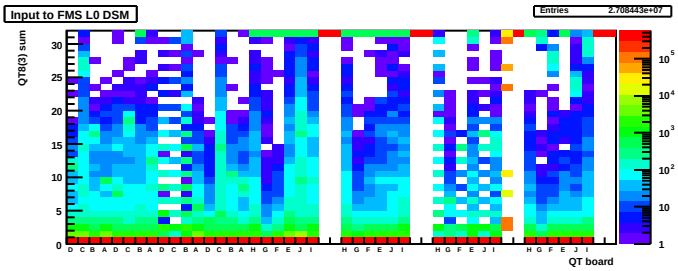

Input to FMS L0 DSM
Entries 11332
QT8(0) sum - simulated
QT board

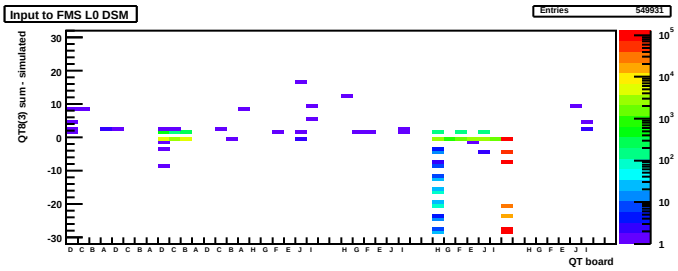

Input to FMS L0 DSM
Entries 2.708443e+07
QT8(1) sum
QT board

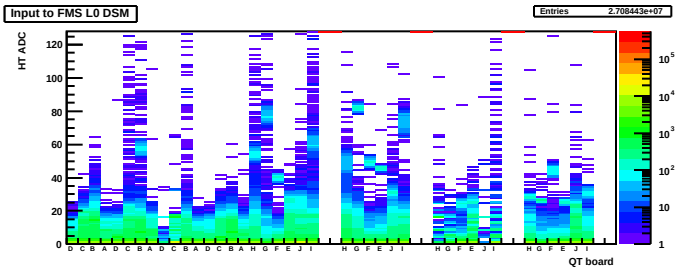

Input to FMS L0 DSM
Entries 13743
QT8(1) sum - simulated
QT board

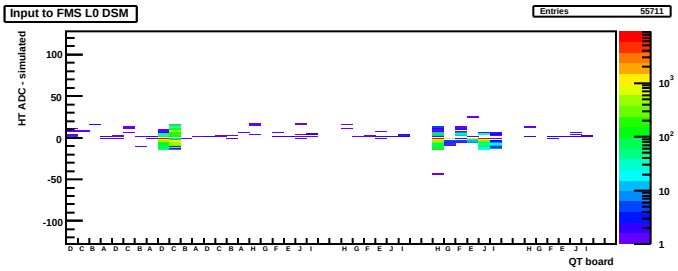

Input to FMS L0 DSM
Entries 2.708443e+07
QT8(2) sum
QT board

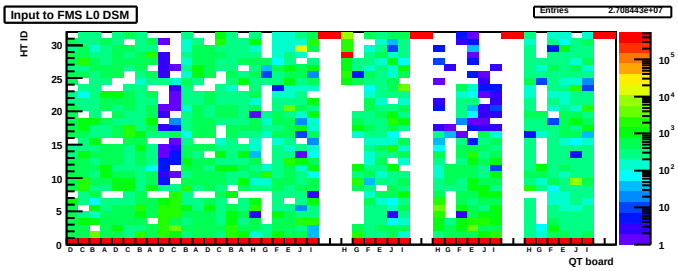

Input to FMS L0 DSM
Entries 408155
QT8(2) sum - simulated
QT board

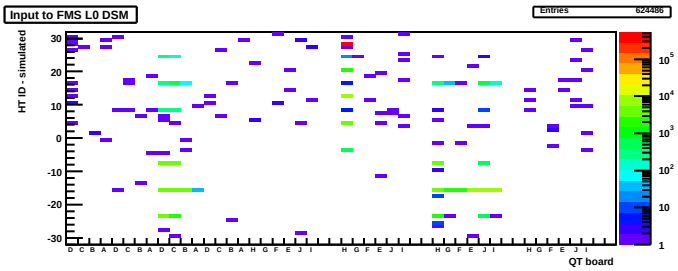

Input to FMS L0 DSM
Entries 2.708443e+07
QT8(3) sum
QT board

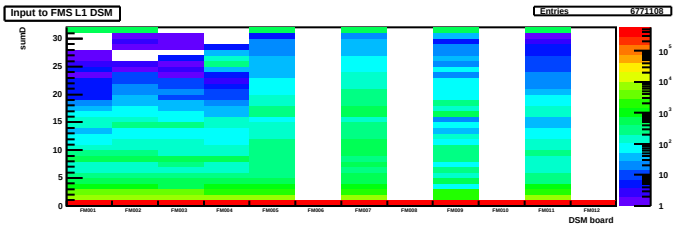

Input to FMS L0 DSM
Entries 549931
QT8(3) sum - simulated
QT board

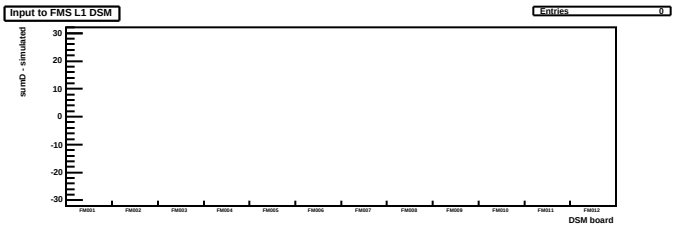

Input to FMS L0 DSM
Entries 2.708443e+07
HT ADC
QT board

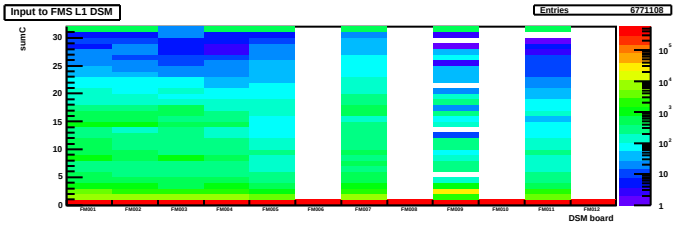

Input to FMS L0 DSM
Entries 55711
HT ADC - simulated
QT board

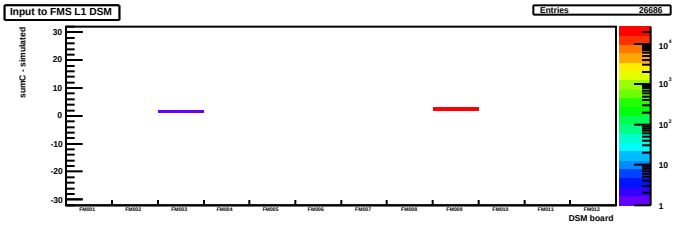

Input to FMS L0 DSM
Entries 2.708443e+07
HT ID
QT board

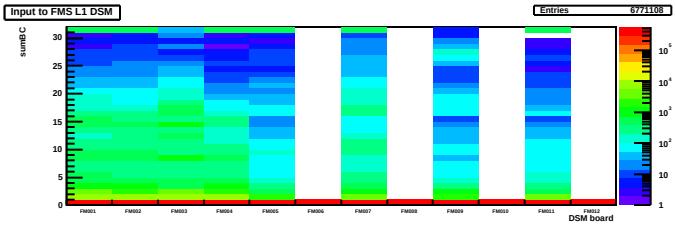

Input to FMS L0 DSM
Entries 624486
HT ID - simulated
QT board

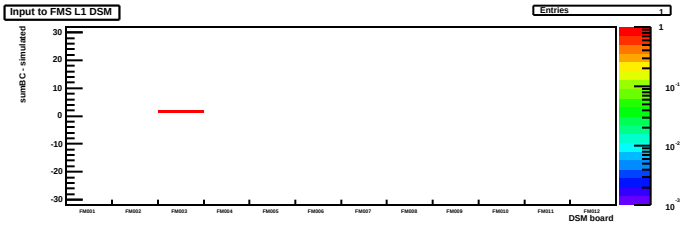

Input to FMS L1 DSM
Entries 6771108
sumD
DSM board

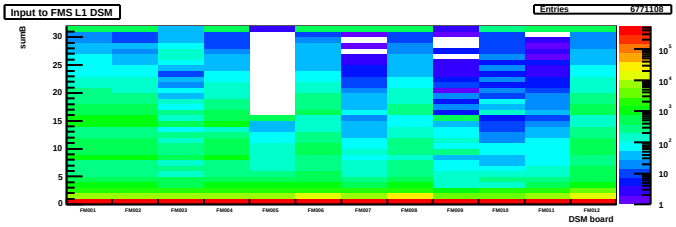

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

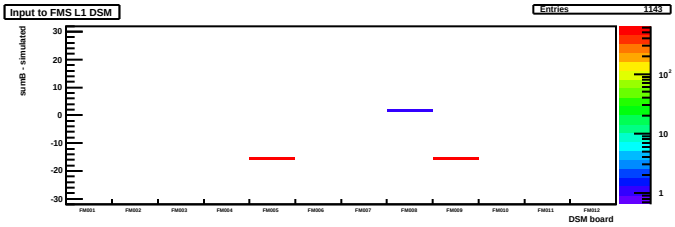

Input to FMS L1 DSM
Entries 6771108
sumC
DSM board

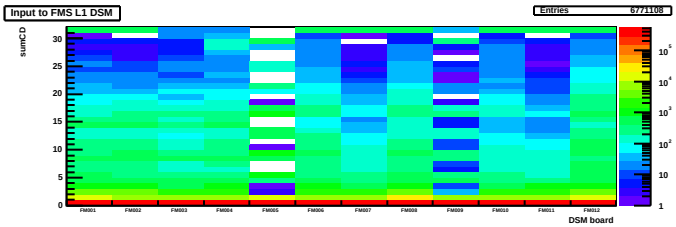

Input to FMS L1 DSM
Entries 26686
sumC - simulated
DSM board

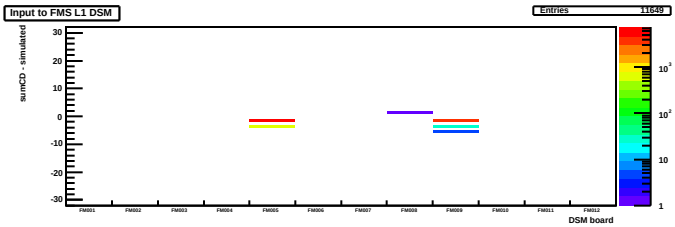

Input to FMS L1 DSM
Entries 6771108
sumBC
DSM board

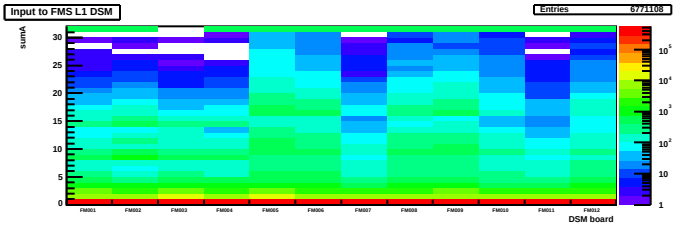

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

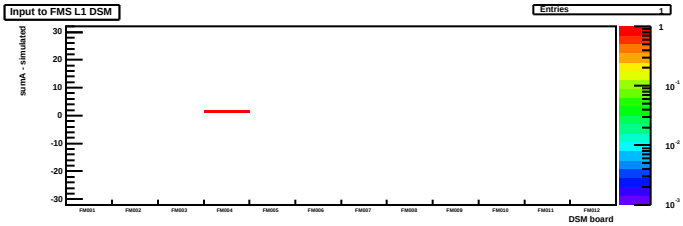

Input to FMS L1 DSM
Entries 6771108
sumB
DSM board

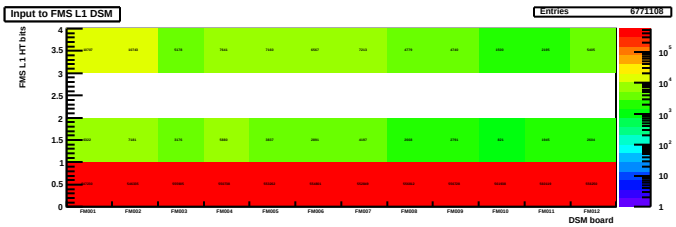

Input to FMS L1 DSM
Entries 1143
sumB - simulated
DSM board

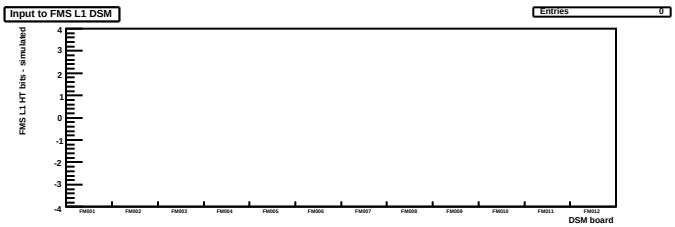

Input to FMS L1 DSM
Entries 6771108
sumCD
DSM board

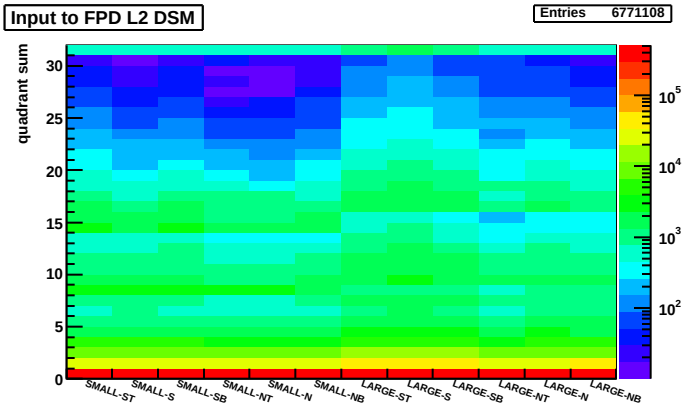

Input to FMS L1 DSM
Entries 11649
sumCD - simulated
DSM board

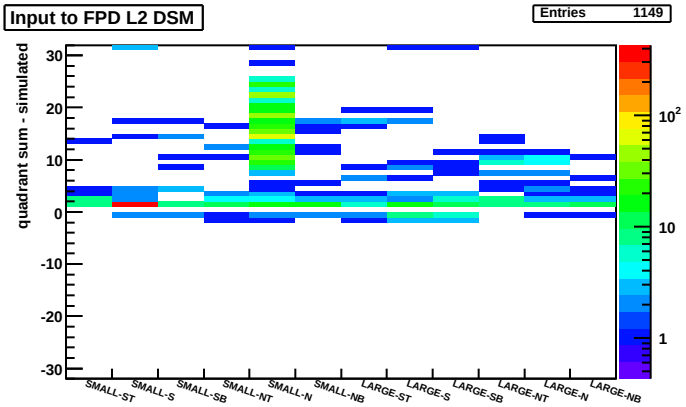

Input to FMS L1 DSM
Entries 6771108
sumA
DSM board

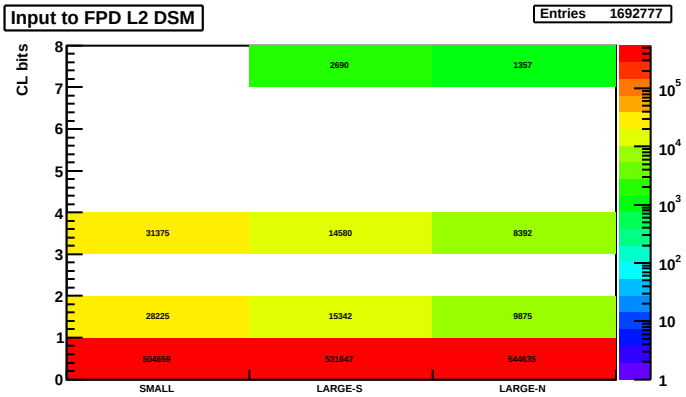

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

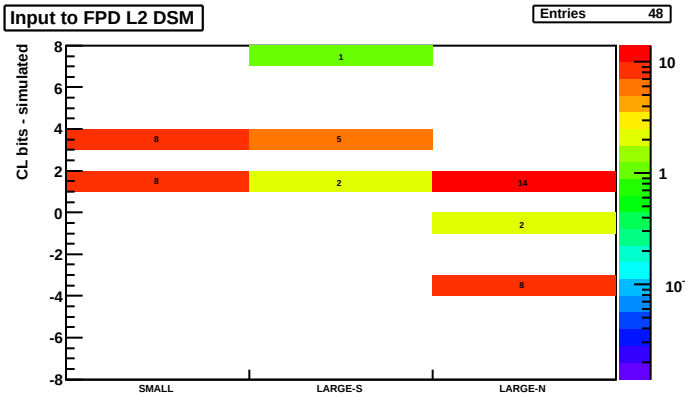

Input to FMS L1 DSM
Entries 6771108
FMS L1 HT bits
DSM board

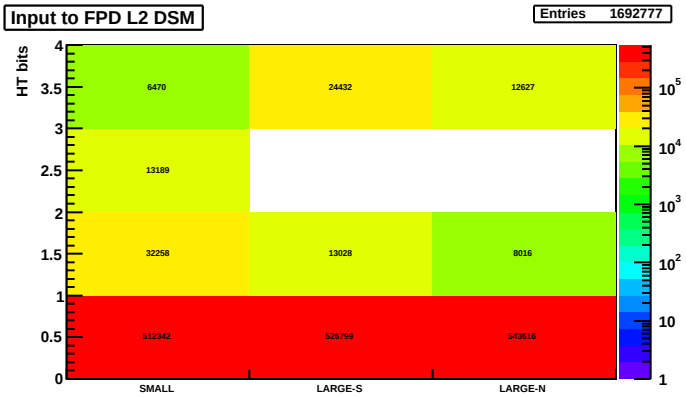

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

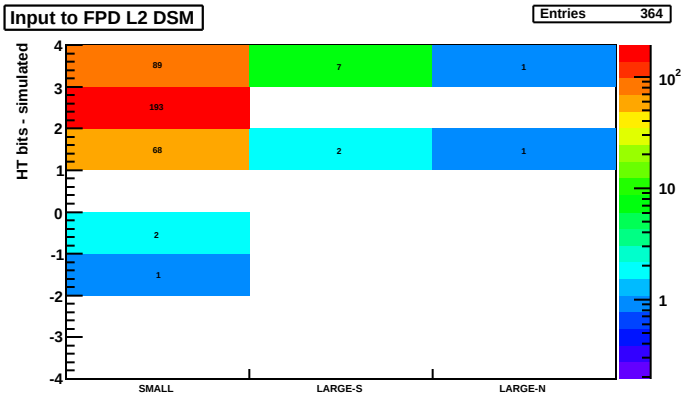

Input to FPD L2 DSM
Entries 6771108
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

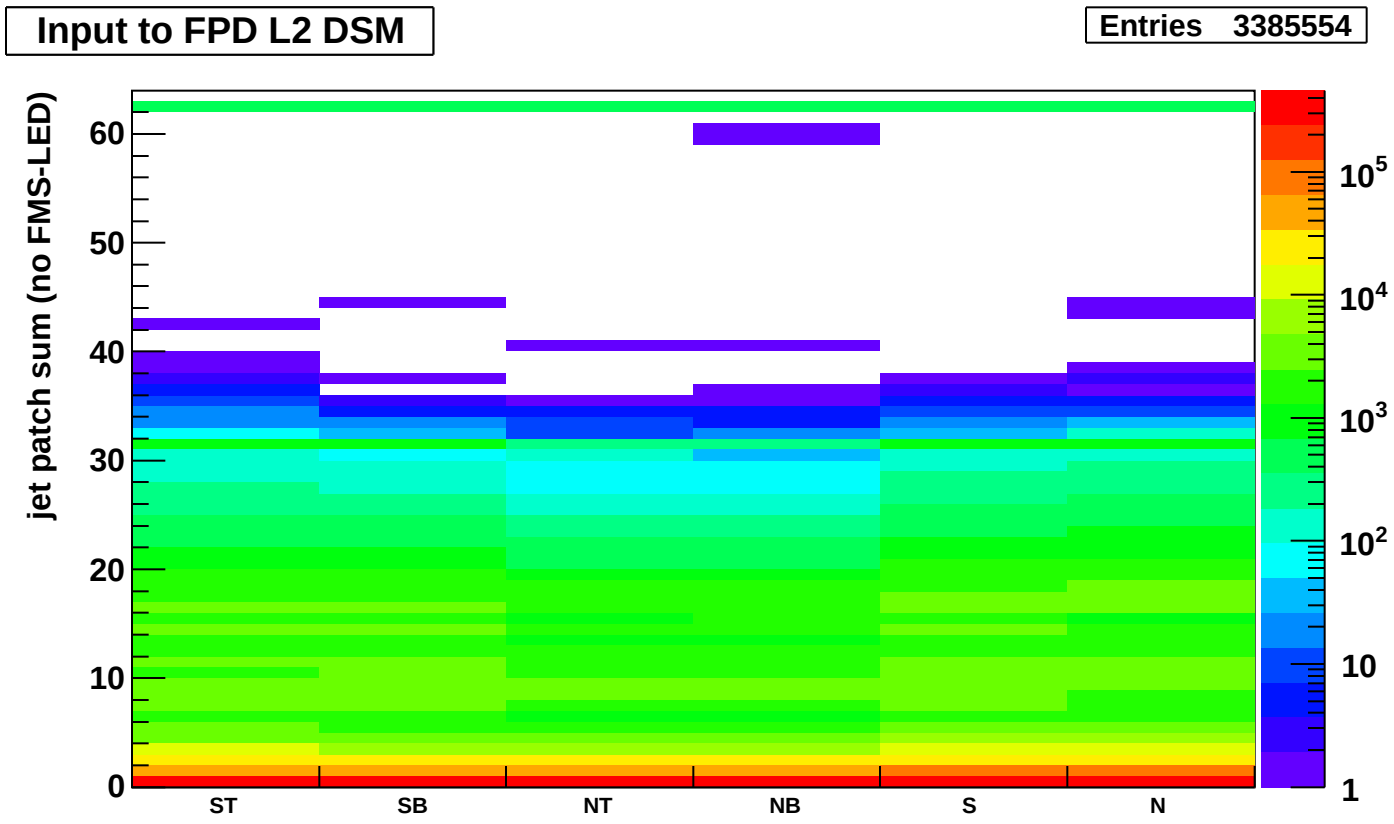

Input to FPD L2 DSM
Entries 1149
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

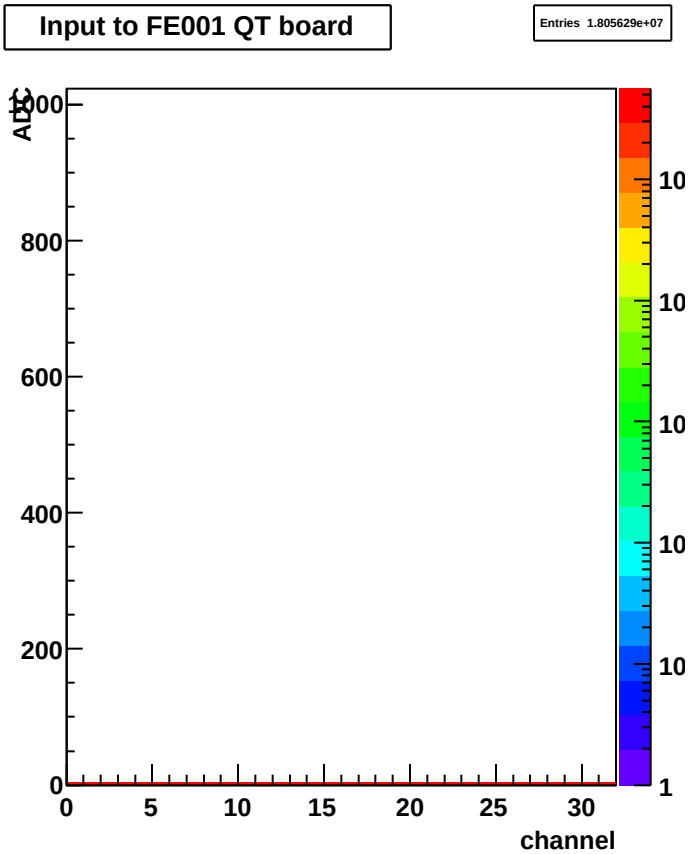

Input to FPD L2 DSM
Entries 1692777
CL bits
2690
1357
31375
14580
8392
28225
15342
9875
504659
531647
544635
SMALL
LARGE-S
LARGE-N

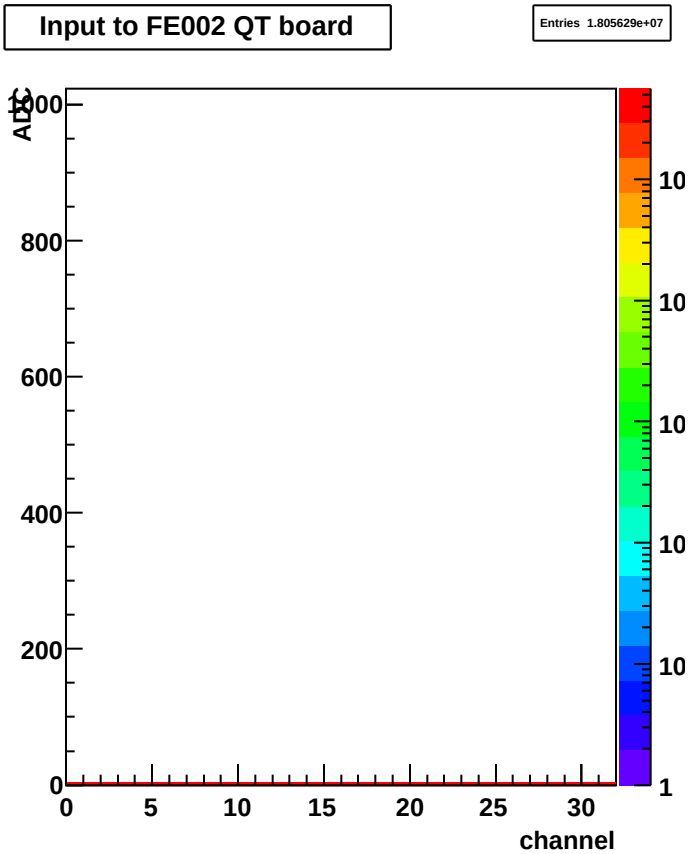

Input to FPD L2 DSM
Entries 48
CL bits - simulated
1
8
5
8
2
14
2
8
SMALL
LARGE-S
LARGE-N

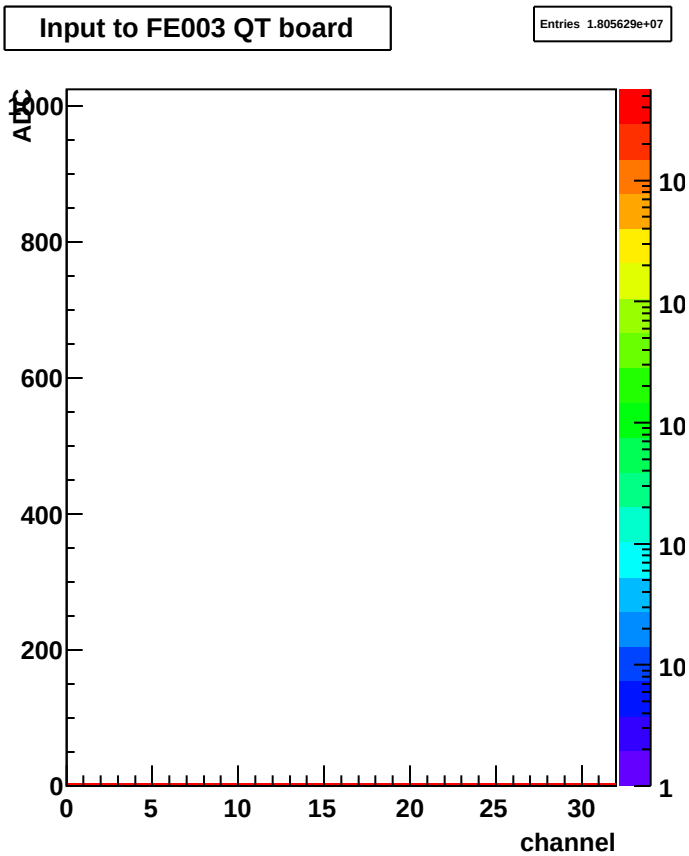

Input to FPD L2 DSM
Entries 1692777
HT bits
6470
24432
12627
13189
32258
13028
8016
512342
526799
543616
SMALL
LARGE-S
LARGE-N

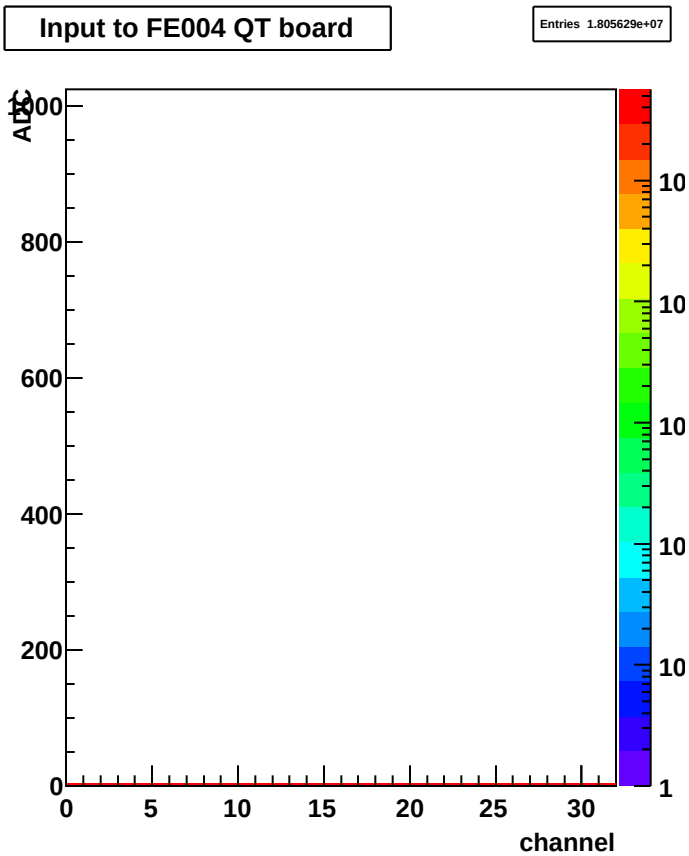

Input to FPD L2 DSM
Entries 364
HT bits - simulated
89
7
1
193
68
2
1
2
1
SMALL
LARGE-S
LARGE-N

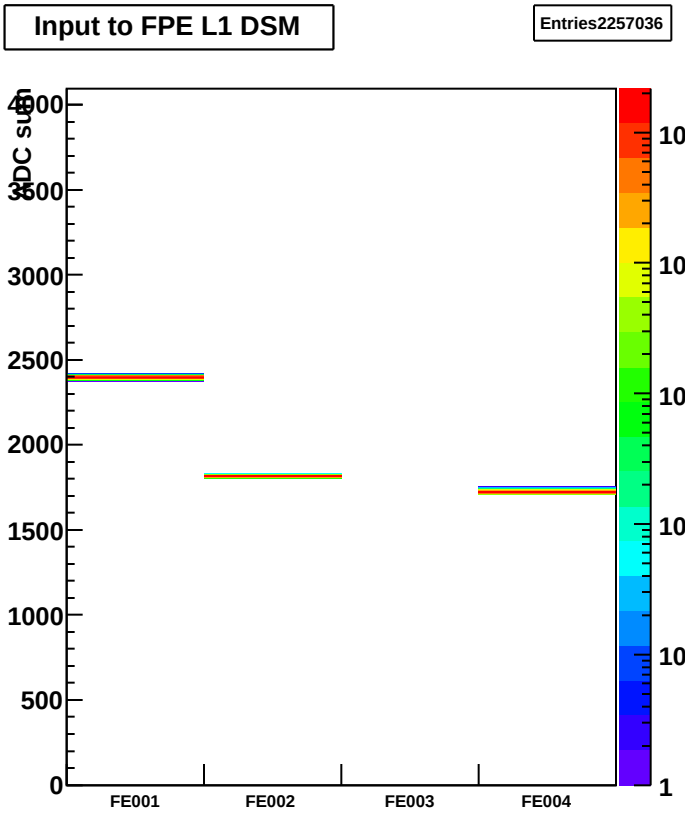

Input to FPD L2 DSM
Entries 3385554
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.805629e+07
ADC
channel

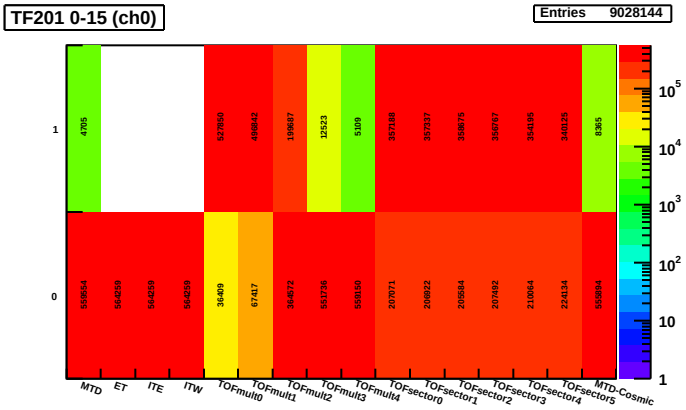

Input to FE002 QT board
Entries 1.805629e+07
ADC
channel


Input to FE003 QT board
Entries 1.805629e+07
ADC
channel

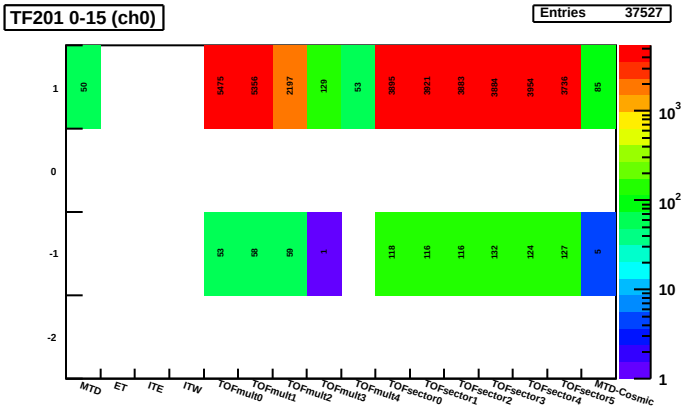

Input to FE004 QT board
Entries 1.805629e+07
ADC
channel


Input to FPE L1 DSM
Entries2257036
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

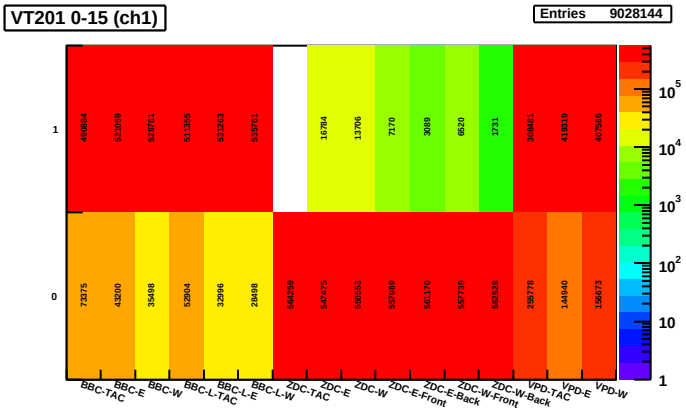
TF201 0-15 (ch0)



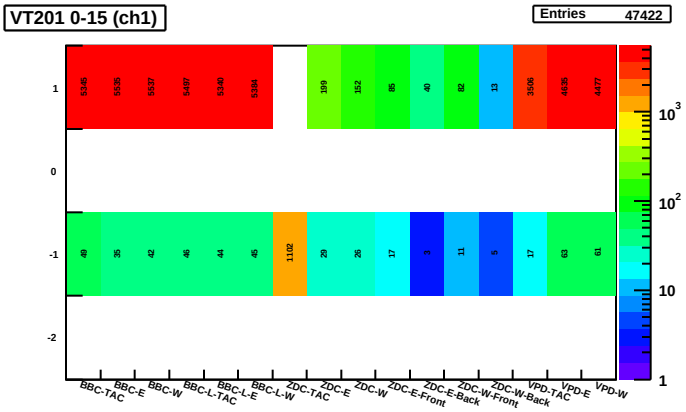
TF201 0-15 (ch0)



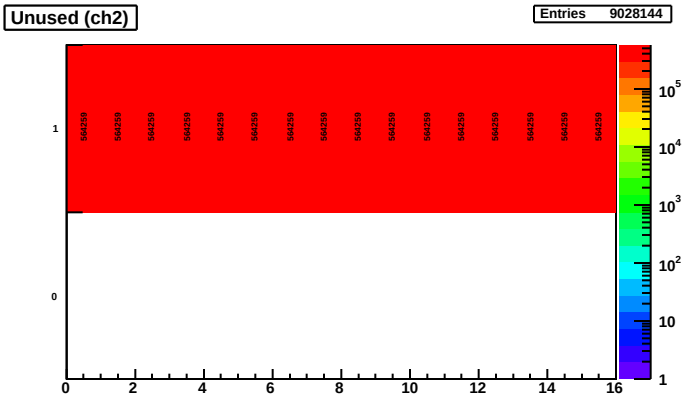
VT201 0-15 (ch1)

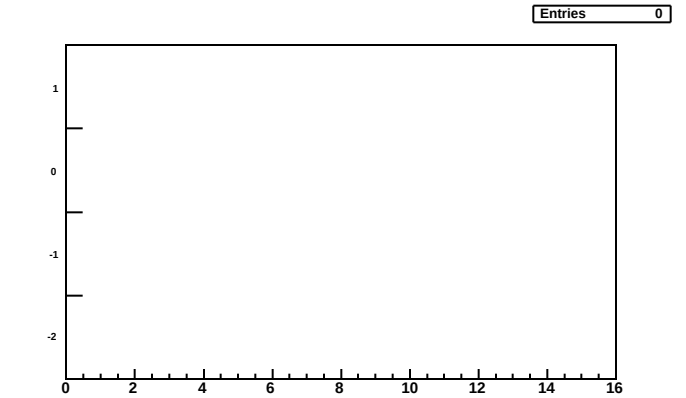


VT201 0-15 (ch1)

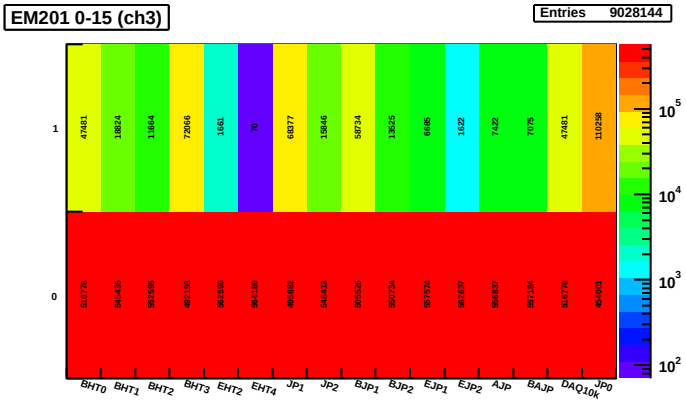


Unused (ch2)

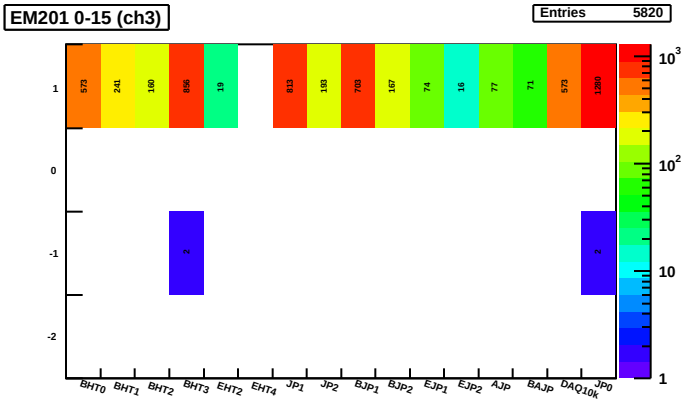




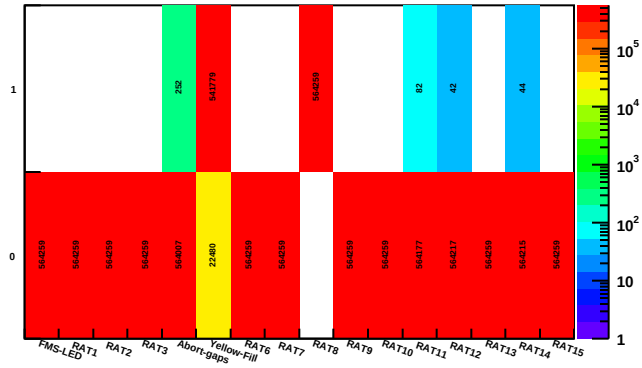
EM201 0-15 (ch3)

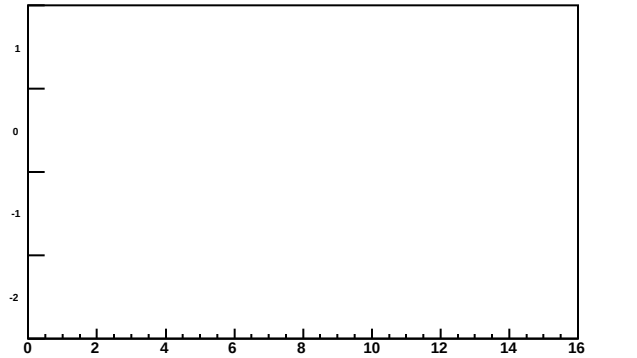


EM201 0-15 (ch3)

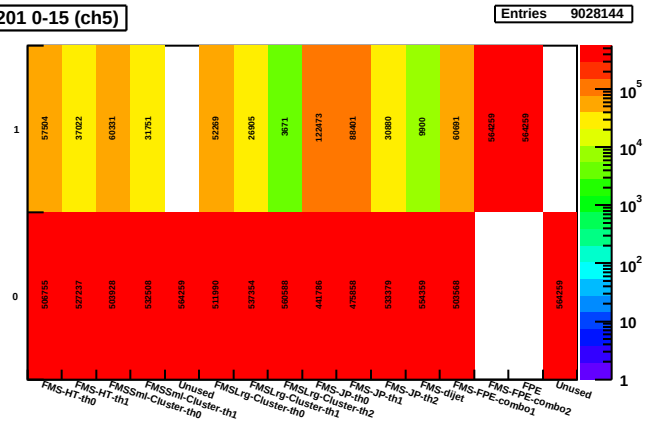


RAT board (ch4)

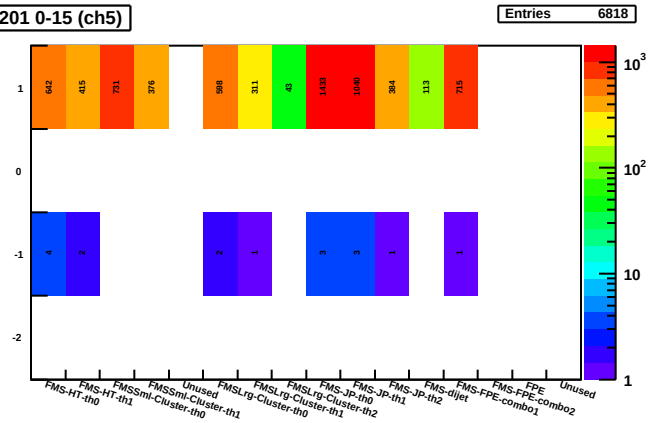




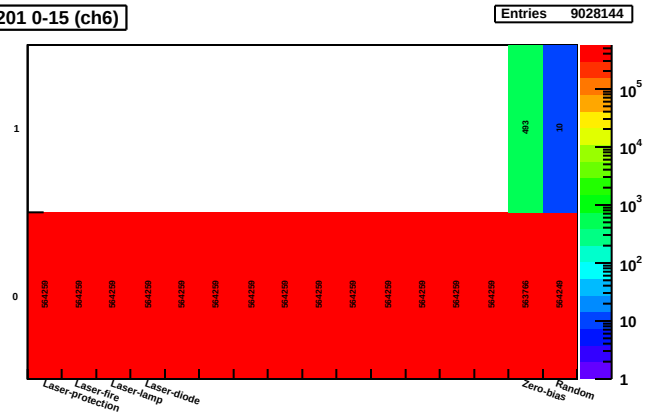
FP201 0-15 (ch5)

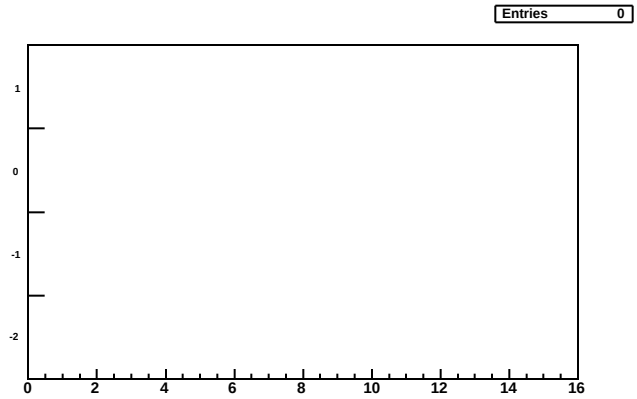
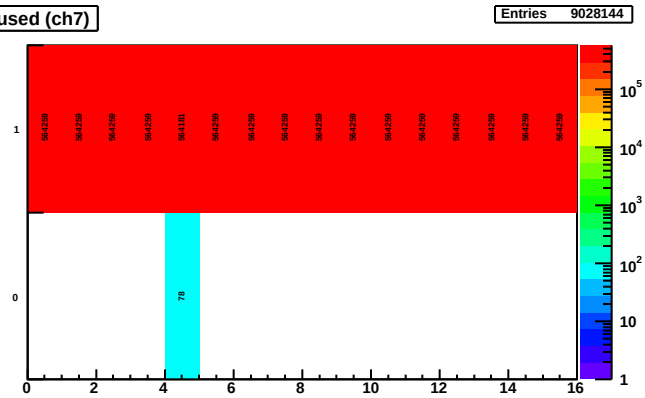
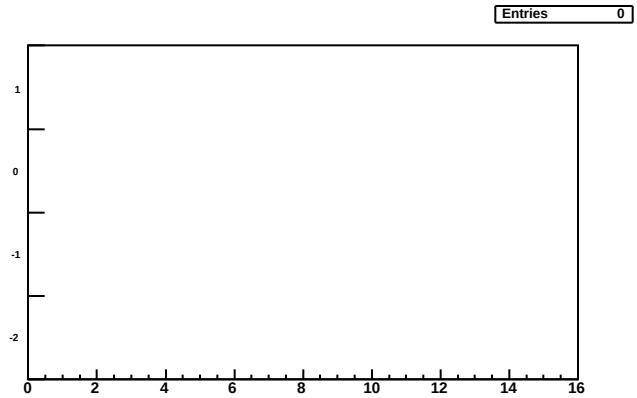


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)