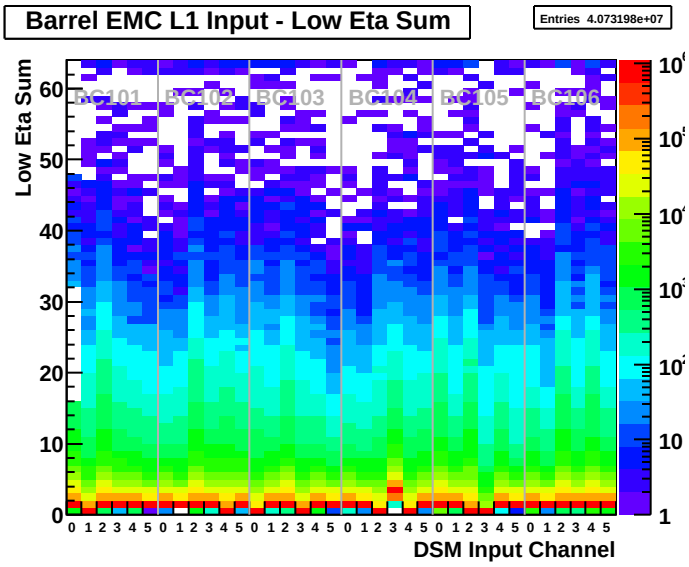
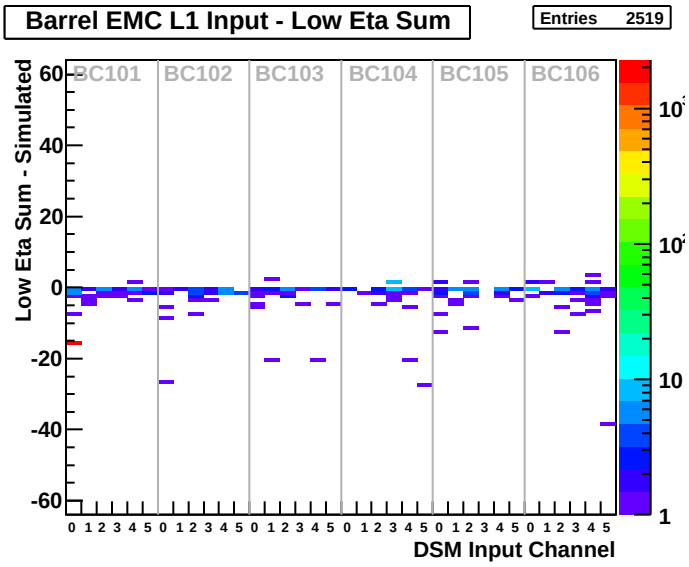
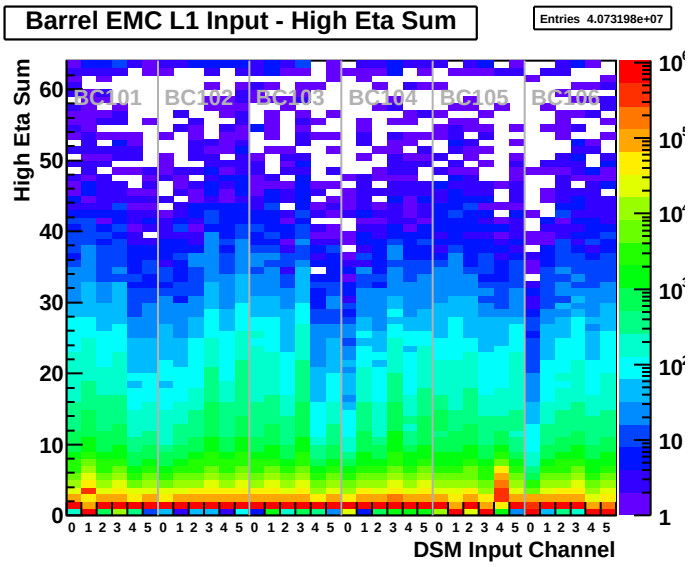
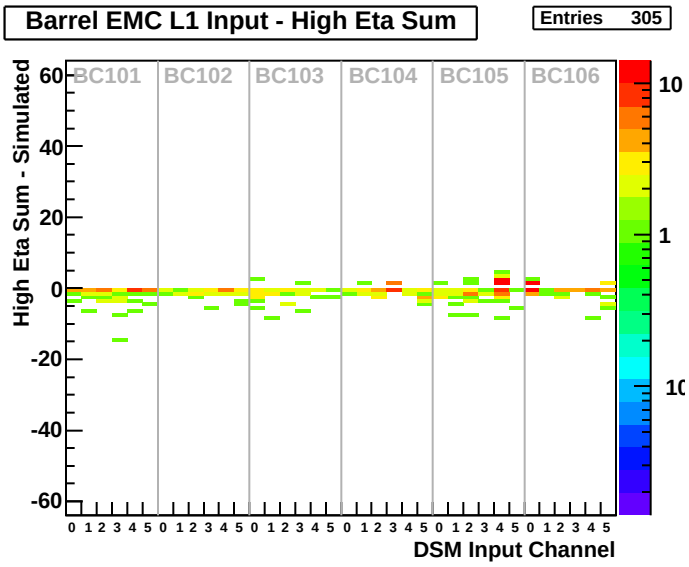
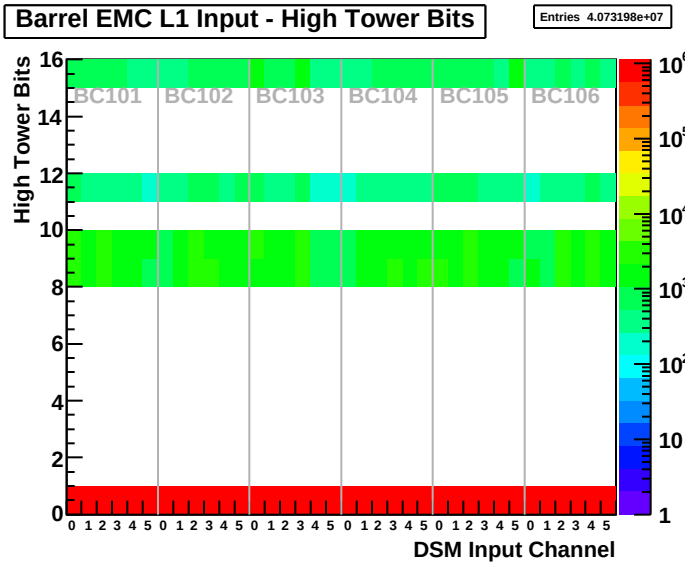
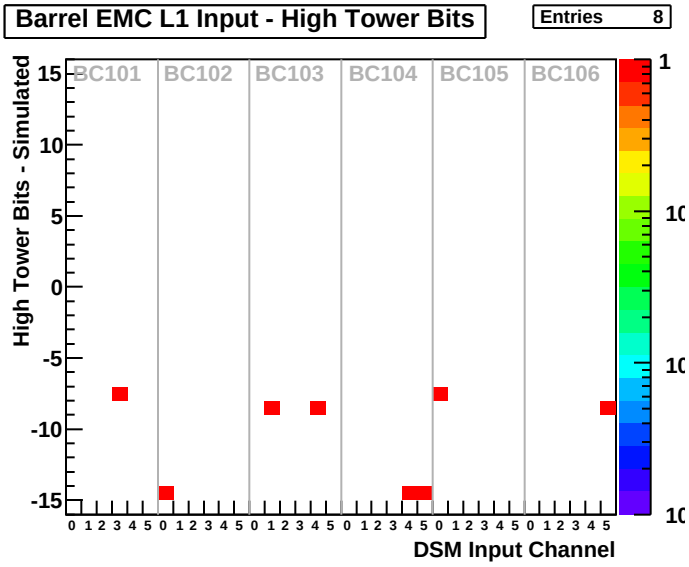

Barrel EMC L1 Input - Low Eta Sum
Entries 4.073198e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

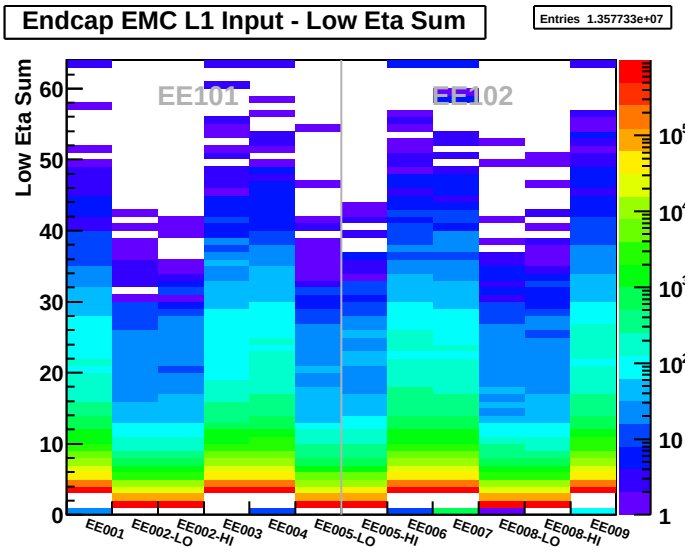

Barrel EMC L1 Input - Low Eta Sum
Entries 2519
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

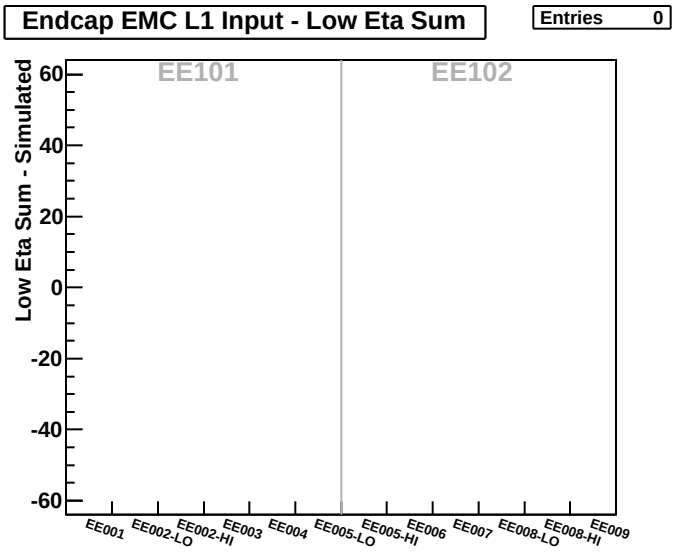

Barrel EMC L1 Input - High Eta Sum
Entries 4.073198e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

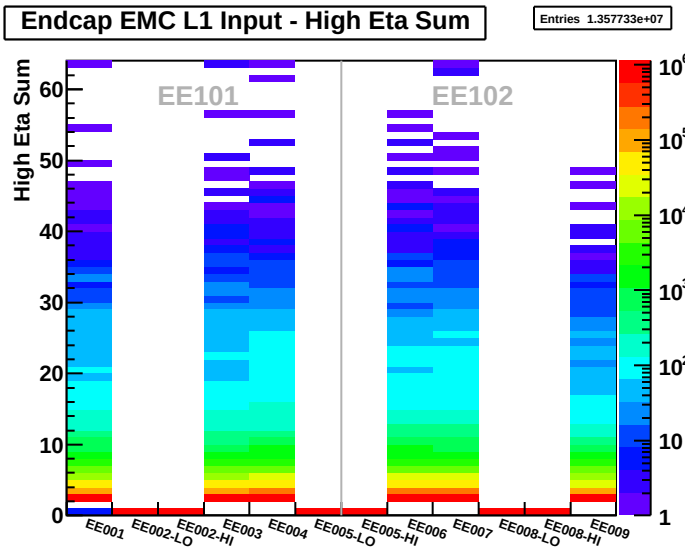

Barrel EMC L1 Input - High Eta Sum
Entries 305
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

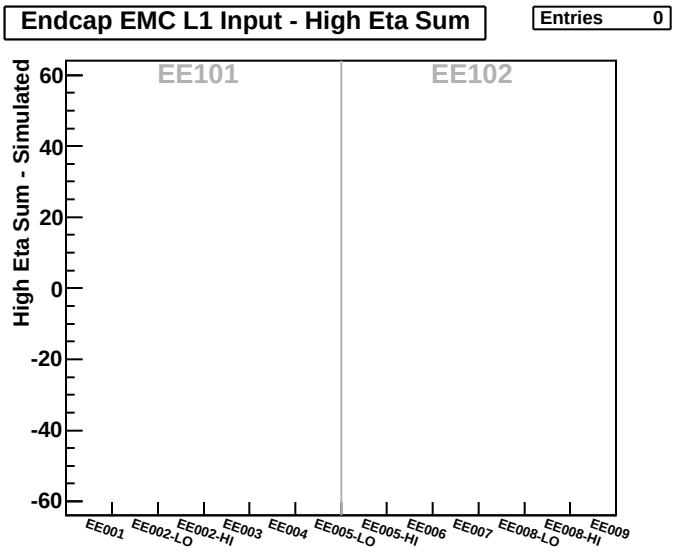

Barrel EMC L1 Input - High Tower Bits
Entries 4.073198e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

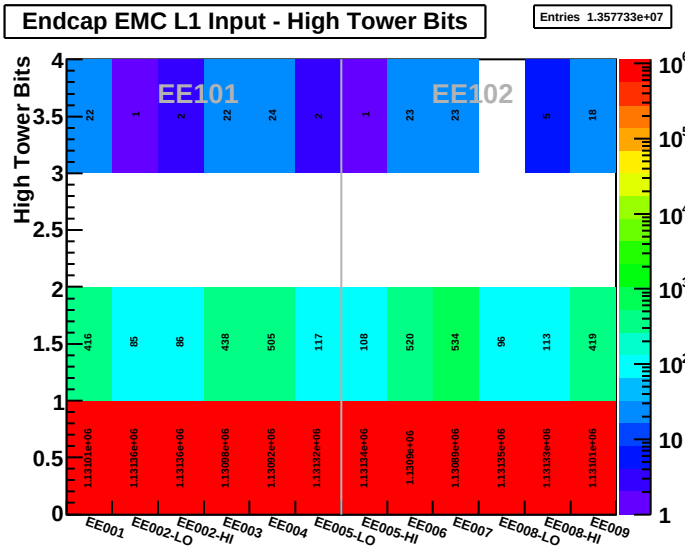

Barrel EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

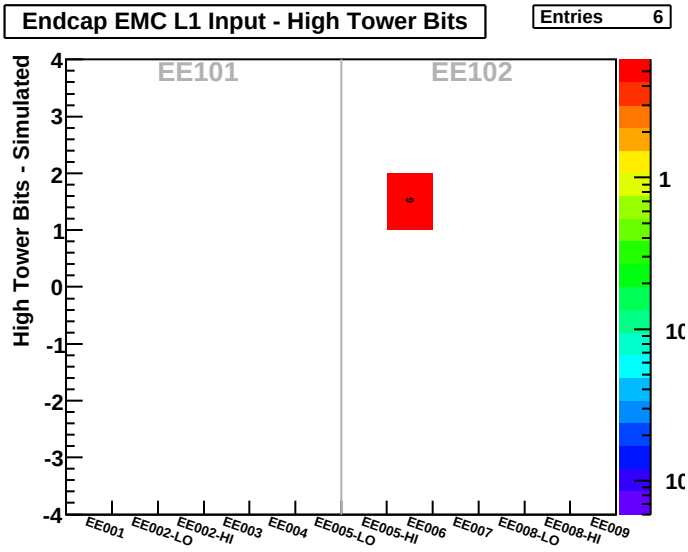

Endcap EMC L1 Input - Low Eta Sum
Entries 1.357733e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

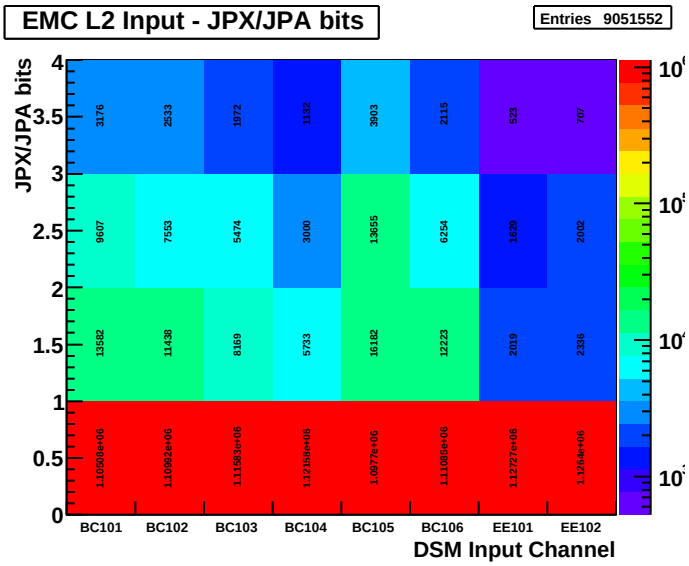

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

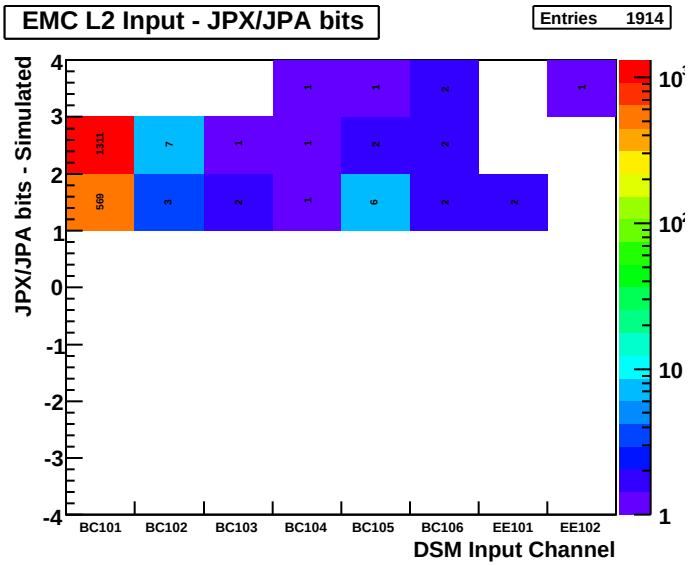

Endcap EMC L1 Input - High Eta Sum
Entries 1.357733e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

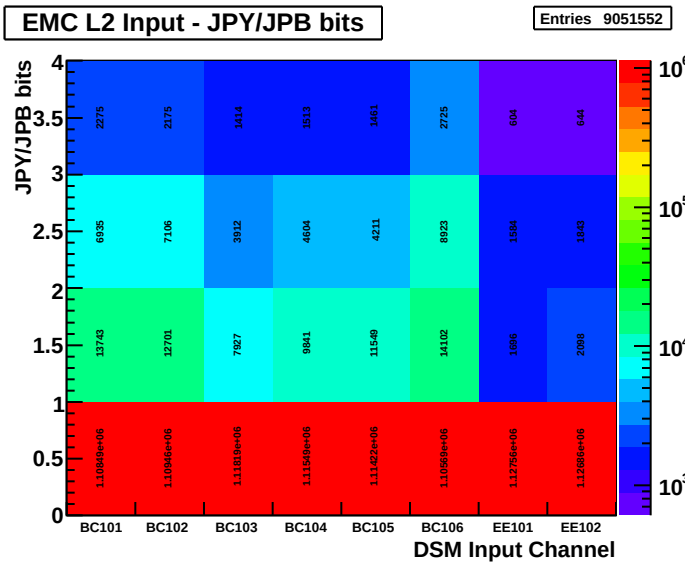

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

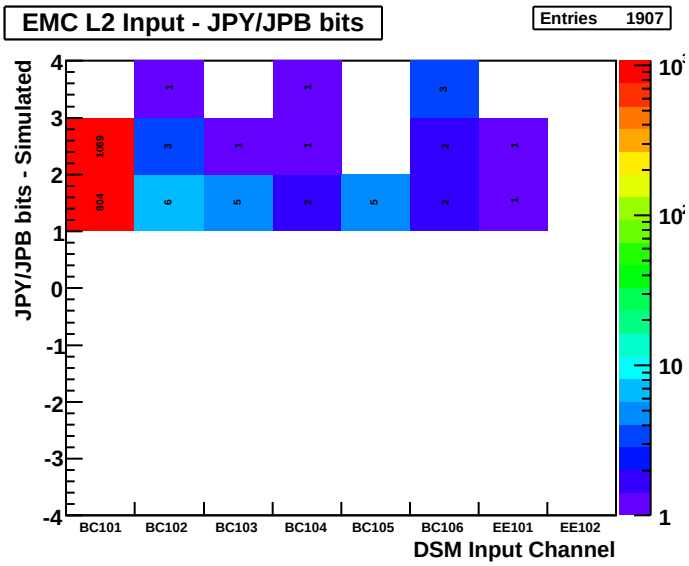

Endcap EMC L1 Input - High Tower Bits
Entries 1.357733e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

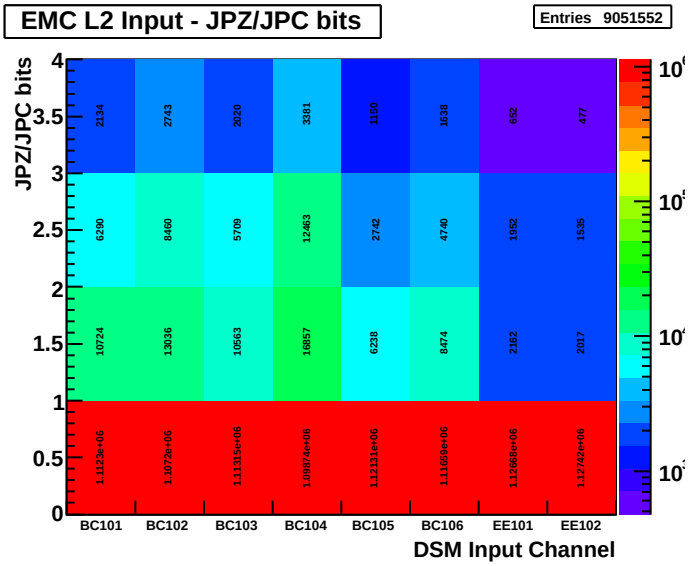

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

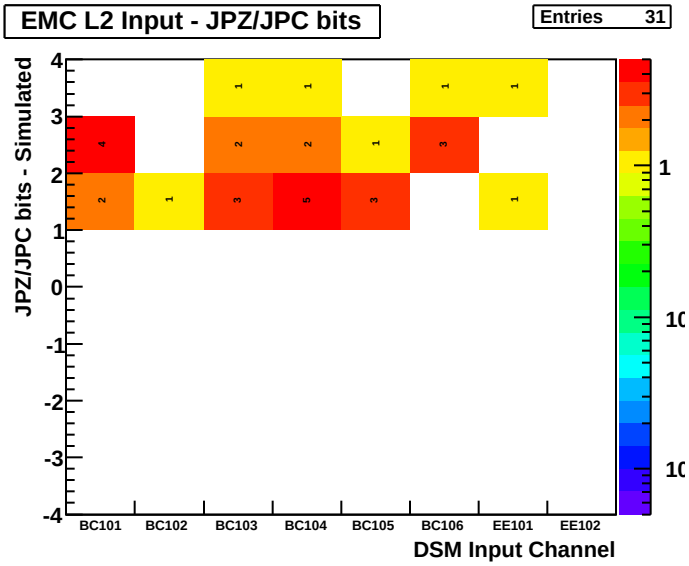

EMC L2 Input - JPX/JPA bits
Entries 9051552
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3176 2533 1972 1132 3903 2115 523 707
9607 7553 5474 3000 13655 6254 1629 2002
13582 11438 8169 5733 16182 12223 2019 2236
1.10508e+06 1.10932e+06 1.11583e+06 1.12158e+06 1.1077e+06 1.11085e+06 1.12727e+06 1.12654e+06

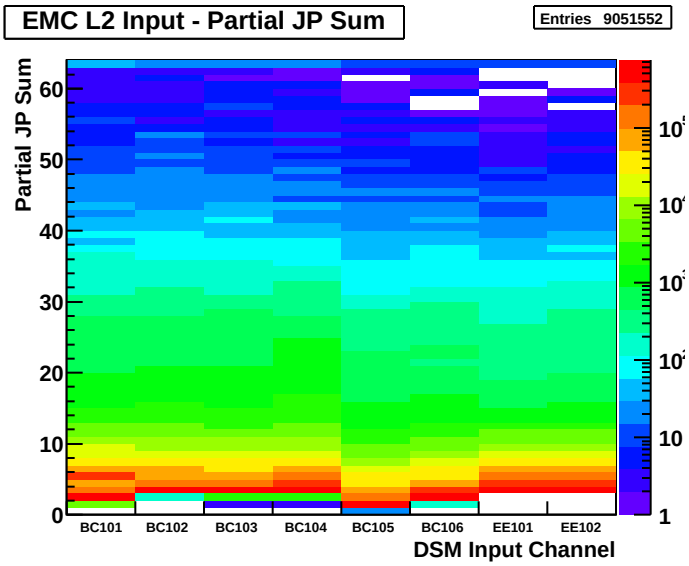

EMC L2 Input - JPX/JPA bits
Entries 1914
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1311 7 1 1 1 2 1
569 3 2 1 6 2 2

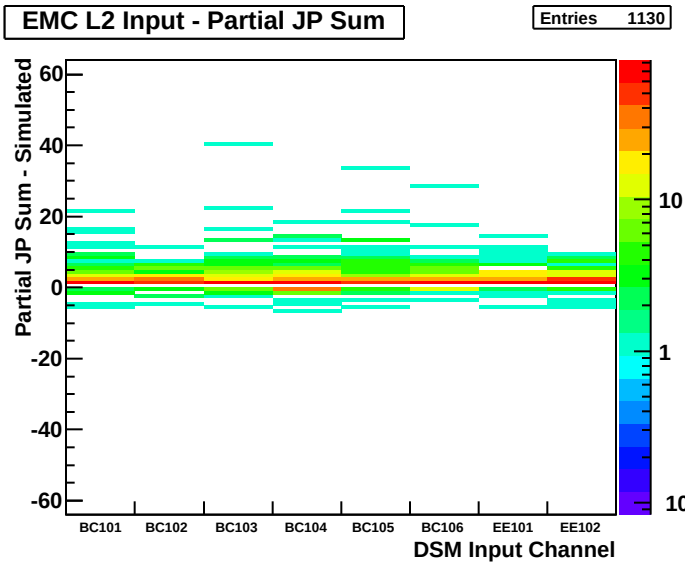

EMC L2 Input - JPY/JPB bits
Entries 9051552
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2275 2175 1414 1513 1461 2725 604 644
6935 7106 3912 4604 4211 8923 1584 1843
13743 12701 7927 9841 11549 14102 1696 2098
1.10639e+06 1.10946e+06 1.11819e+06 1.11549e+06 1.11422e+06 1.10569e+06 1.12756e+06 1.12686e+06

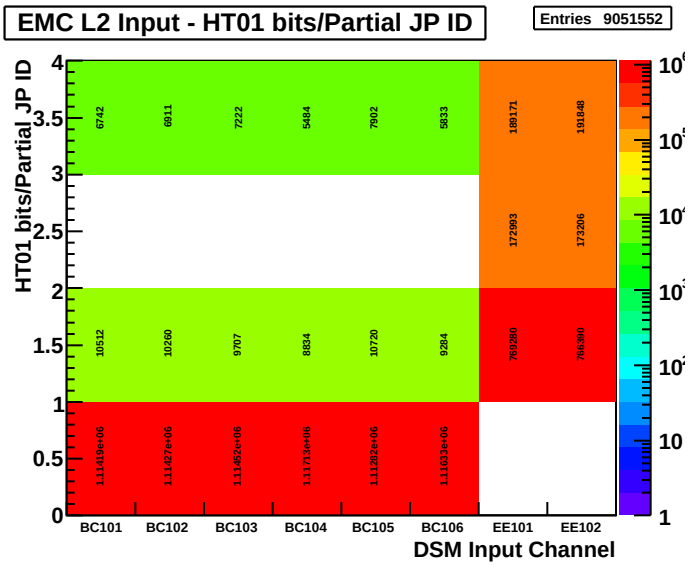

EMC L2 Input - JPY/JPB bits
Entries 1907
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 3
1069 3 1 1 2 1
804 6 5 2 5 2 1

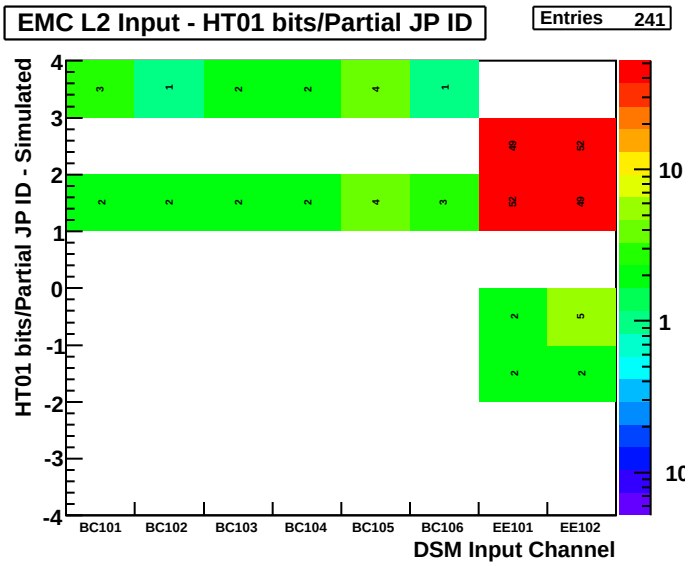

EMC L2 Input - JPZ/JPC bits
Entries 9051552
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2134 2743 2020 3381 1150 1638 652 477
6290 8460 5709 12463 2742 4740 1952 1535
10724 13936 10563 16857 6238 8474 2162 2017
1.1123e+06 1.1072e+06 1.11315e+06 1.09874e+06 1.12131e+06 1.11659e+06 1.12668e+06 1.12742e+06

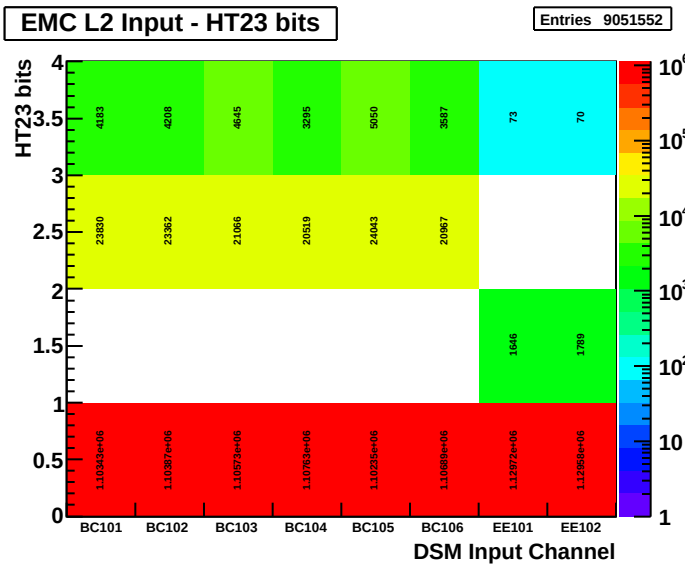

EMC L2 Input - JPZ/JPC bits
Entries 31
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 1
4 2 2 1 3
2 1 3 5 3 1

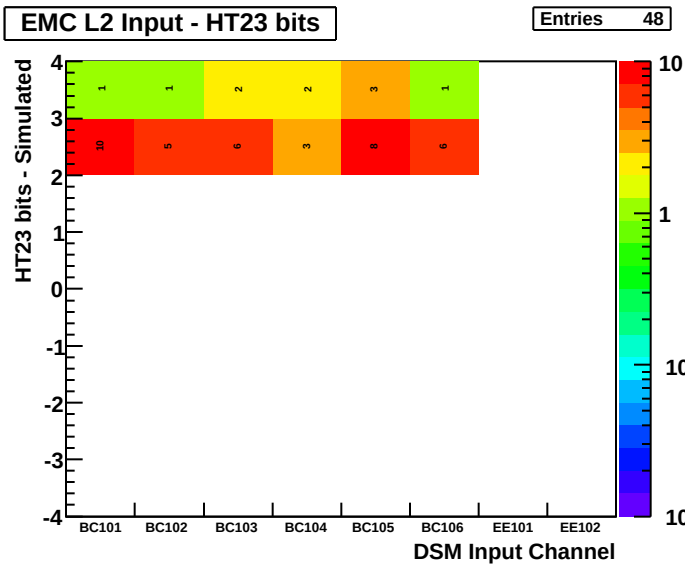

EMC L2 Input - Partial JP Sum
Entries 9051552
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

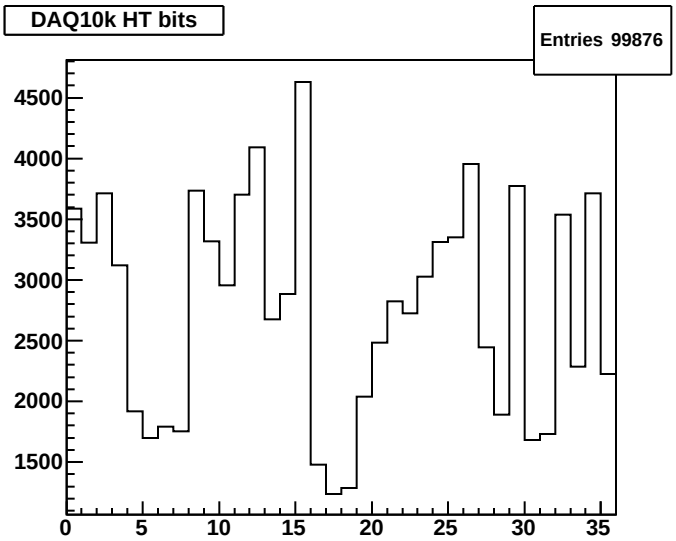

EMC L2 Input - Partial JP Sum
Entries 1130
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

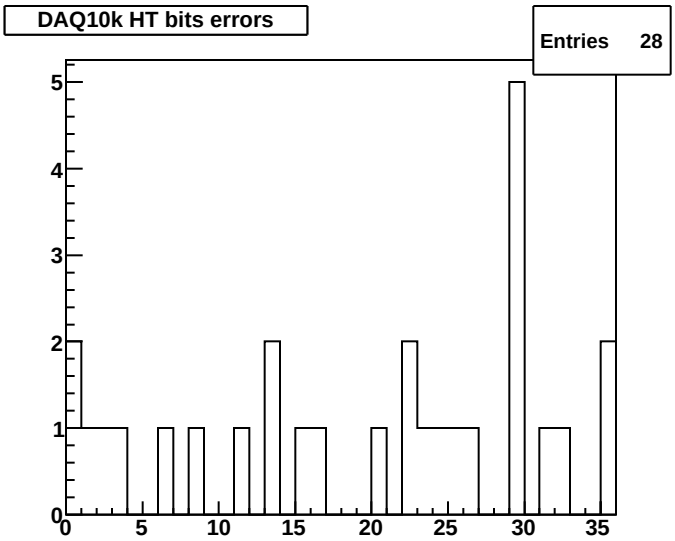

EMC L2 Input - HT01 bits/Partial JP ID
Entries 9051552
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

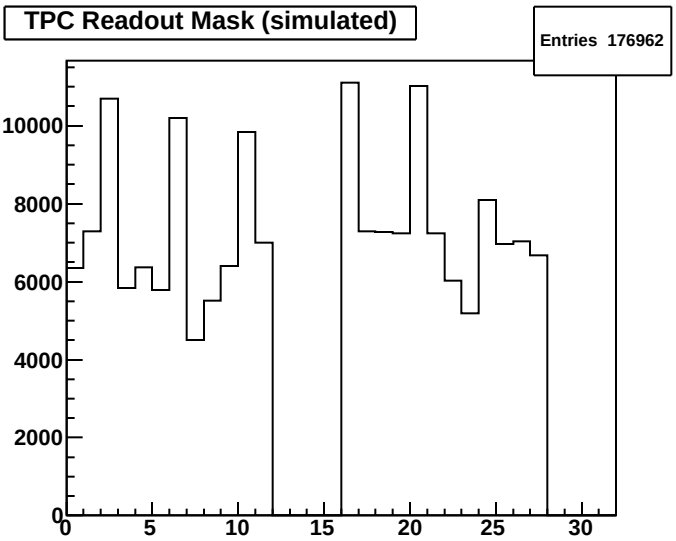

EMC L2 Input - HT01 bits/Partial JP ID
Entries 241
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

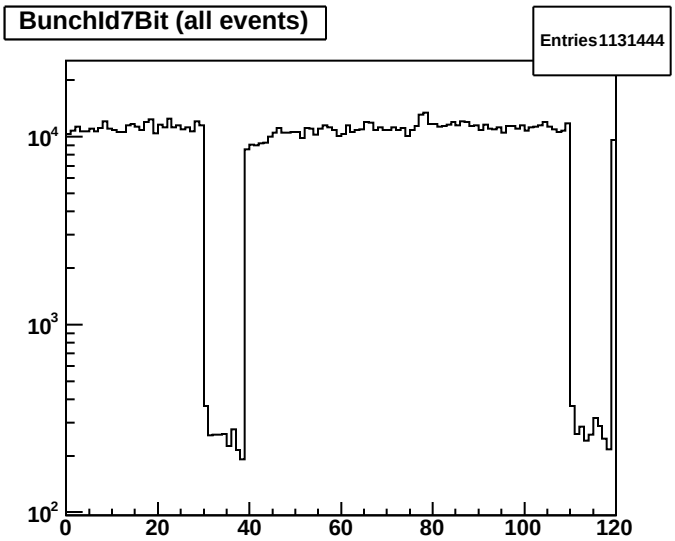

EMC L2 Input - HT23 bits
Entries 9051552
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

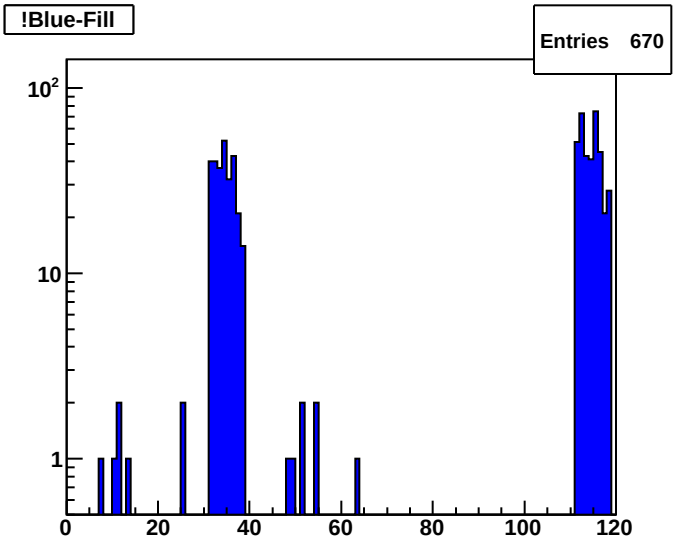

EMC L2 Input - HT23 bits
Entries 48
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

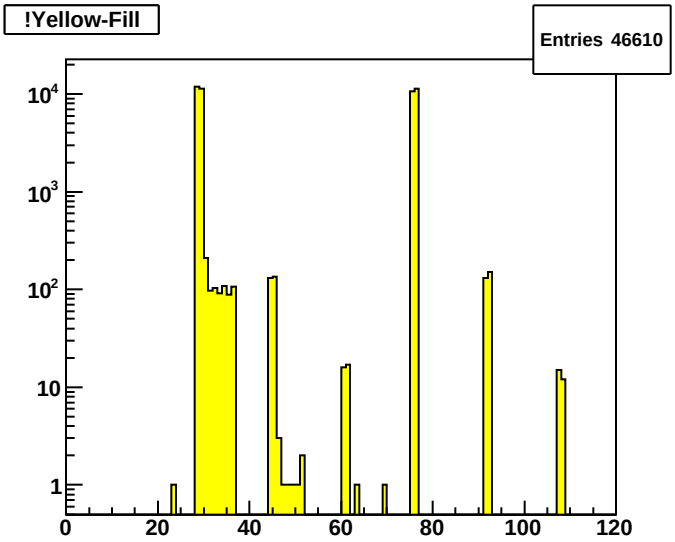

DAQ10k HT bits
Entries 99876

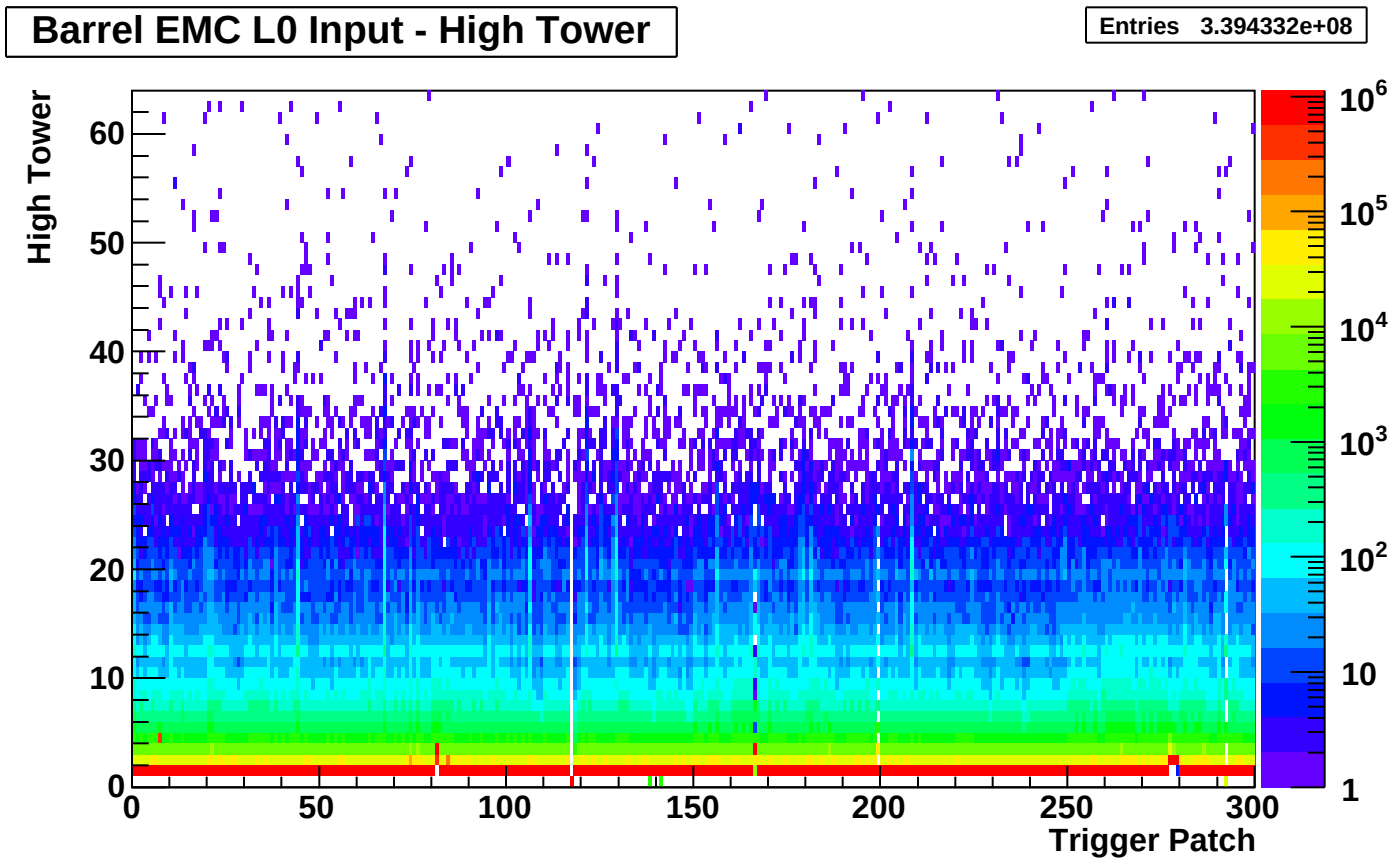

DAQ10k HT bits errors
Entries 28

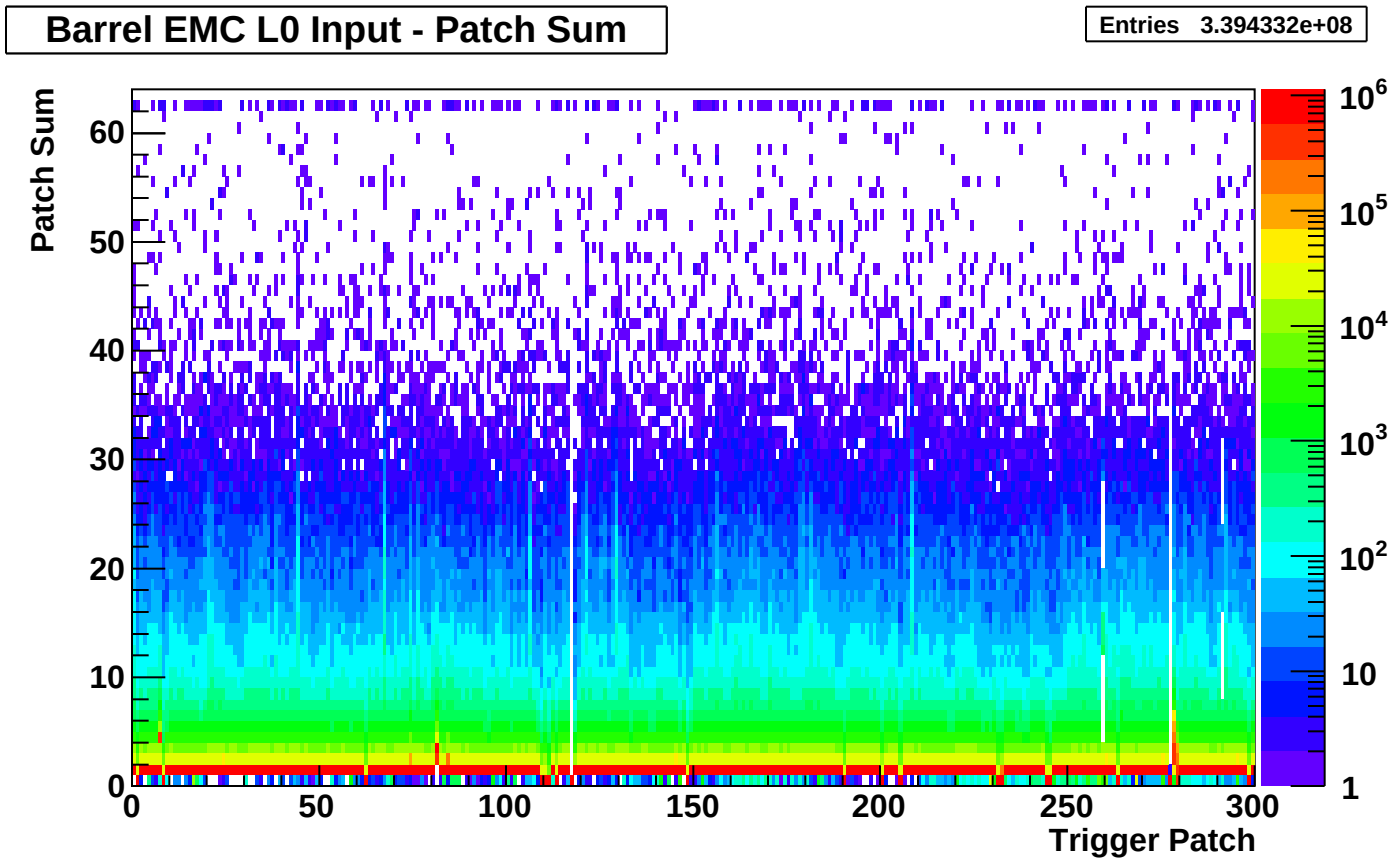

TPC Readout Mask (simulated)
Entries 176962

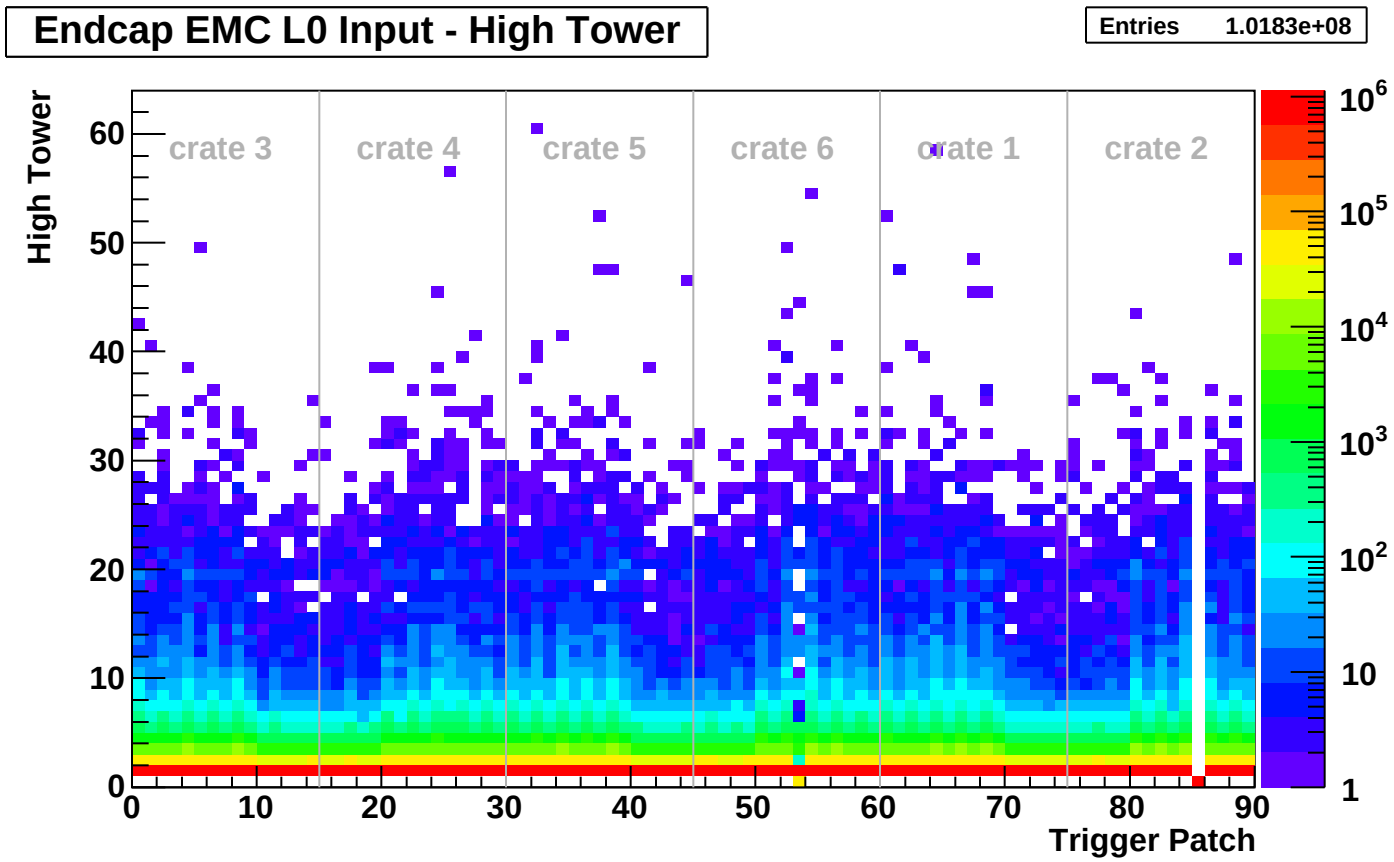

BunchId7Bit (all events)
Entries 1131444

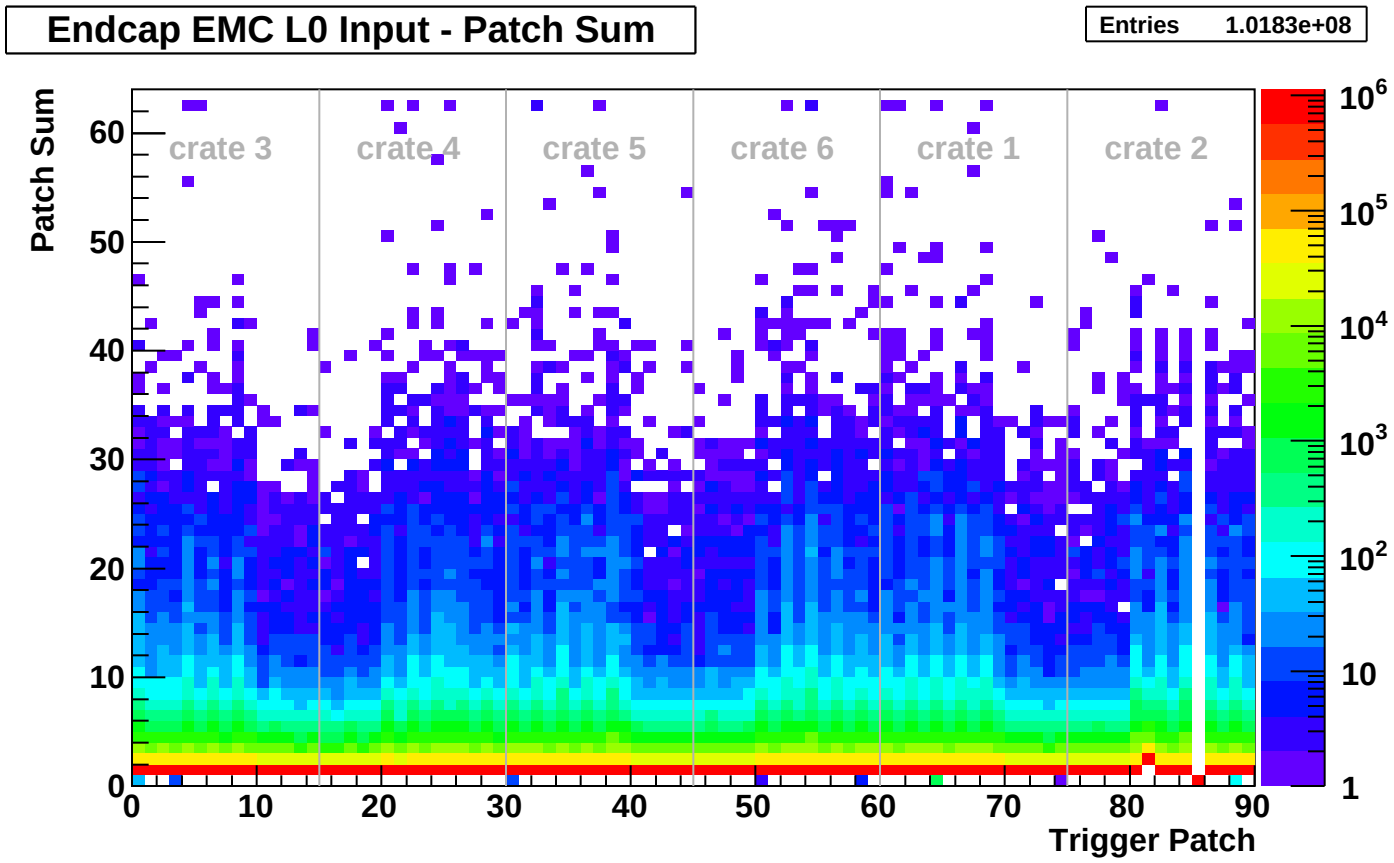

!Blue-Fill
Entries 670

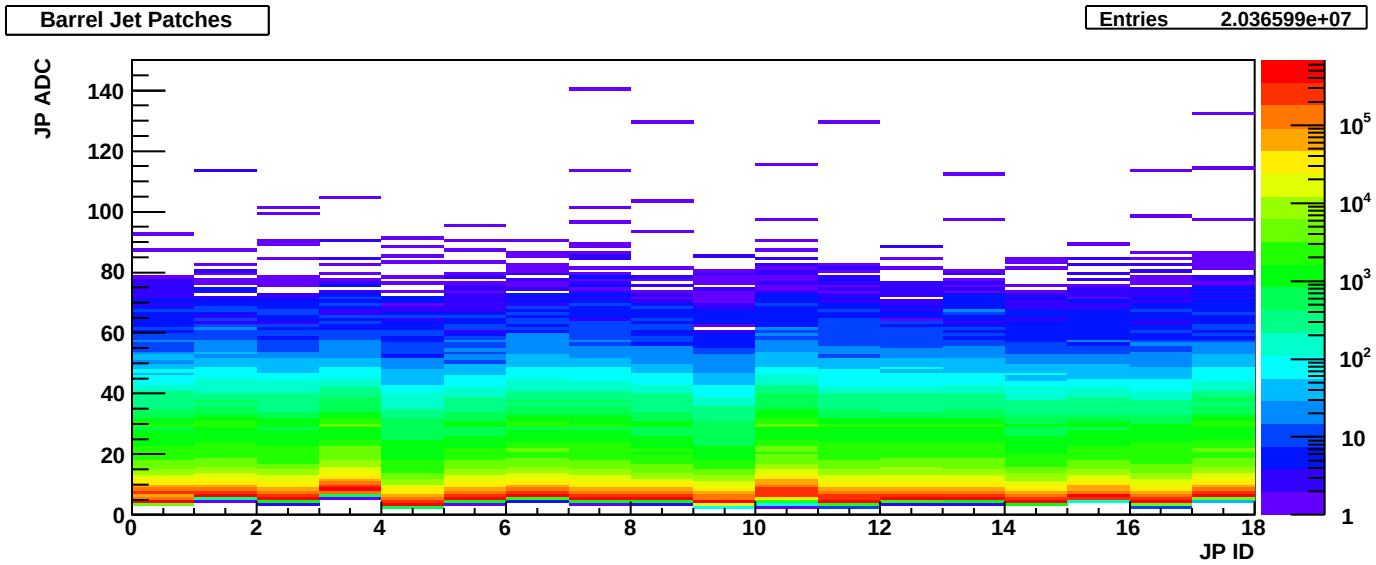

!Yellow-Fill
Entries 46610

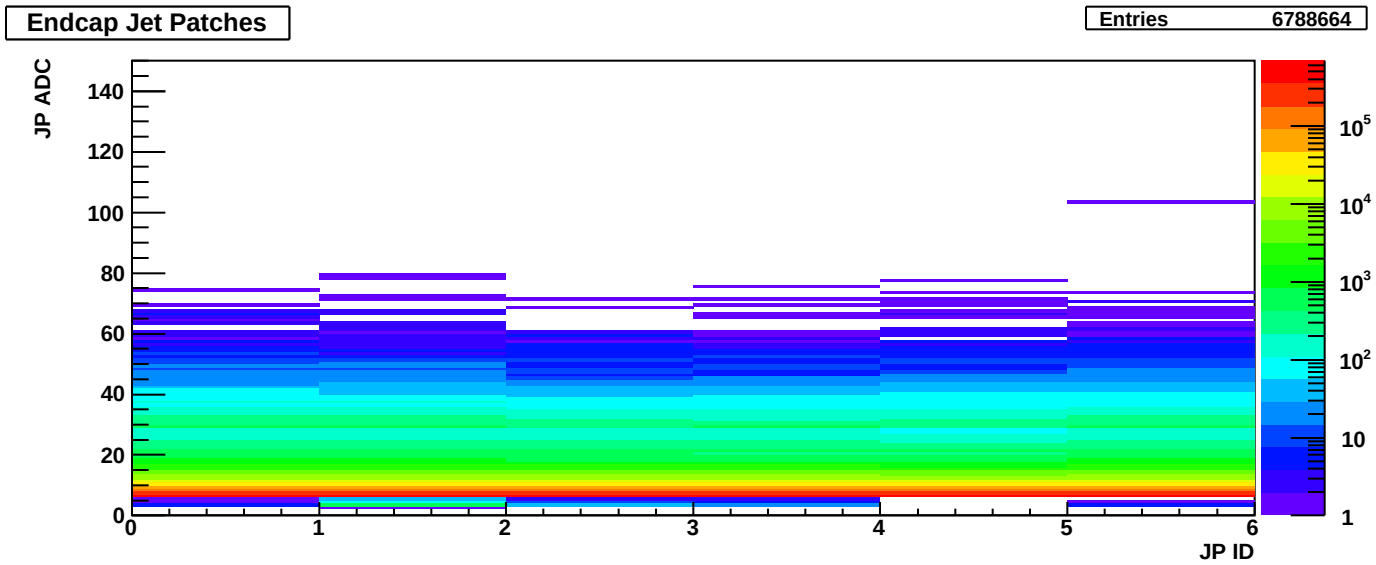

Barrel EMC L0 Input - High Tower
Entries 3.394332e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

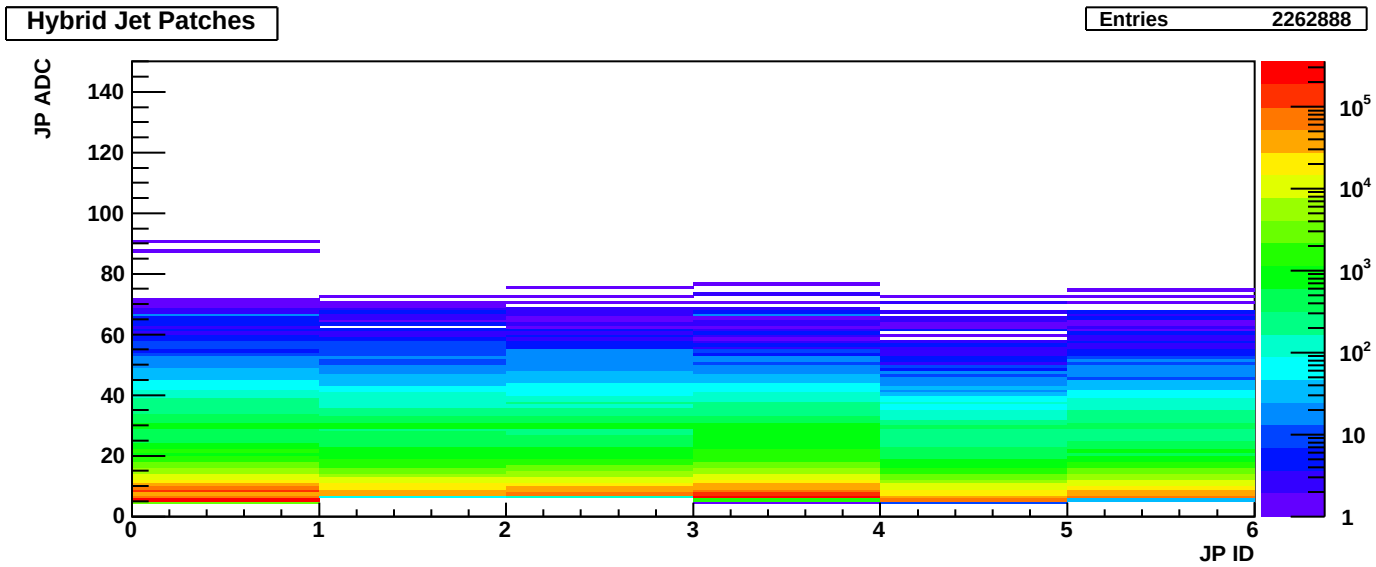

Barrel EMC L0 Input - Patch Sum
Entries 3.394332e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

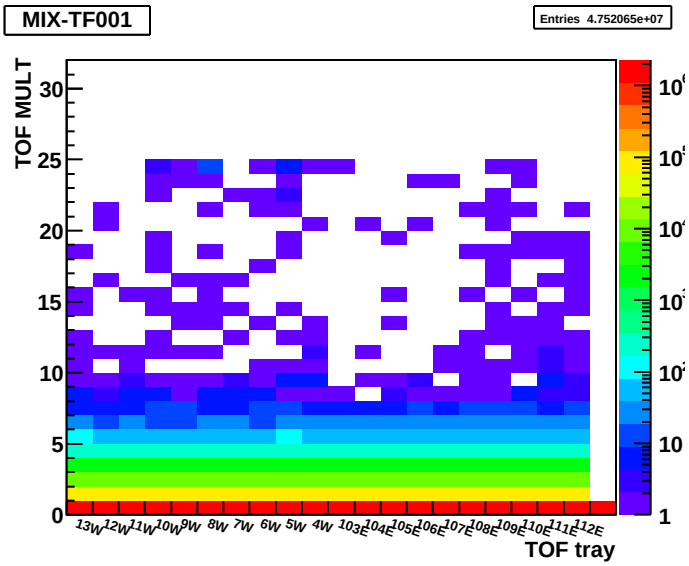

Endcap EMC L0 Input - High Tower
Entries 1.0183e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

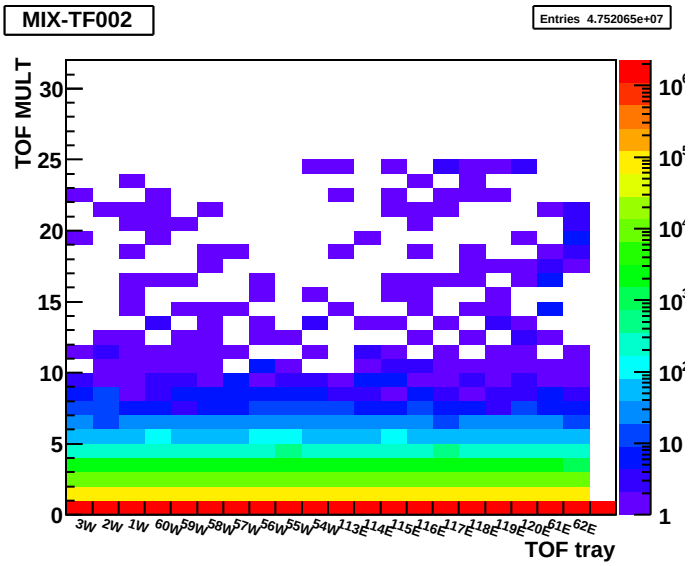

Endcap EMC L0 Input - Patch Sum
Entries 1.0183e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

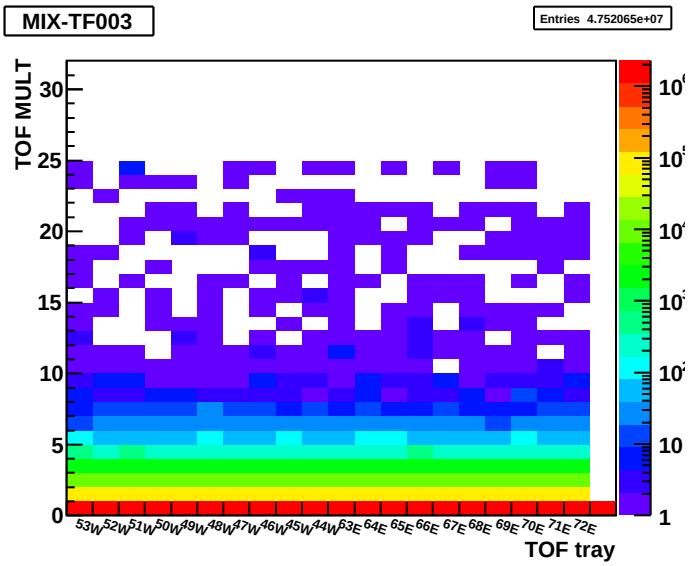

Barrel Jet Patches
Entries 2.036599e+07
JP ADC
JP ID

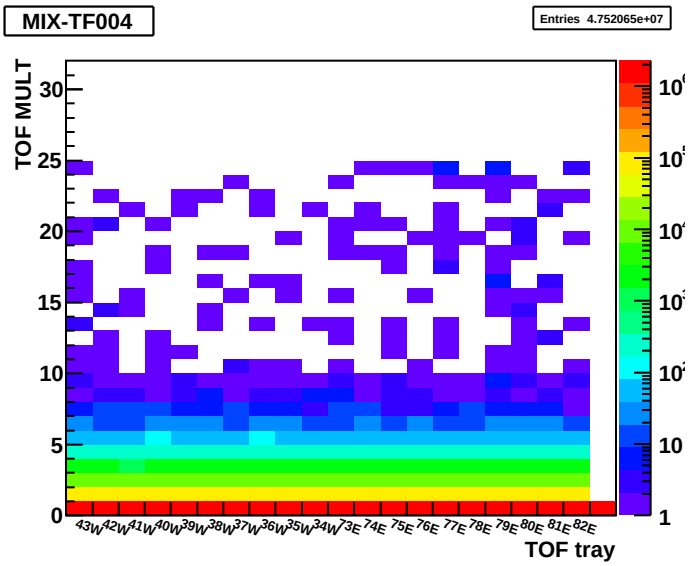

Endcap Jet Patches
Entries 6788664
JP ADC
JP ID

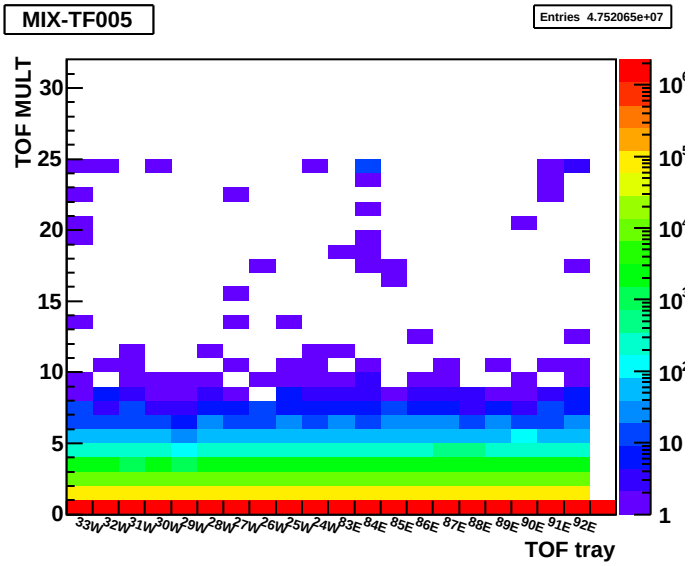

Hybrid Jet Patches
Entries 2262888
JP ADC
JP ID

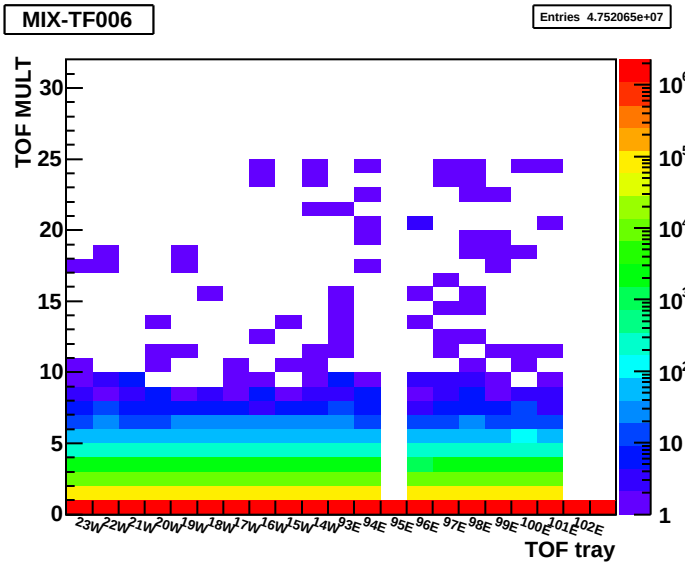

MIX-TF001
Entries 4.752065e+07
TOF MULT
TOF tray

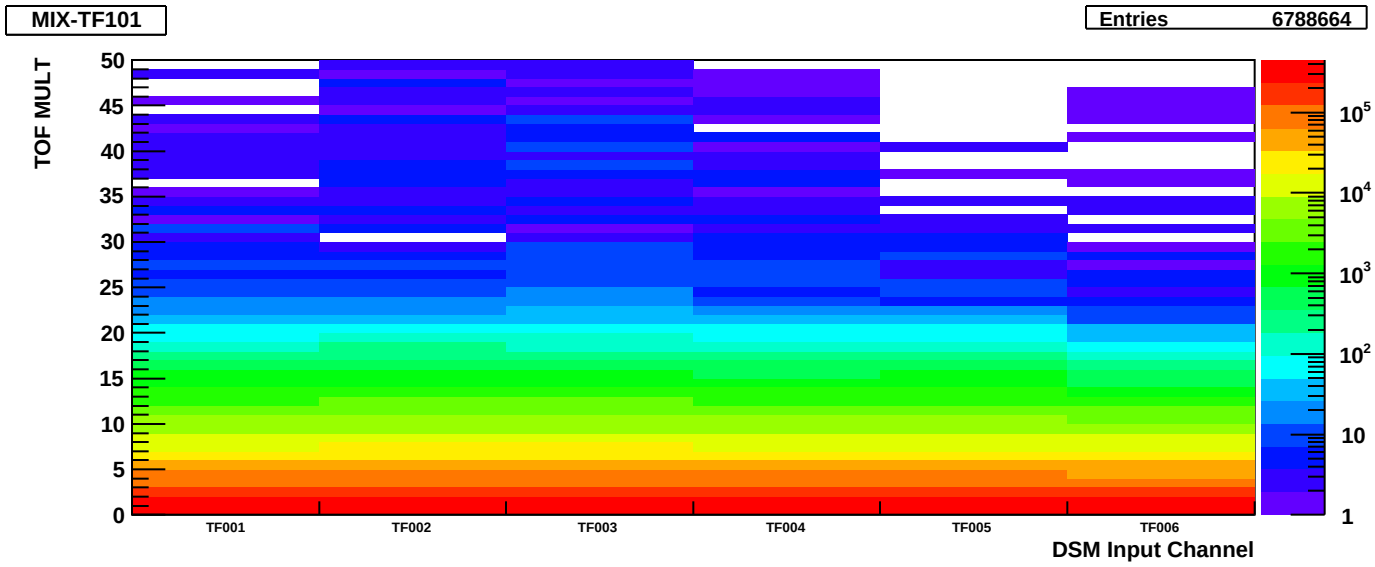

MIX-TF002
Entries 4.752065e+07
TOF MULT
TOF tray

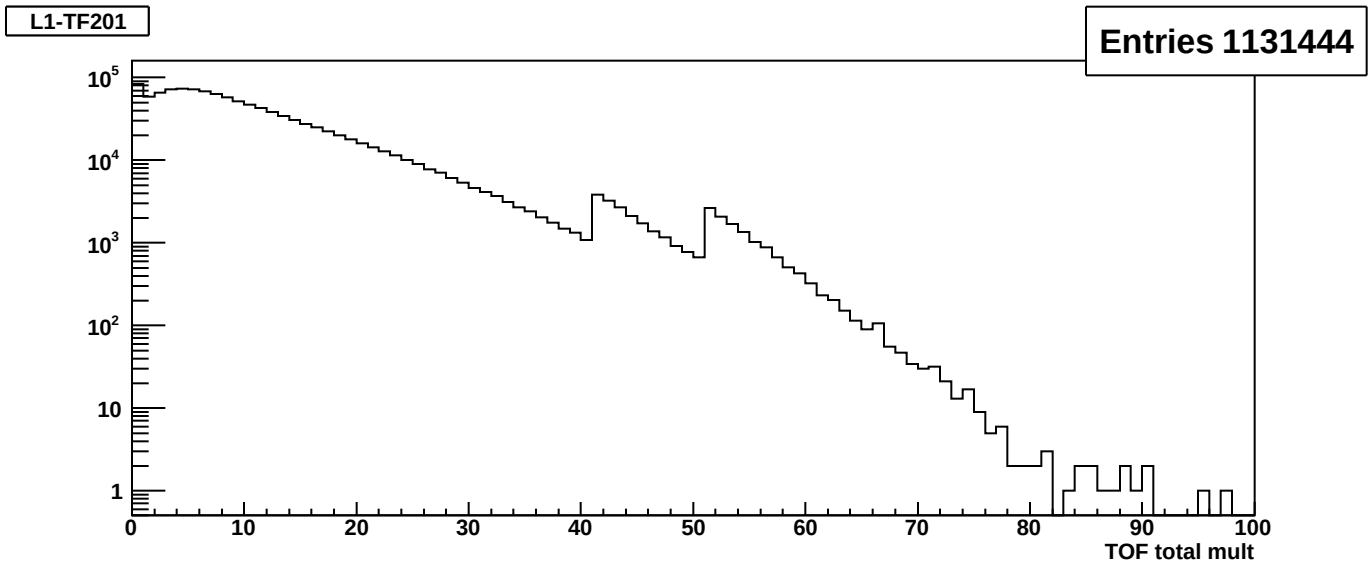

MIX-TF003
Entries 4.752065e+07
TOF MULT
TOF tray

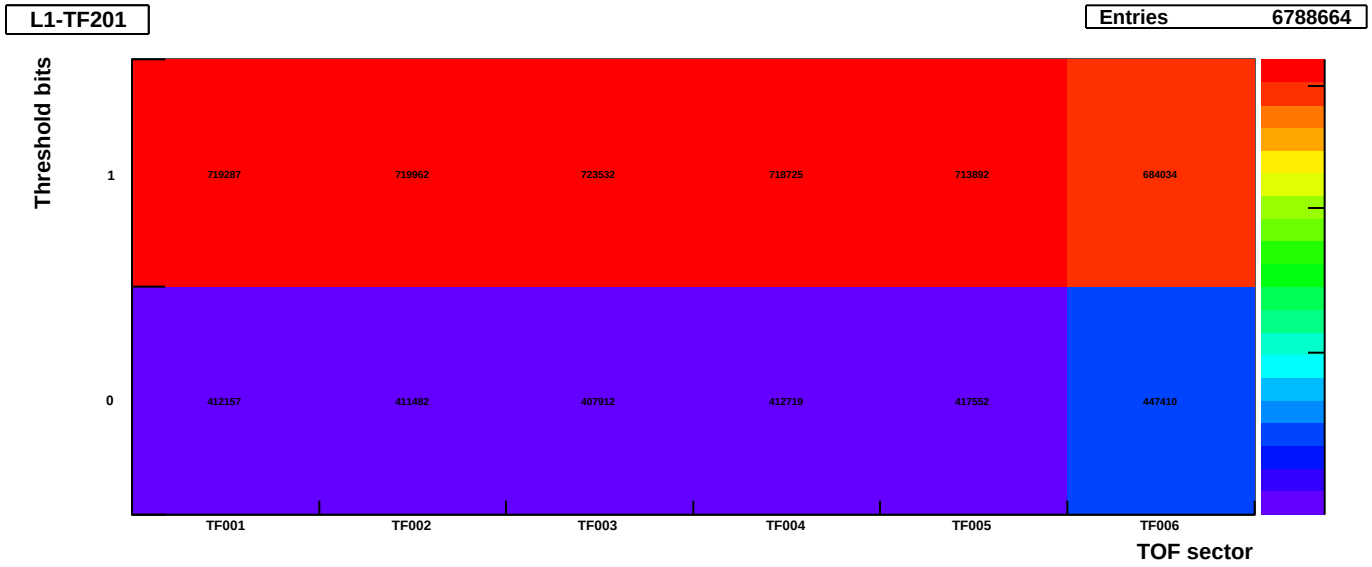

MIX-TF004
Entries 4.752065e+07
TOF MULT
TOF tray

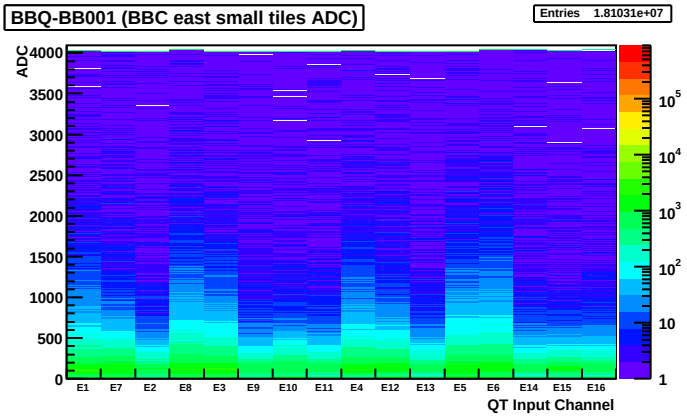

MIX-TF005
Entries 4.752065e+07
TOF MULT
TOF tray

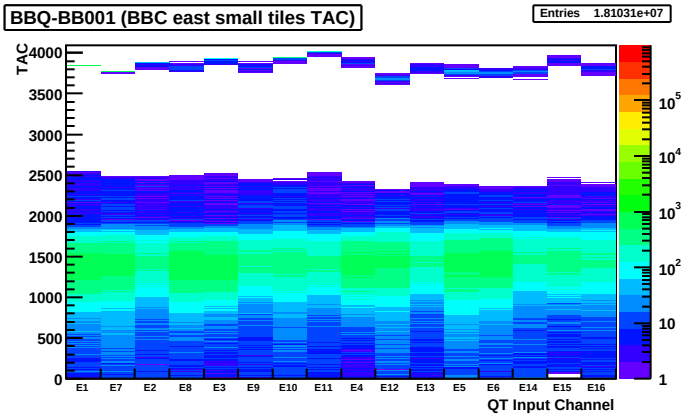

MIX-TF006
Entries 4.752065e+07
TOF MULT
TOF tray

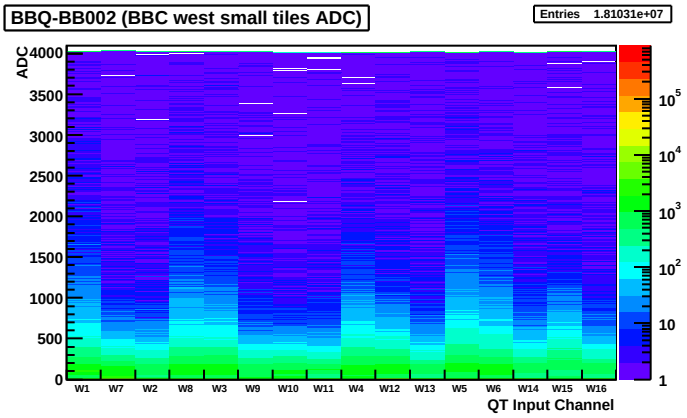

MIX-TF101
Entries 6788664
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

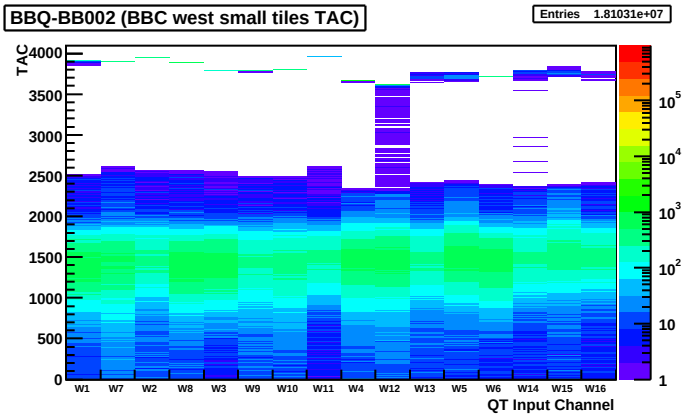

L1-TF201
Entries 1131444
TOF total mult

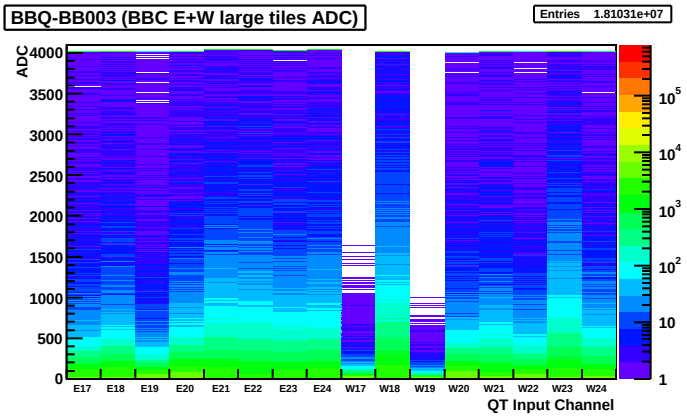

L1-TF201
Entries 6788664
Threshold bits
719287
719962
723532
718725
713892
684034
412157
411482
407912
412719
417552
447410
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

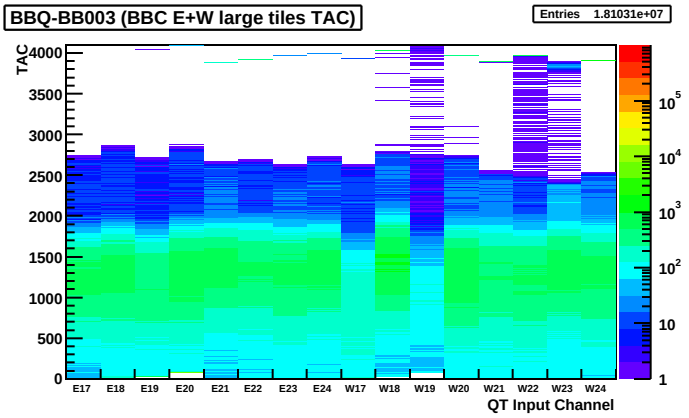

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.81031e+07
ADC
QT Input Channel

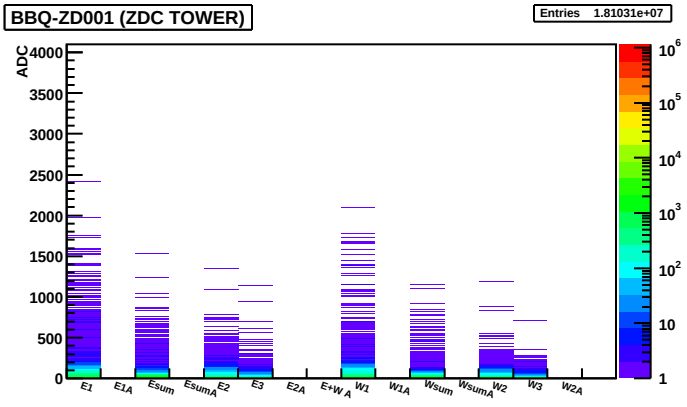

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.81031e+07
TAC
QT Input Channel

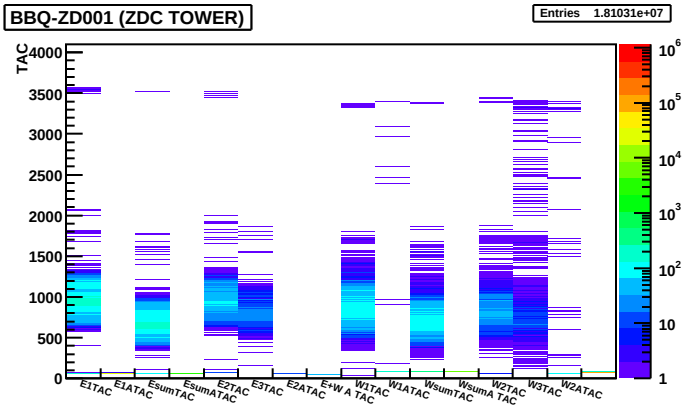

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.81031e+07
ADC
QT Input Channel

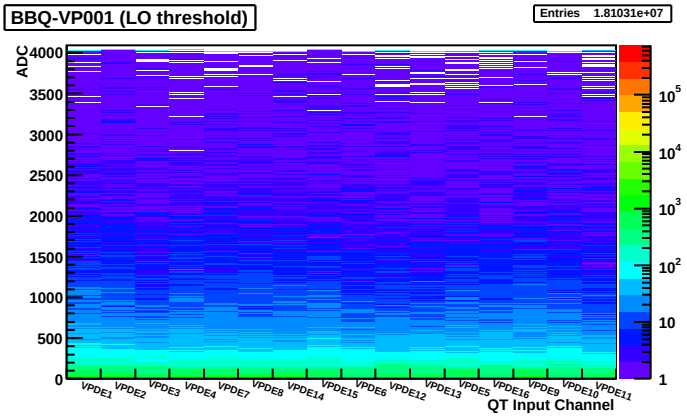

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.81031e+07
TAC
QT Input Channel

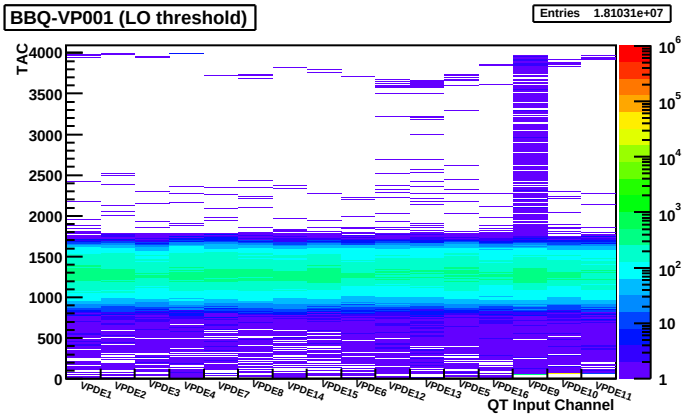

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.81031e+07
ADC
QT Input Channel

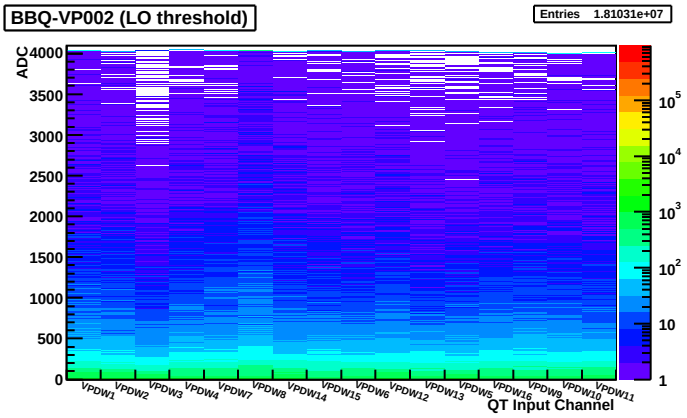

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.81031e+07
TAC
QT Input Channel

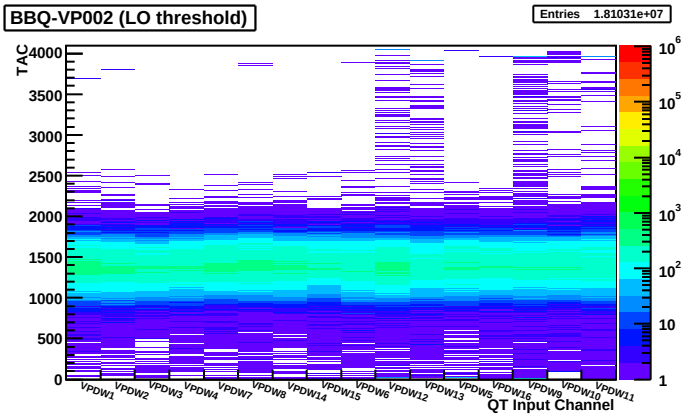

BBQ-ZD001 (ZDC TOWER)
Entries 1.81031e+07
ADC

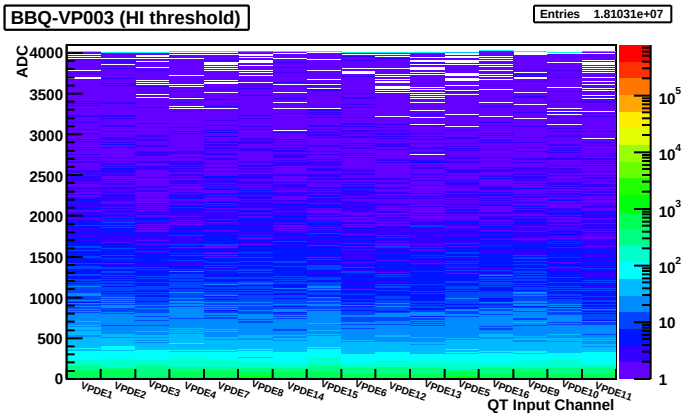

BBQ-ZD001 (ZDC TOWER)
Entries 1.81031e+07
TAC

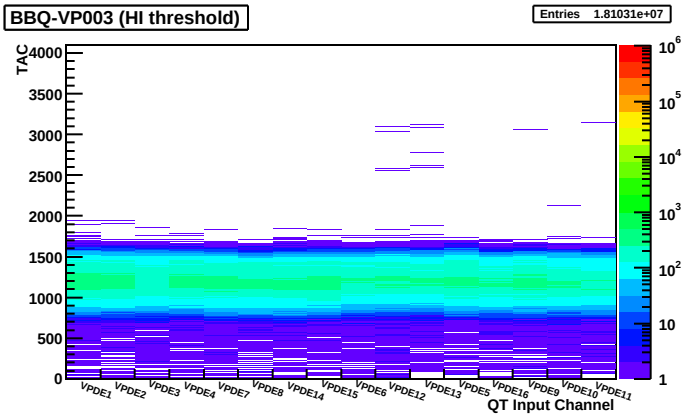

BBQ-VP001 (LO threshold)
Entries 1.81031e+07
ADC
QT Input Channel

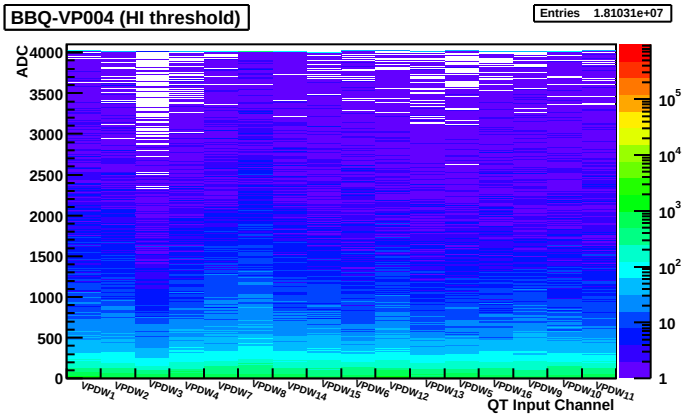

BBQ-VP001 (LO threshold)
Entries 1.81031e+07
TAC
QT Input Channel

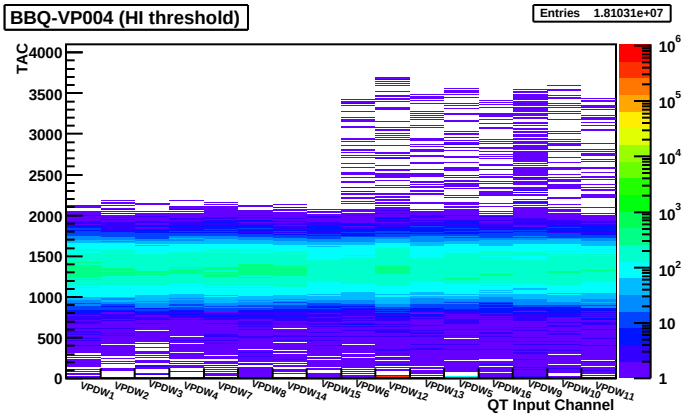

BBQ-VP002 (LO threshold)
Entries 1.81031e+07
ADC
QT Input Channel

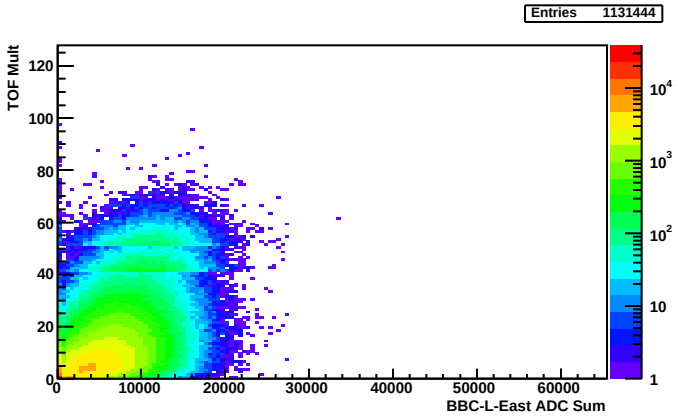

BBQ-VP002 (LO threshold)
Entries 1.81031e+07
TAC
QT Input Channel

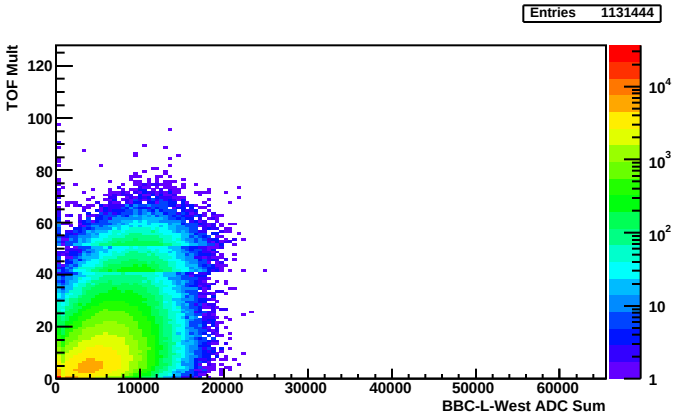

BBQ-VP003 (HI threshold)
Entries 1.81031e+07
ADC
QT Input Channel

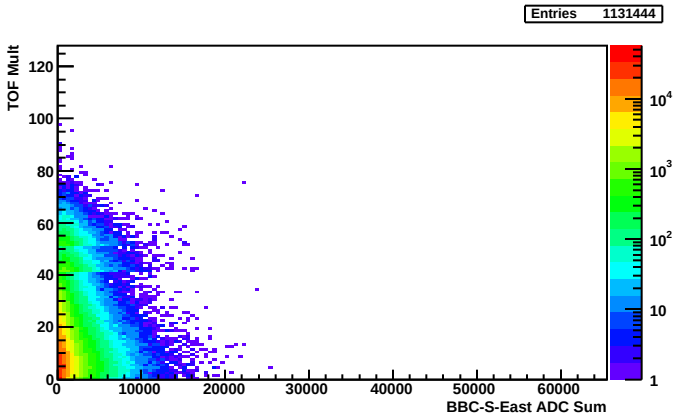

BBQ-VP003 (HI threshold)
Entries 1.81031e+07
TAC
QT Input Channel

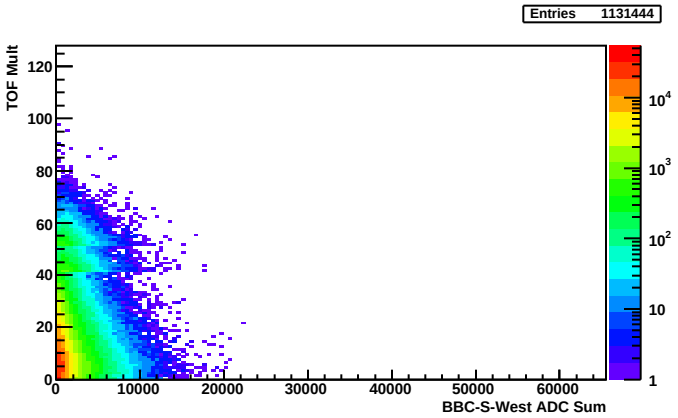

BBQ-VP004 (HI threshold)
Entries 1.81031e+07
ADC
QT Input Channel

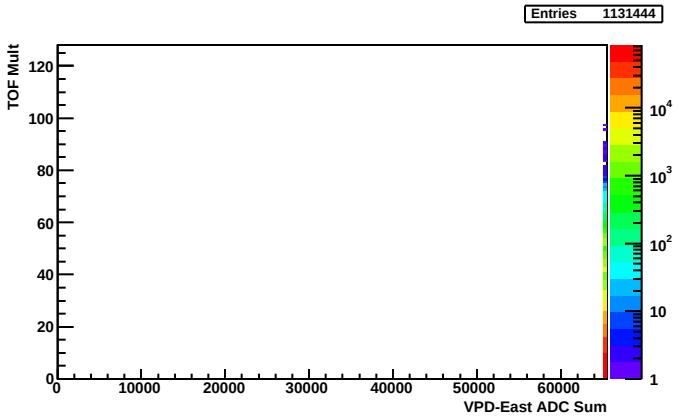

BBQ-VP004 (HI threshold)
Entries 1.81031e+07
TAC
QT Input Channel

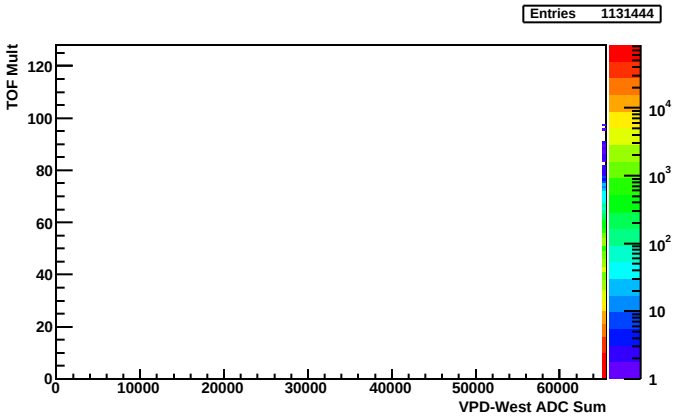

Entries 1131444
TOF Mult
BBC-L-East ADC Sum

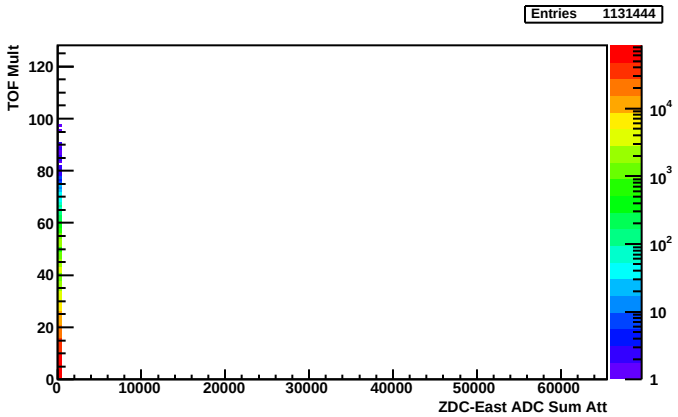

Entries 1131444
TOF Mult
BBC-L-West ADC Sum

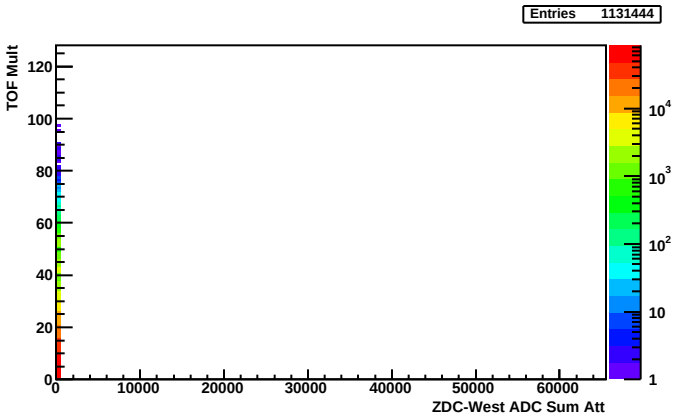

Entries 1131444
TOF Mult
BBC-S-East ADC Sum

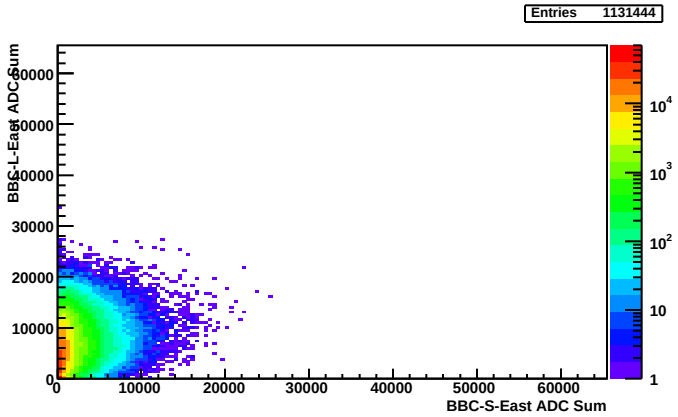

Entries 1131444
TOF Mult
BBC-S-West ADC Sum

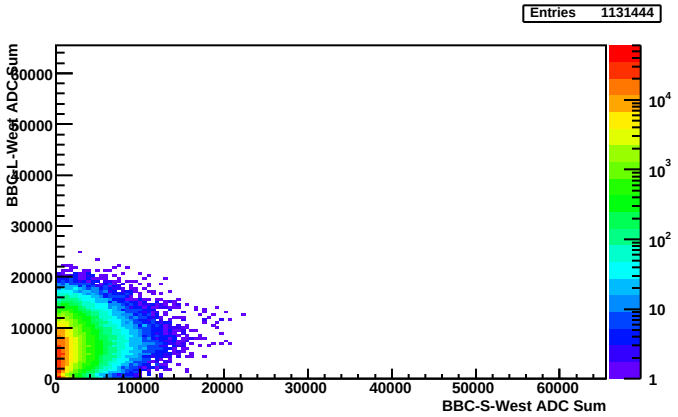

Entries 1131444
TOF Mult
VPD-East ADC Sum

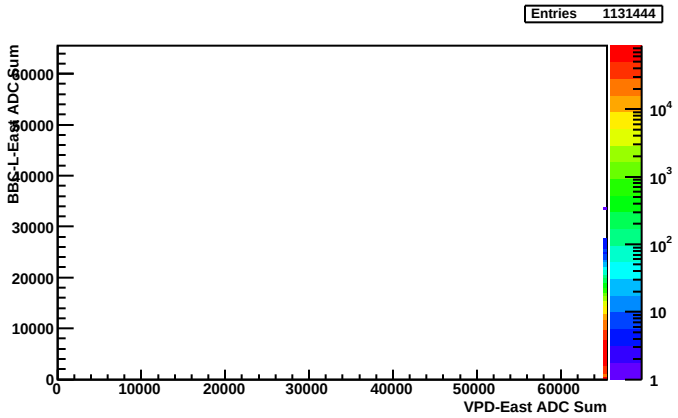

Entries 1131444
TOF Mult
VPD-West ADC Sum

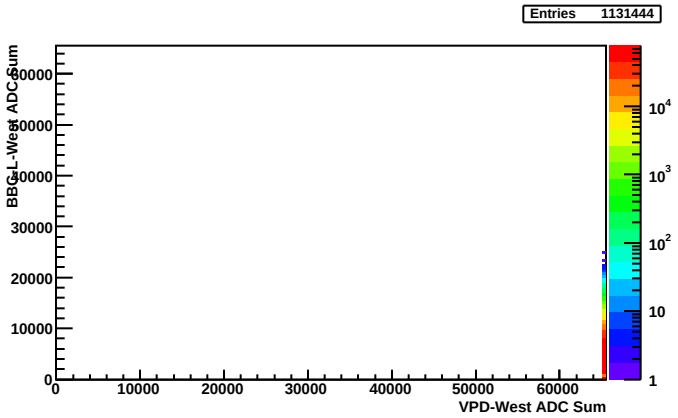

Entries 1131444
TOF Mult
ZDC-East ADC Sum Att

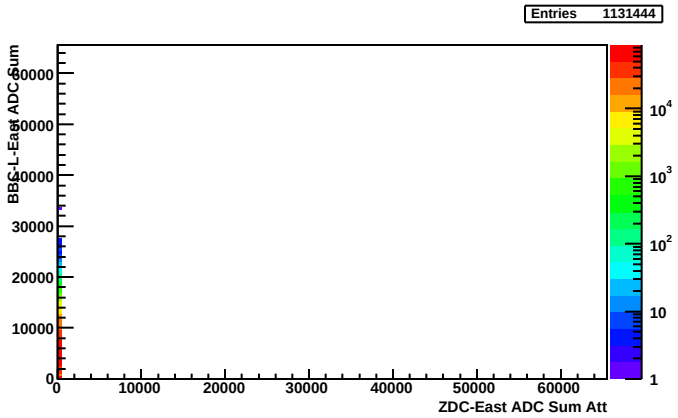

Entries 1131444
TOF Mult
ZDC-West ADC Sum Att

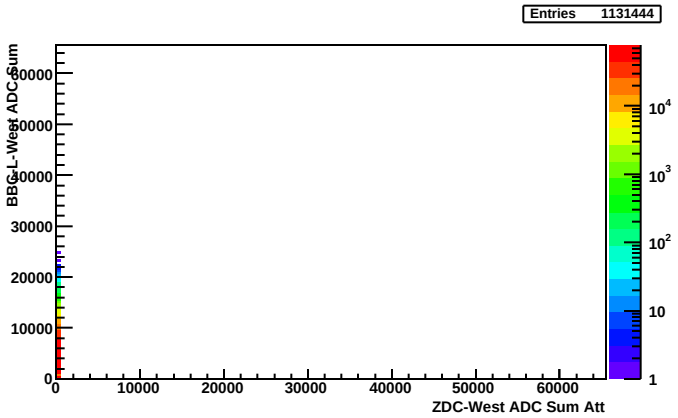

Entries 1131444
BBC-L-East ADC Sum
BBC-S-East ADC Sum

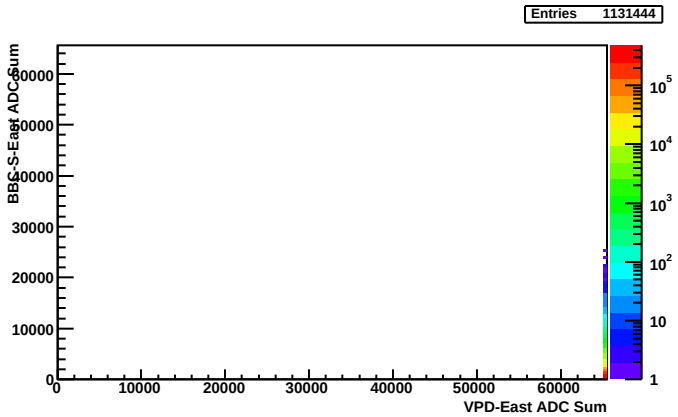

Entries 1131444
BBC-L-West ADC Sum
BBC-S-West ADC Sum

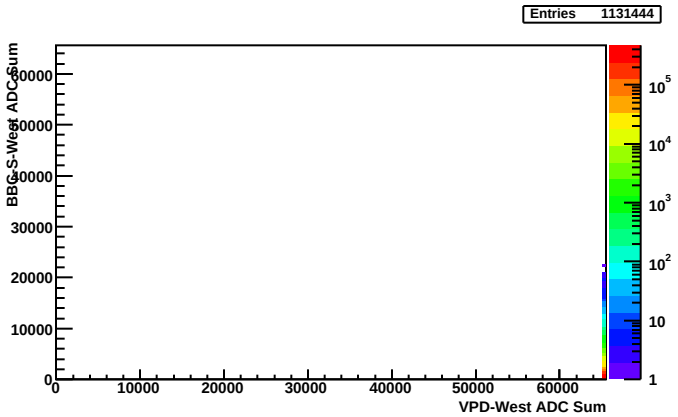

Entries 1131444
BBC-L-East ADC Sum
VPD-East ADC Sum

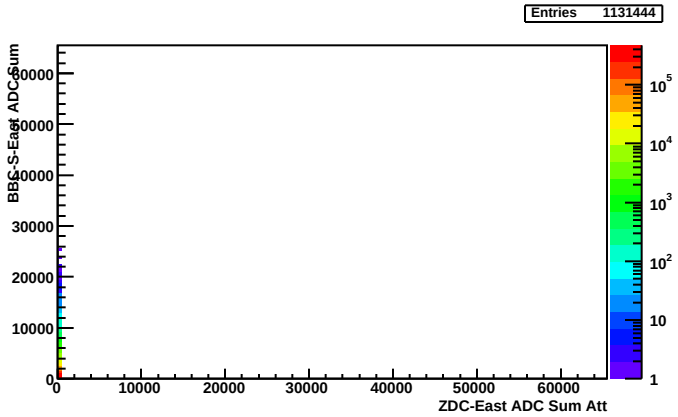

Entries 1131444
BBC-L-West ADC Sum
VPD-West ADC Sum

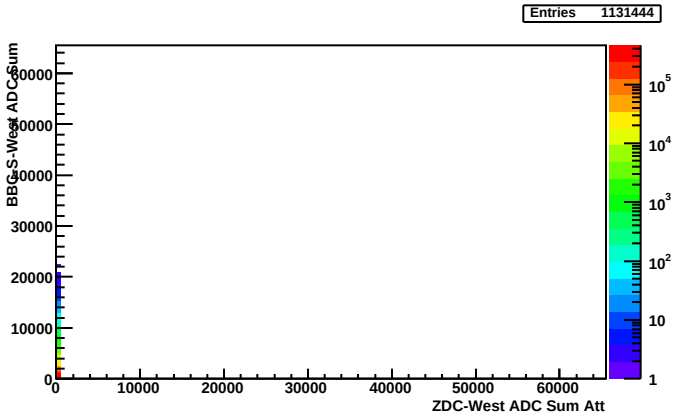

Entries 1131444
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

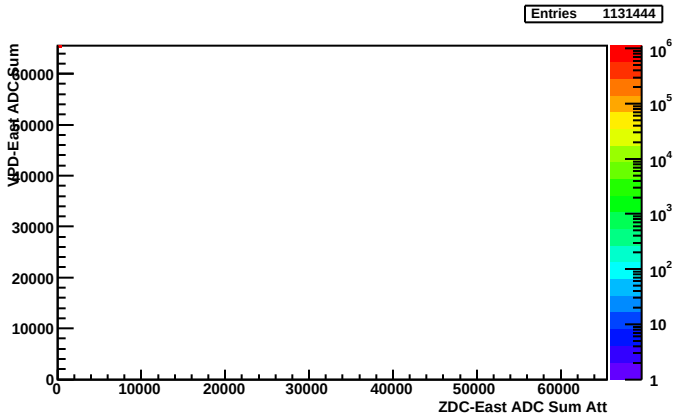

Entries 1131444
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

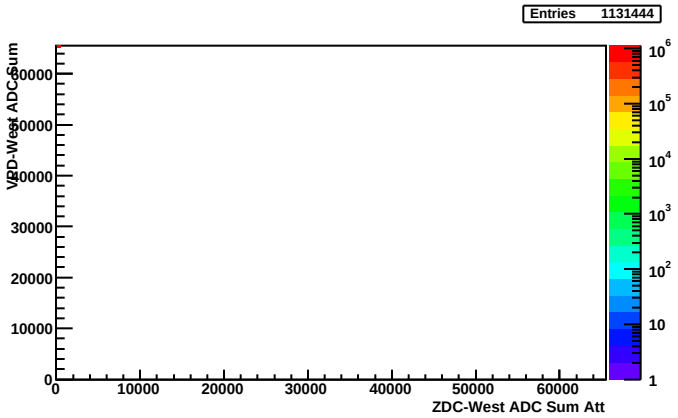

Entries 1131444
BBC-S-East ADC Sum
VPD-East ADC Sum

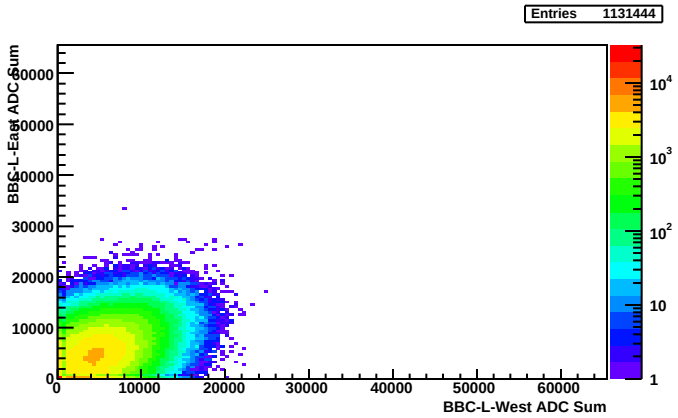

Entries 1131444
BBC-S-West ADC Sum
VPD-West ADC Sum

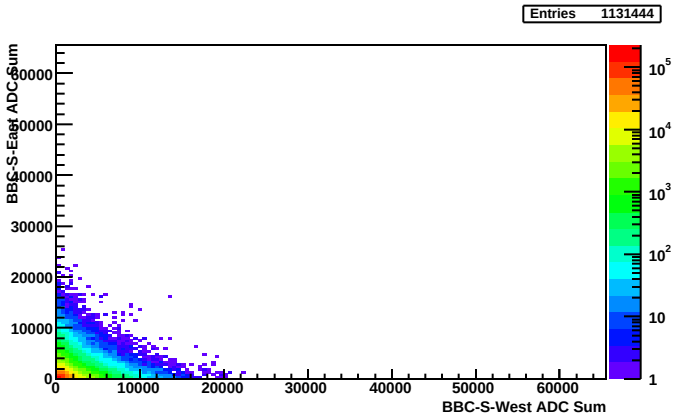

Entries 1131444
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

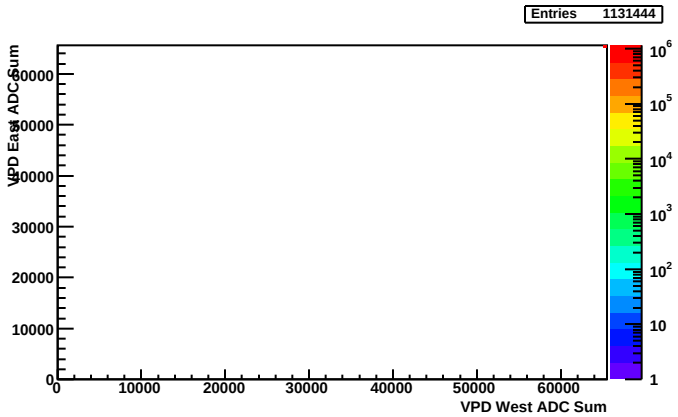

Entries 1131444
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

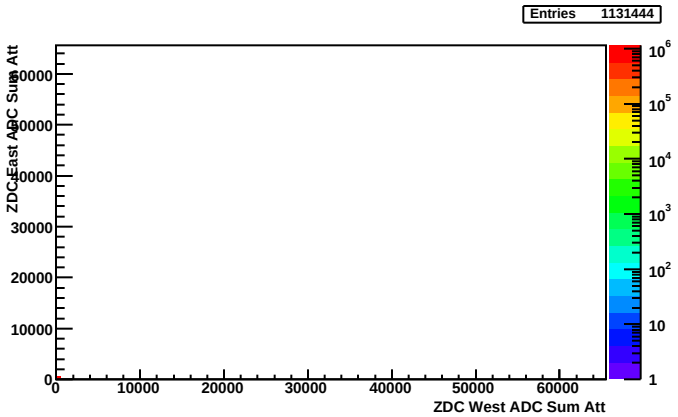

Entries 1131444
VPD-East ADC Sum
ZDC-East ADC Sum Att

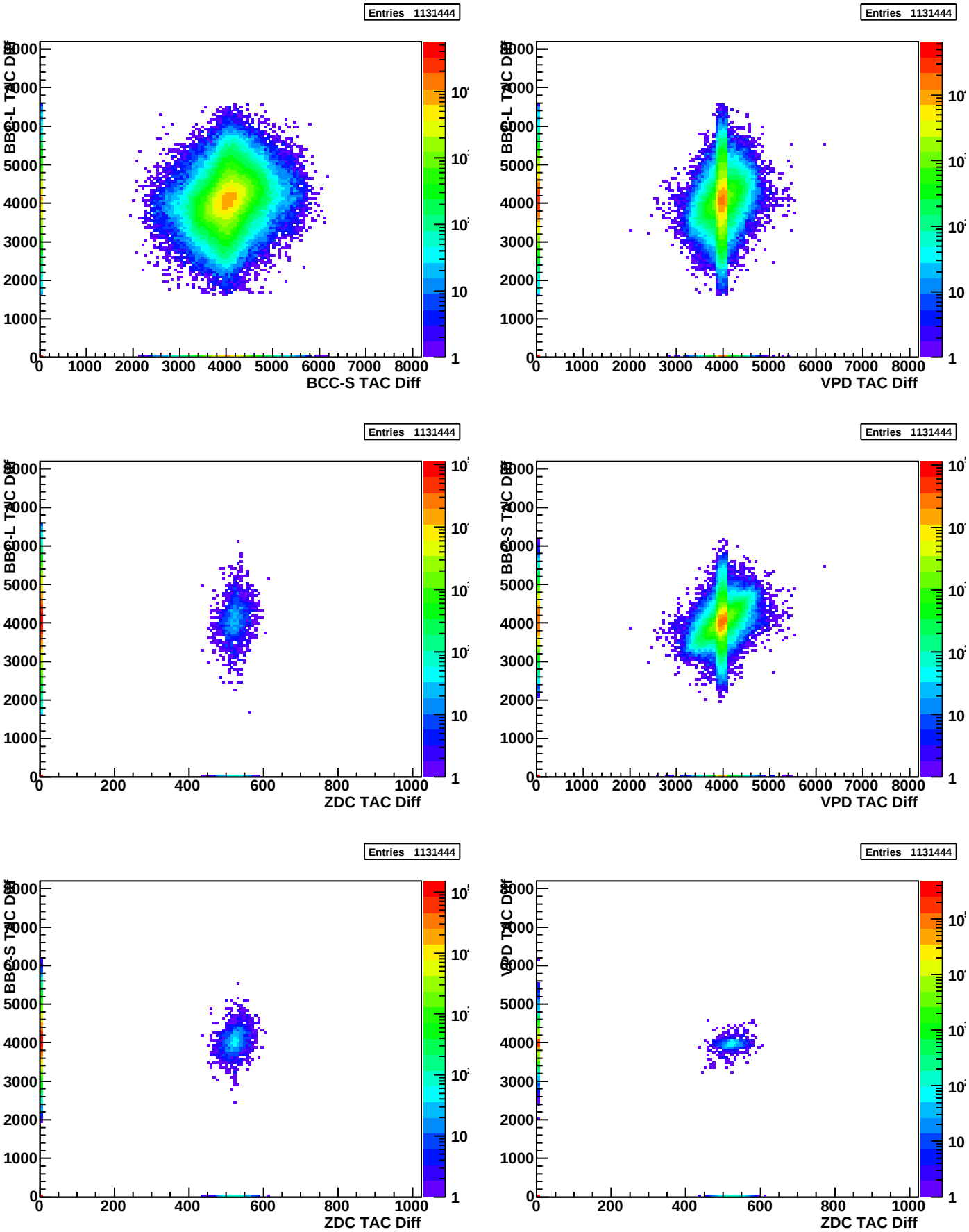

Entries 1131444
VPD-West ADC Sum
ZDC-West ADC Sum Att

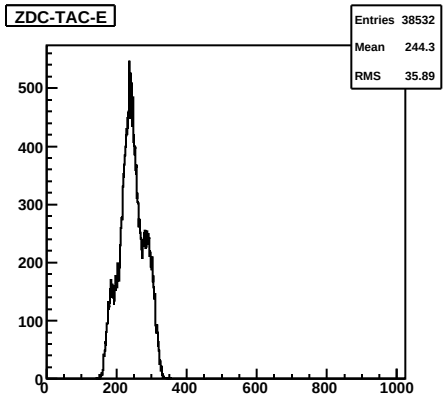

Entries 1131444
BBC-L-East ADC Sum
BBC-L-West ADC Sum

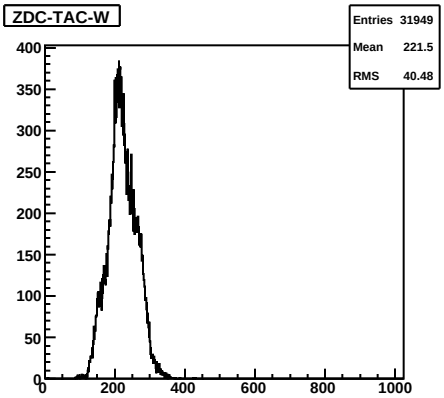

Entries 1131444
BBC-S-East ADC Sum
BBC-S-West ADC Sum

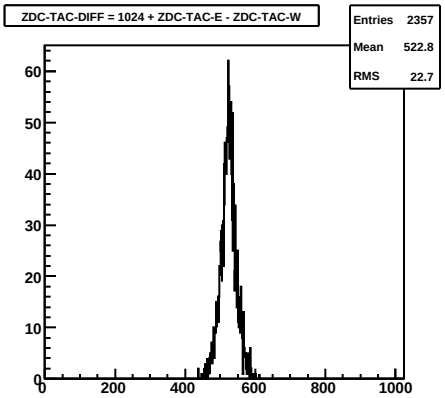

Entries 1131444
VPD East ADC Sum
VPD West ADC Sum

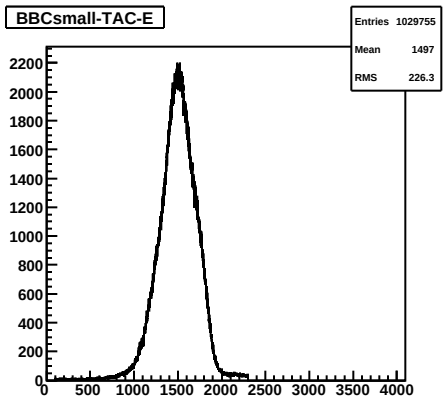

Entries 1131444
ZDC East ADC Sum Att
ZDC West ADC Sum Att

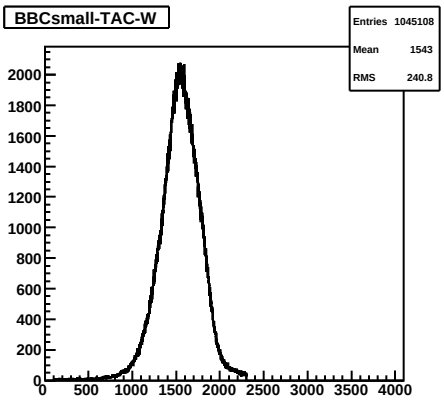

Entries 1131444
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1131444
BBC-L TAC Diff
VPD TAC Diff
Entries 1131444
BBC-L TAC Diff
ZDC TAC Diff
Entries 1131444
BBC-S TAC Diff
VPD TAC Diff
Entries 1131444
BBC-S TAC Diff
ZDC TAC Diff
Entries 1131444
VPD TAC Diff
ZDC TAC Diff

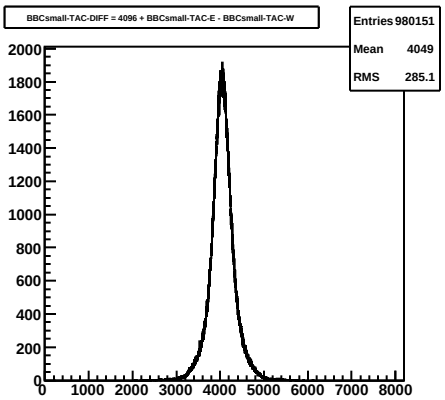

ZDC-TAC-E
Entries 38532
Mean 244.3
RMS 35.89

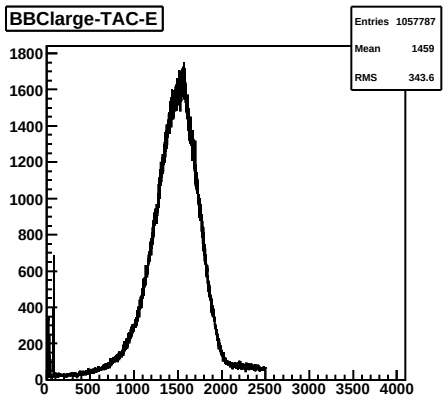

ZDC-TAC-W
Entries 31949
Mean 221.5
RMS 40.48

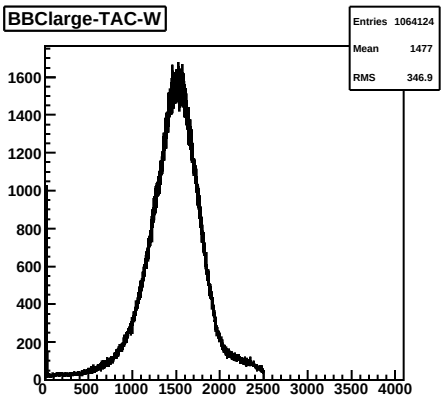

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2357
Mean 522.8
RMS 22.7

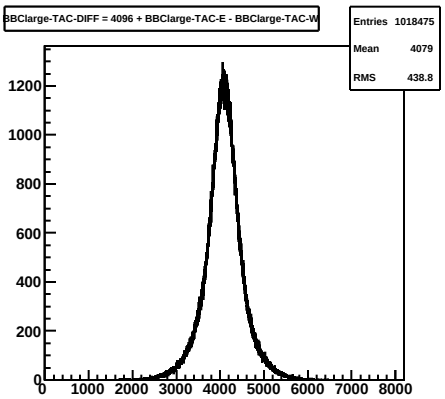

BBCsmall-TAC-E
Entries 1029755
Mean 1497
RMS 226.3

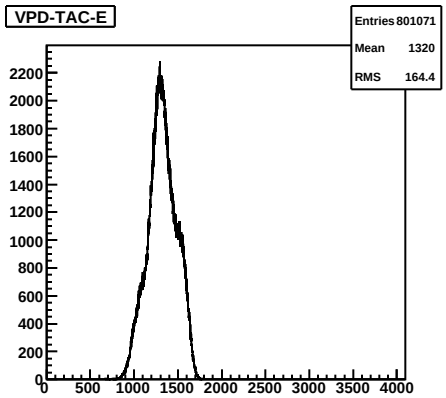

BBCsmall-TAC-W
Entries 1045108
Mean 1543
RMS 240.8

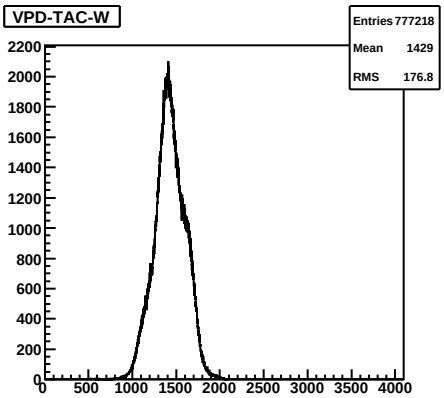

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 980151
Mean 4049
RMS 285.1

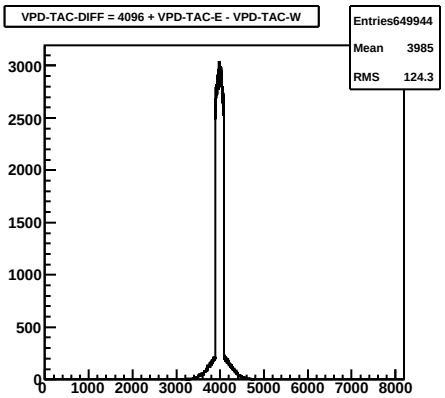

BBClarge-TAC-E
Entries 1057787
Mean 1459
RMS 343.6

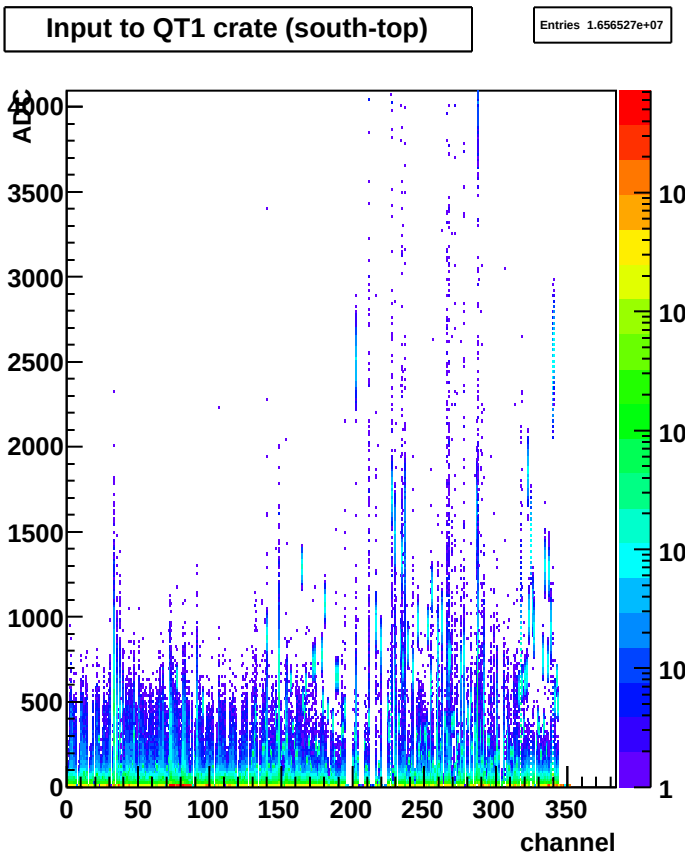

BBClarge-TAC-W
Entries 1064124
Mean 1477
RMS 346.9

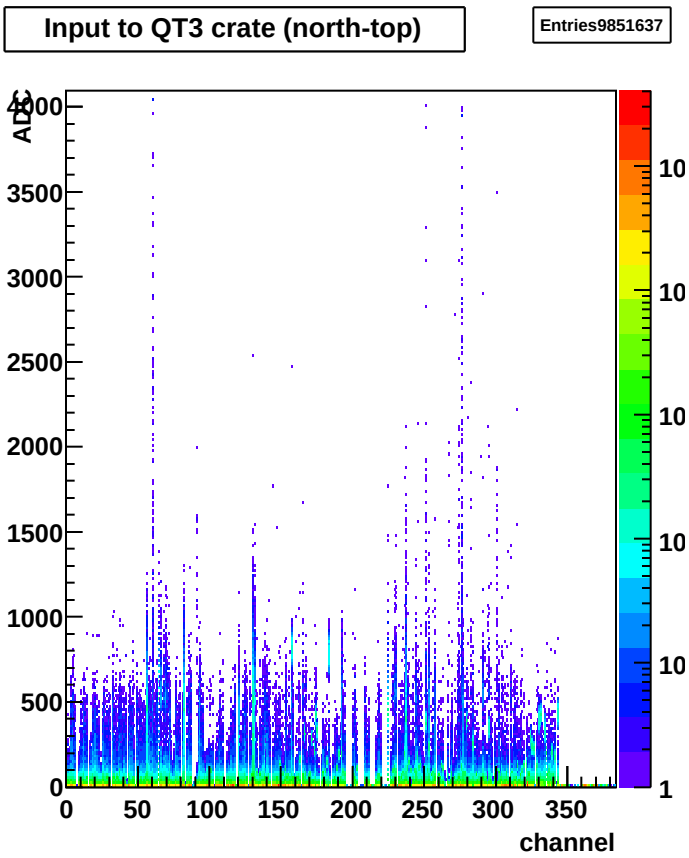

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1018475
Mean 4079
RMS 438.8

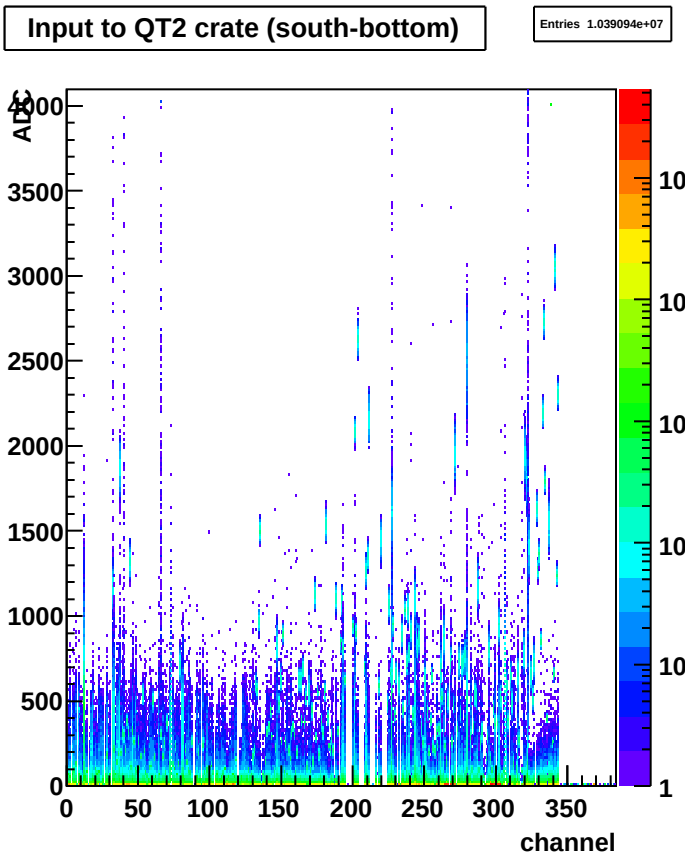

VPD-TAC-E
Entries 801071
Mean 1320
RMS 164.4

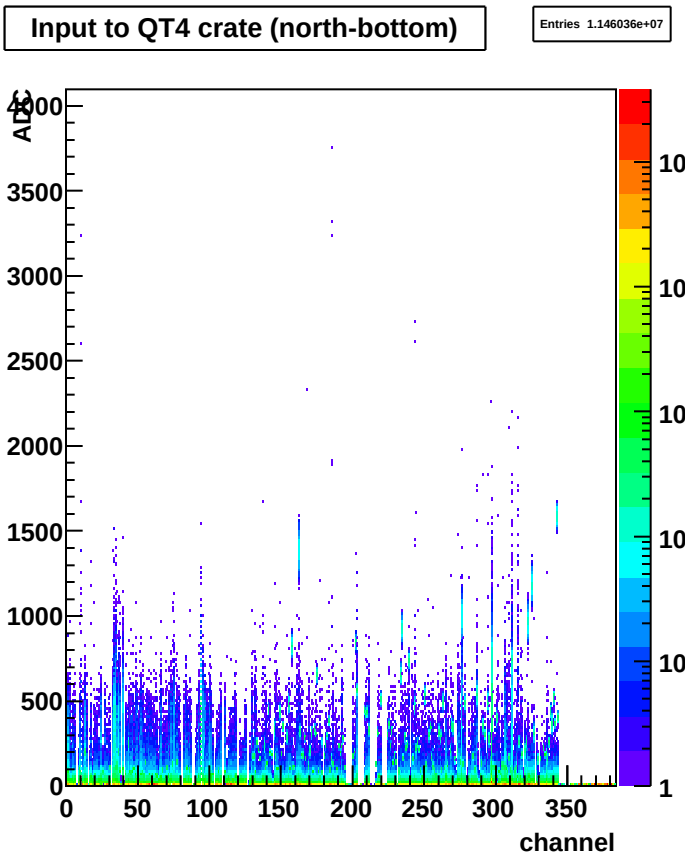

VPD-TAC-W
Entries 777218
Mean 1429
RMS 176.8

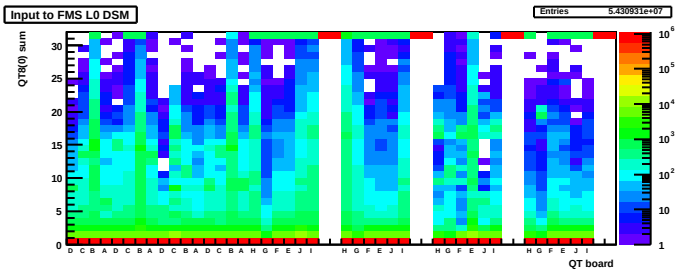

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 649944
Mean 3985
RMS 124.3

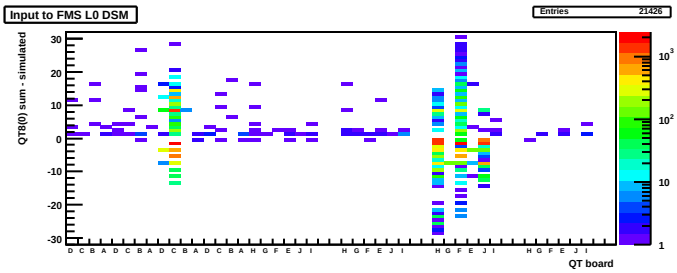

Input to QT1 crate (south-top)
Entries 1.656527e+07
ADC
channel

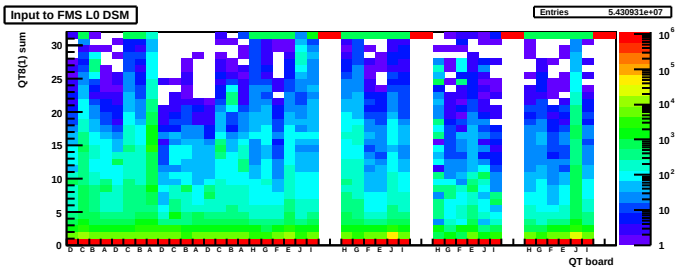

Input to QT3 crate (north-top)
Entries9851637
ADC
channel

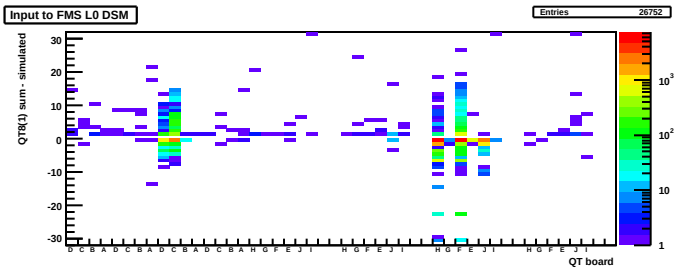

Input to QT2 crate (south-bottom)
Entries 1.039094e+07
ADC
channel

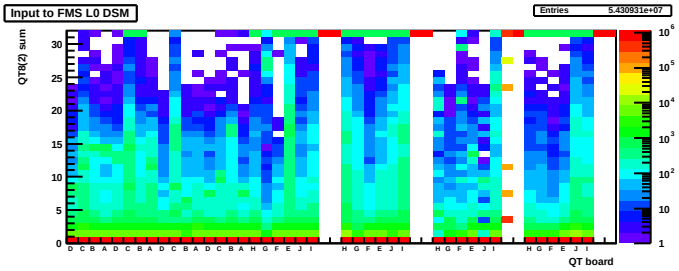

Input to QT4 crate (north-bottom)
Entries 1.146036e+07
ADC
channel

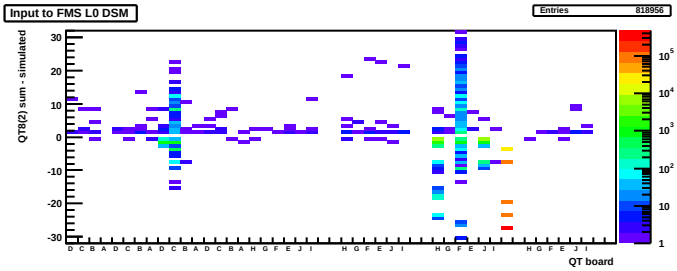

Input to FMS L0 DSM
Entries 5.430931e+07
QT8(0) sum
QT board

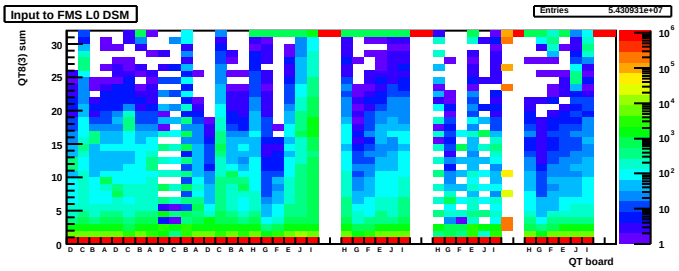

Input to FMS L0 DSM
Entries 21426
QT8(0) sum - simulated
QT board

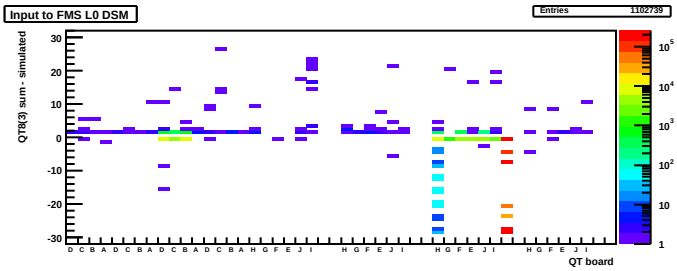

Input to FMS L0 DSM
Entries 5.430931e+07
QT8(1) sum
QT board

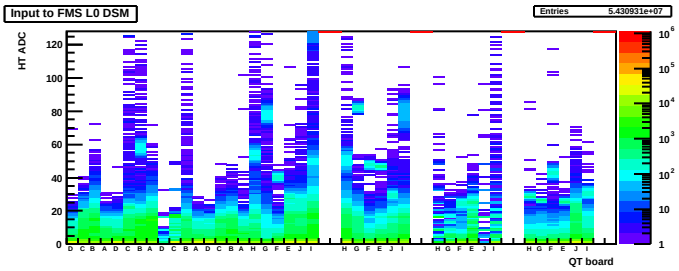

Input to FMS L0 DSM
Entries 26752
QT8(1) sum - simulated
QT board

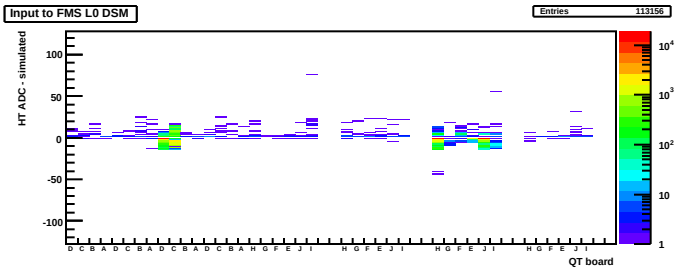

Input to FMS L0 DSM
Entries 5.430931e+07
QT8(2) sum
QT board

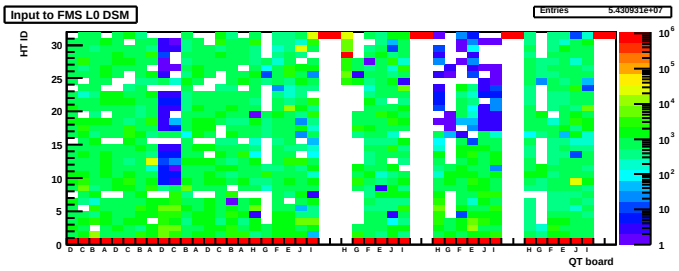

Input to FMS L0 DSM
Entries 818956
QT8(2) sum - simulated
QT board

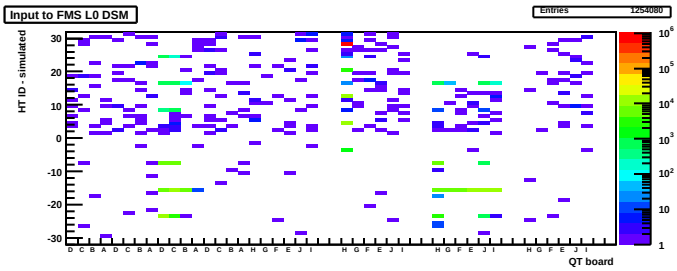

Input to FMS L0 DSM
Entries 5.430931e+07
QT8(3) sum
QT board

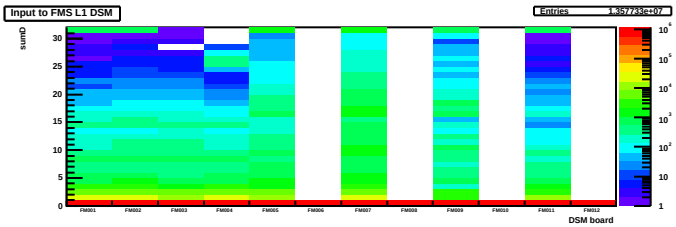

Input to FMS L0 DSM
Entries 1102739
QT8(3) sum - simulated
QT board

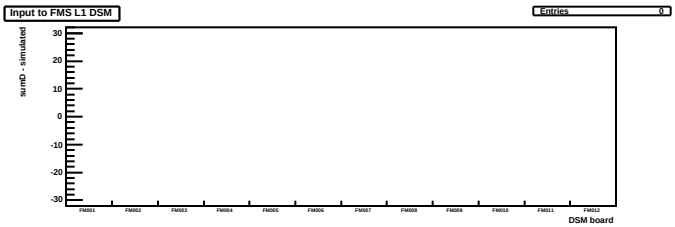

Input to FMS L0 DSM
Entries 5.430931e+07
HT ADC
QT board

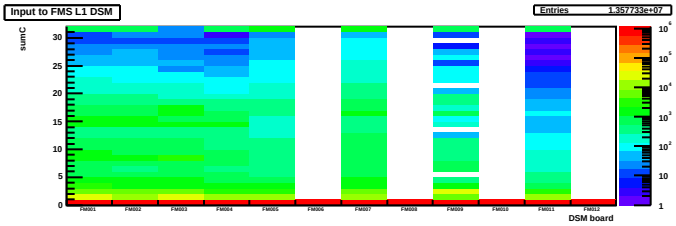

Input to FMS L0 DSM
Entries 113156
HT ADC - simulated
QT board

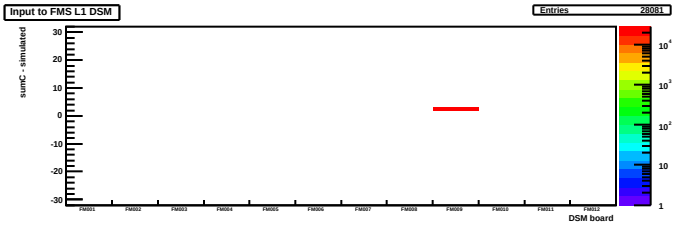

Input to FMS L0 DSM
Entries 5.430931e+07
HT ID
QT board

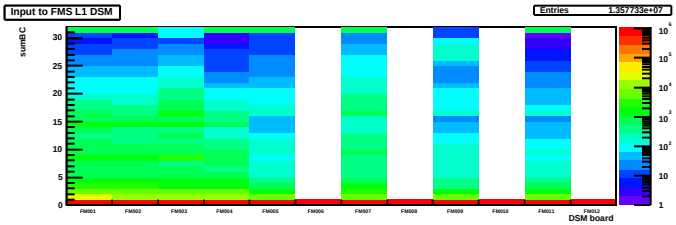

Input to FMS L0 DSM
Entries 1254080
HT ID - simulated
QT board

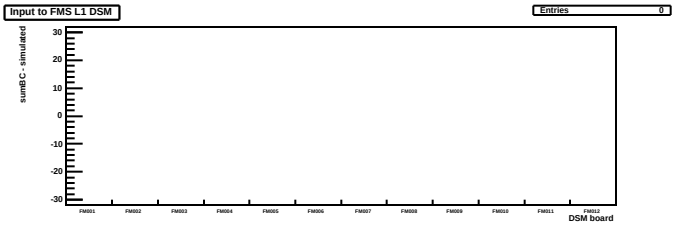

Input to FMS L1 DSM
Entries 1.357733e+07
sumD
DSM board

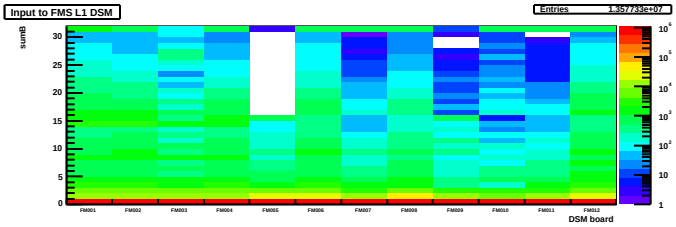

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

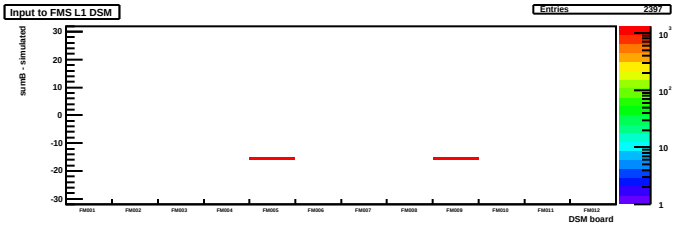

Input to FMS L1 DSM
Entries 1.357733e+07
sumC
DSM board

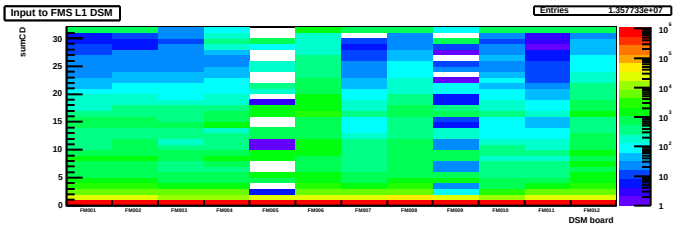

Input to FMS L1 DSM
Entries 28081
sumC - simulated
DSM board

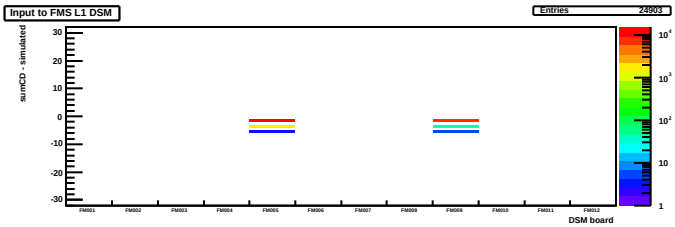

Input to FMS L1 DSM
Entries 1.357733e+07
sumBC
DSM board

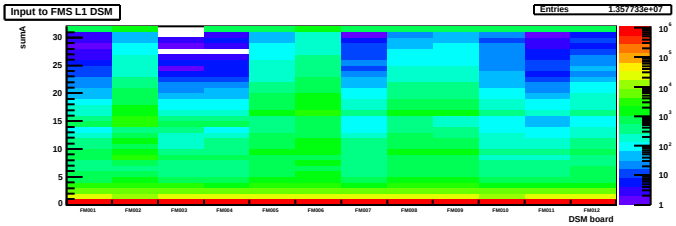

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

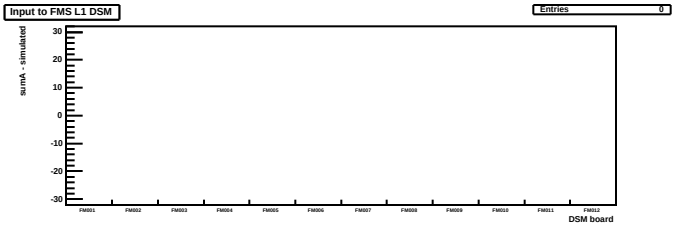

Input to FMS L1 DSM
Entries 1.357733e+07
sumB
DSM board

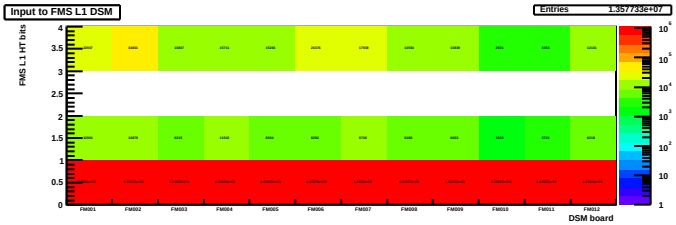

Input to FMS L1 DSM
Entries 2397
sumB - simulated
DSM board

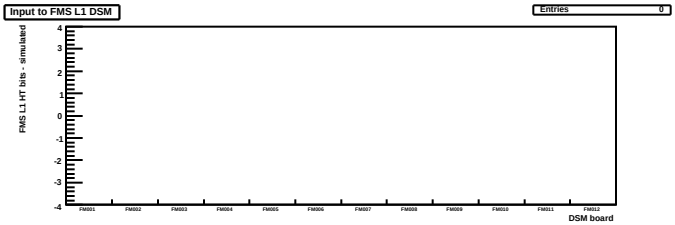

Input to FMS L1 DSM
Entries 1.357733e+07
sumCD
DSM board

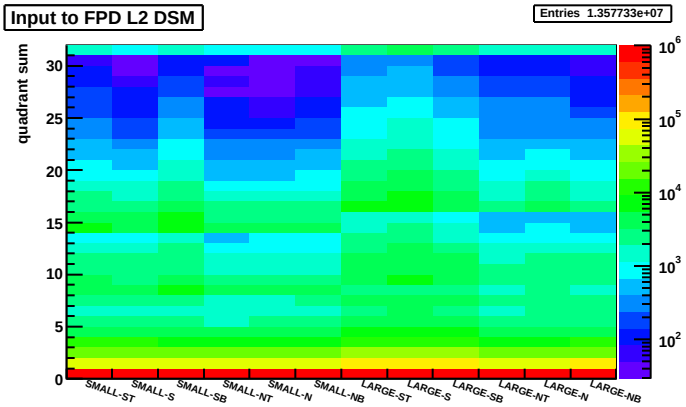

Input to FMS L1 DSM
Entries 24903
sumCD - simulated
DSM board

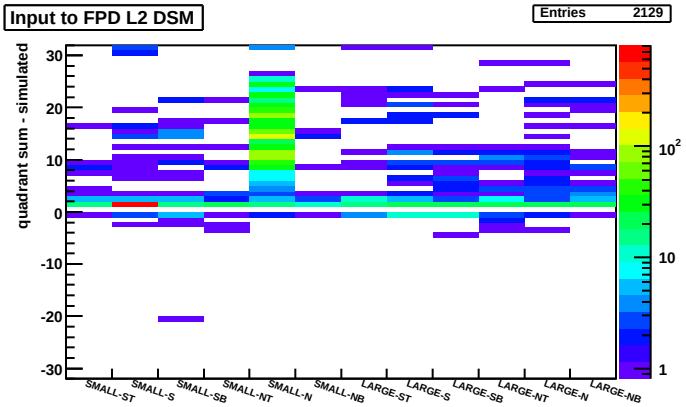

Input to FMS L1 DSM
Entries 1.357733e+07
sumA
DSM board

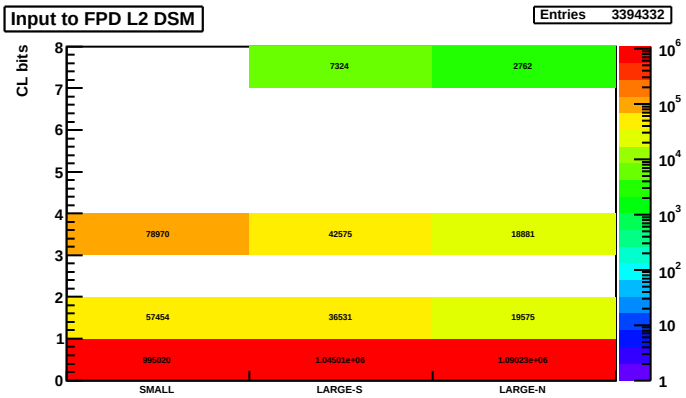

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

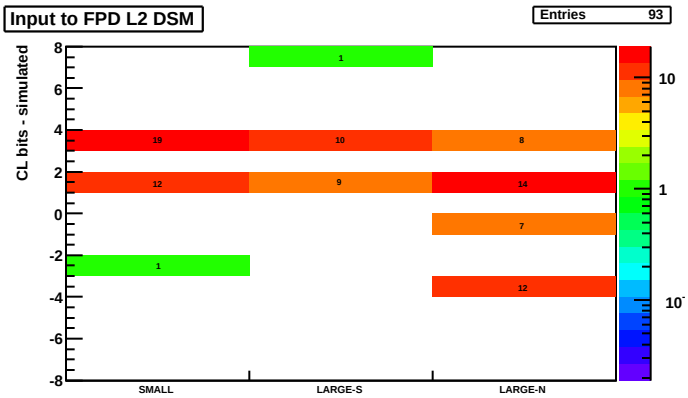

Input to FMS L1 DSM
Entries 1.357733e+07
FMS L1 HT bits
DSM board

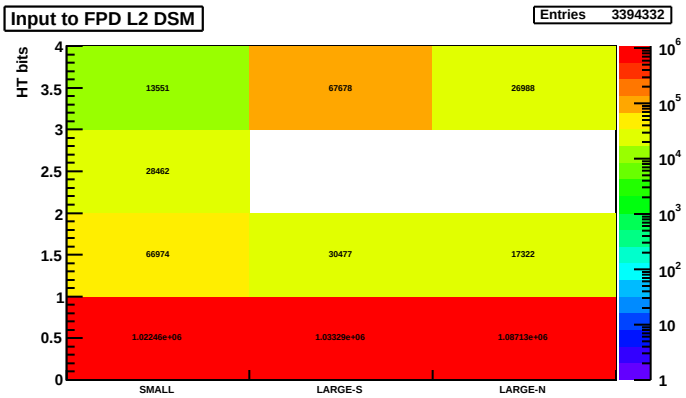

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

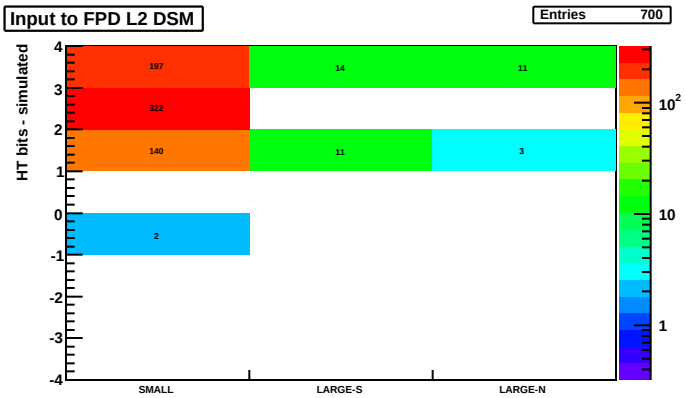

Input to FPD L2 DSM
Entries 1.357733e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

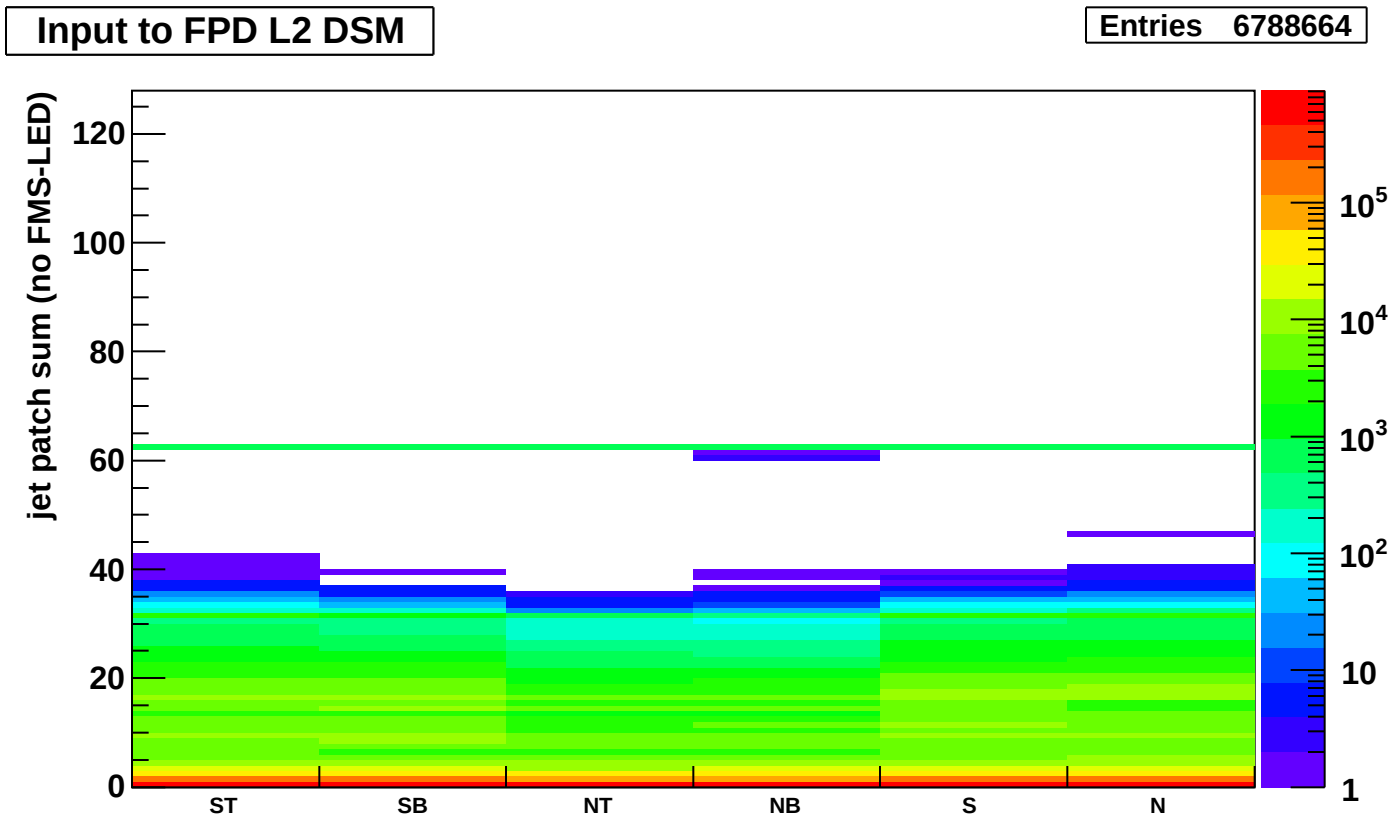

Input to FPD L2 DSM
Entries 2129
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

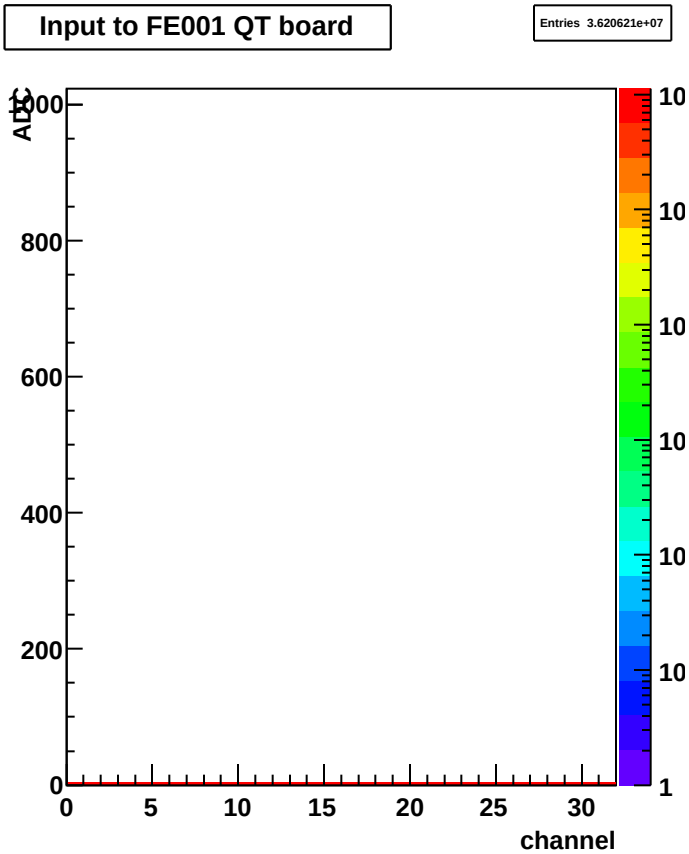

Input to FPD L2 DSM
Entries 3394332
CL bits
7324
2762
78970
42575
18881
57454
36531
19575
995020
1.04501e+06
1.09023e+06
SMALL
LARGE-S
LARGE-N

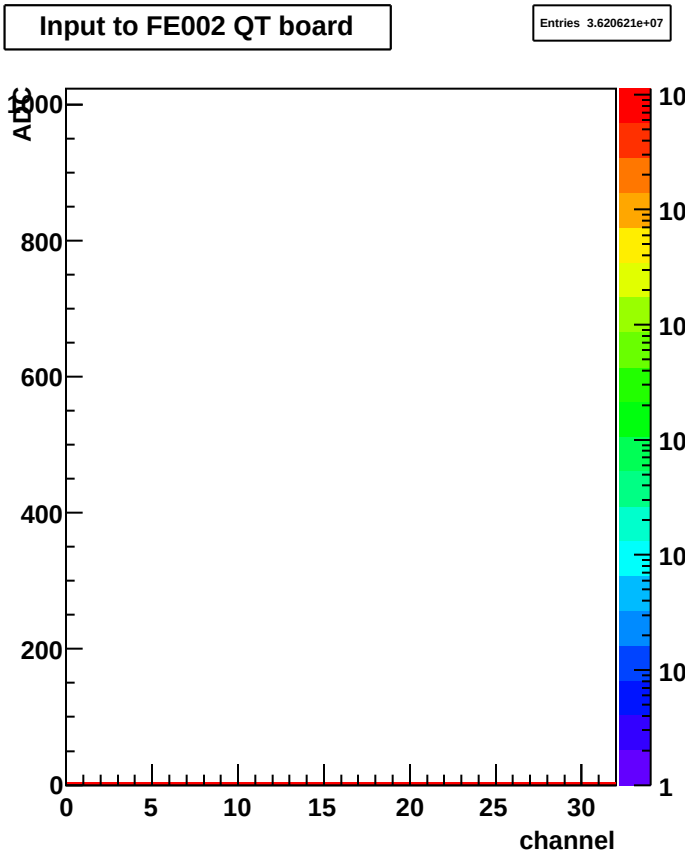

Input to FPD L2 DSM
Entries 93
CL bits - simulated
1
19
10
8
12
9
14
7
1
12
SMALL
LARGE-S
LARGE-N

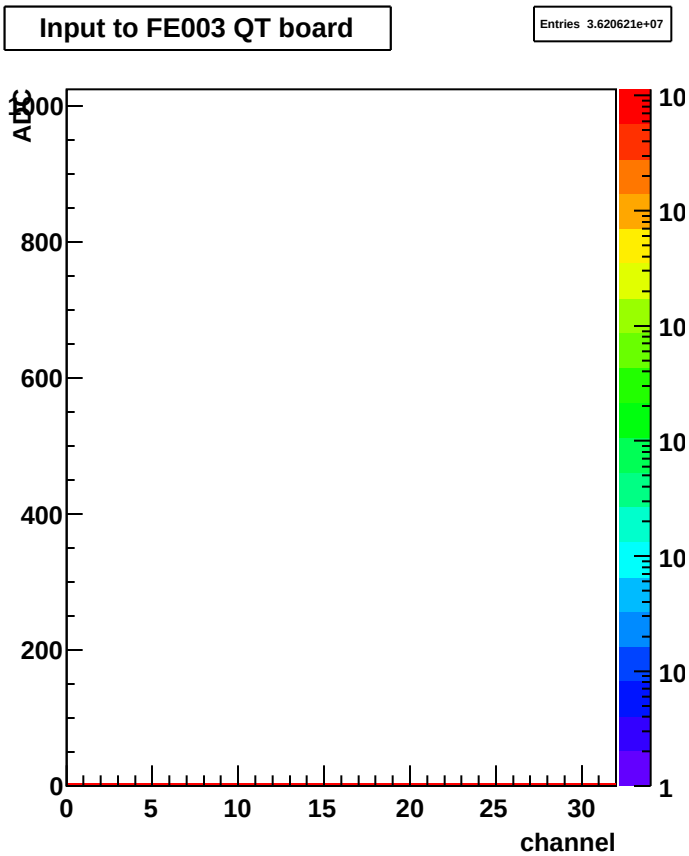

Input to FPD L2 DSM
Entries 3394332
HT bits
13551
67678
26988
28462
66974
30477
17322
1.02246e+06
1.03329e+06
1.08713e+06
SMALL
LARGE-S
LARGE-N

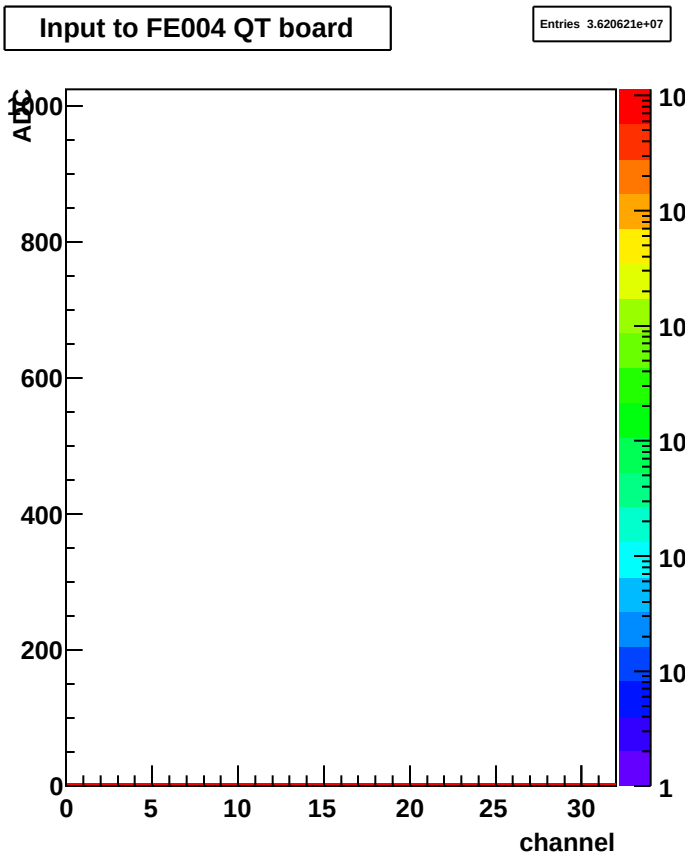

Input to FPD L2 DSM
Entries 700
HT bits - simulated
197
14
11
322
140
11
3
2
SMALL
LARGE-S
LARGE-N

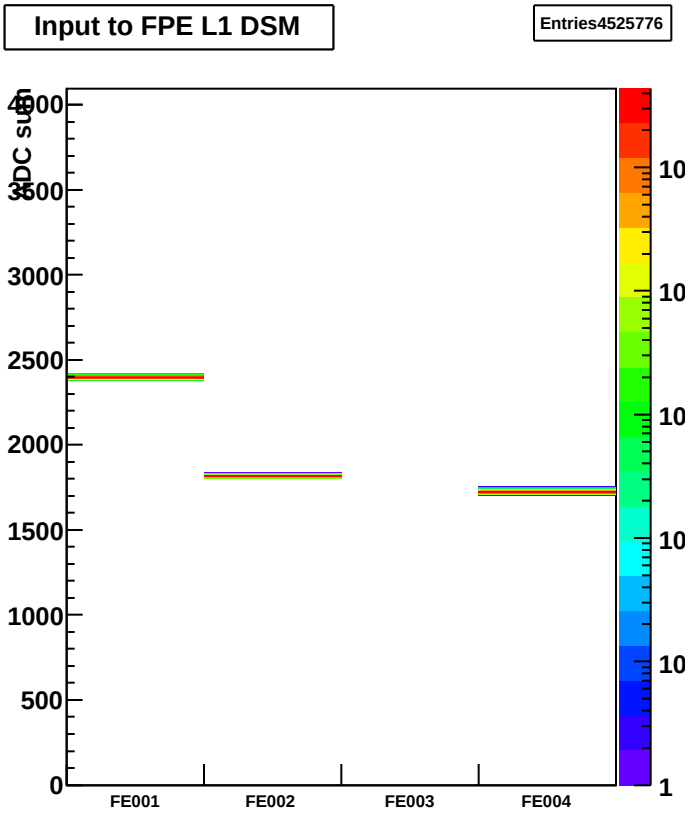

Input to FPD L2 DSM
Entries 6788664
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

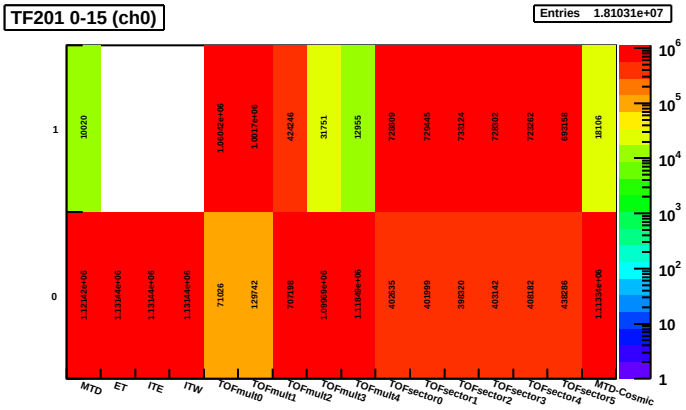

Input to FE001 QT board
Entries 3.620621e+07
ADC
channel

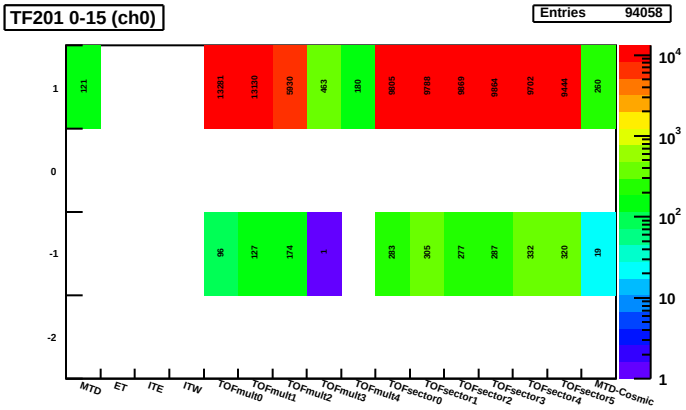

Input to FE002 QT board
Entries 3.620621e+07
ADC
channel

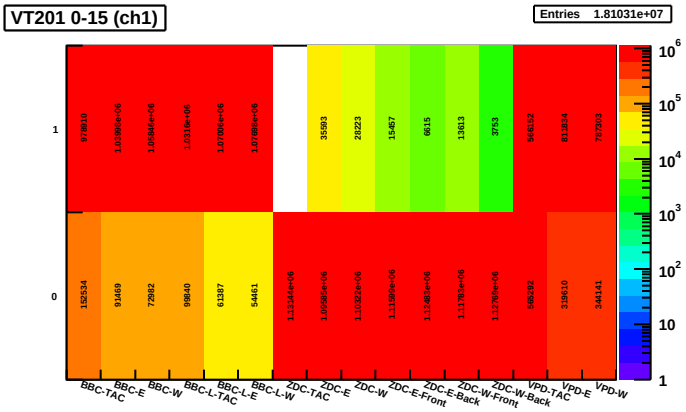

Input to FE003 QT board
Entries 3.620621e+07
ADC
channel

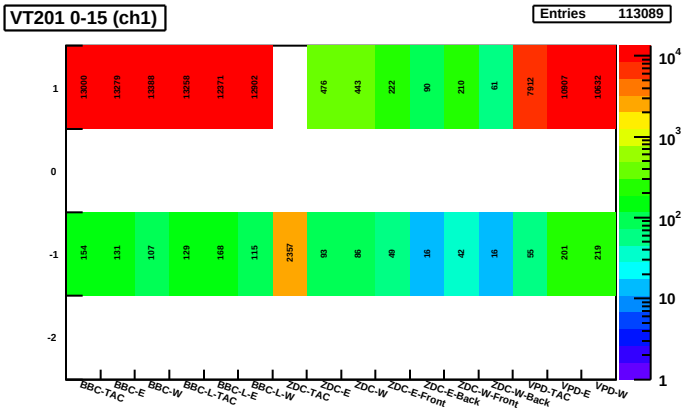

Input to FE004 QT board
Entries 3.620621e+07
ADC
channel

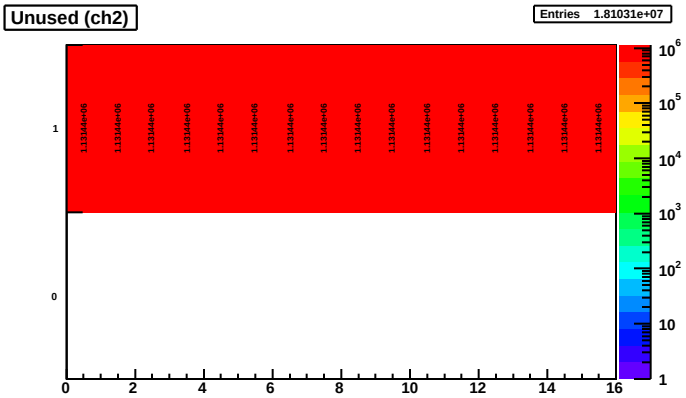

Input to FPE L1 DSM
Entries4525776
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

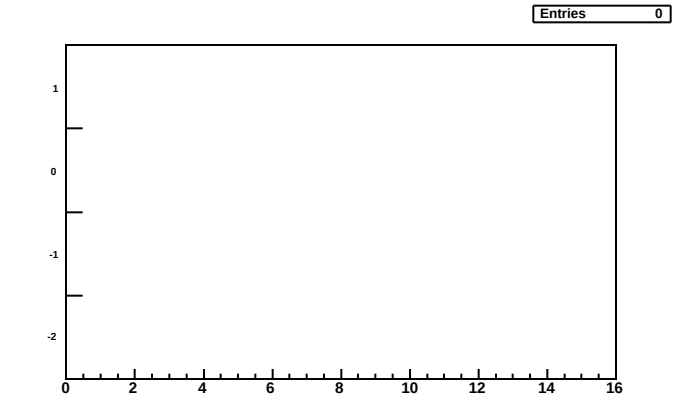

TF201 0-15 (ch0)
Entries 1.81031e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

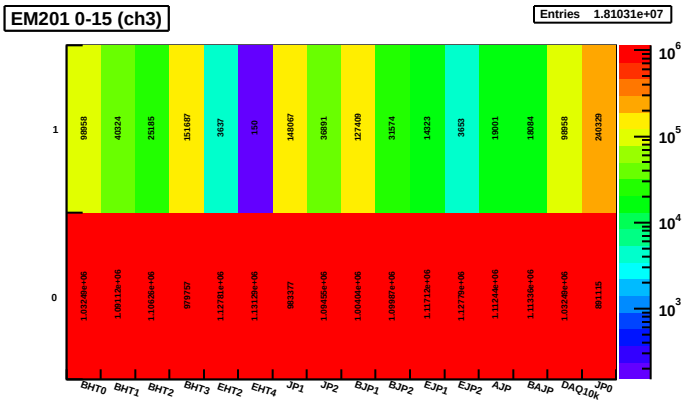

TF201 0-15 (ch0)
Entries 94058
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

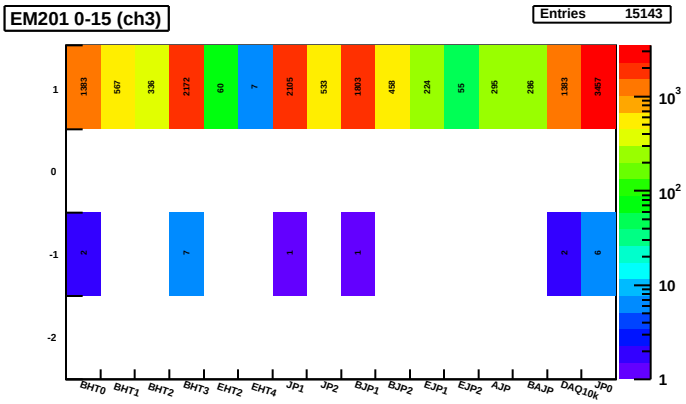

VT201 0-15 (ch1)
Entries 1.81031e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 113089
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

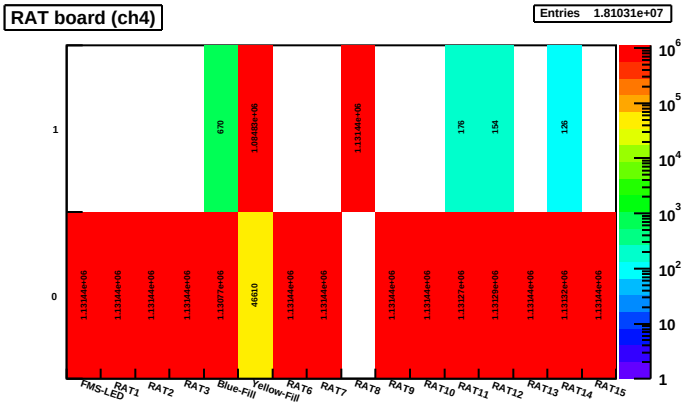

Unused (ch2)
Entries 1.81031e+07

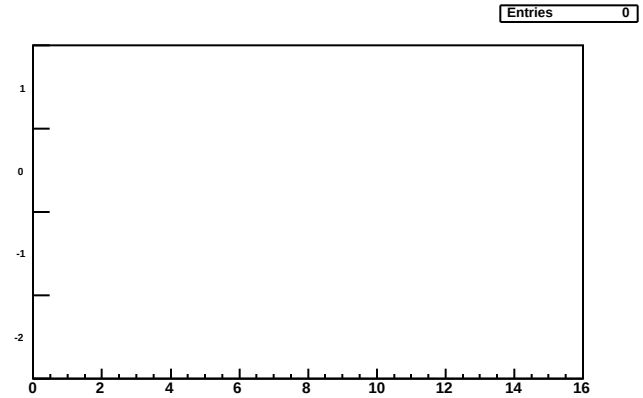

Entries 0


EM201 0-15 (ch3)
Entries 1.81031e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

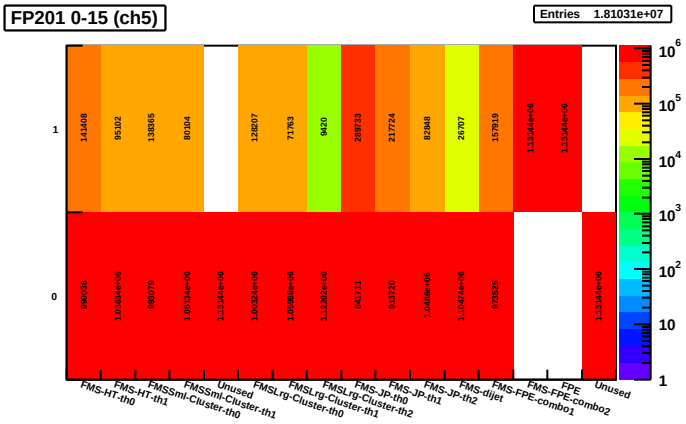

EM201 0-15 (ch3)
Entries 15143
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

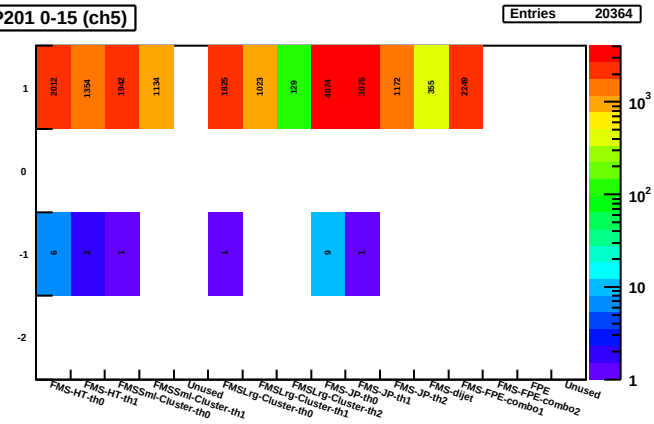




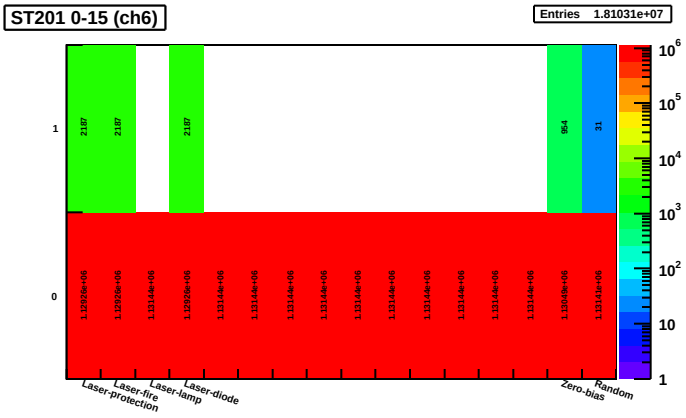
FP201 0-15 (ch5)

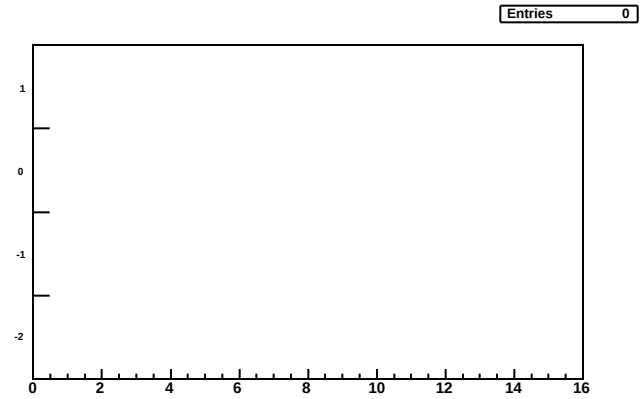
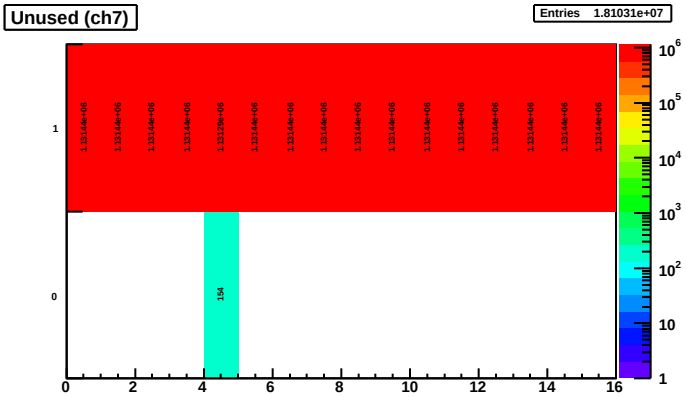
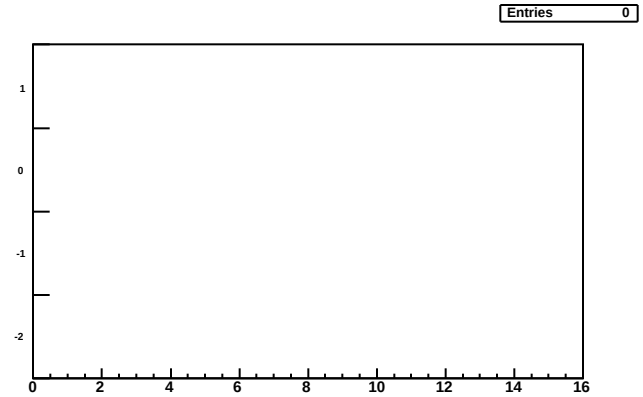


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)