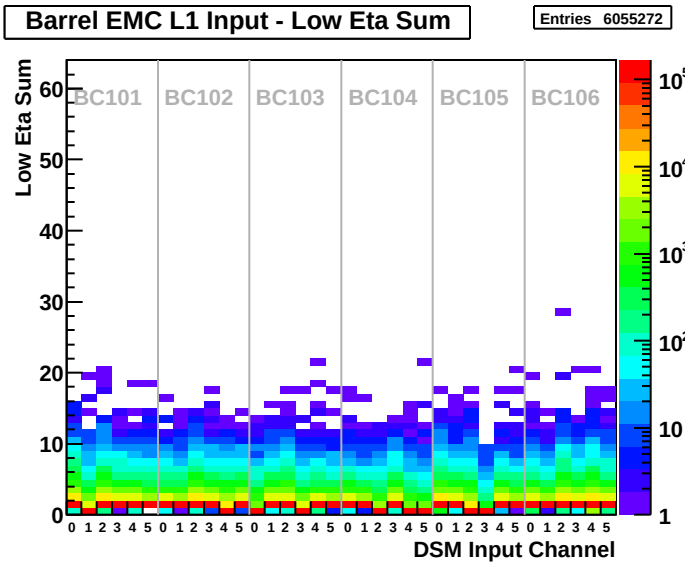
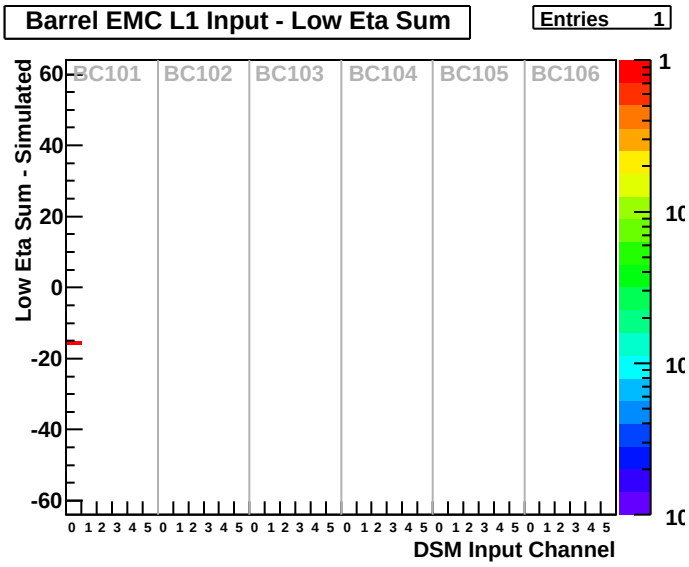
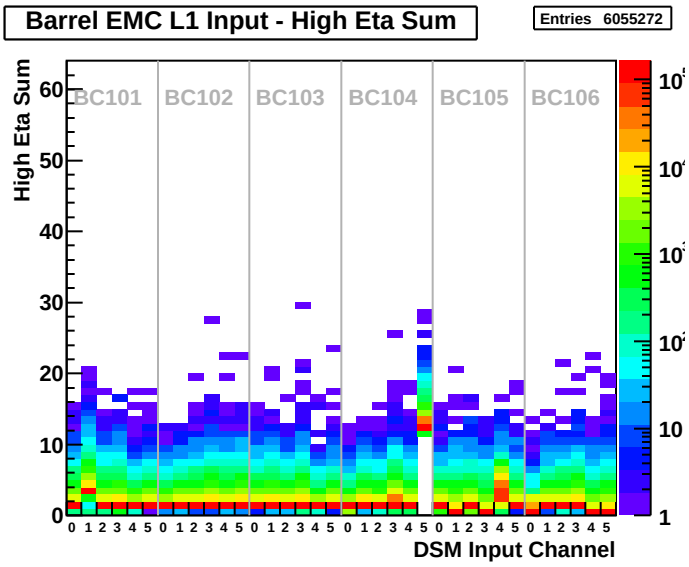
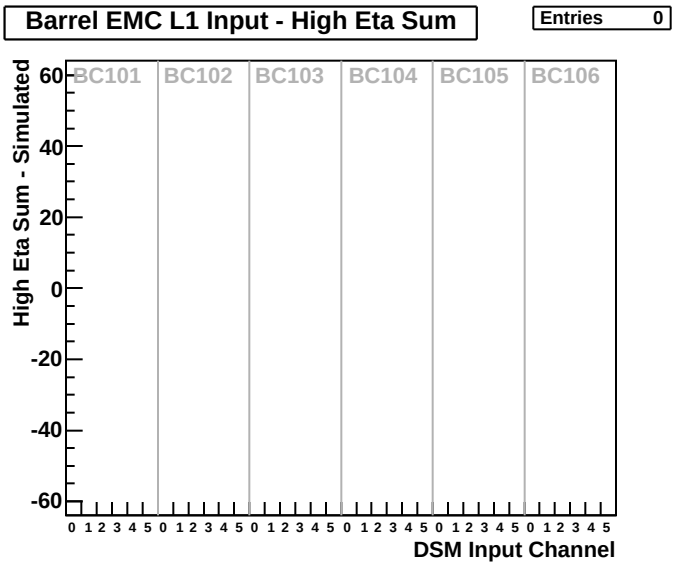
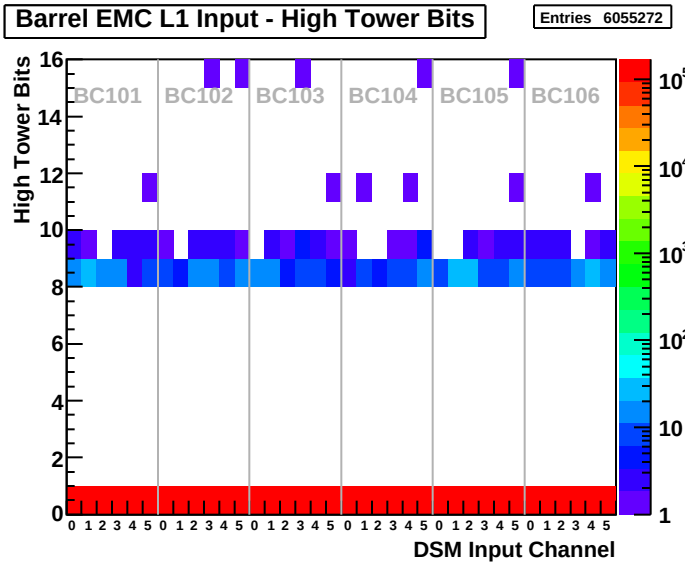
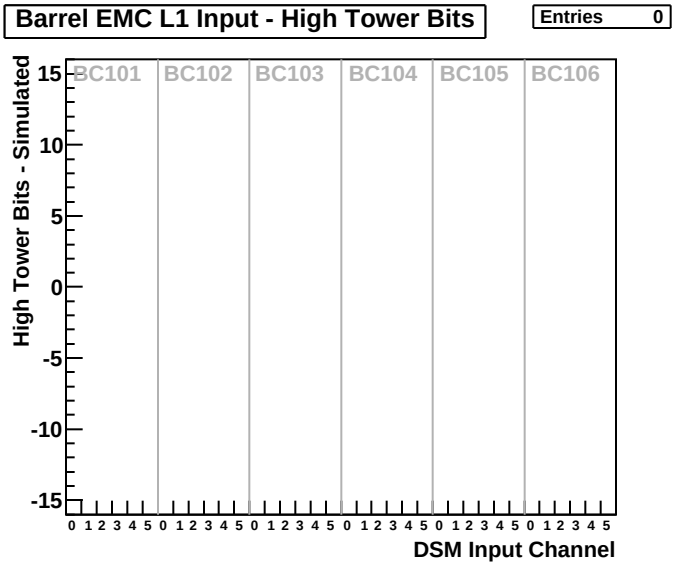

Barrel EMC L1 Input - Low Eta Sum
Entries 6055272
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

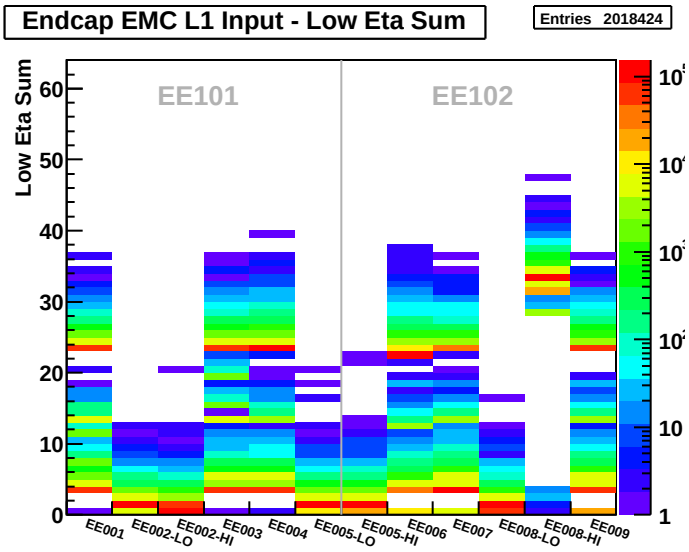

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

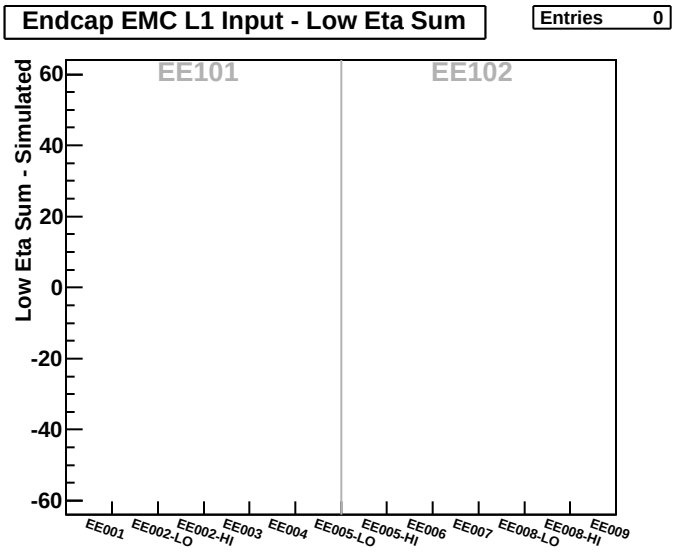

Barrel EMC L1 Input - High Eta Sum
Entries 6055272
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

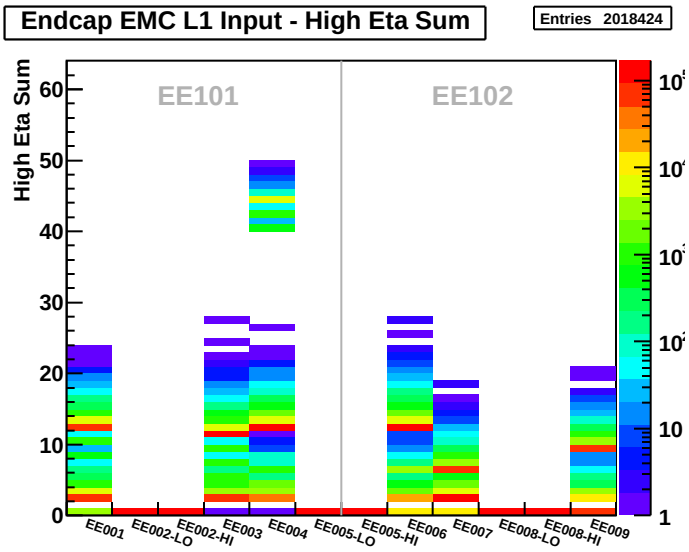

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

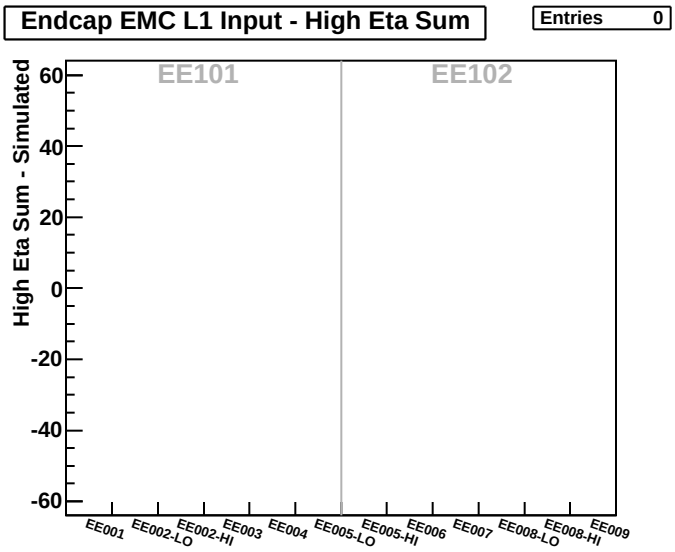

Barrel EMC L1 Input - High Tower Bits
Entries 6055272
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

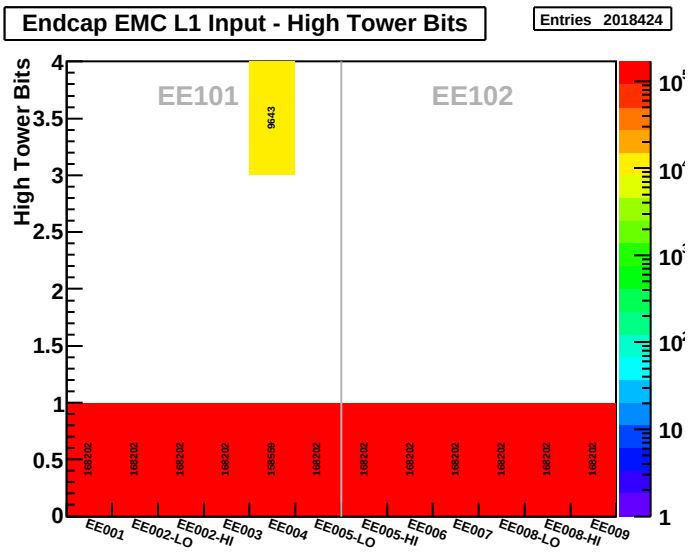

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

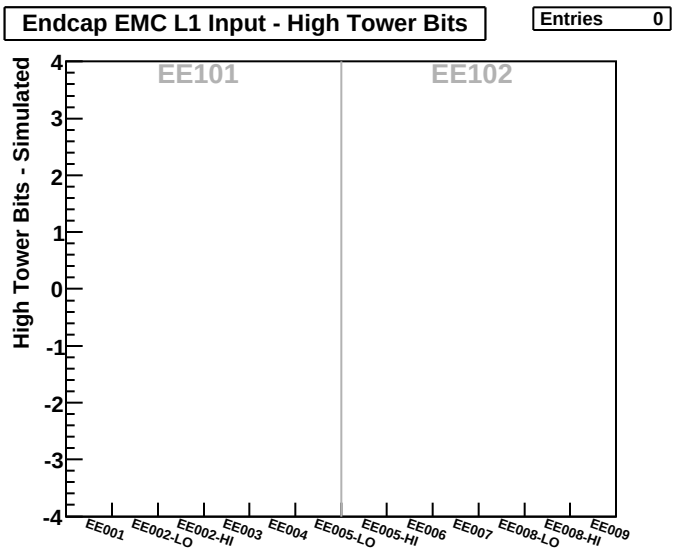

Endcap EMC L1 Input - Low Eta Sum
Entries 2018424
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

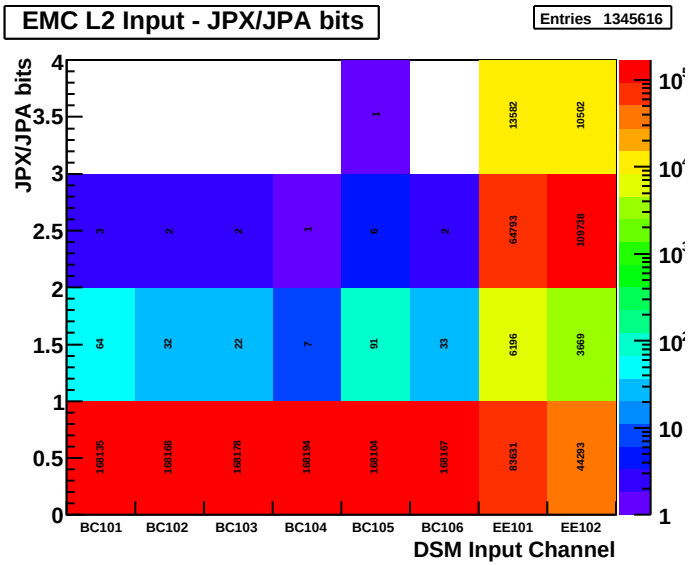

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

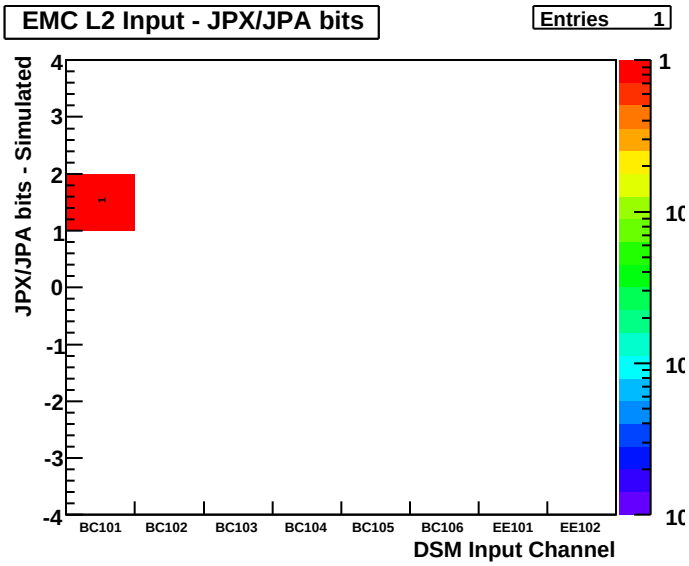

Endcap EMC L1 Input - High Eta Sum
Entries 2018424
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

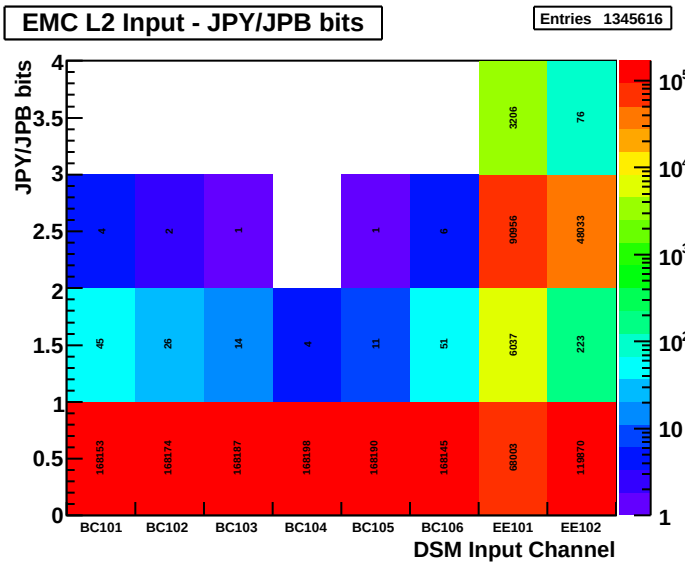

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

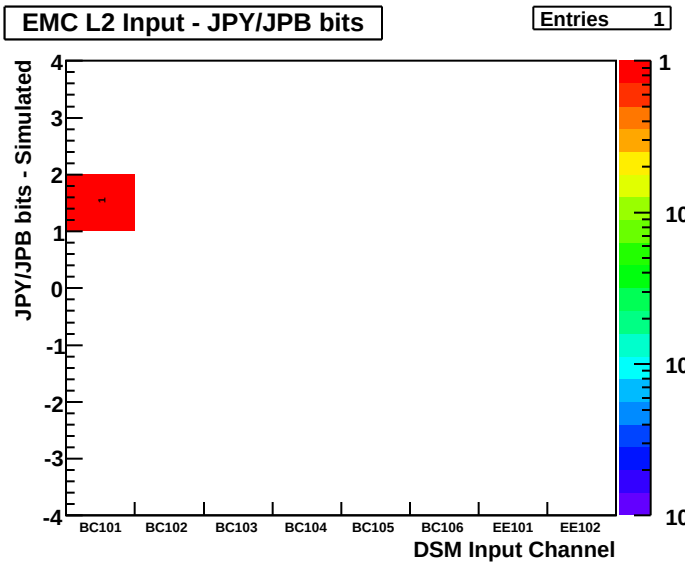

Endcap EMC L1 Input - High Tower Bits
Entries 2018424
High Tower Bits
EE101
EE102
9643
168202 168202 168202 168202 158559 168202 168202 168202 168202 168202 168202
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

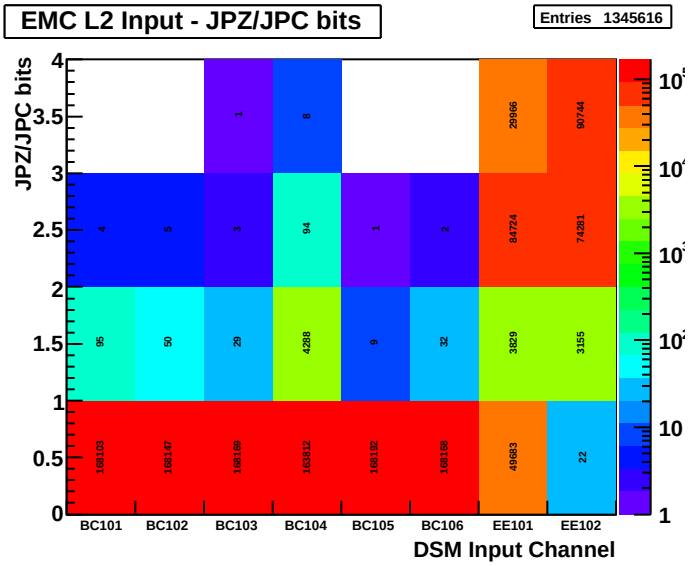

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

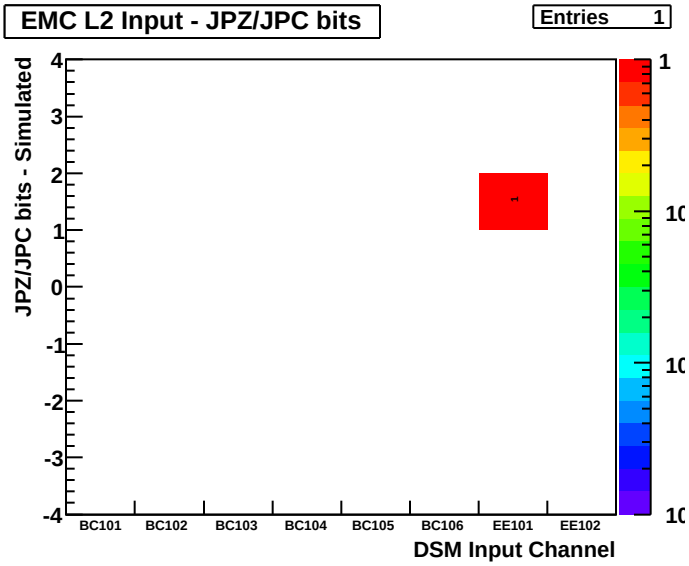

EMC L2 Input - JPX/JPA bits
Entries 1345616
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

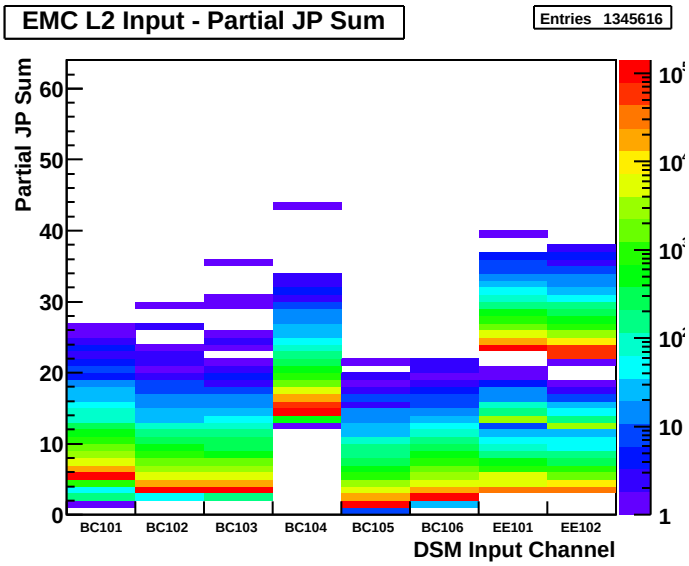

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

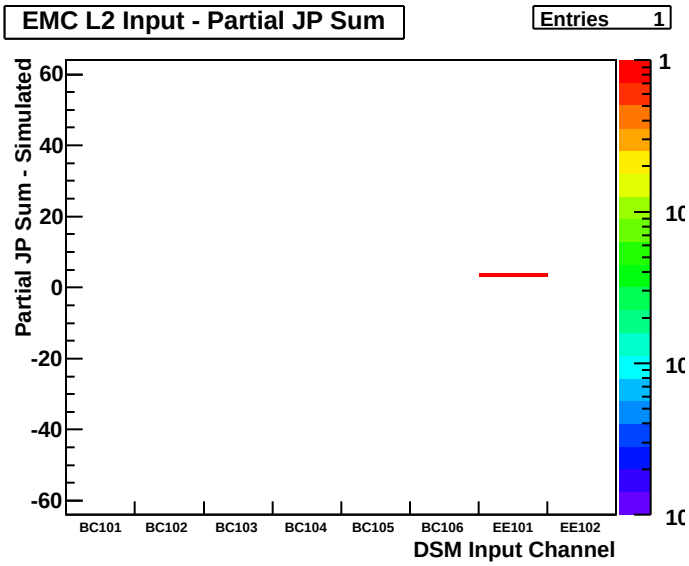

EMC L2 Input - JPY/JPB bits
Entries 1345616
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

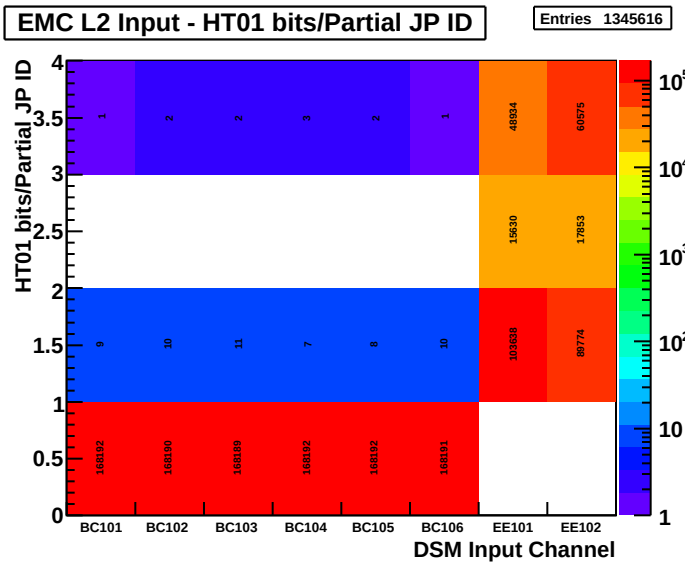

EMC L2 Input - JPY/JPB bits
Entries 1
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

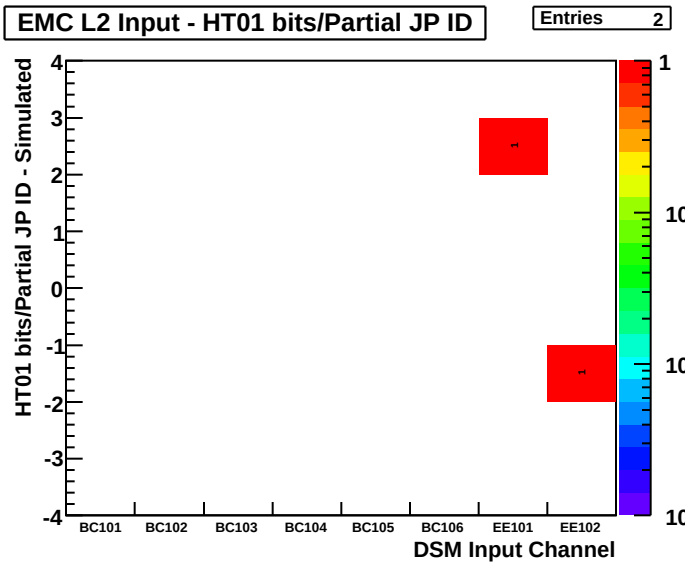

EMC L2 Input - JPZ/JPC bits
Entries 1345616
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

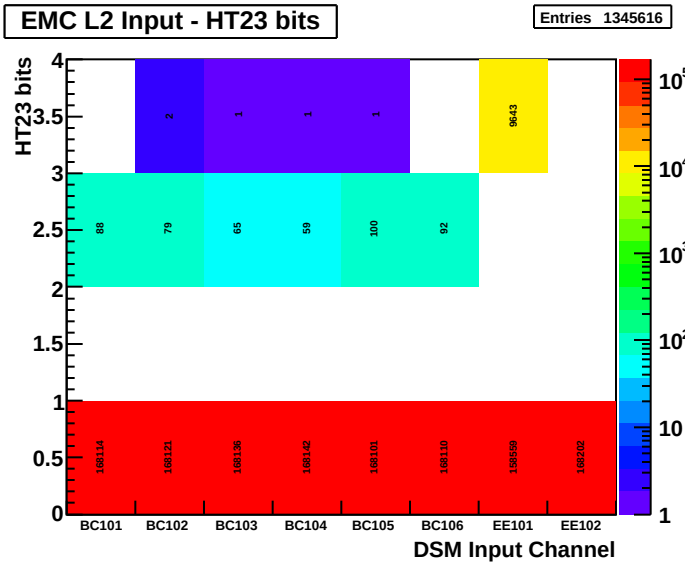

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

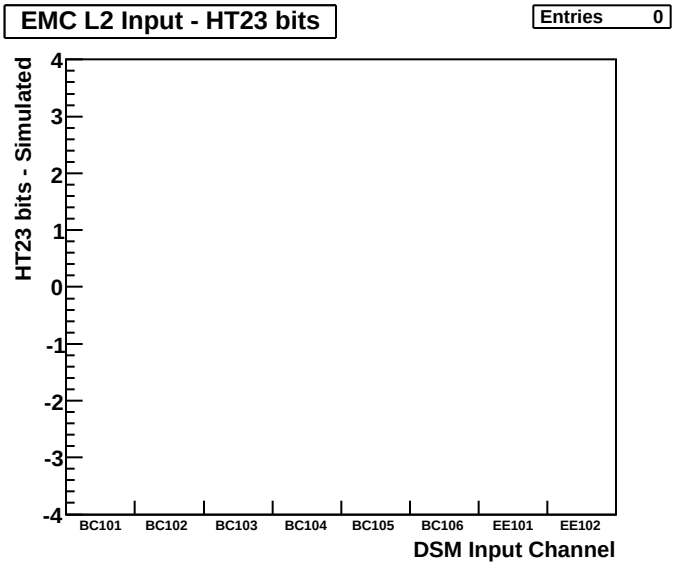

EMC L2 Input - Partial JP Sum
Entries 1345616
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

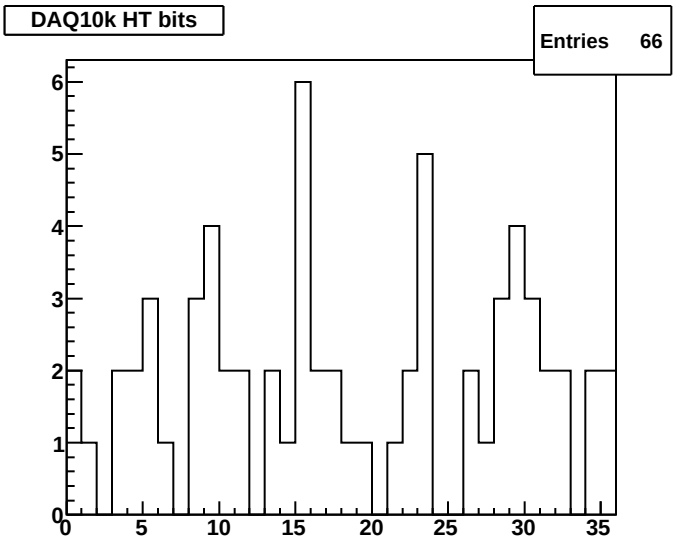

EMC L2 Input - Partial JP Sum
Entries 1
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

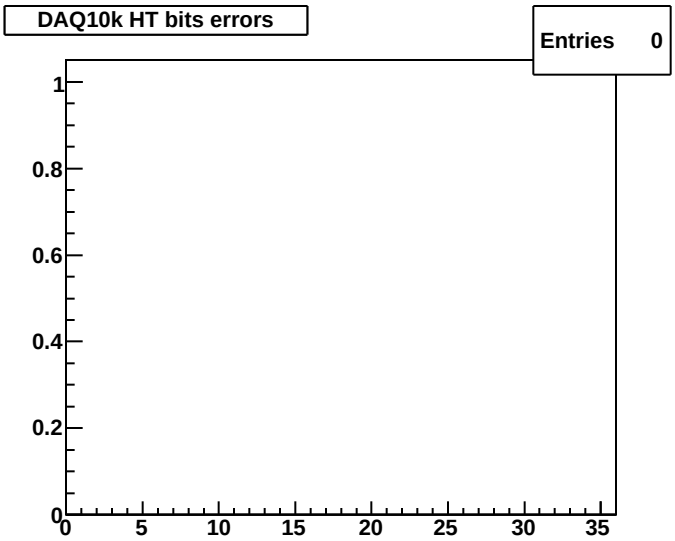

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1345616
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

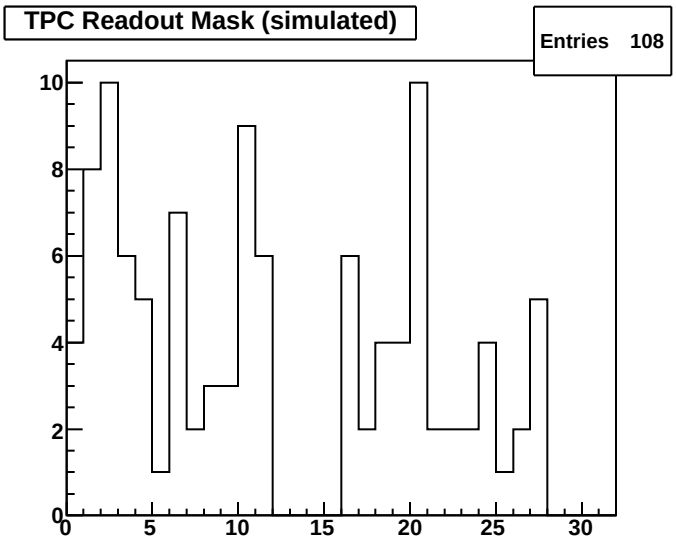

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

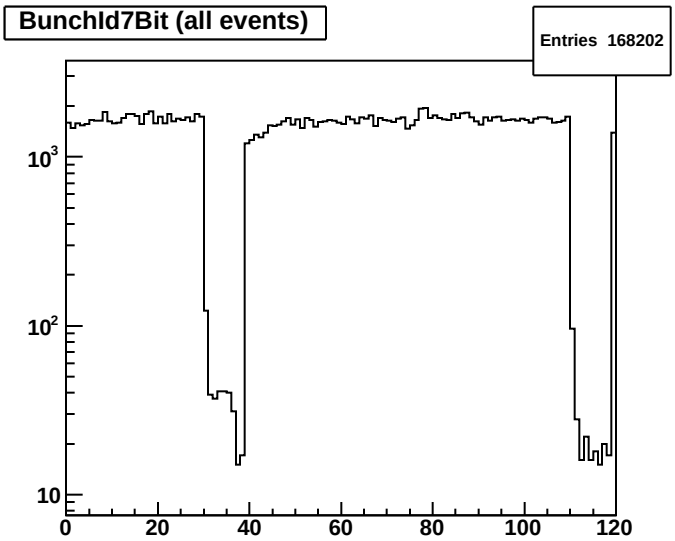

EMC L2 Input - HT23 bits
Entries 1345616
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

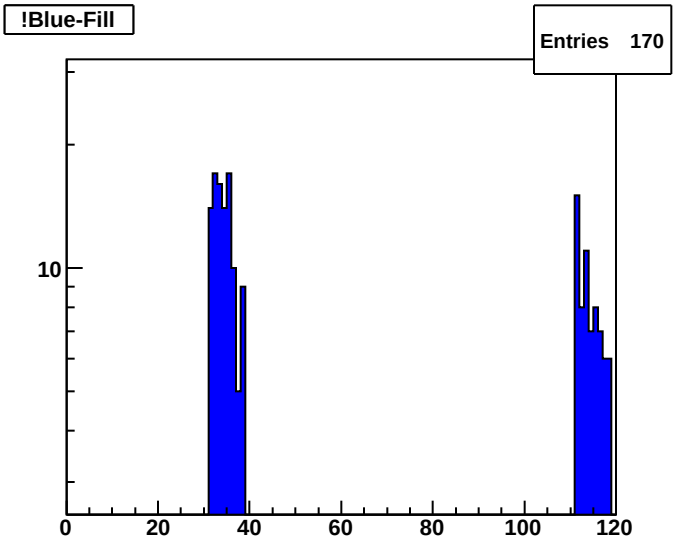

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

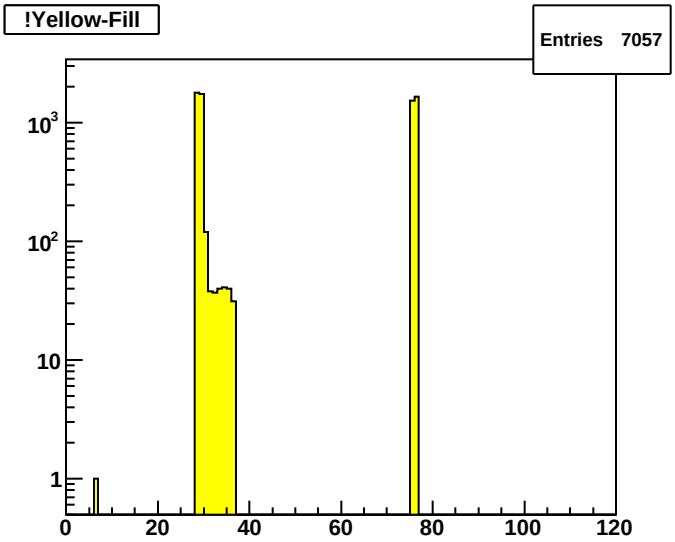

DAQ10k HT bits
Entries 66

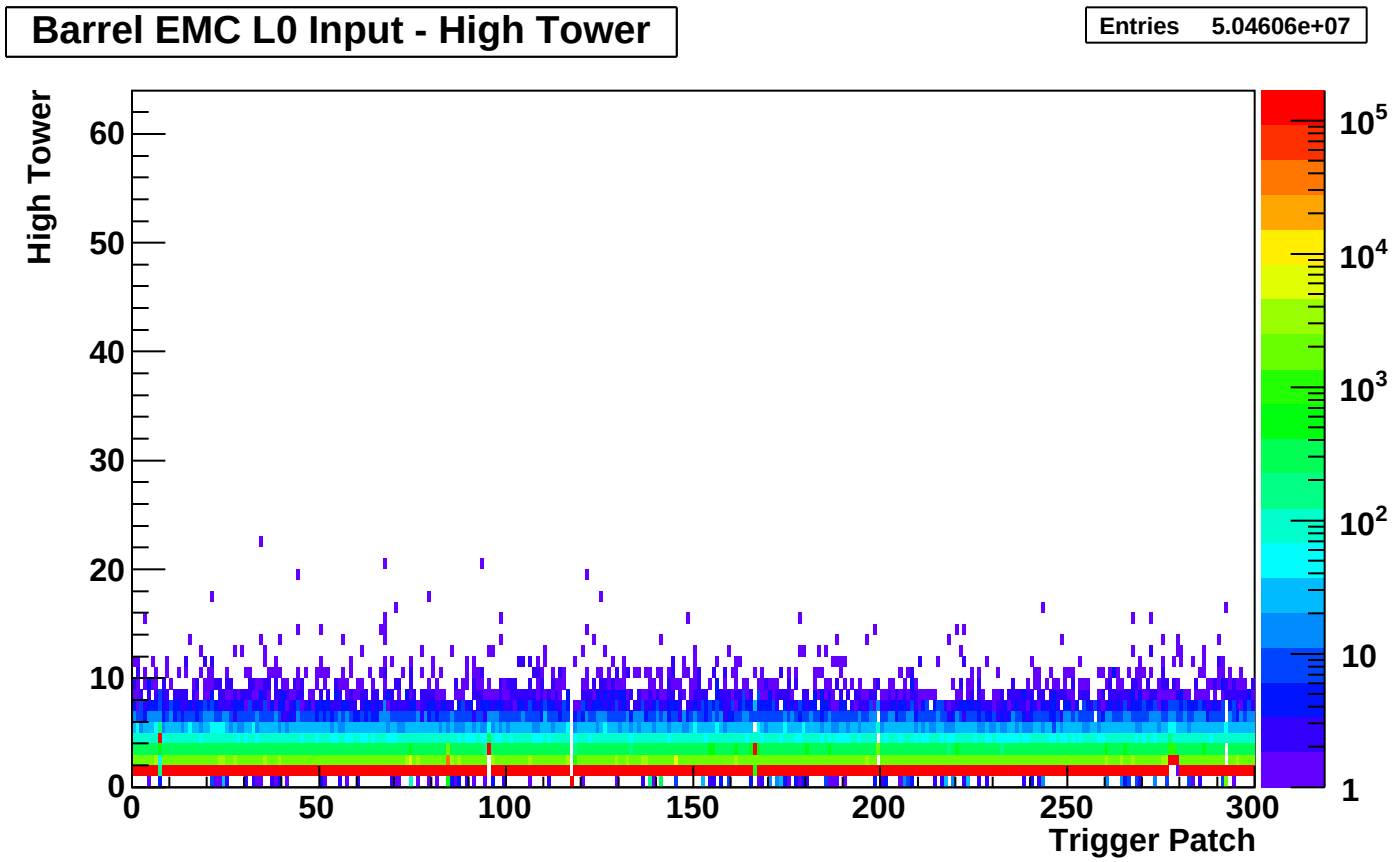

DAQ10k HT bits errors
Entries 0

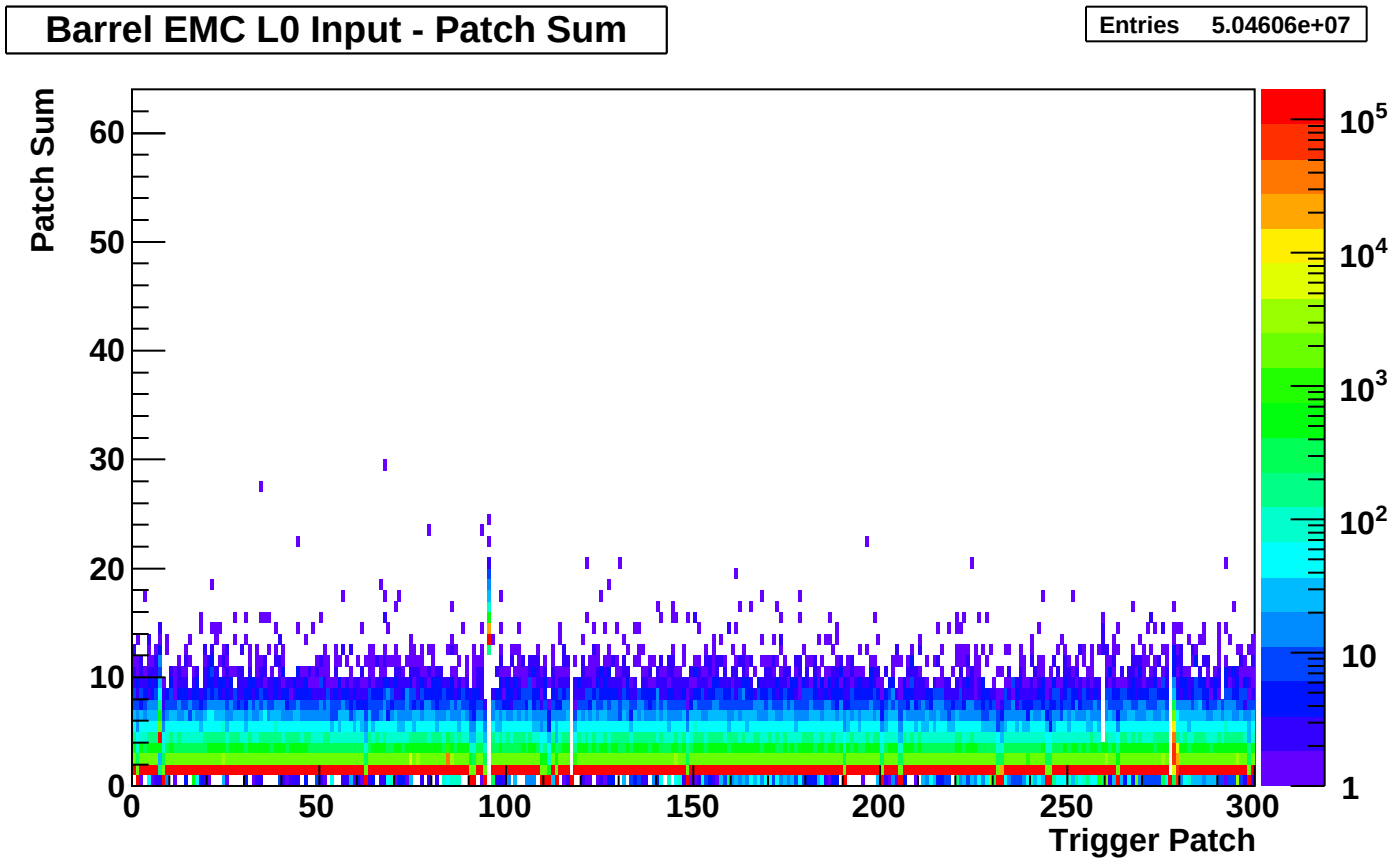

TPC Readout Mask (simulated)
Entries 108

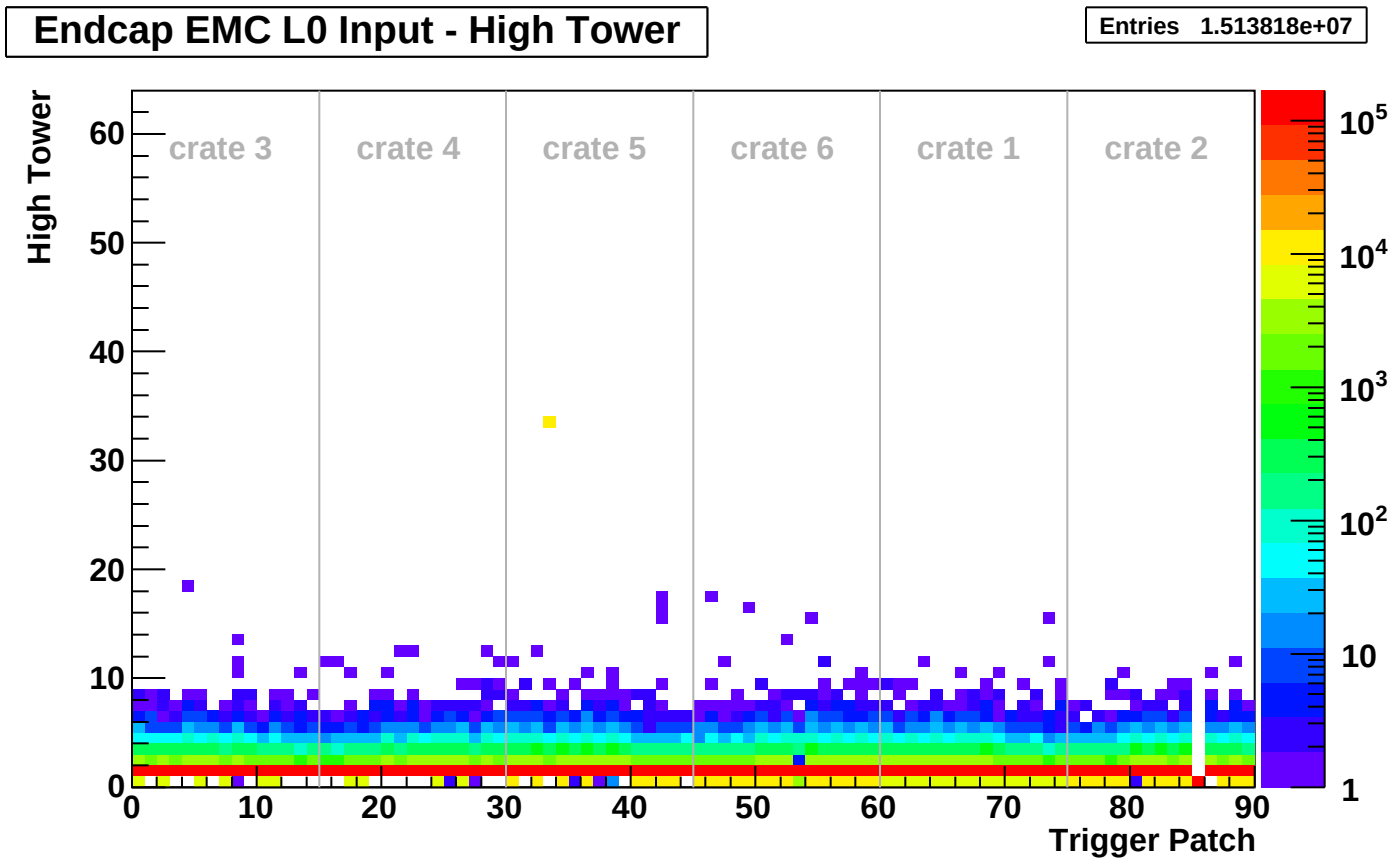

BunchId7Bit (all events)
Entries 168202

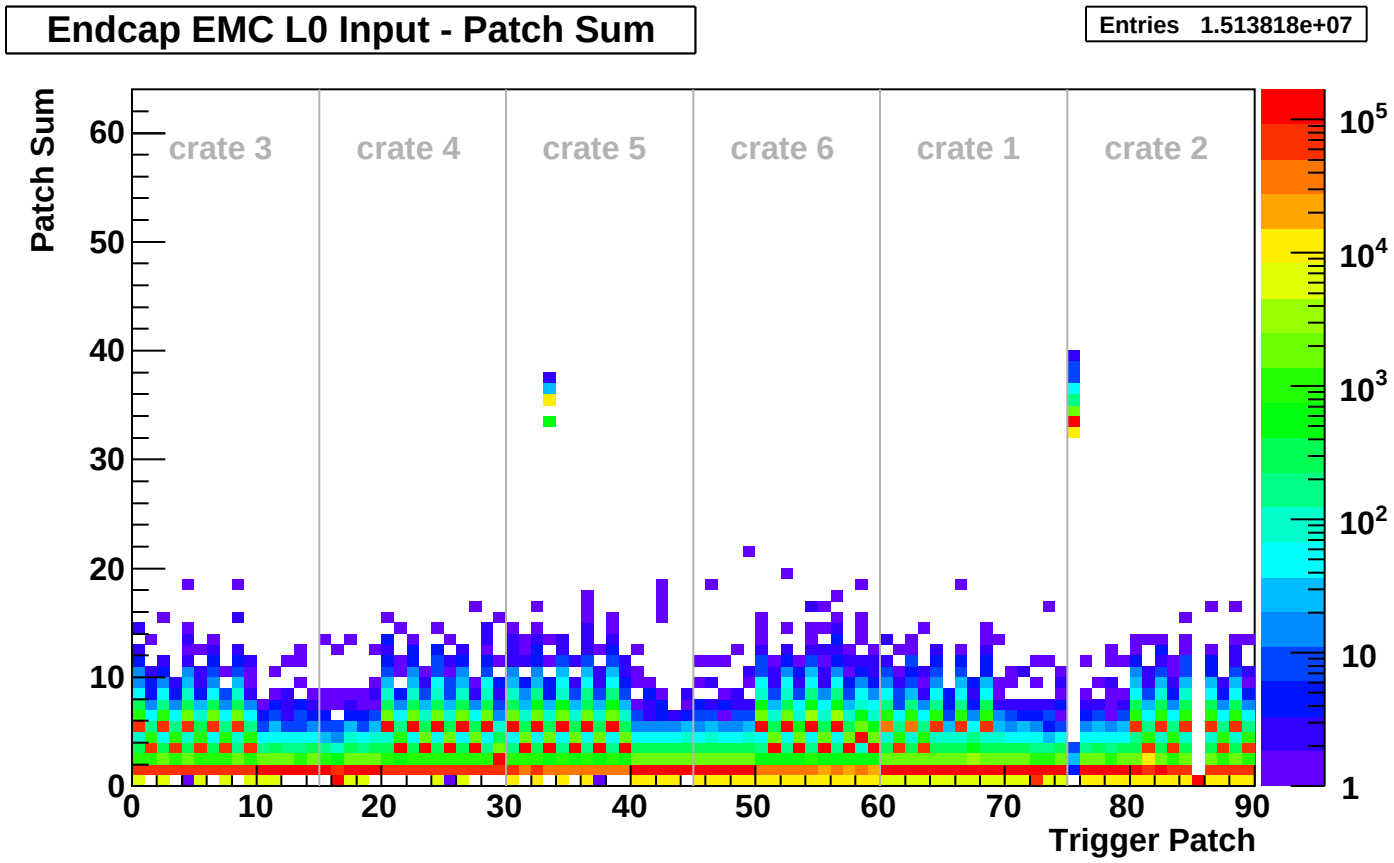

!Blue-Fill
Entries 170

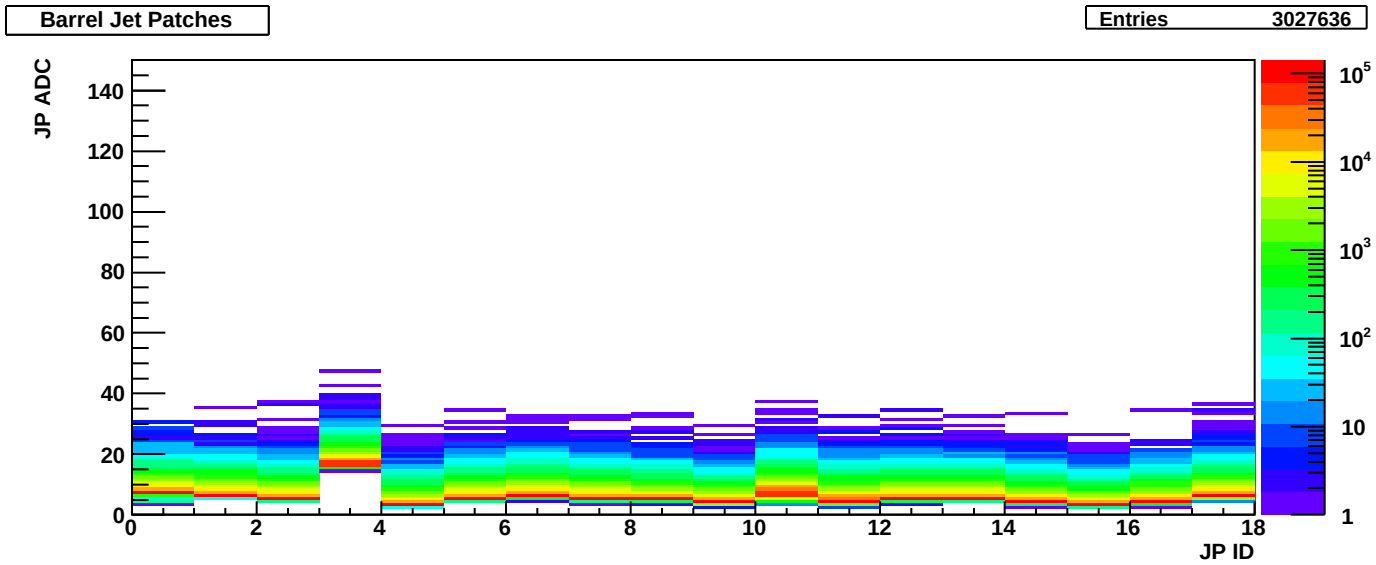

!Yellow-Fill
Entries 7057

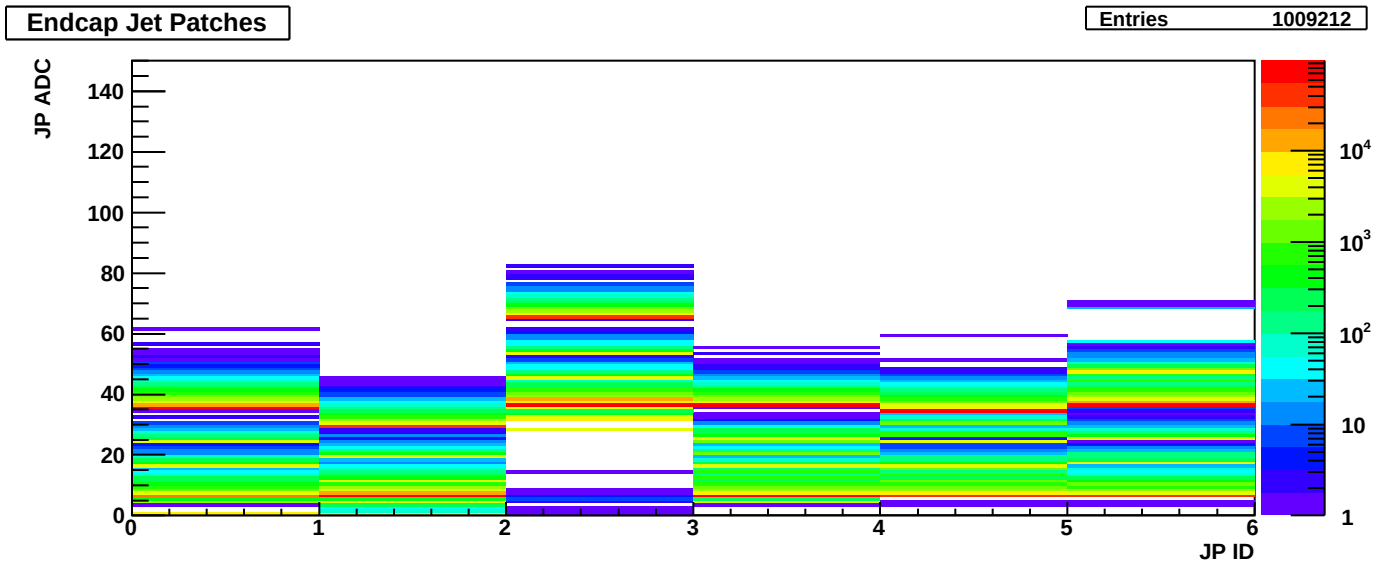

Barrel EMC L0 Input - High Tower
Entries 5.04606e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

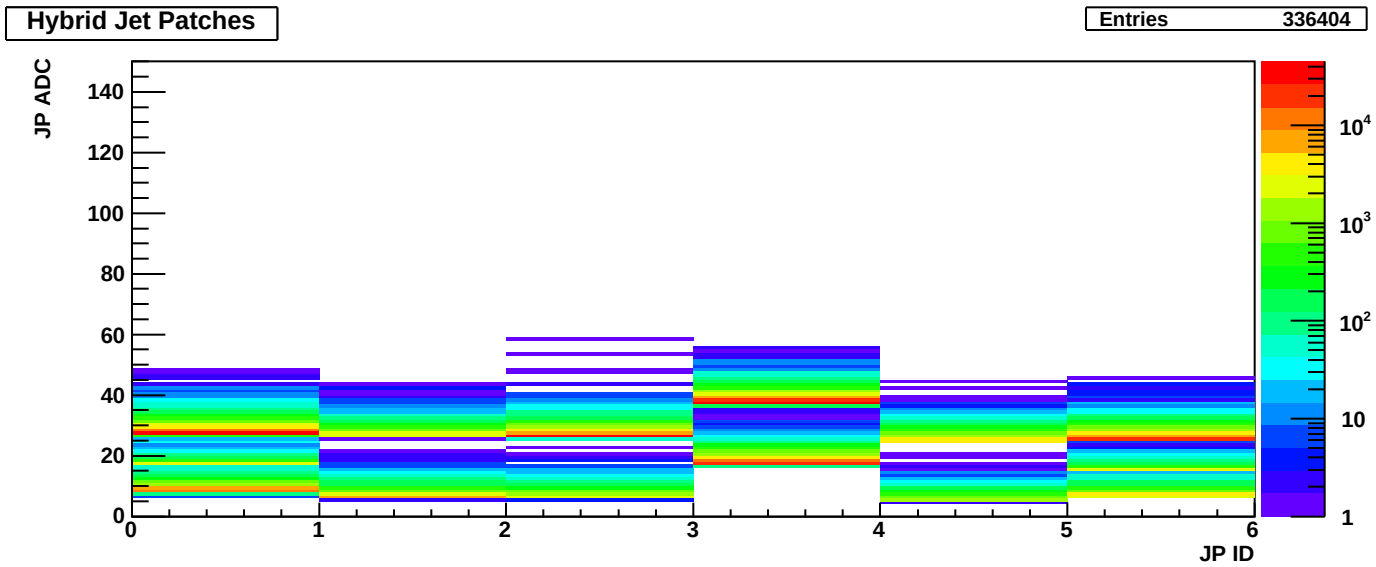

Barrel EMC L0 Input - Patch Sum
Entries 5.04606e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

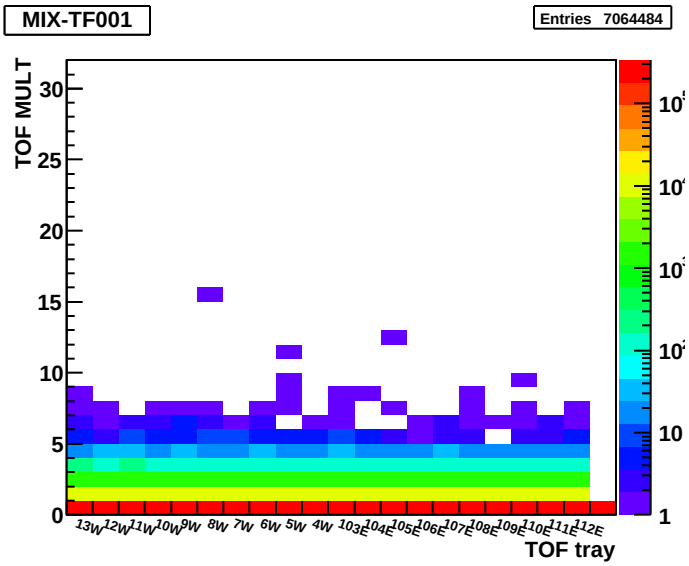

Endcap EMC L0 Input - High Tower
Entries 1.513818e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

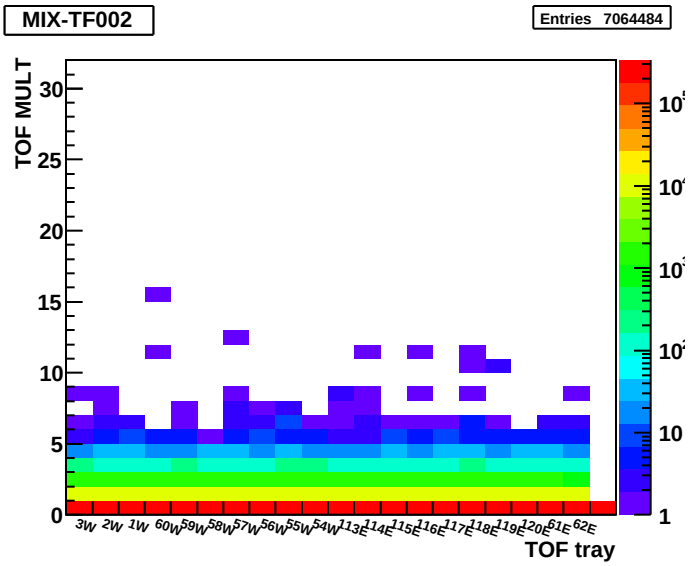

Endcap EMC L0 Input - Patch Sum
Entries 1.513818e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

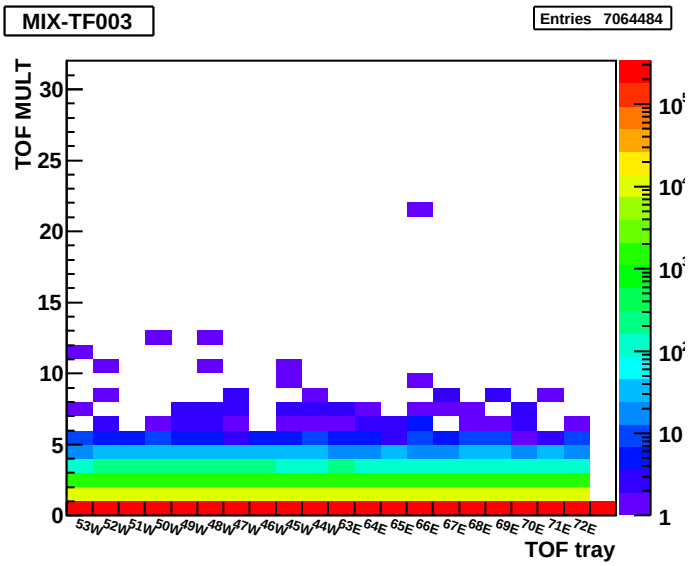

Barrel Jet Patches
Entries 3027636
JP ADC
JP ID

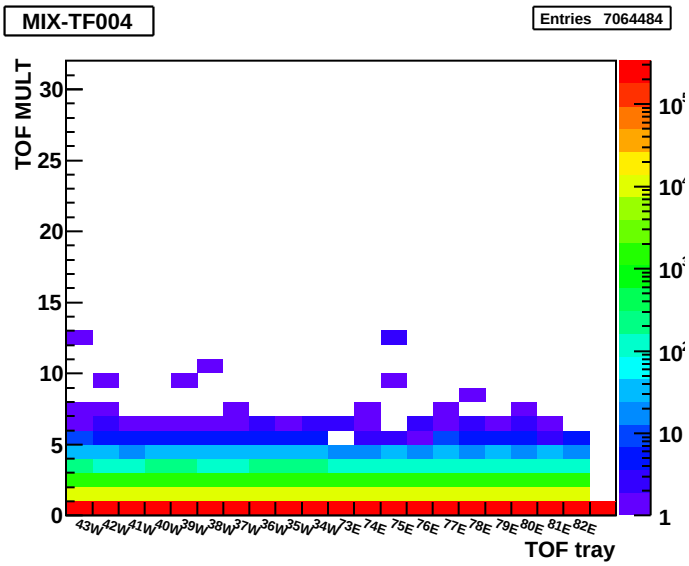

Endcap Jet Patches
Entries 1009212
JP ADC
JP ID

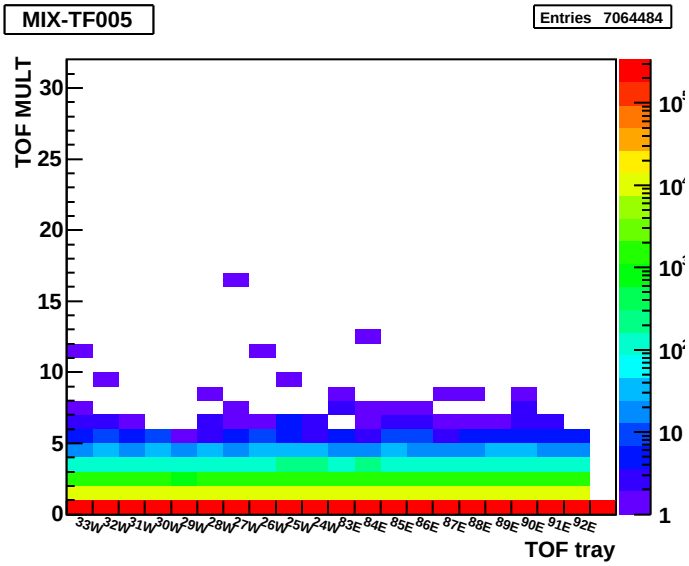

Hybrid Jet Patches
Entries 336404
JP ADC
JP ID

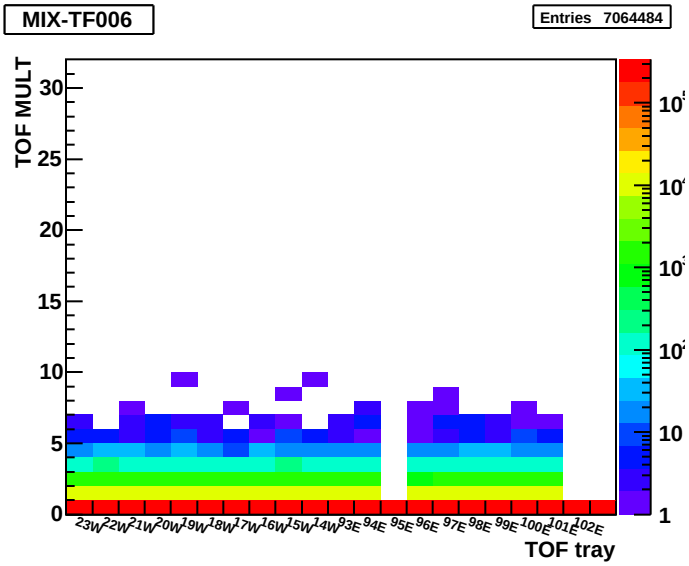

MIX-TF001
Entries 7064484
TOF MULT
TOF tray

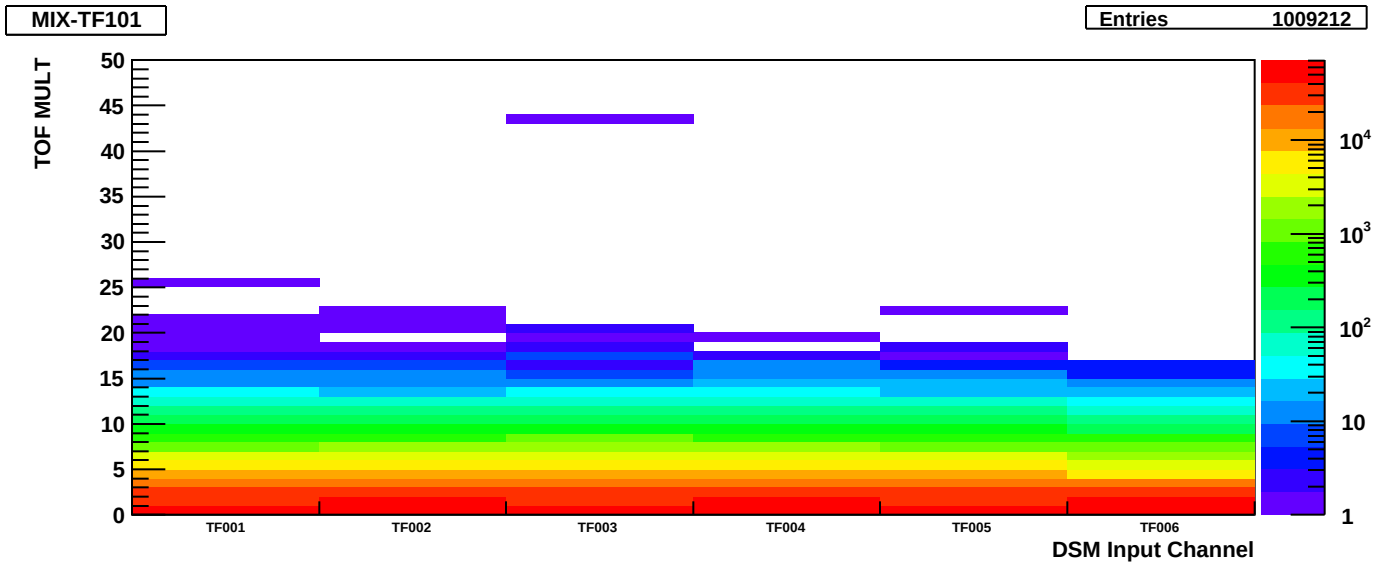

MIX-TF002
Entries 7064484
TOF MULT
TOF tray

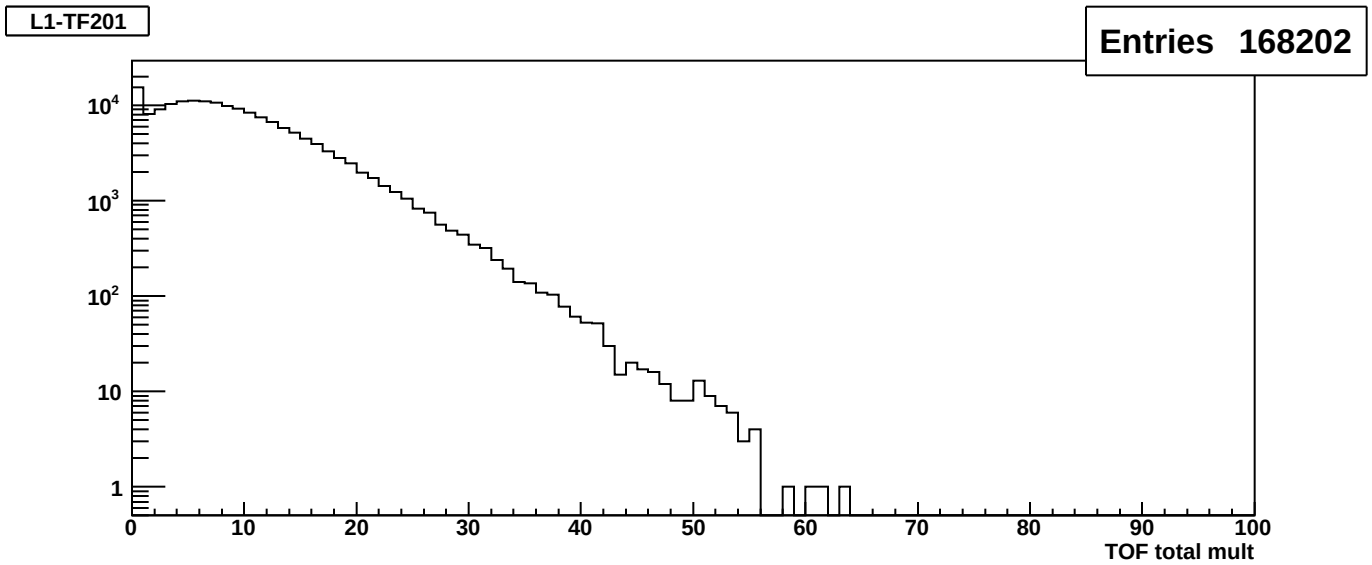

MIX-TF003
Entries 7064484
TOF MULT
TOF tray

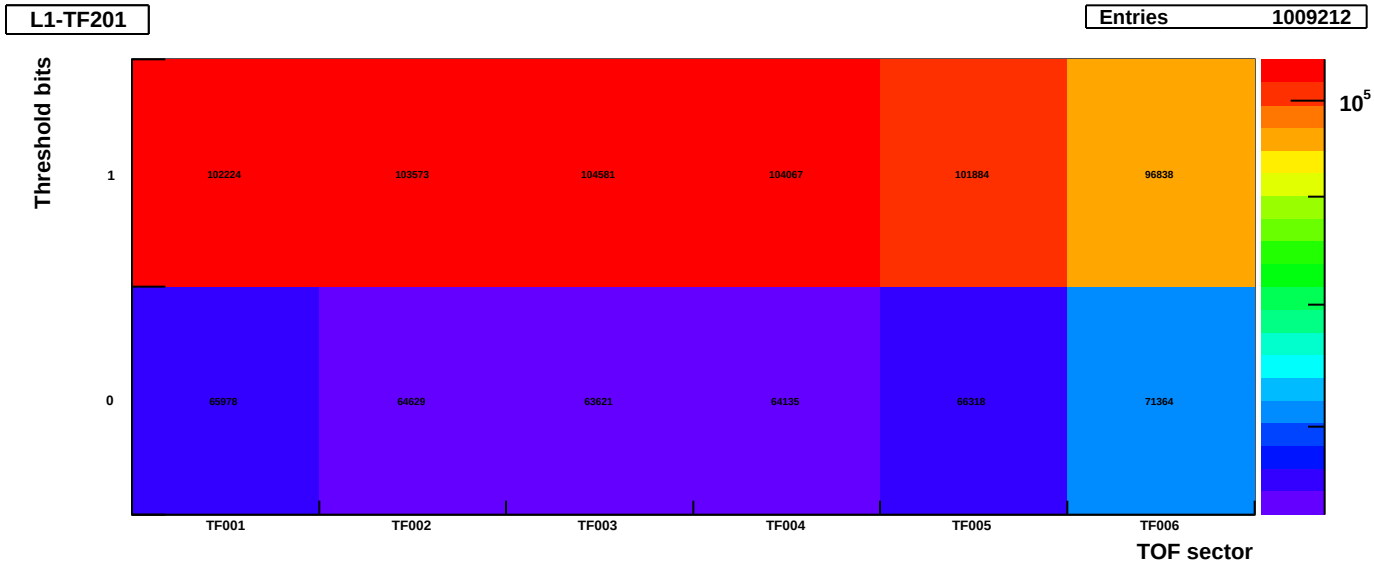

MIX-TF004
Entries 7064484
TOF MULT
TOF tray

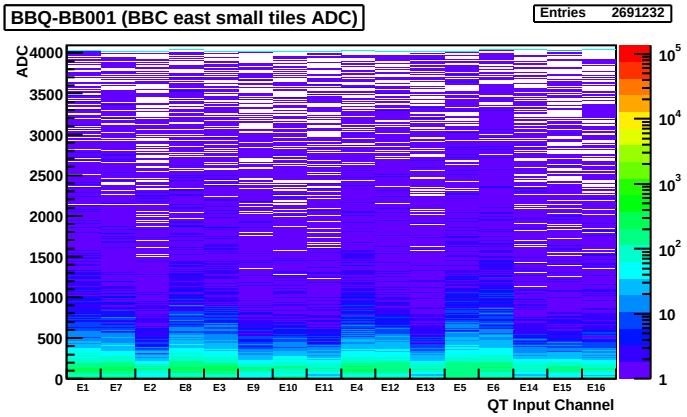

MIX-TF005
Entries 7064484
TOF MULT
TOF tray

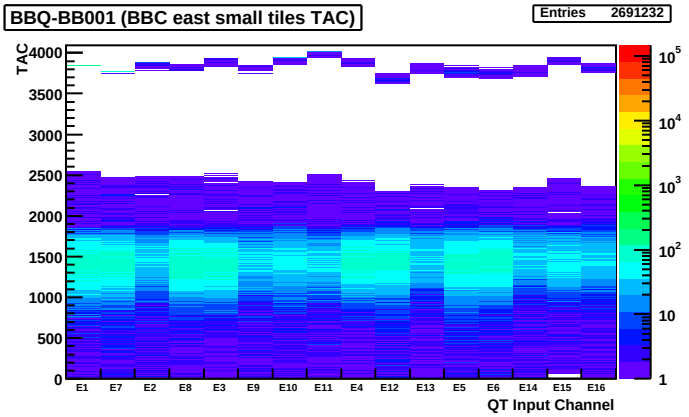

MIX-TF006
Entries 7064484
TOF MULT
TOF tray

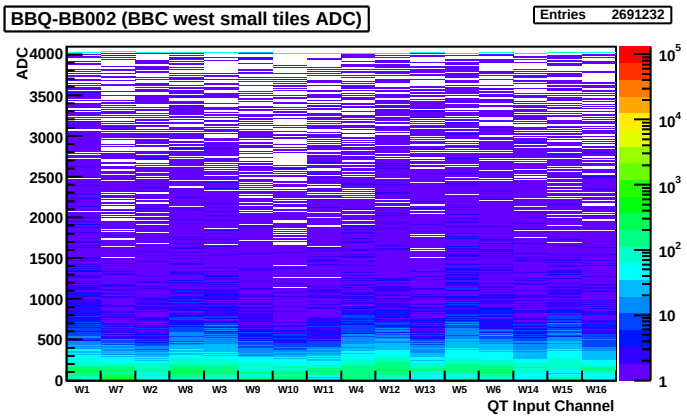

MIX-TF101
Entries 1009212
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

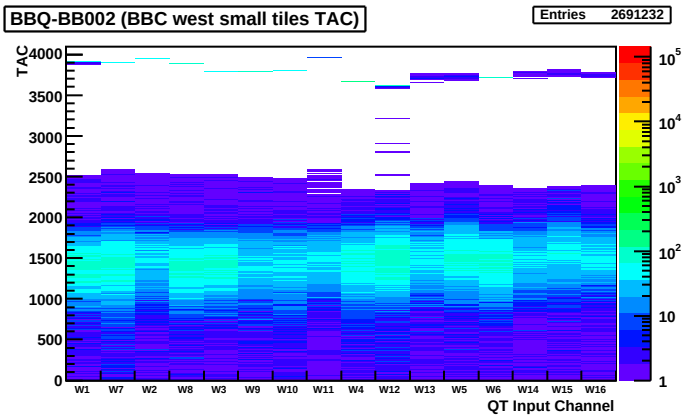

L1-TF201
Entries 168202
TOF total mult

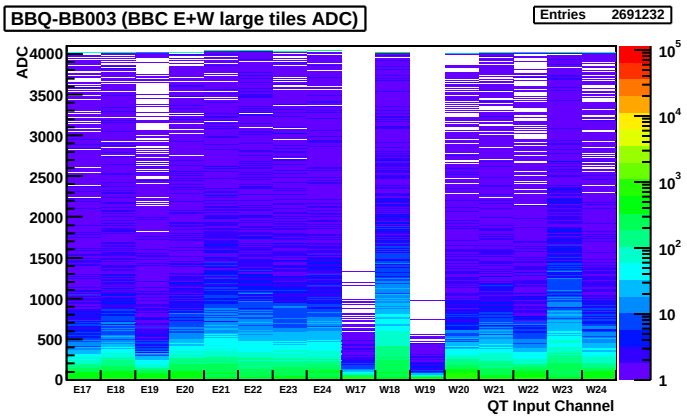

L1-TF201
Entries 1009212
Threshold bits
102224
103573
104581
104067
101884
96838
65978
64629
63621
64135
66318
71364
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

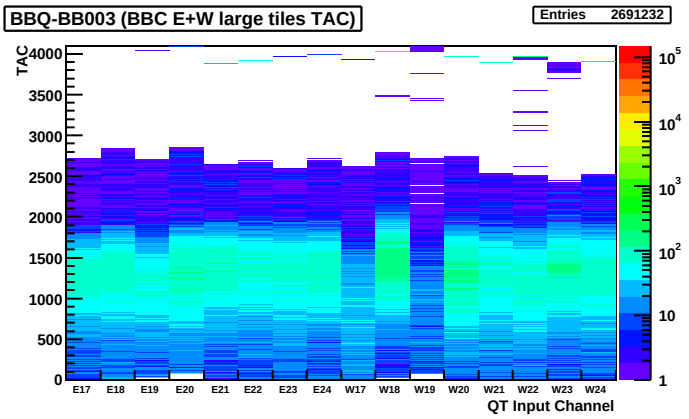

BBQ-BB001 (BBC east small tiles ADC)
Entries 2691232
ADC
QT Input Channel

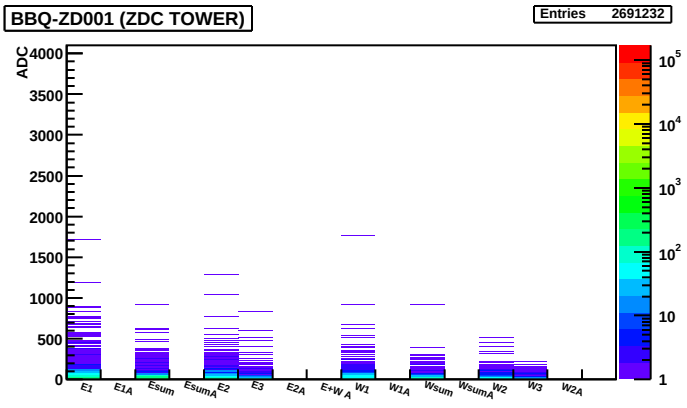

BBQ-BB001 (BBC east small tiles TAC)
Entries 2691232
TAC
QT Input Channel

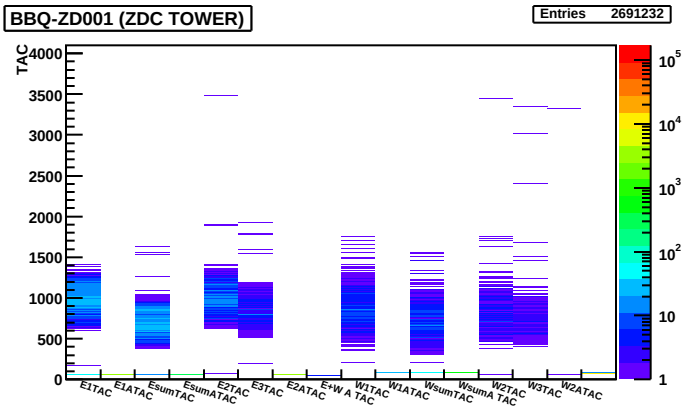

BBQ-BB002 (BBC west small tiles ADC)
Entries 2691232
ADC
QT Input Channel

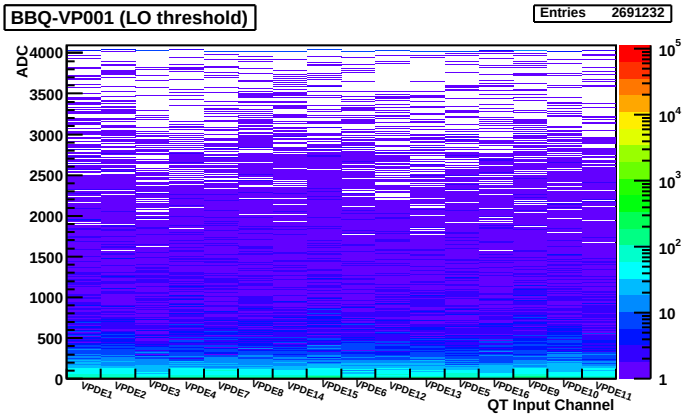

BBQ-BB002 (BBC west small tiles TAC)
Entries 2691232
TAC
QT Input Channel

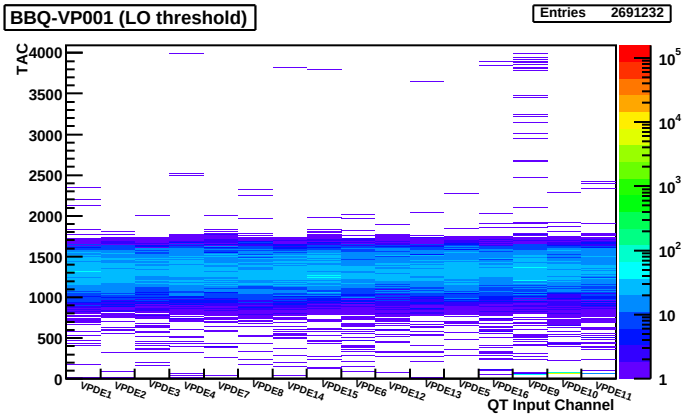

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2691232
ADC
QT Input Channel

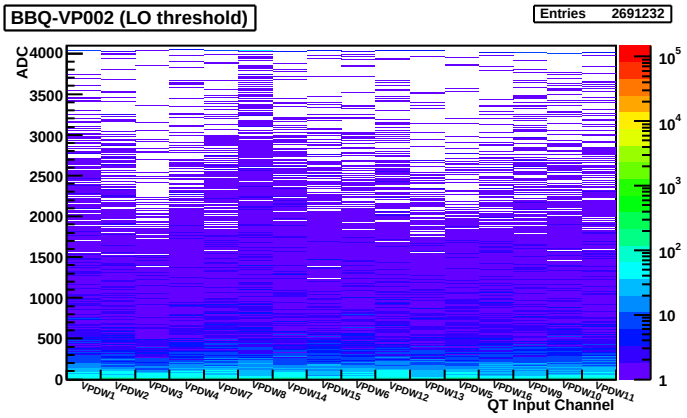

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2691232
TAC
QT Input Channel

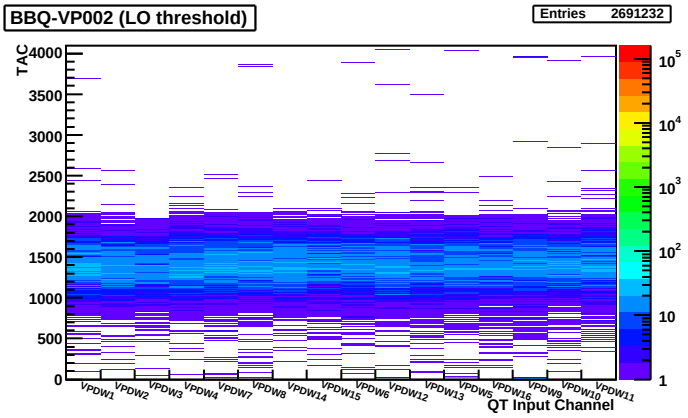

BBQ-ZD001 (ZDC TOWER)
Entries 2691232
ADC

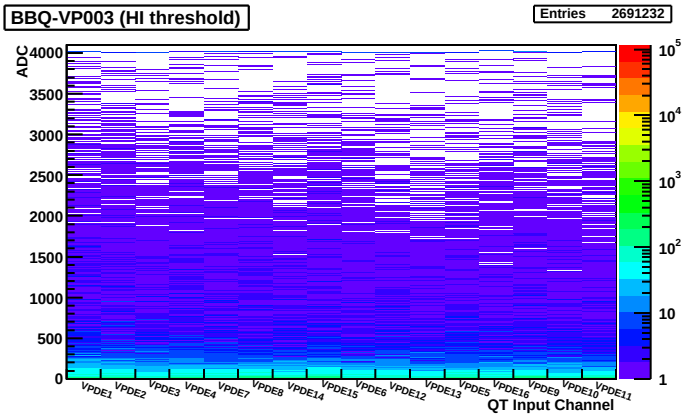

BBQ-ZD001 (ZDC TOWER)
Entries 2691232
TAC

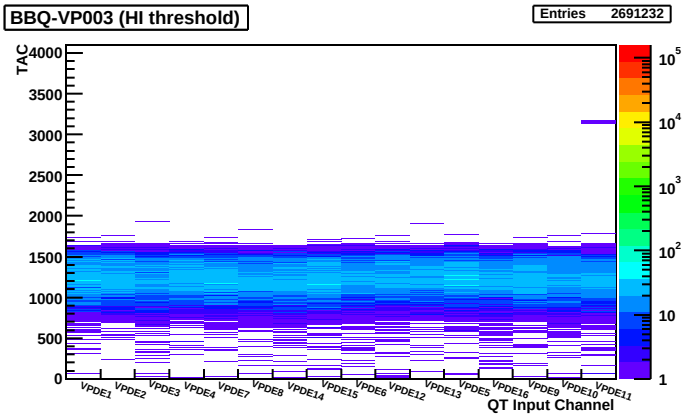

BBQ-VP001 (LO threshold)
Entries 2691232
ADC
QT Input Channel

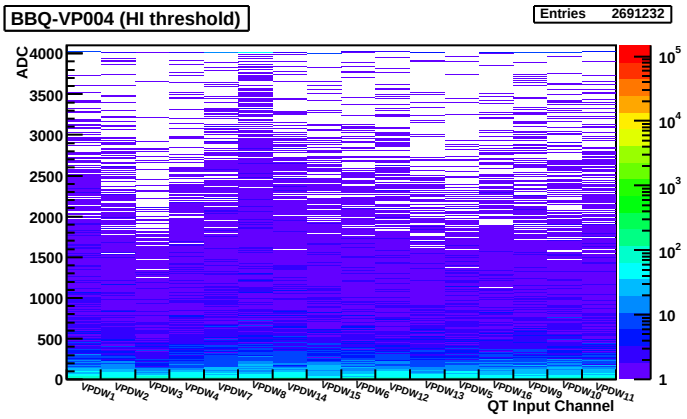

BBQ-VP001 (LO threshold)
Entries 2691232
TAC
QT Input Channel

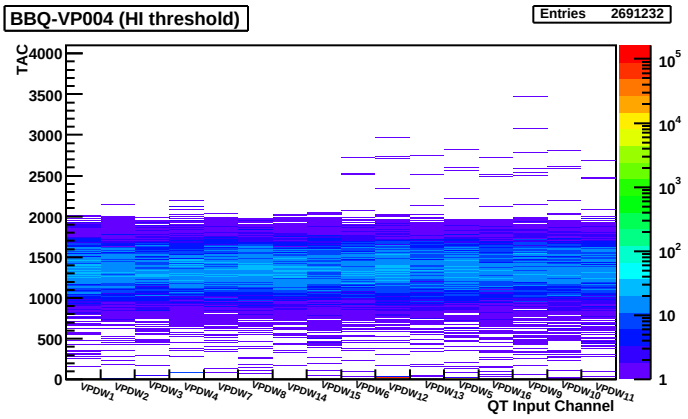

BBQ-VP002 (LO threshold)
Entries 2691232
ADC
QT Input Channel

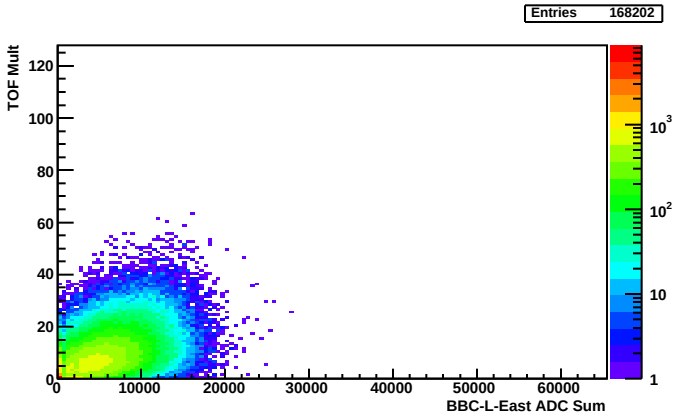

BBQ-VP002 (LO threshold)
Entries 2691232
TAC
QT Input Channel

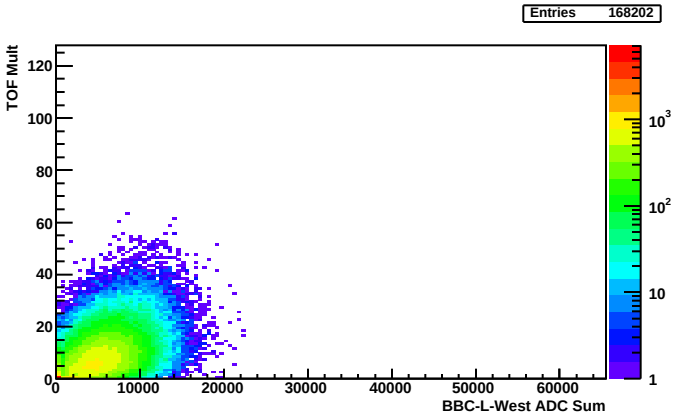

BBQ-VP003 (HI threshold)
Entries 2691232
ADC
QT Input Channel

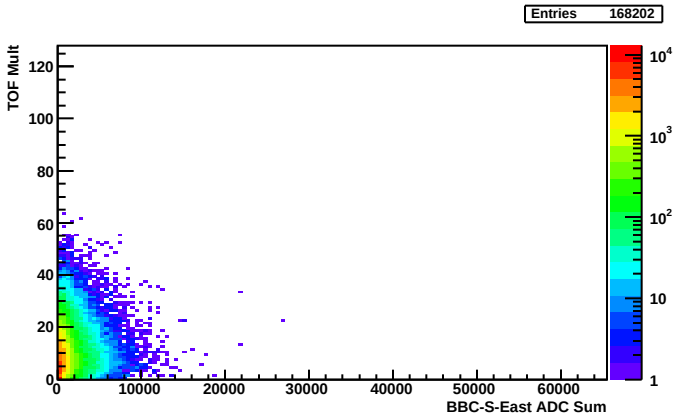

BBQ-VP003 (HI threshold)
Entries 2691232
TAC
QT Input Channel

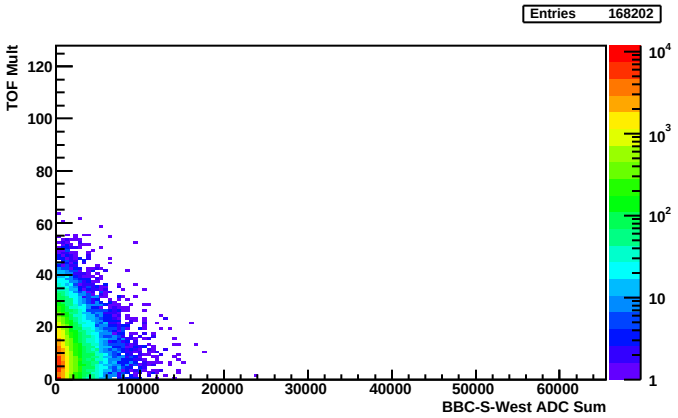

BBQ-VP004 (HI threshold)
Entries 2691232
ADC
QT Input Channel

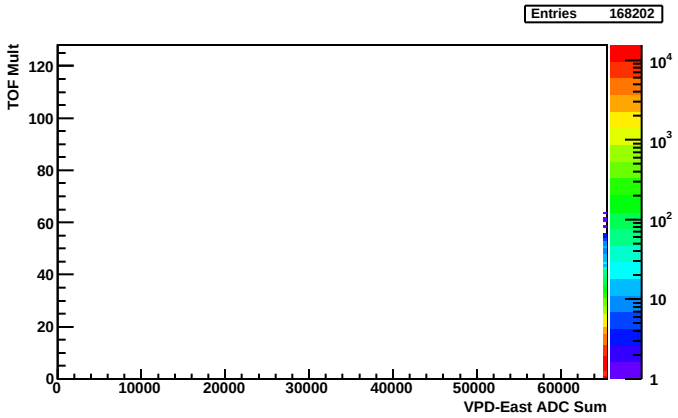

BBQ-VP004 (HI threshold)
Entries 2691232
TAC
QT Input Channel

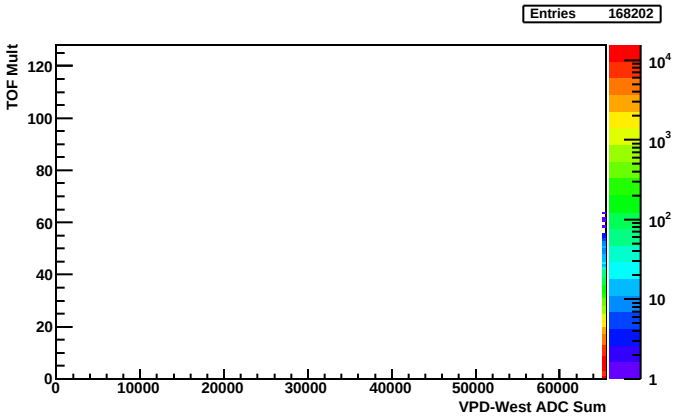

Entries 168202
TOF Mult
BBC-L-East ADC Sum

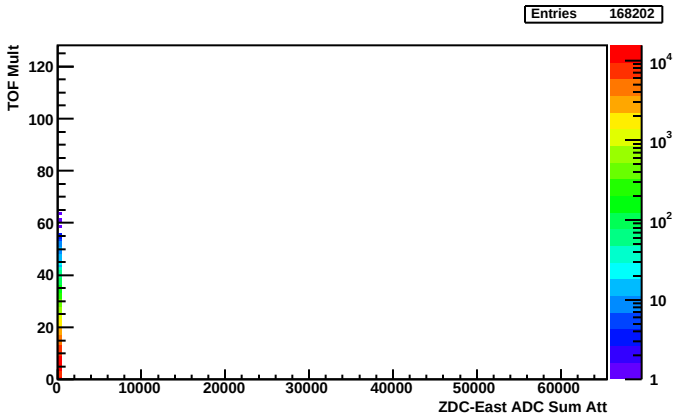

Entries 168202
TOF Mult
BBC-L-West ADC Sum

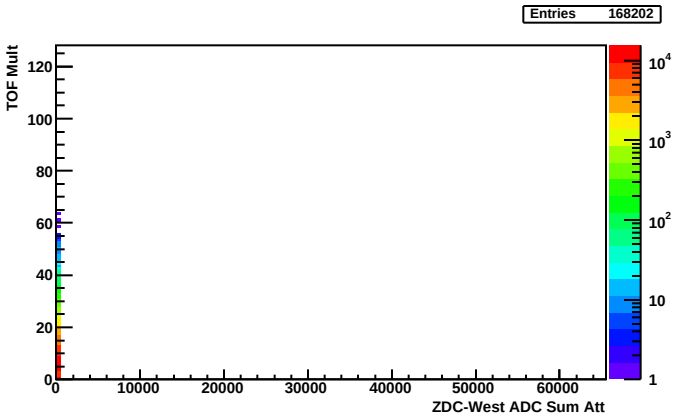

Entries 168202
TOF Mult
BBC-S-East ADC Sum

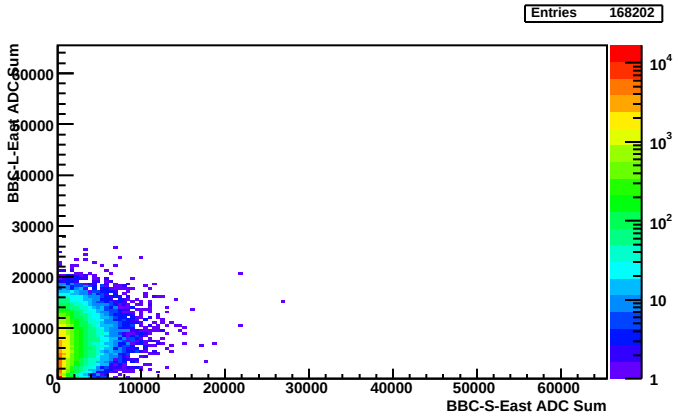

Entries 168202
TOF Mult
BBC-S-West ADC Sum

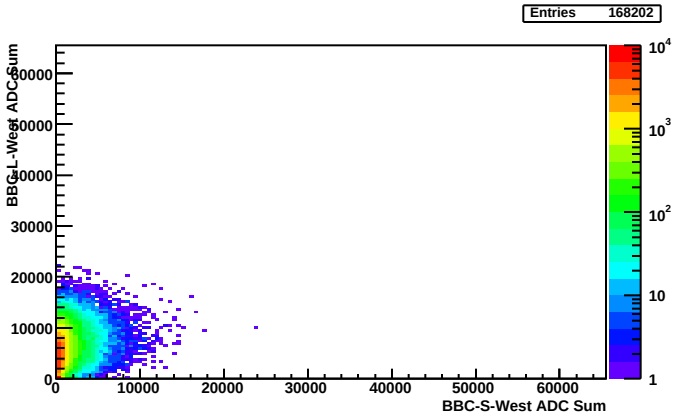

Entries 168202
TOF Mult
VPD-East ADC Sum

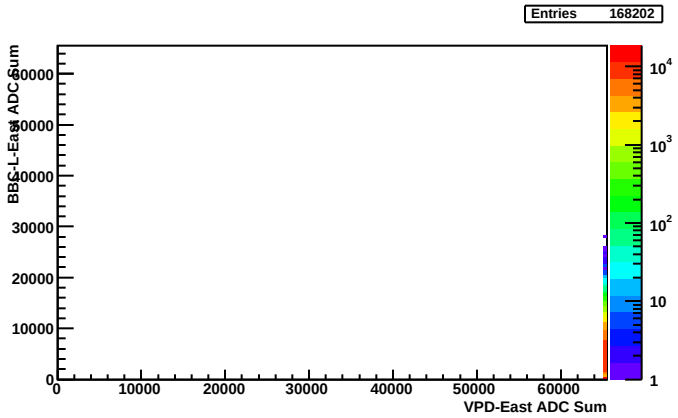

Entries 168202
TOF Mult
VPD-West ADC Sum

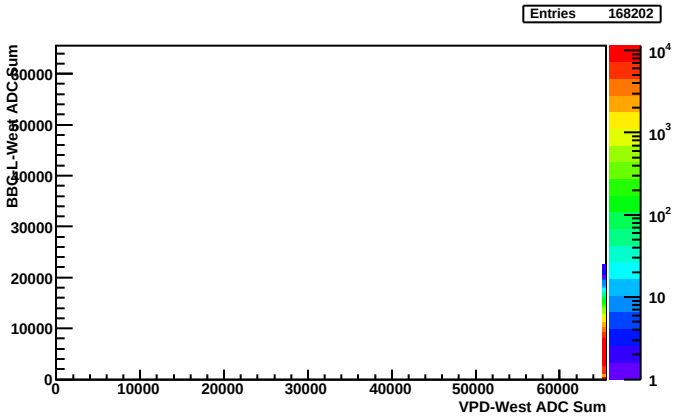

Entries 168202
TOF Mult
ZDC-East ADC Sum Att

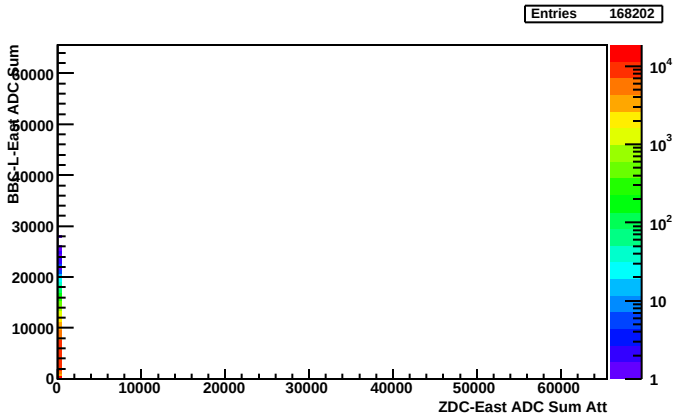

Entries 168202
TOF Mult
ZDC-West ADC Sum Att

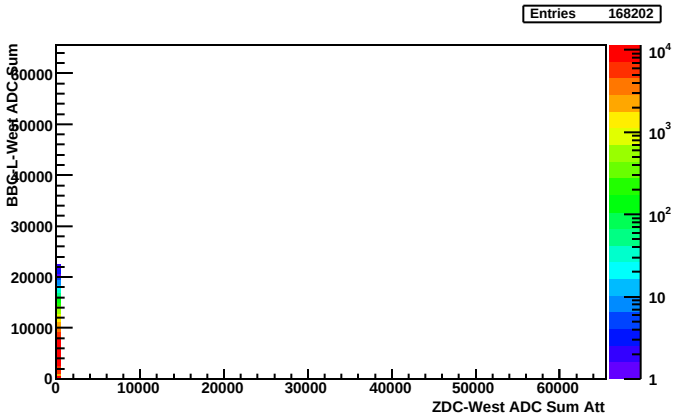

Entries 168202
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 168202
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 168202
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 168202
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 168202
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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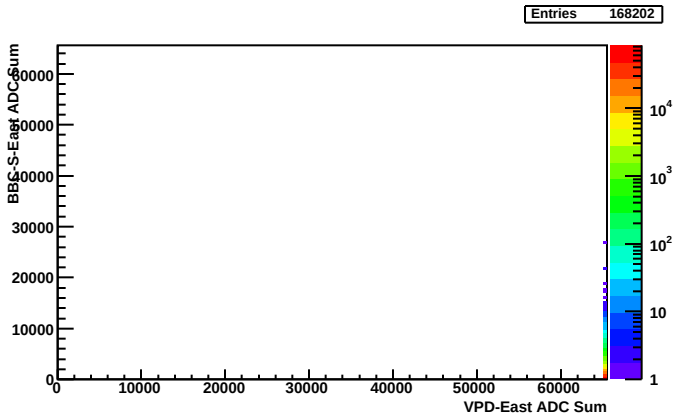
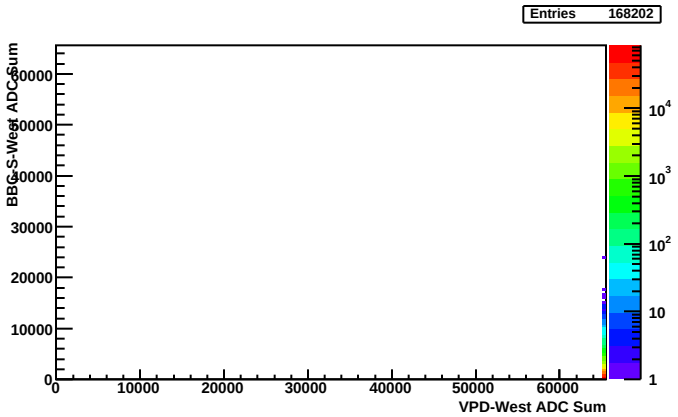
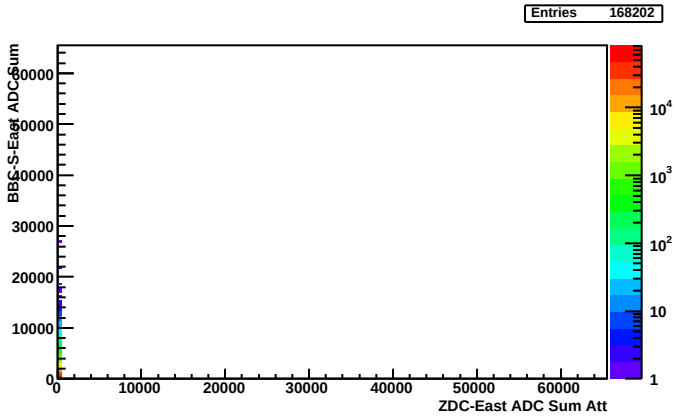
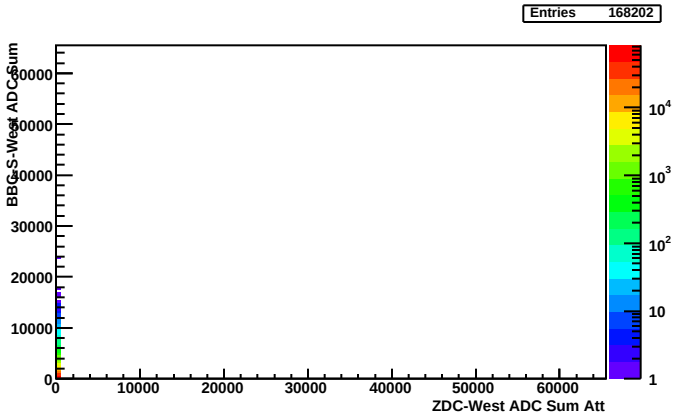
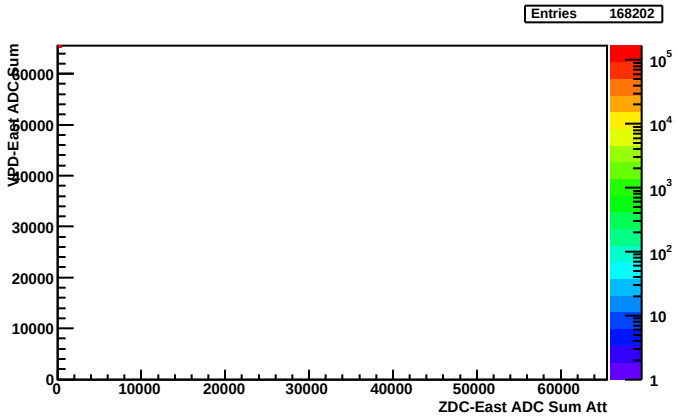
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

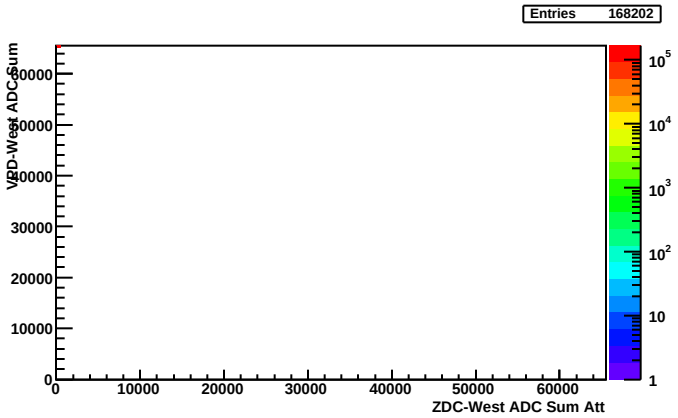

Entries 168202
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 168202
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 168202
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 168202
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 168202
VPD-East ADC Sum
ZDC-East ADC Sum Att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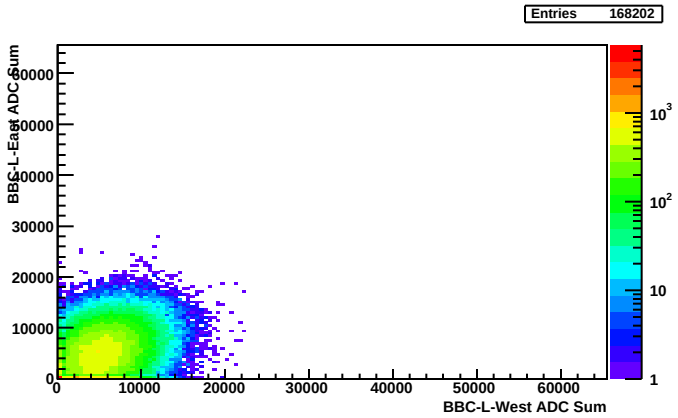
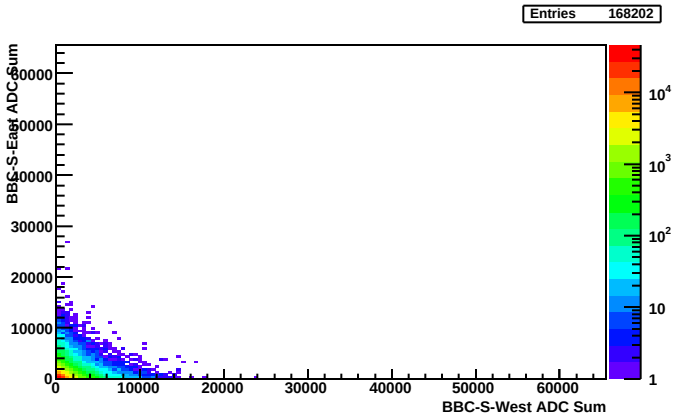
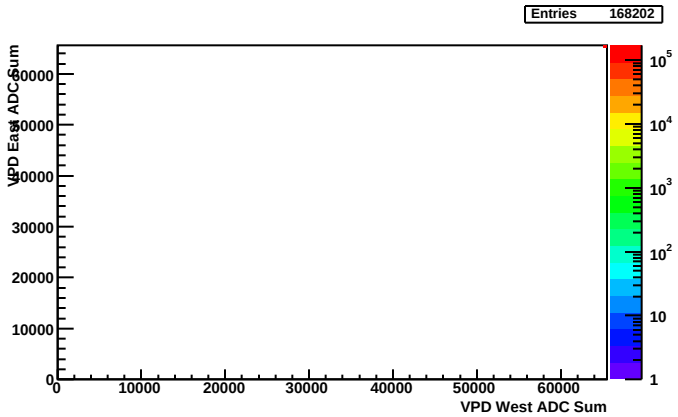
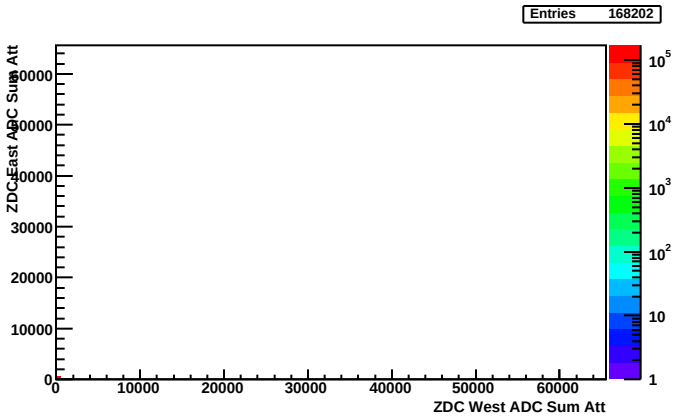
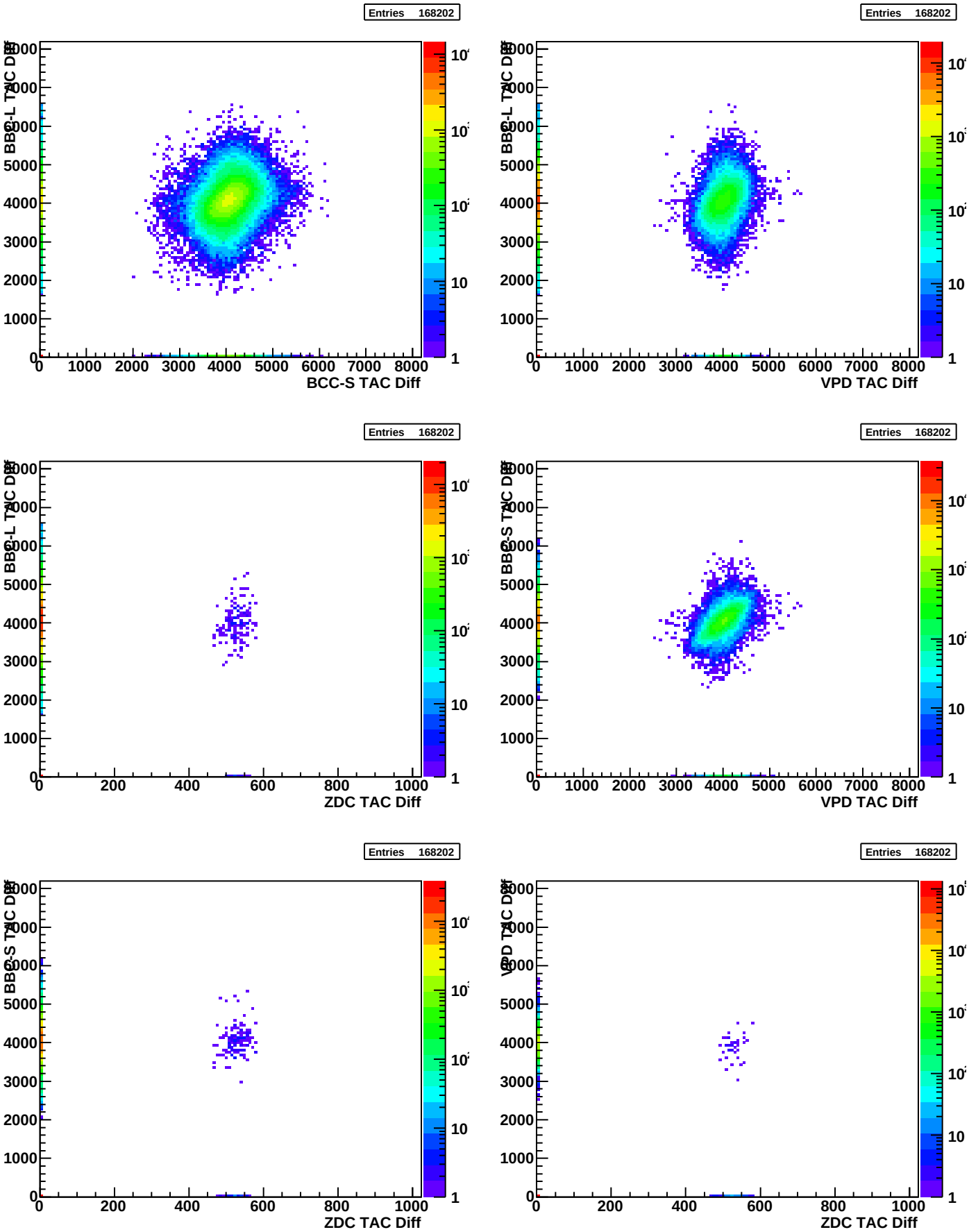
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 168202
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 168202
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 168202
VPD East ADC Sum
VPD West ADC Sum


Entries 168202
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 168202
BBC-L TAC Diff
BCC-S TAC Diff
Entries 168202
BBC-L TAC Diff
VPD TAC Diff
Entries 168202
BBC-L TAC Diff
ZDC TAC Diff
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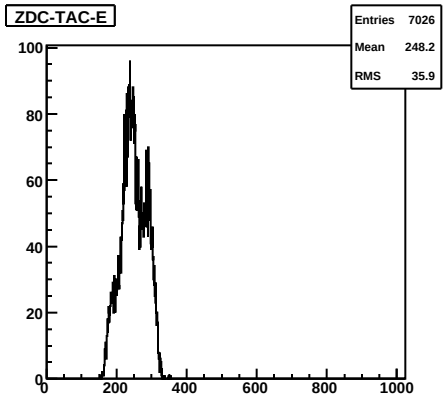
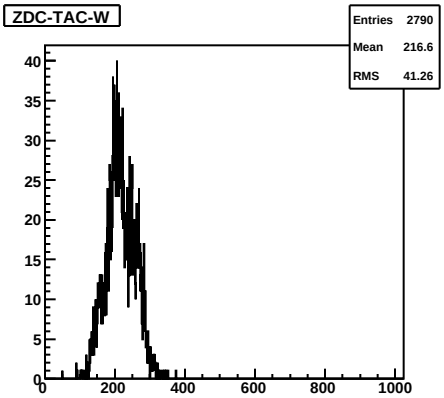
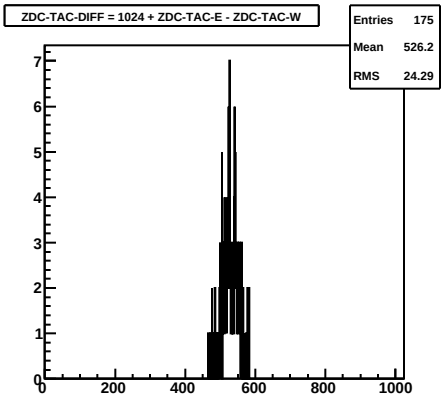
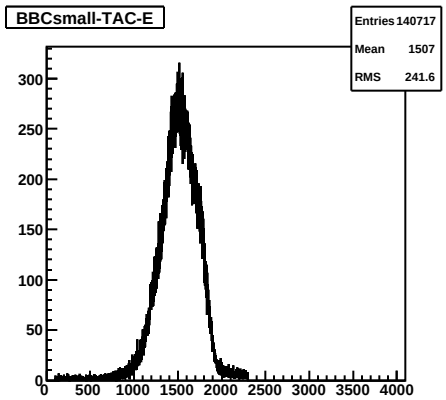
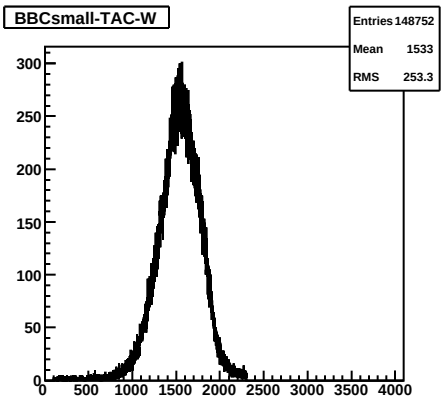
VPD TAC Diff
Entries 168202
BBC-S TAC Diff
ZDC TAC Diff
Entries 168202
VPD TAC Diff
ZDC TAC Diff

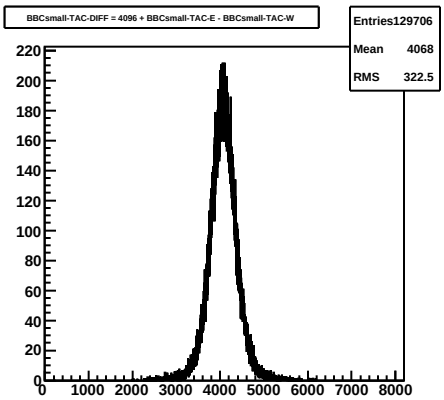

ZDC-TAC-E
Entries 7026
Mean 248.2
RMS 35.9

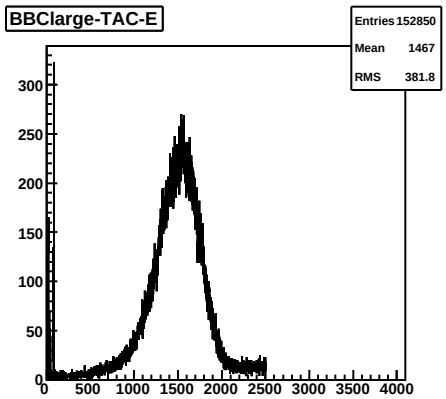

ZDC-TAC-W
Entries 2790
Mean 216.6
RMS 41.26

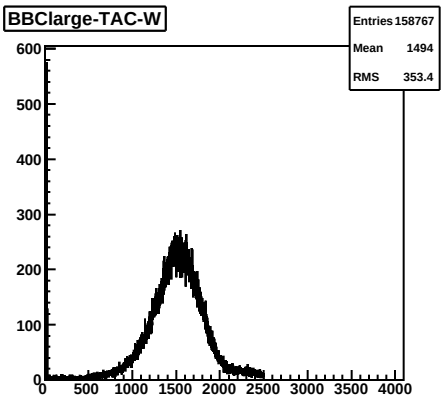

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 175
Mean 526.2
RMS 24.29

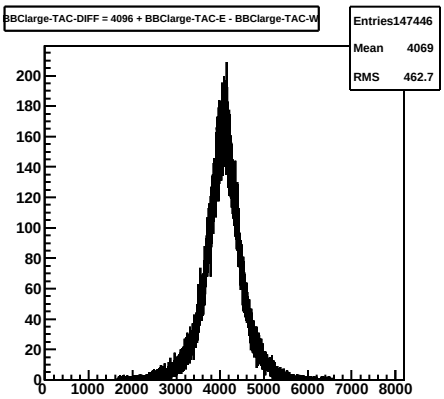

BBCsmall-TAC-E
Entries 140717
Mean 1507
RMS 241.6

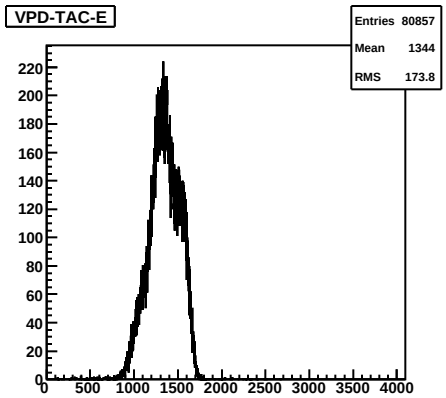

BBCsmall-TAC-W
Entries 148752
Mean 1533
RMS 253.3

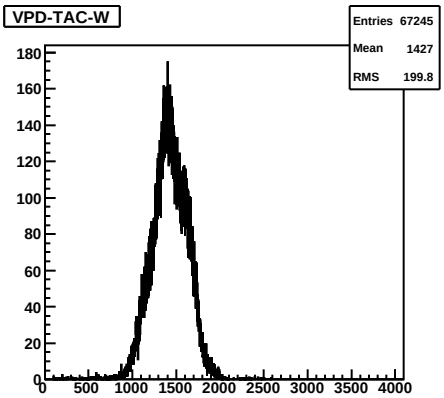

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 129706
Mean 4068
RMS 322.5

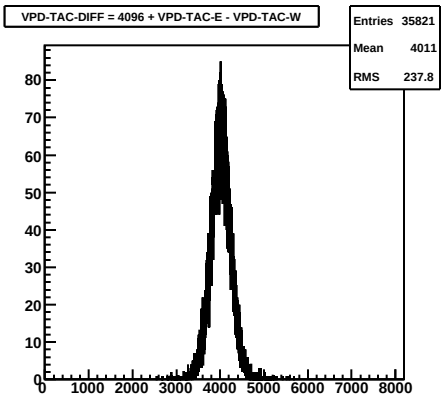

BBClarge-TAC-E
Entries 152850
Mean 1467
RMS 381.8

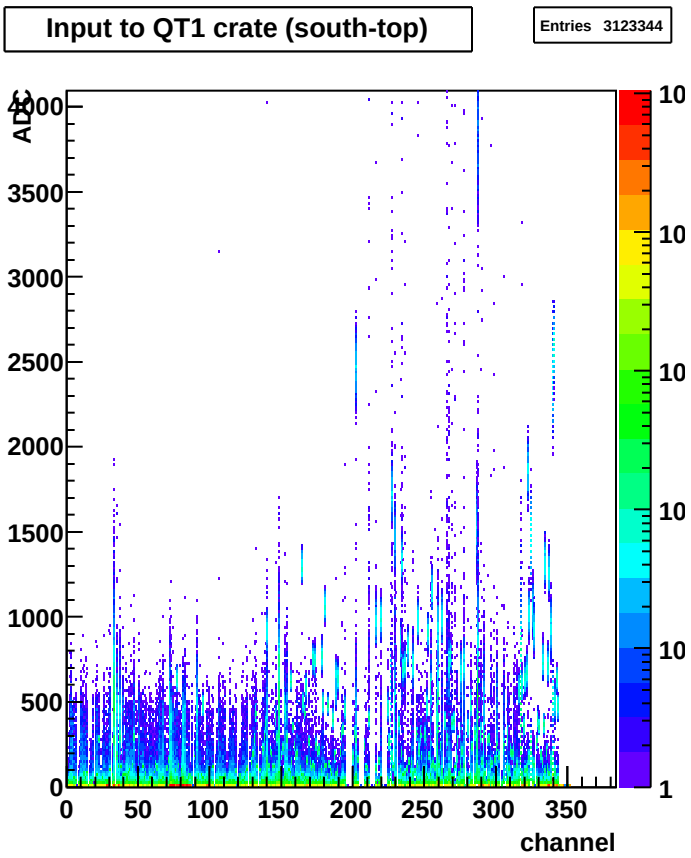

BBClarge-TAC-W
Entries 158767
Mean 1494
RMS 353.4

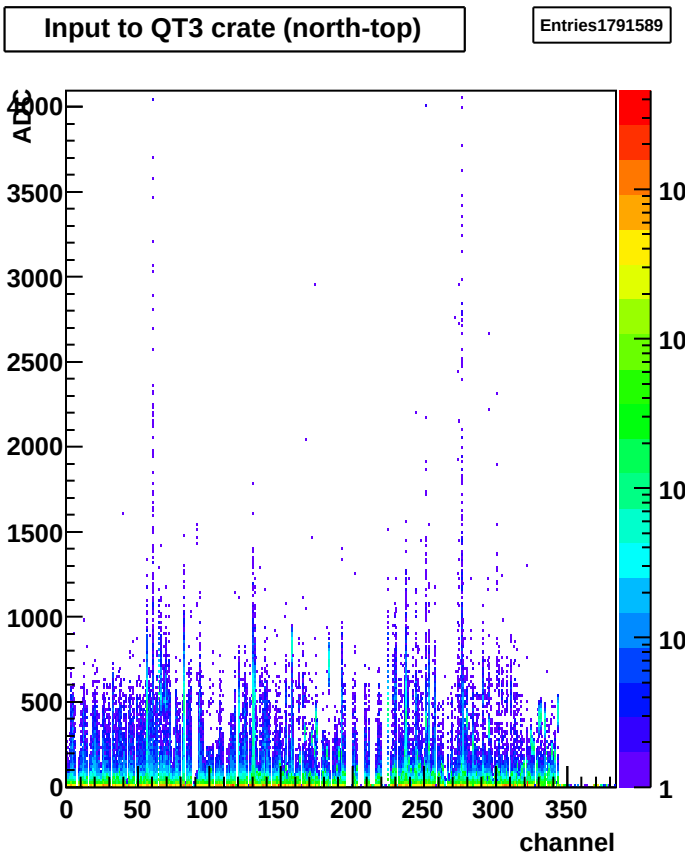

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 147446
Mean 4069
RMS 462.7

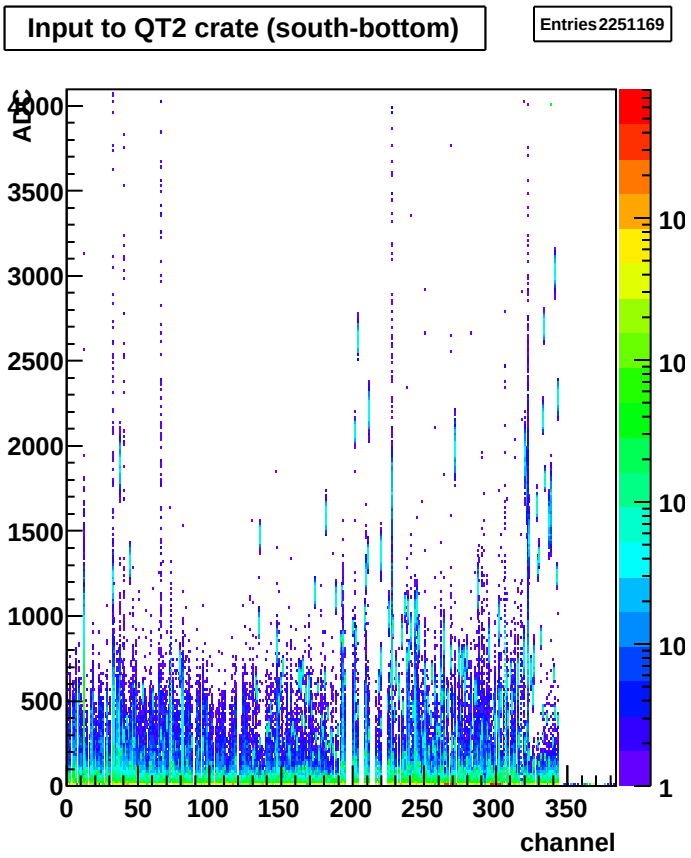

VPD-TAC-E
Entries 80857
Mean 1344
RMS 173.8

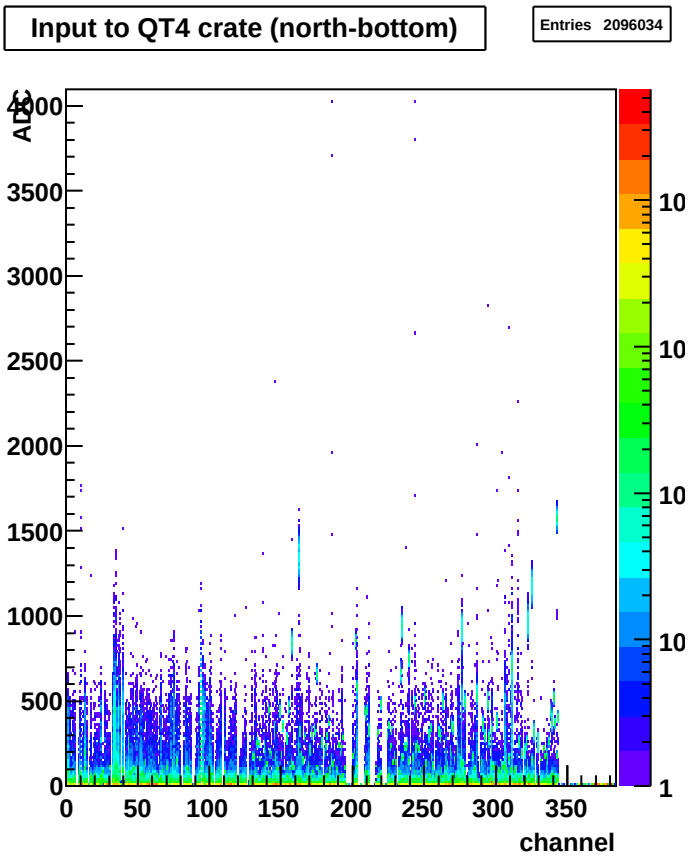

VPD-TAC-W
Entries 67245
Mean 1427
RMS 199.8

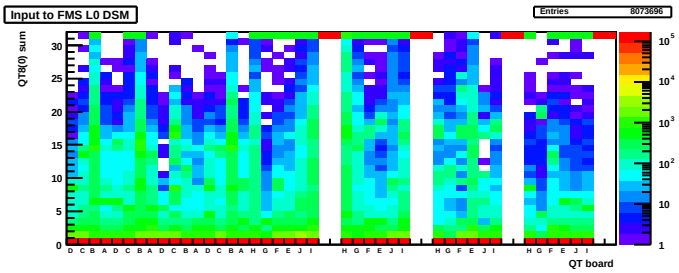

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 35821
Mean 4011
RMS 237.8

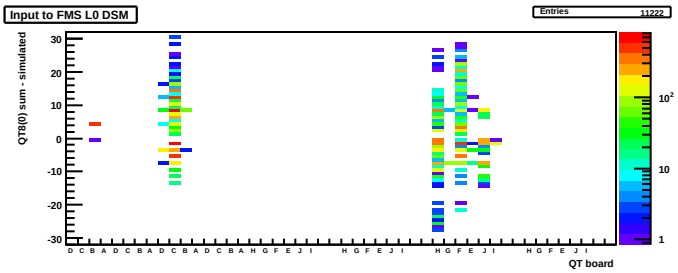

Input to QT1 crate (south-top)
Entries 3123344
ADC
channel

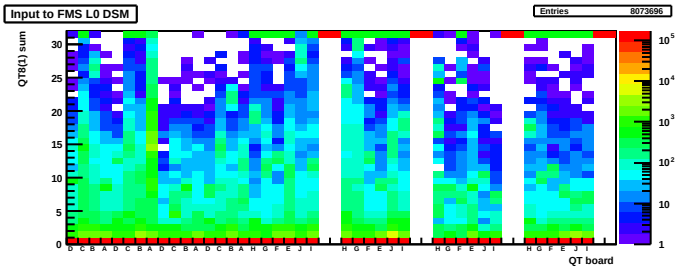

Input to QT3 crate (north-top)
Entries1791589
ADC
channel

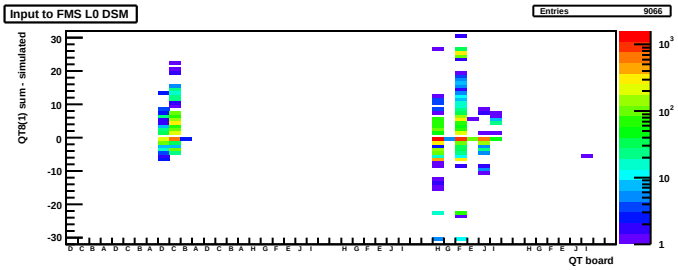

Input to QT2 crate (south-bottom)
Entries2251169
ADC
channel

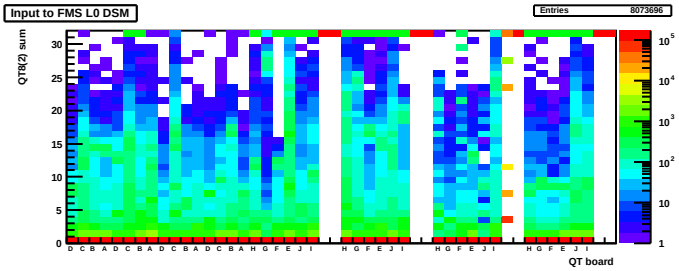

Input to QT4 crate (north-bottom)
Entries 2096034
ADC
channel

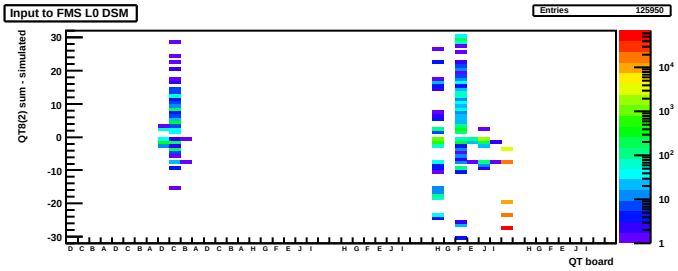

Input to FMS L0 DSM
Entries 8073696
QT8(0) sum
QT board

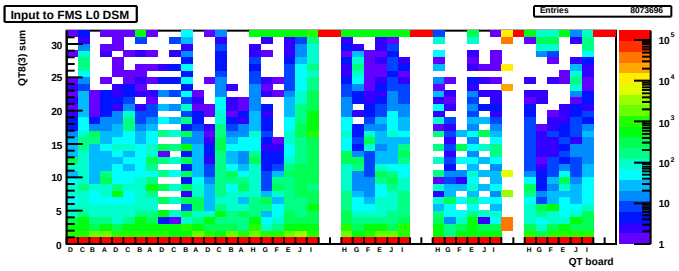

Input to FMS L0 DSM
Entries 11222
QT8(0) sum - simulated
QT board

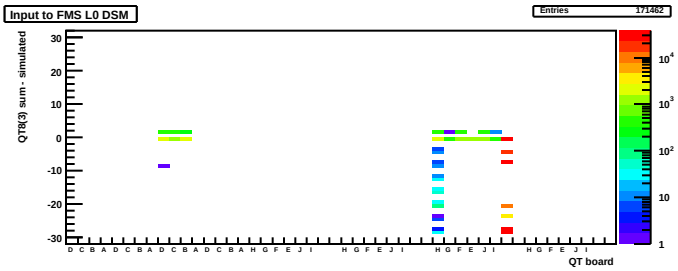

Input to FMS L0 DSM
Entries 8073696
QT8(1) sum
QT board

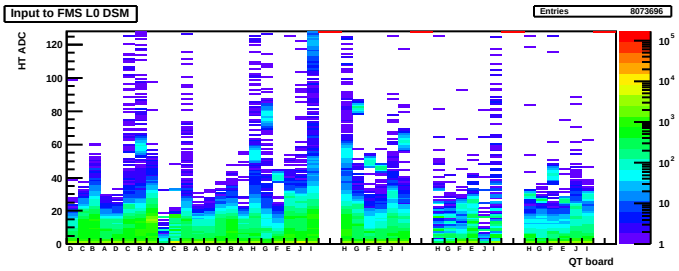

Input to FMS L0 DSM
Entries 9066
QT8(1) sum - simulated
QT board

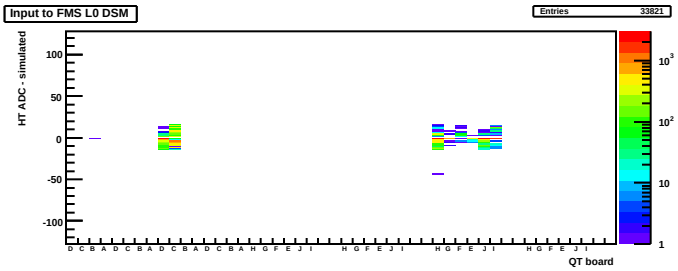

Input to FMS L0 DSM
Entries 8073696
QT8(2) sum
QT board

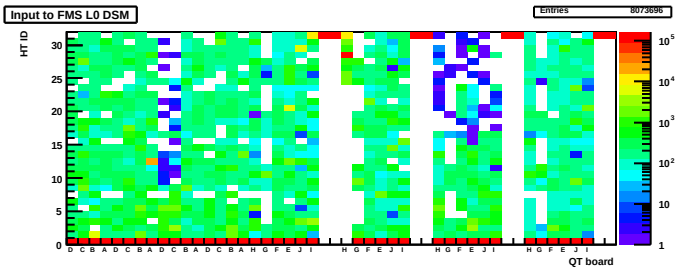

Input to FMS L0 DSM
Entries 125950
QT8(2) sum - simulated
QT board

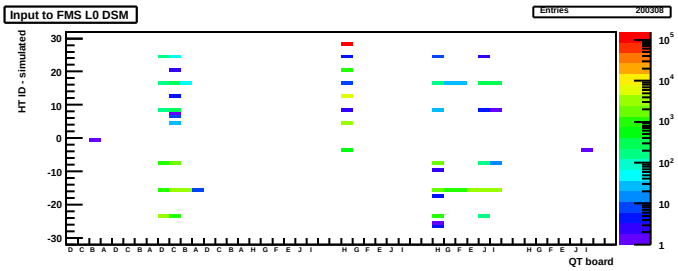

Input to FMS L0 DSM
Entries 8073696
QT8(3) sum
QT board

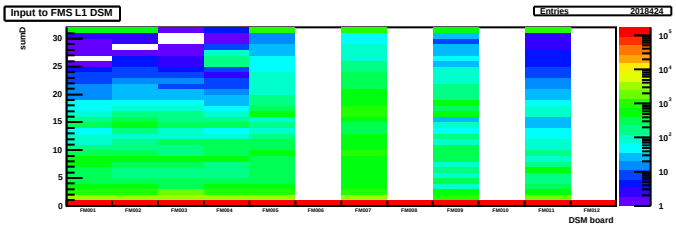

Input to FMS L0 DSM
Entries 171462
QT8(3) sum - simulated
QT board

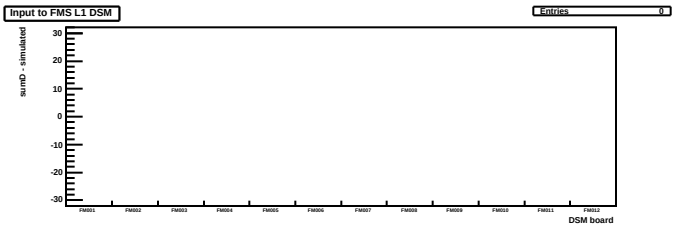

Input to FMS L0 DSM
Entries 8073696
HT ADC
QT board

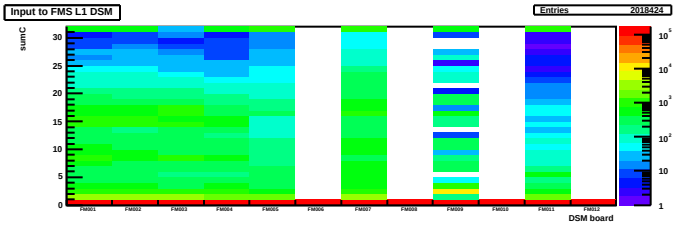

Input to FMS L0 DSM
Entries 33821
HT ADC - simulated
QT board

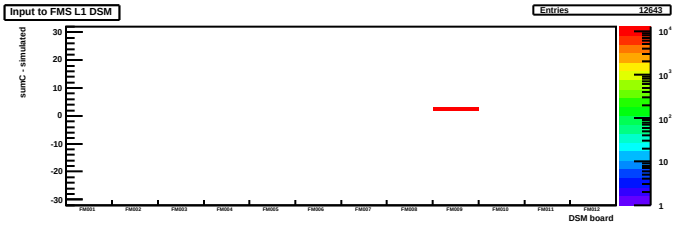

Input to FMS L0 DSM
Entries 8073696
HT ID
QT board

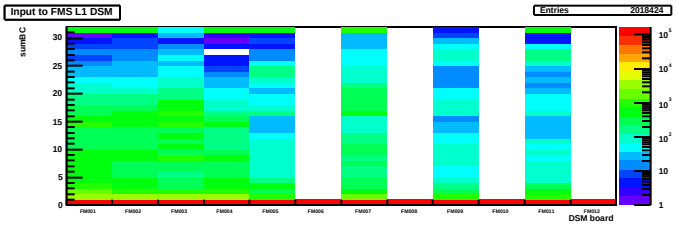

Input to FMS L0 DSM
Entries 200308
HT ID - simulated
QT board

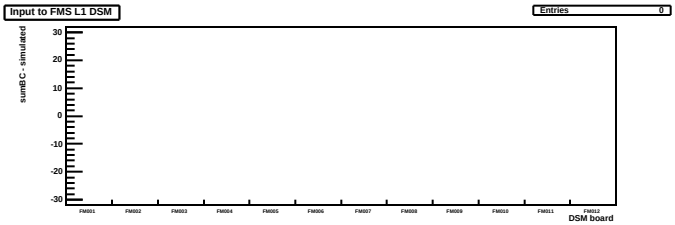

Input to FMS L1 DSM
Entries 2018424
sumD
DSM board

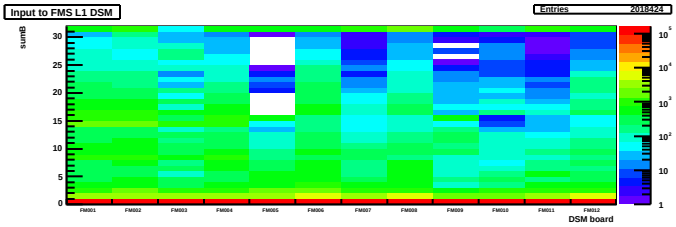

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

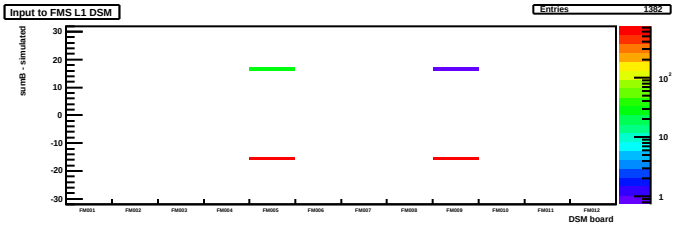

Input to FMS L1 DSM
Entries 2018424
sumC
DSM board

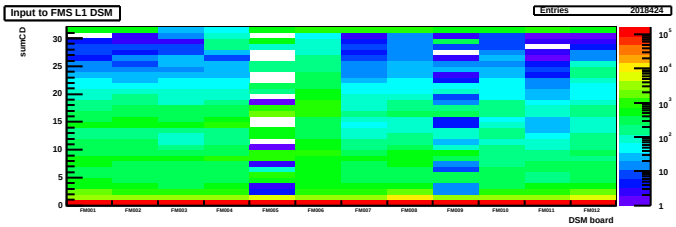

Input to FMS L1 DSM
Entries 12643
sumC - simulated
DSM board

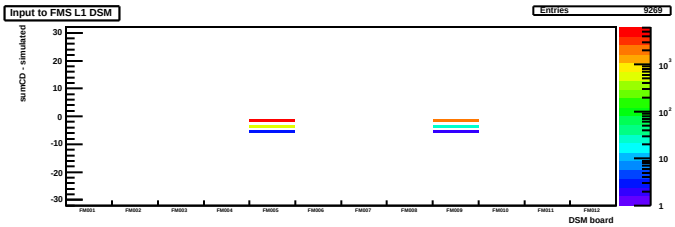

Input to FMS L1 DSM
Entries 2018424
sumBC
DSM board

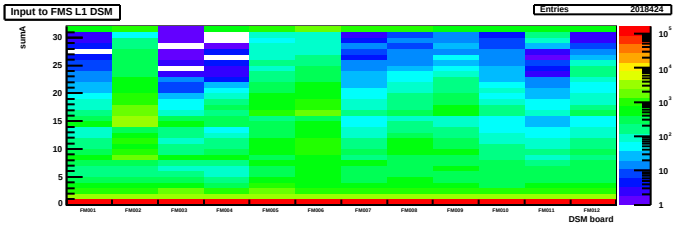

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

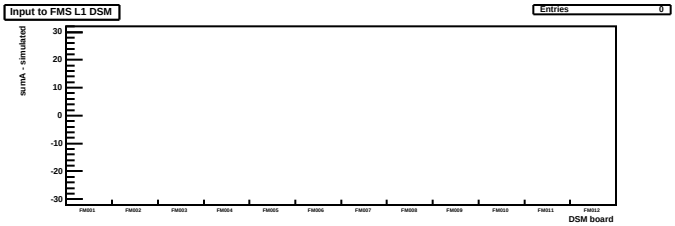

Input to FMS L1 DSM
Entries 2018424
sumB
DSM board

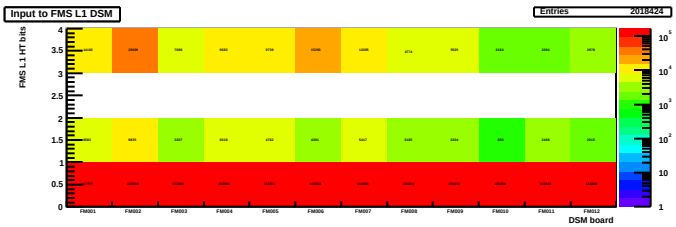

Input to FMS L1 DSM
Entries 1382
sumB - simulated
DSM board

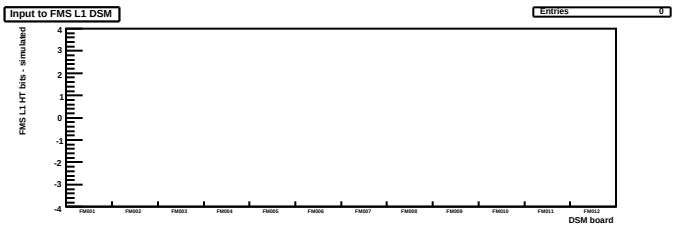

Input to FMS L1 DSM
Entries 2018424
sumCD
DSM board

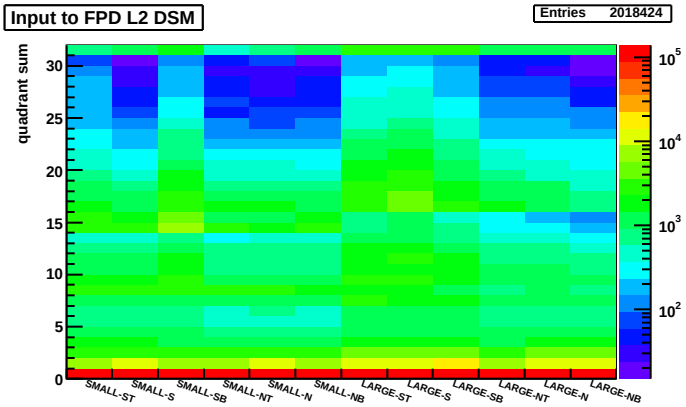

Input to FMS L1 DSM
Entries 9269
sumCD - simulated
DSM board

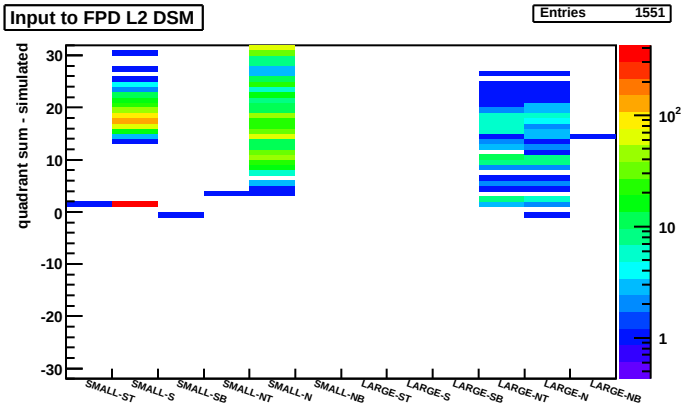

Input to FMS L1 DSM
Entries 2018424
sumA
DSM board

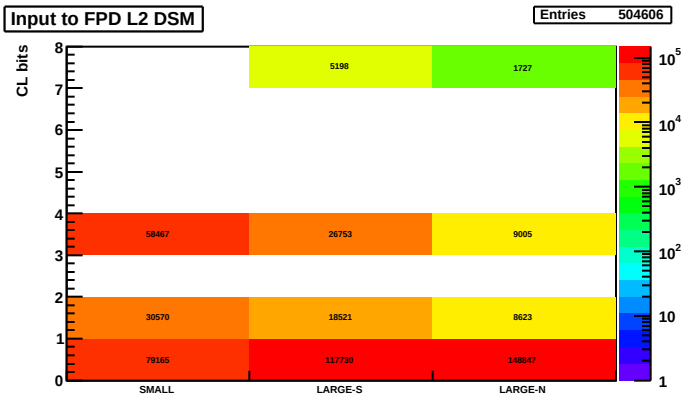

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

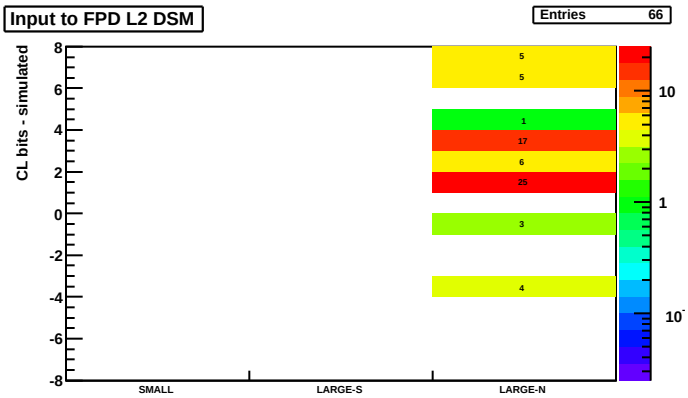

Input to FMS L1 DSM
Entries 2018424
FMS L1 HT bits
DSM board

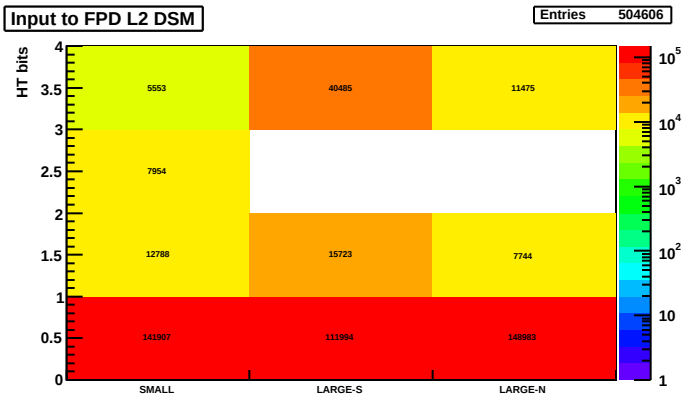

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

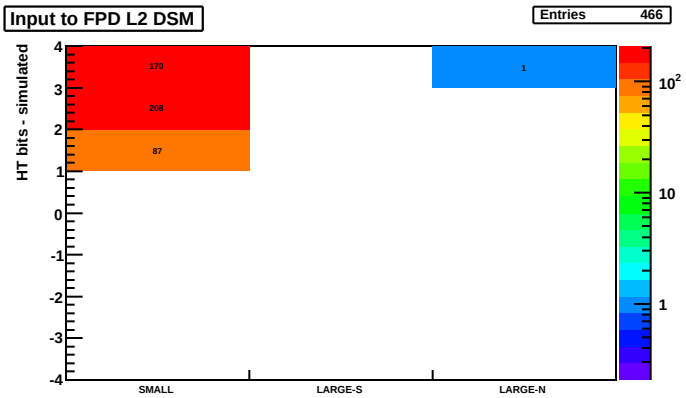

Input to FPD L2 DSM
Entries 2018424
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

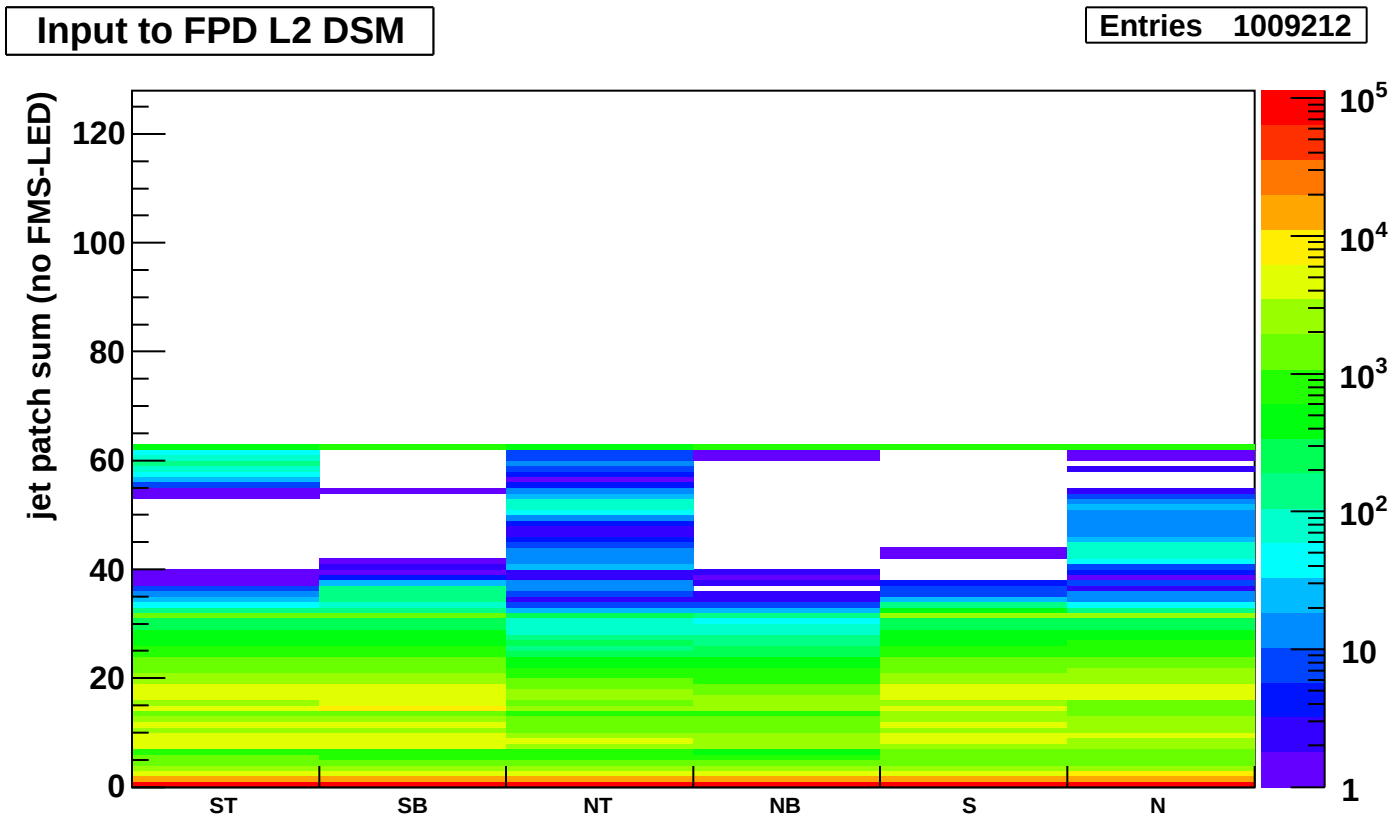

Input to FPD L2 DSM
Entries 1551
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

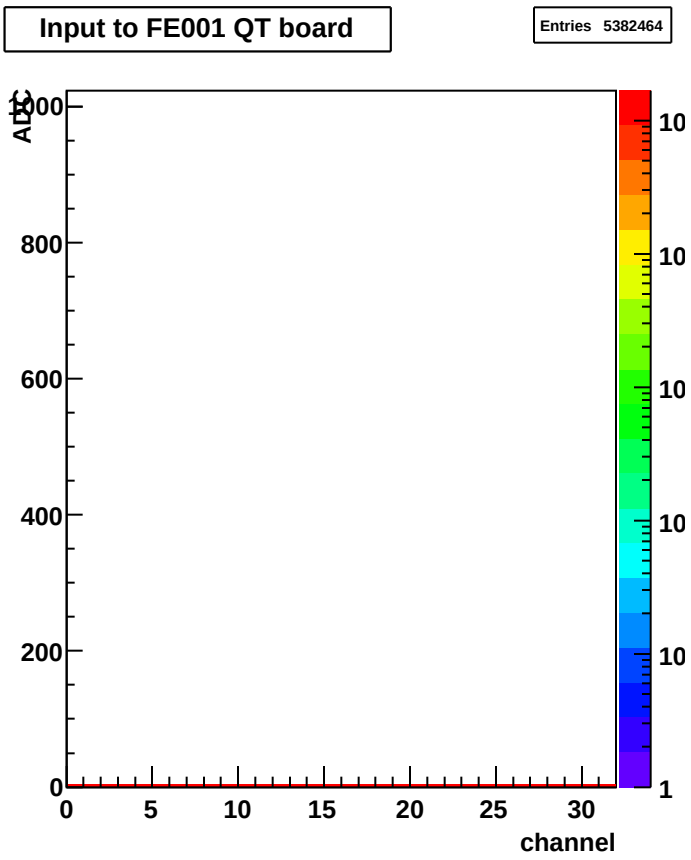

Input to FPD L2 DSM
Entries 504606
CL bits
5198
1727
58467
26753
9005
30570
18521
8623
79165
117730
148847
SMALL
LARGE-S
LARGE-N

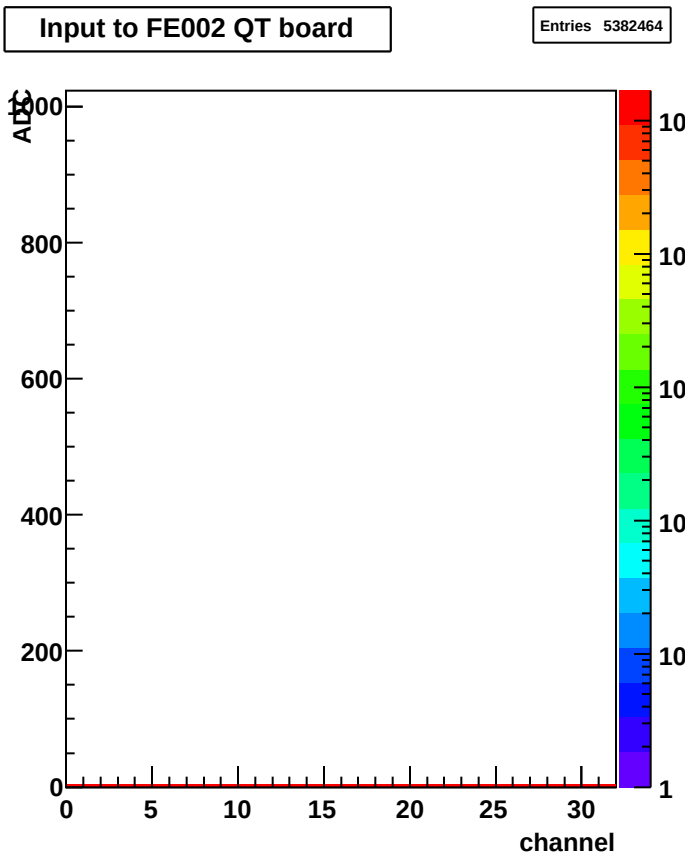

Input to FPD L2 DSM
Entries 66
CL bits - simulated
5
5
1
17
6
25
3
4
SMALL
LARGE-S
LARGE-N

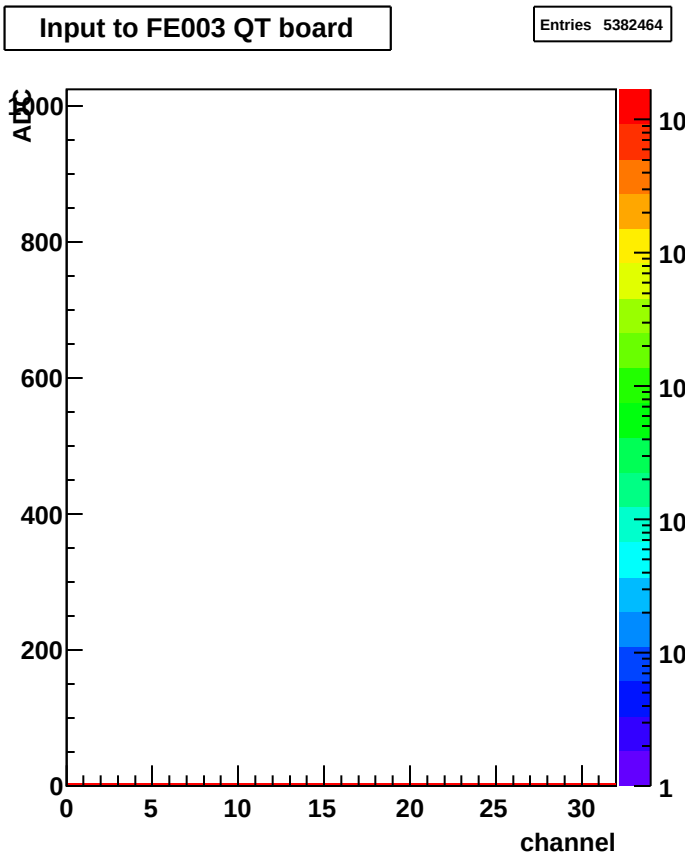

Input to FPD L2 DSM
Entries 504606
HT bits
5553
40485
11475
7954
12788
15723
7744
141907
111994
148983
SMALL
LARGE-S
LARGE-N

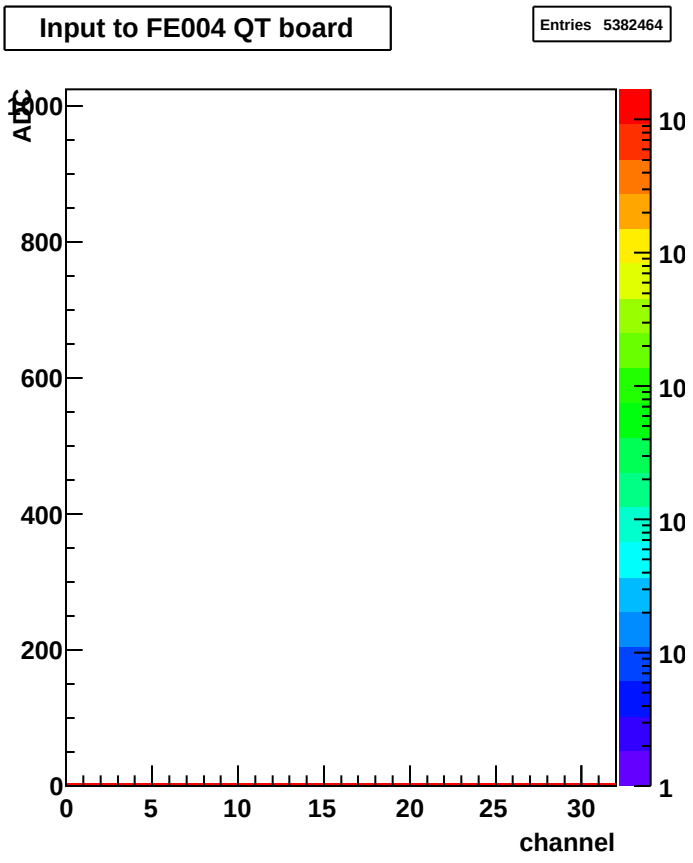

Input to FPD L2 DSM
Entries 466
HT bits - simulated
170
1
208
87
SMALL
LARGE-S
LARGE-N

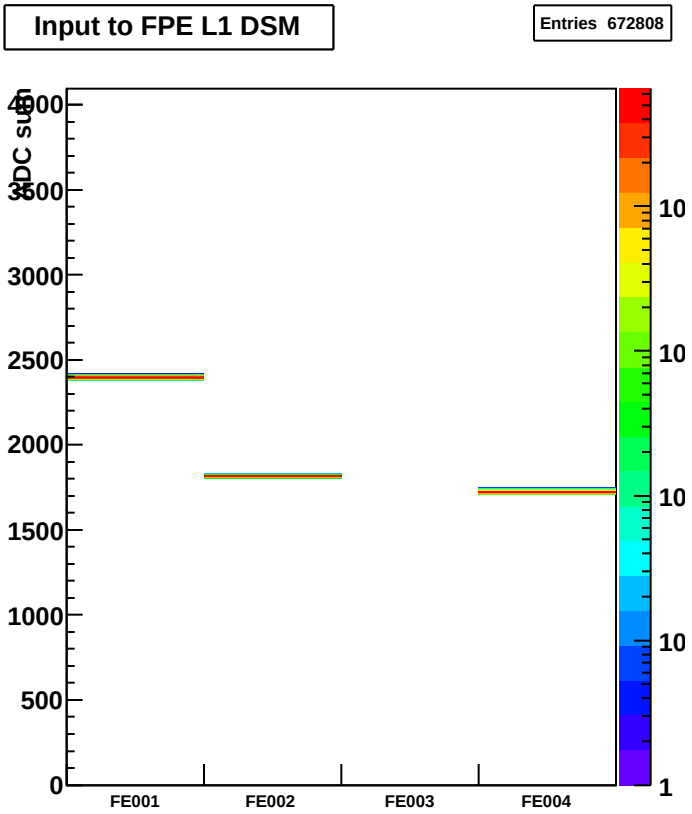

Input to FPD L2 DSM
Entries 1009212
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

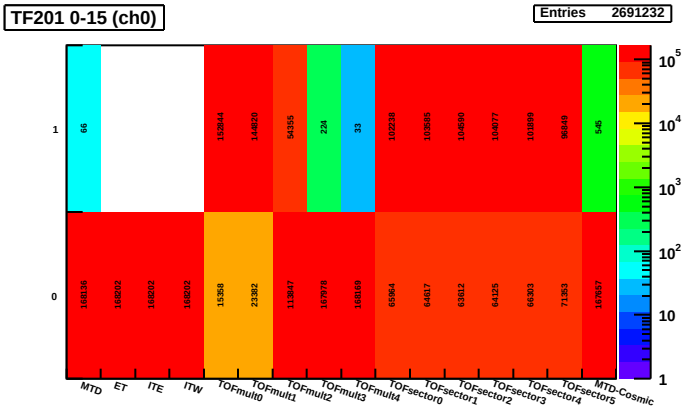

Input to FE001 QT board
Entries 5382464
ADC
channel

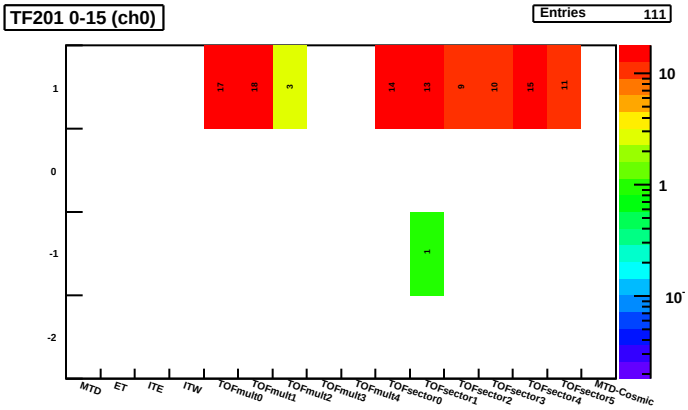

Input to FE002 QT board
Entries 5382464
ADC
channel

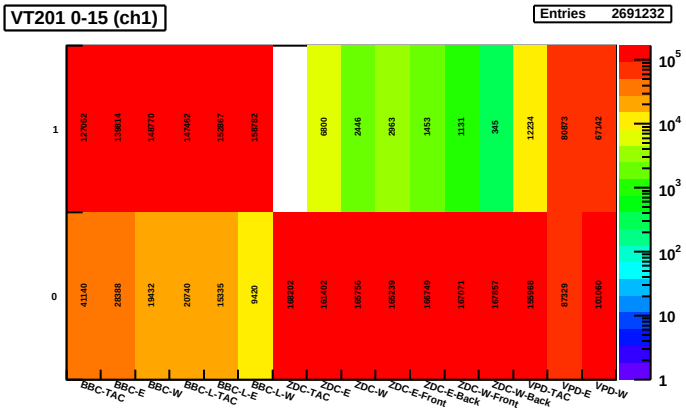

Input to FE003 QT board
Entries 5382464
ADC
channel

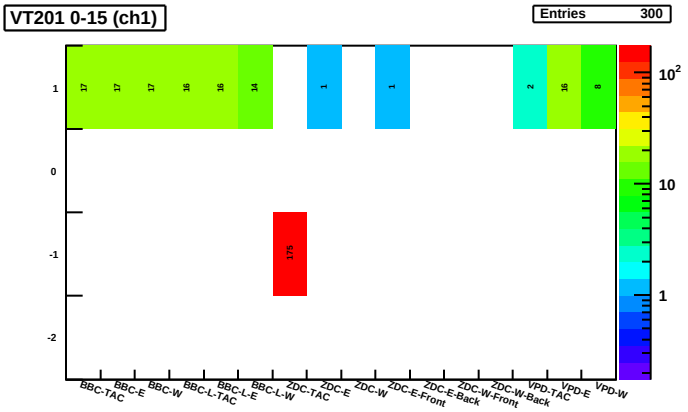

Input to FE004 QT board
Entries 5382464
ADC
channel

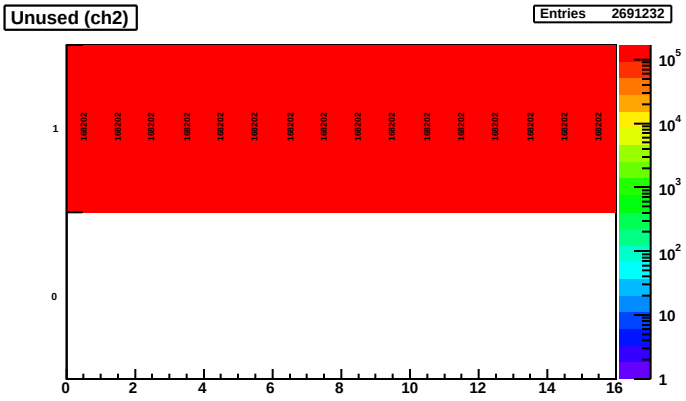

Input to FPE L1 DSM
Entries 672808
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

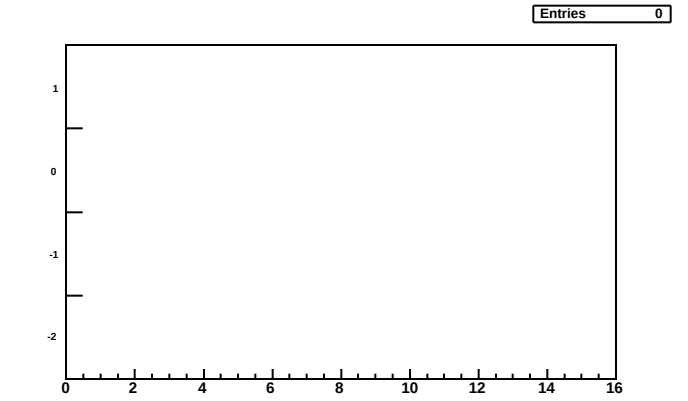

TF201 0-15 (ch0)
Entries 2691232
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 111
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 2691232
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 300
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 2691232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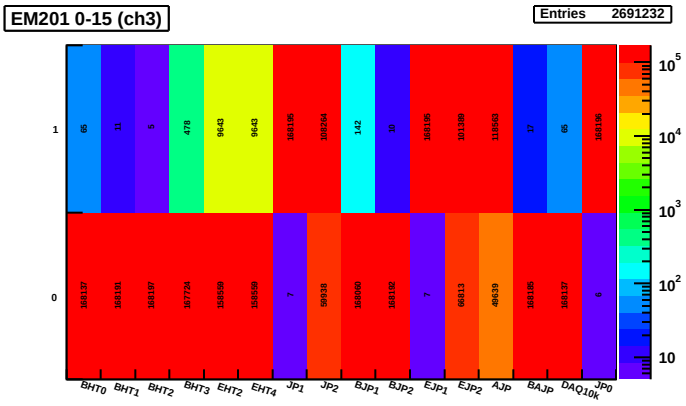
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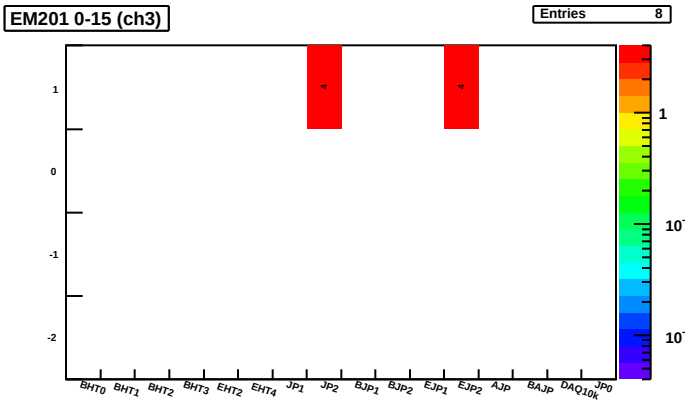
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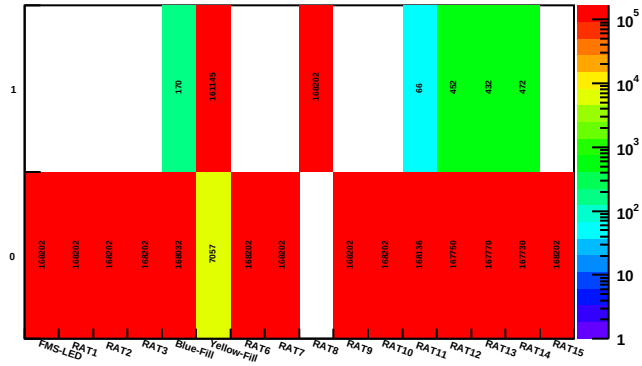
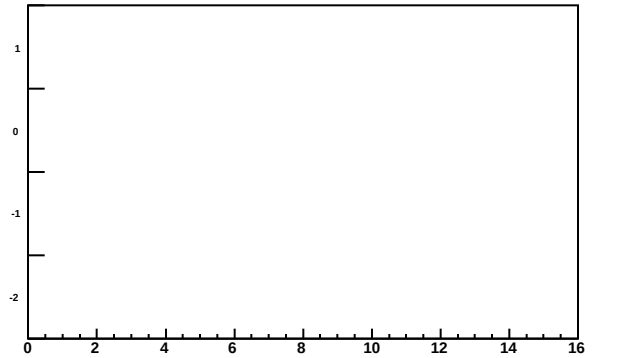
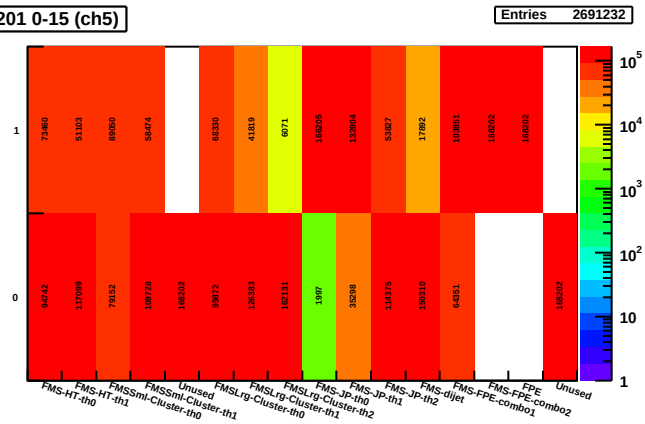
RAT board (ch4)





FP201 0-15 (ch5)



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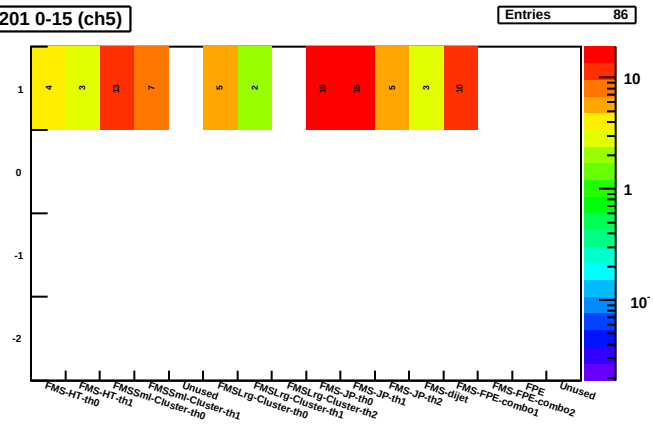
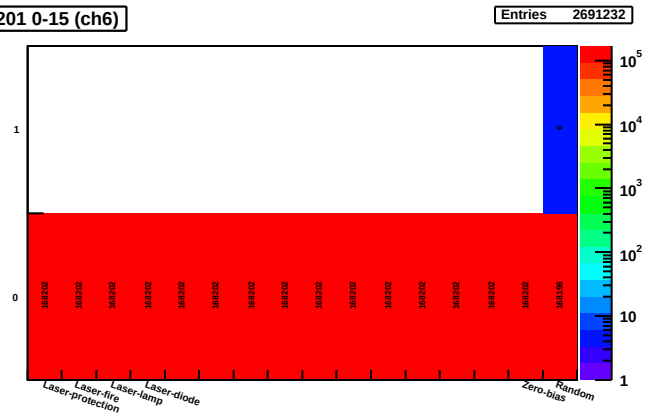
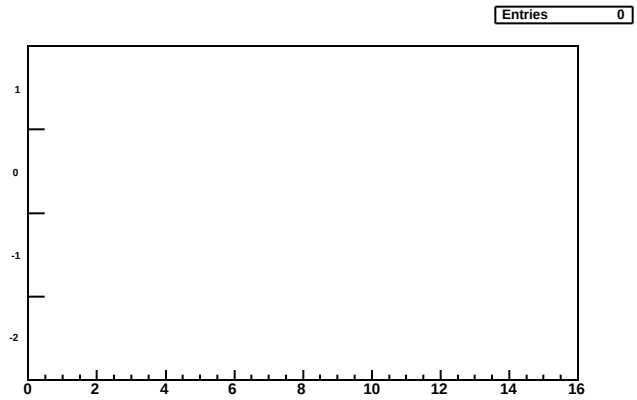
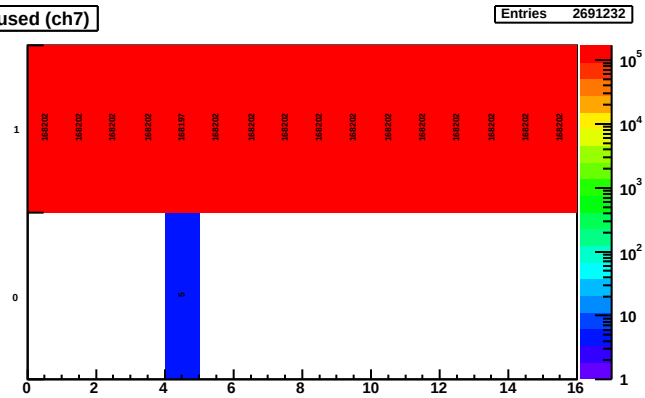
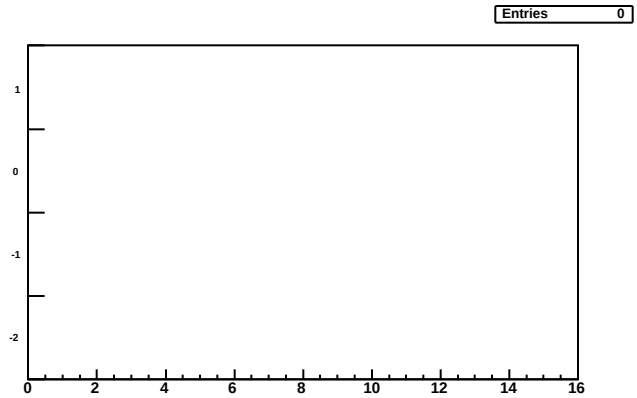


ST201 0-15 (ch6)





Unused (ch7)