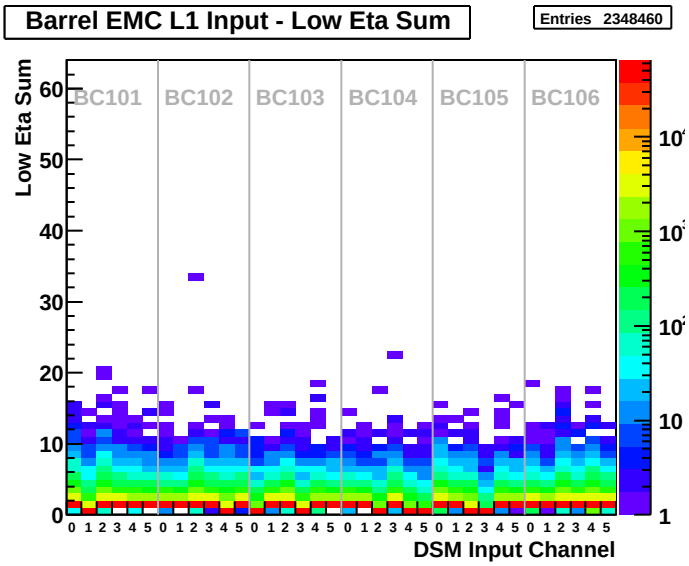
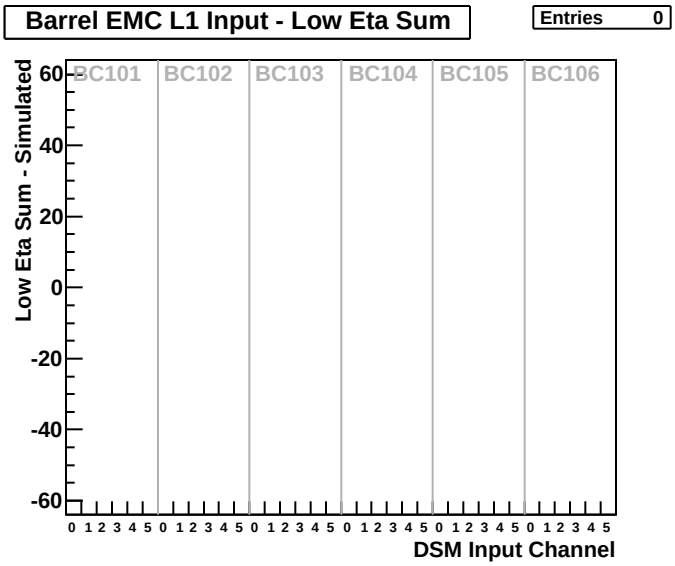
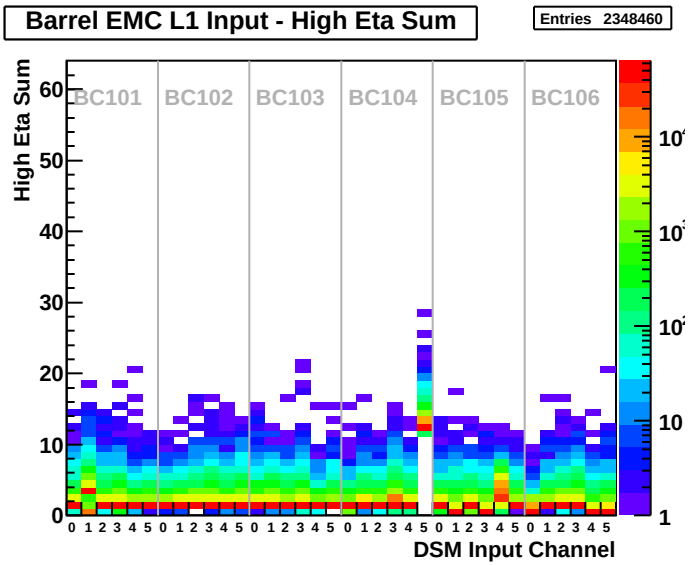
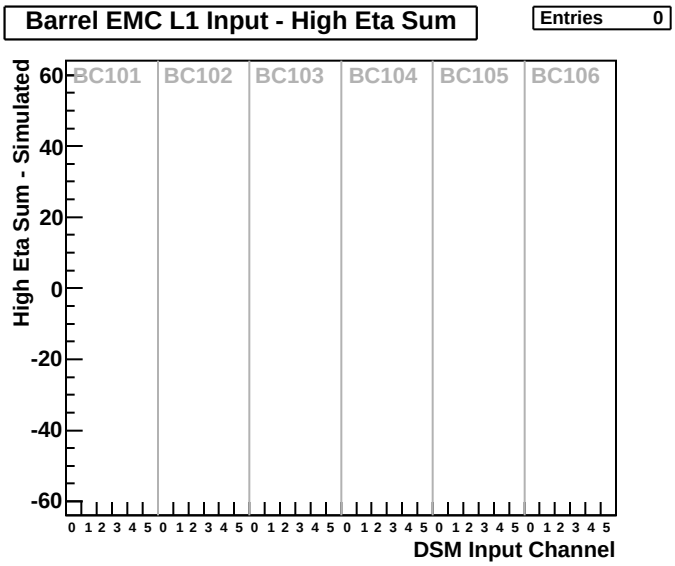
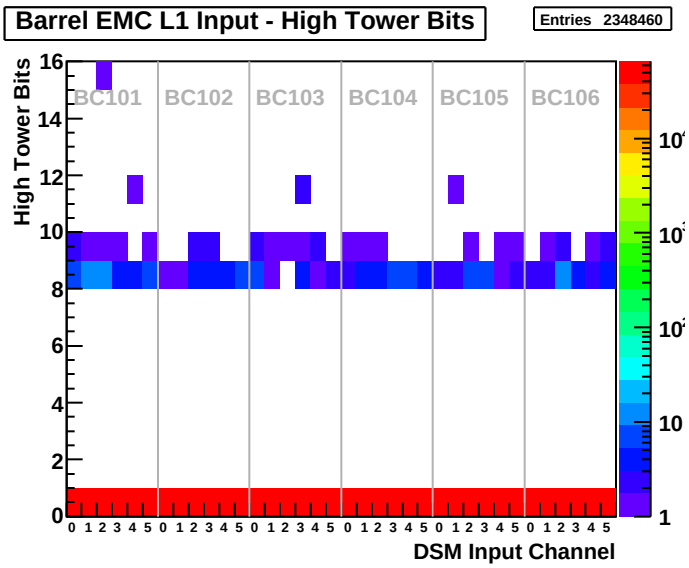
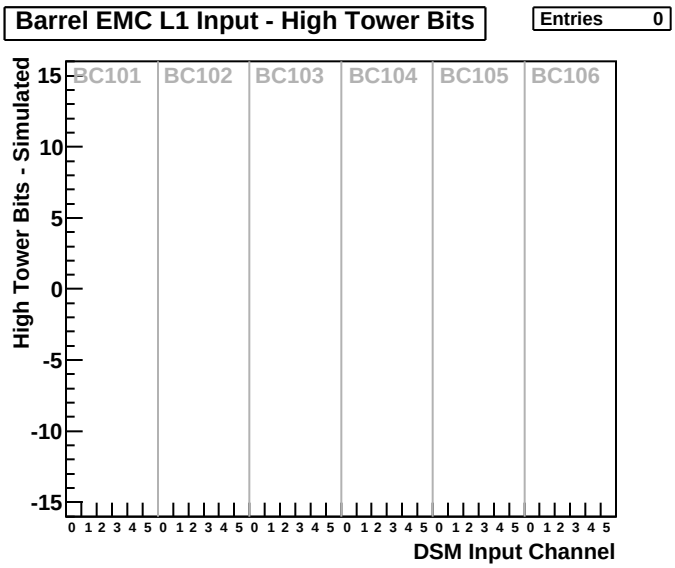

Barrel EMC L1 Input - Low Eta Sum
Entries 2348460
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

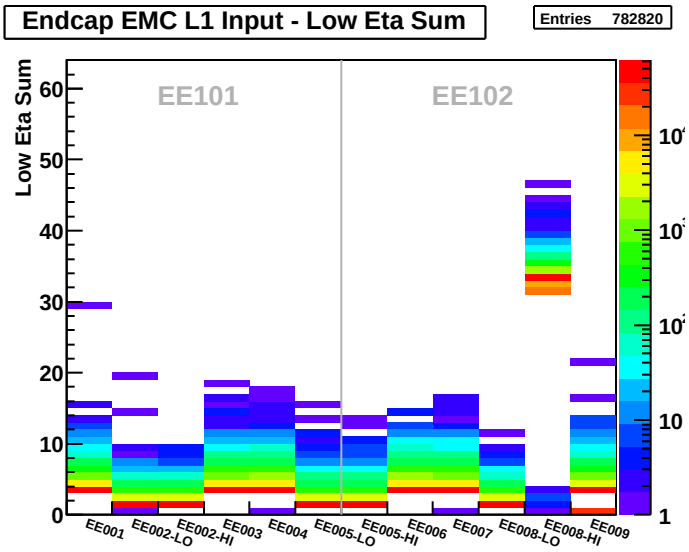

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

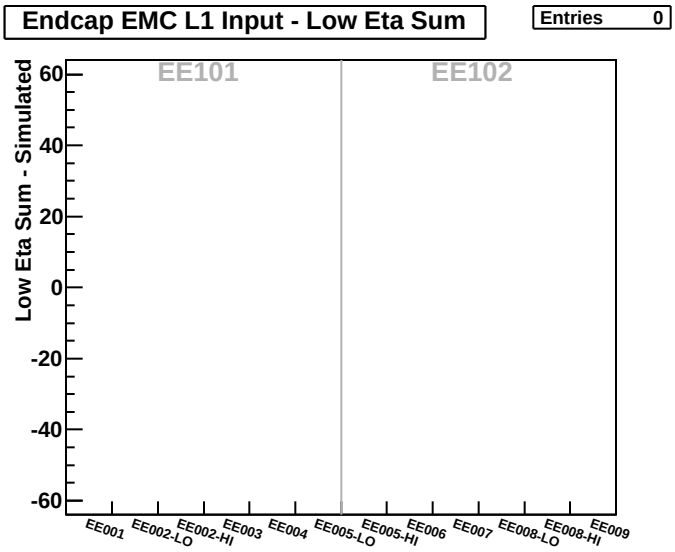

Barrel EMC L1 Input - High Eta Sum
Entries 2348460
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

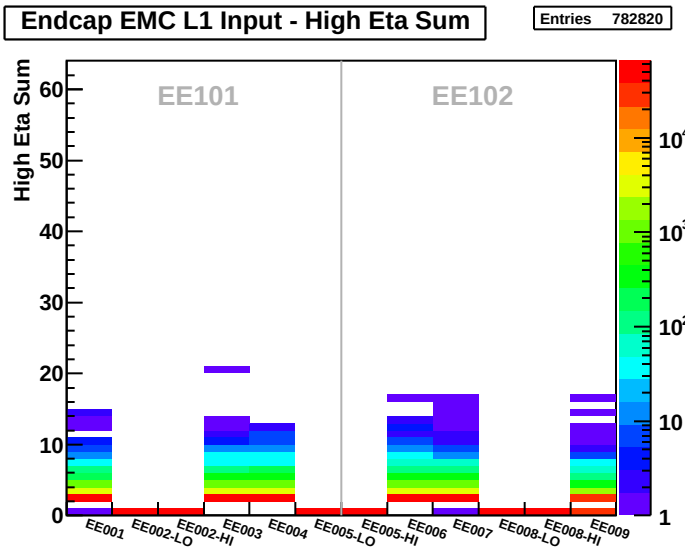

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

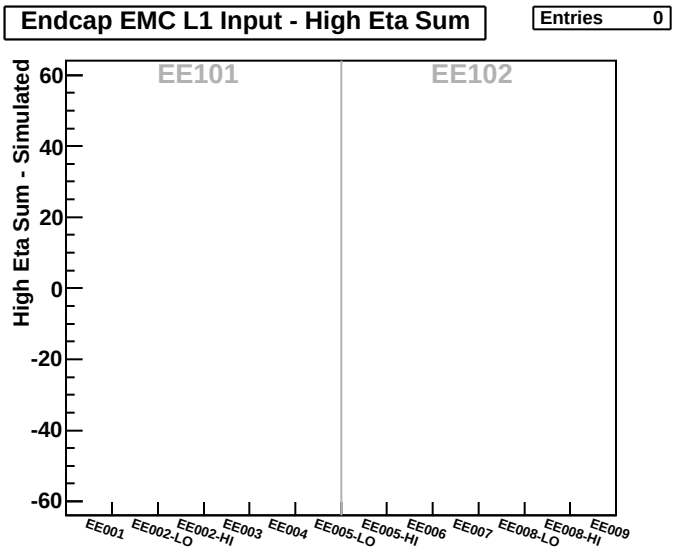

Barrel EMC L1 Input - High Tower Bits
Entries 2348460
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

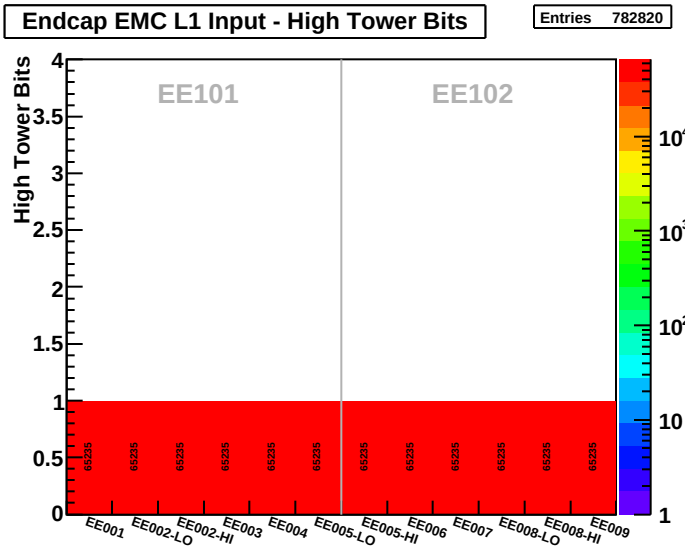

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

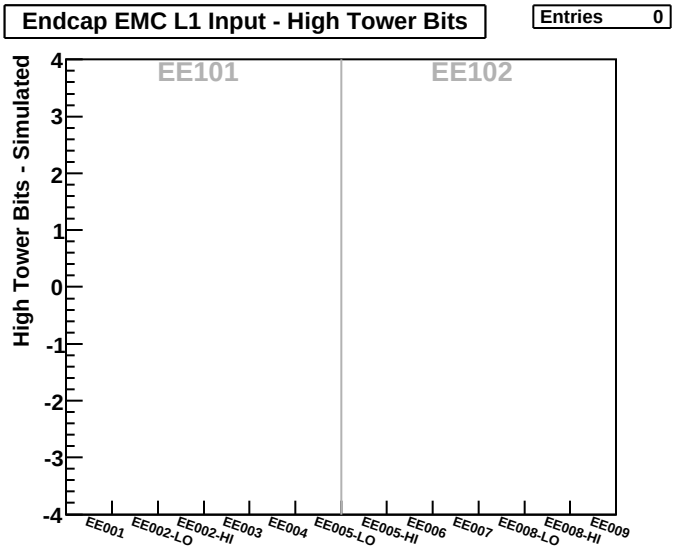

Endcap EMC L1 Input - Low Eta Sum
Entries 782820
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

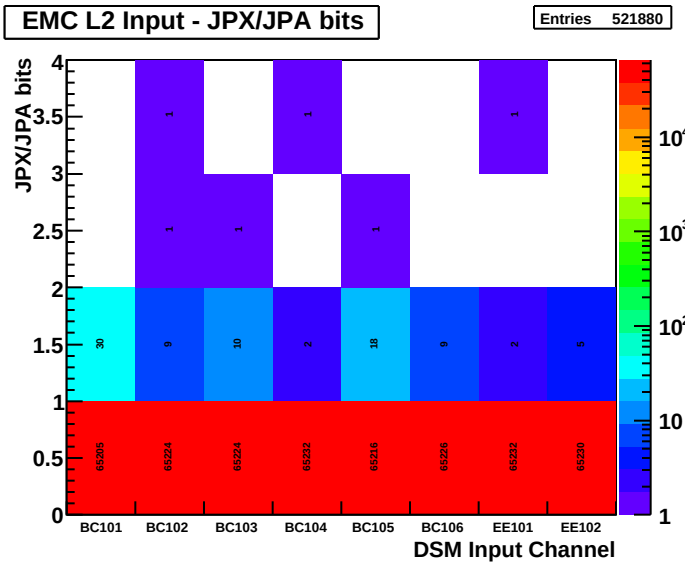

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

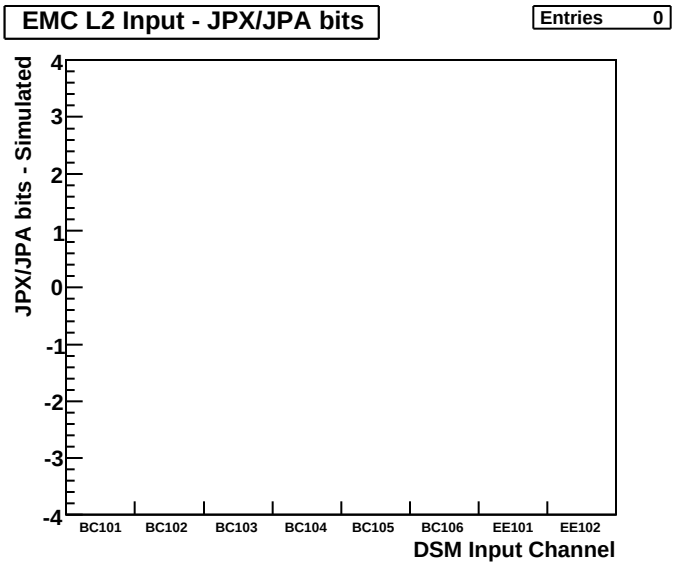

Endcap EMC L1 Input - High Eta Sum
Entries 782820
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

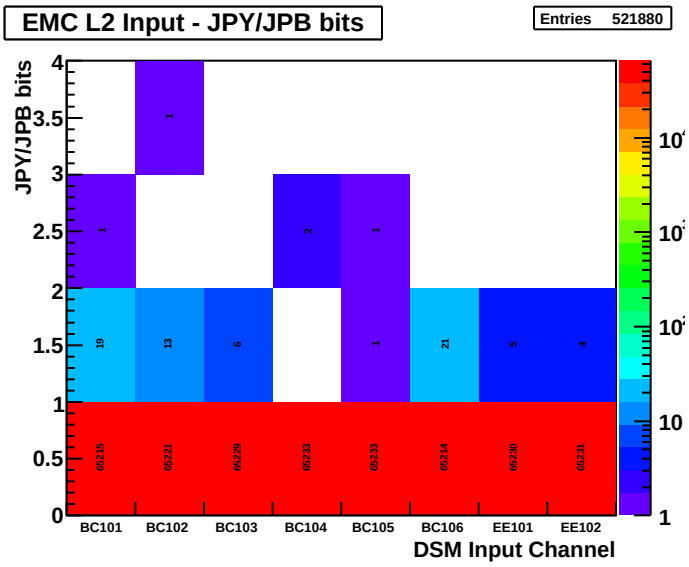

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

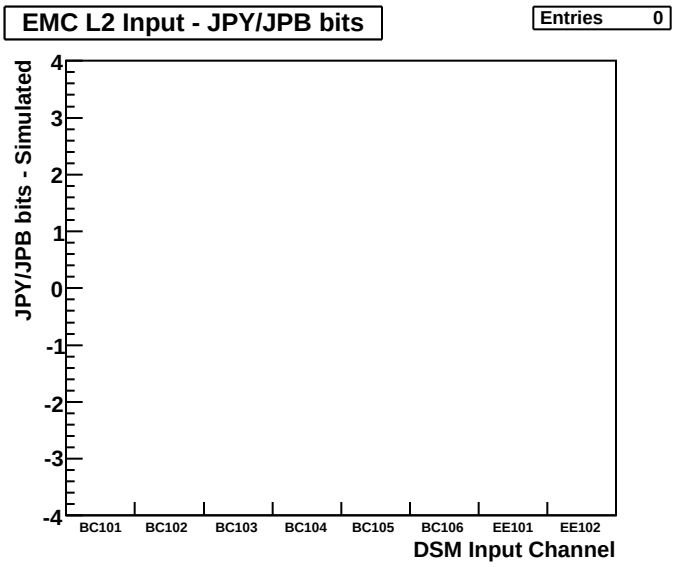

Endcap EMC L1 Input - High Tower Bits
Entries 782820
High Tower Bits
EE101
EE102
65535
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

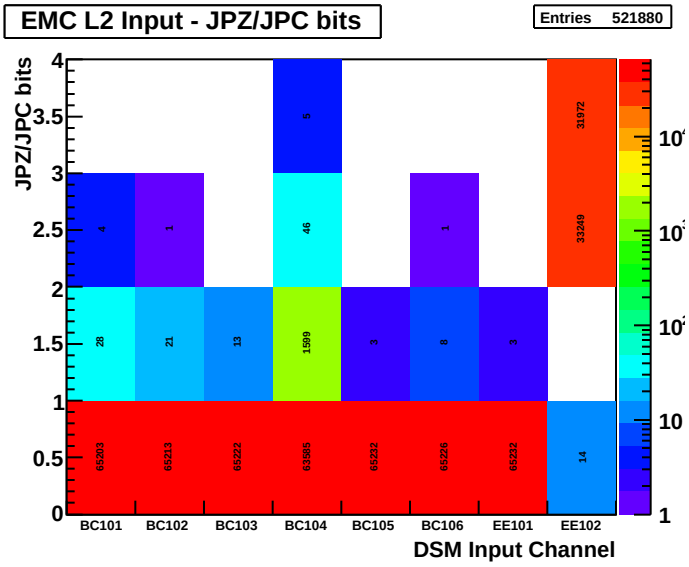

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

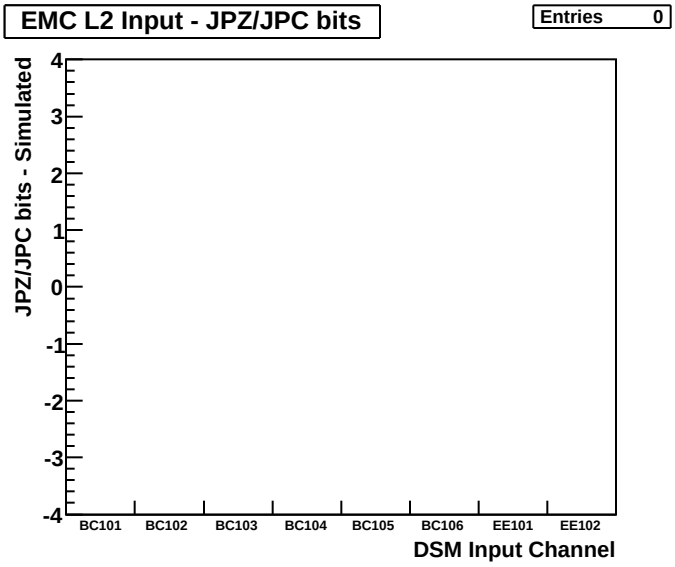

EMC L2 Input - JPX/JPA bits
Entries 521880
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

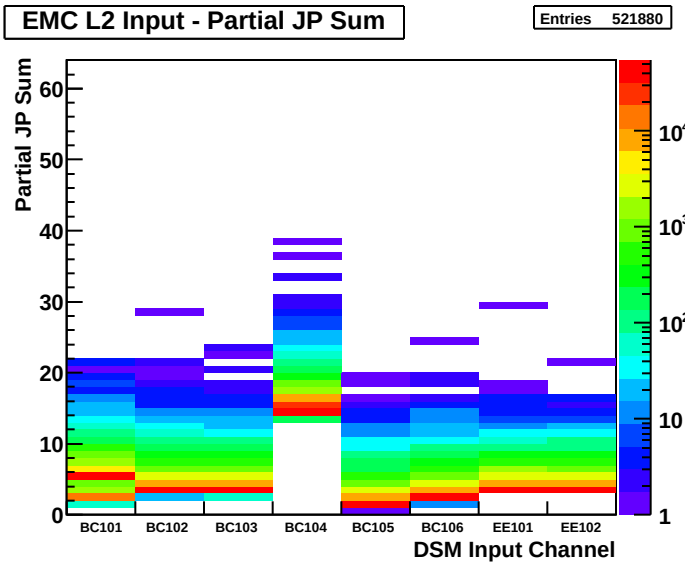

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

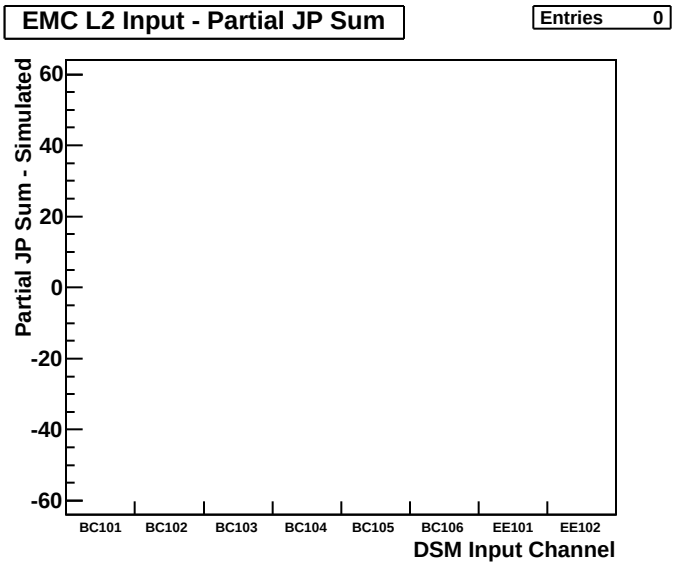

EMC L2 Input - JPY/JPB bits
Entries 521880
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

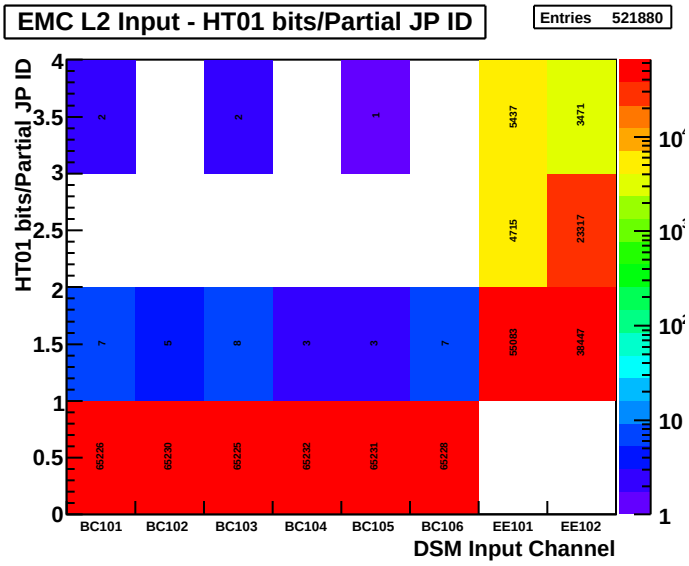

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

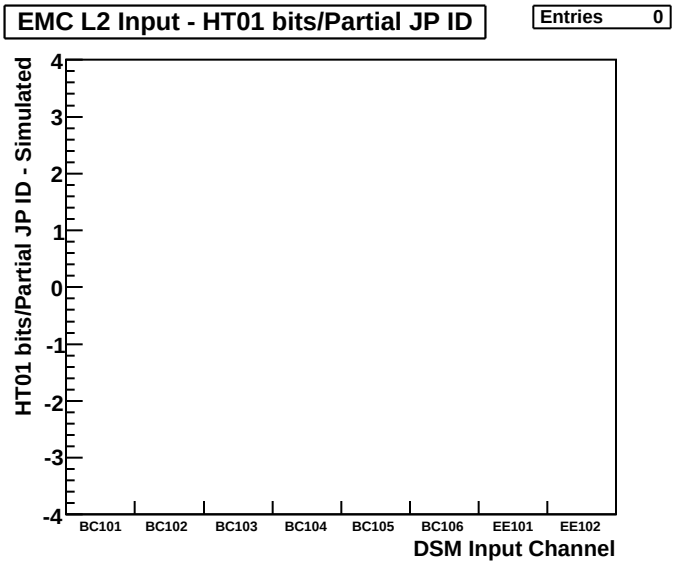

EMC L2 Input - JPZ/JPC bits
Entries 521880
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

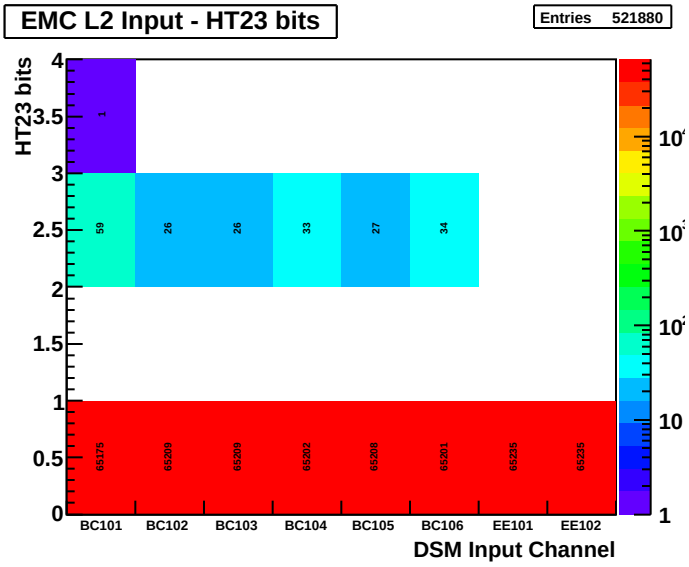

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

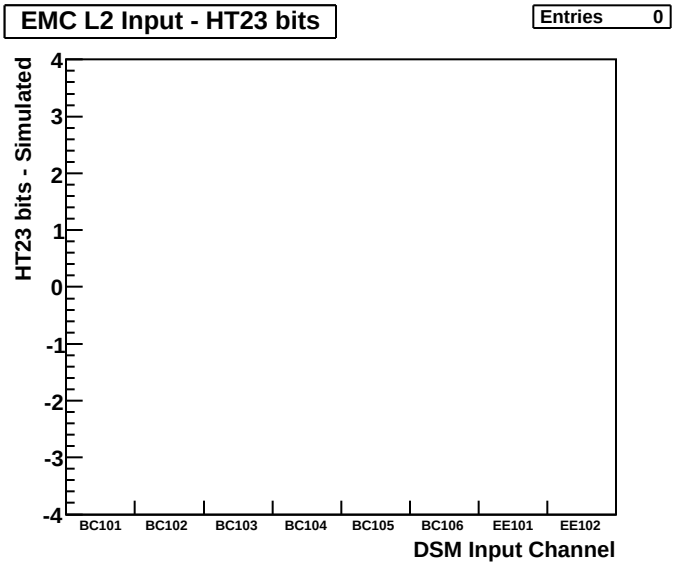

EMC L2 Input - Partial JP Sum
Entries 521880
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

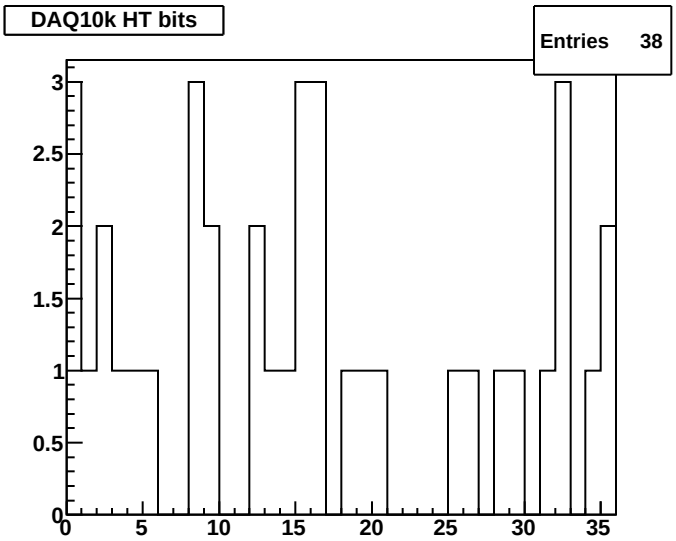

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

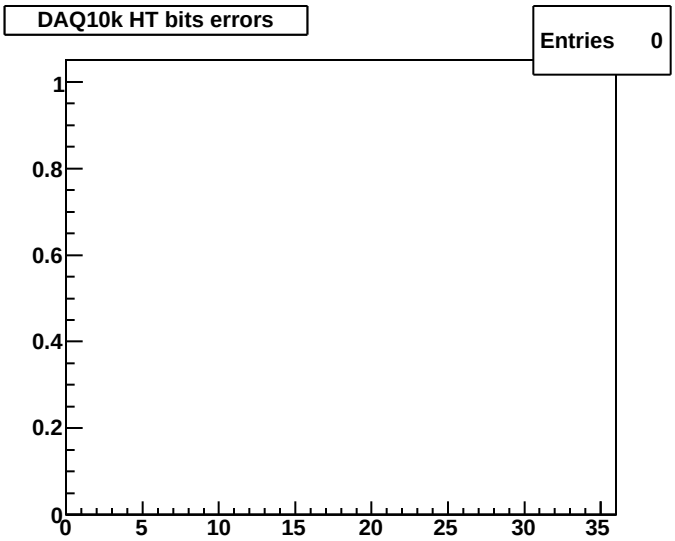

EMC L2 Input - HT01 bits/Partial JP ID
Entries 521880
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

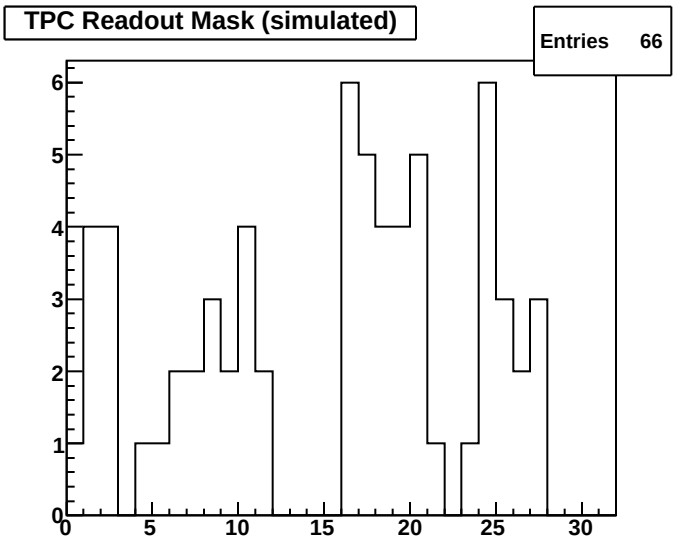

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

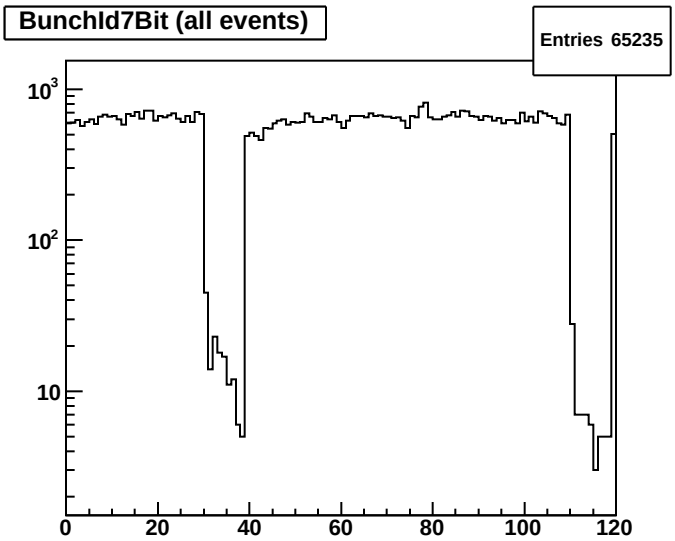

EMC L2 Input - HT23 bits
Entries 521880
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

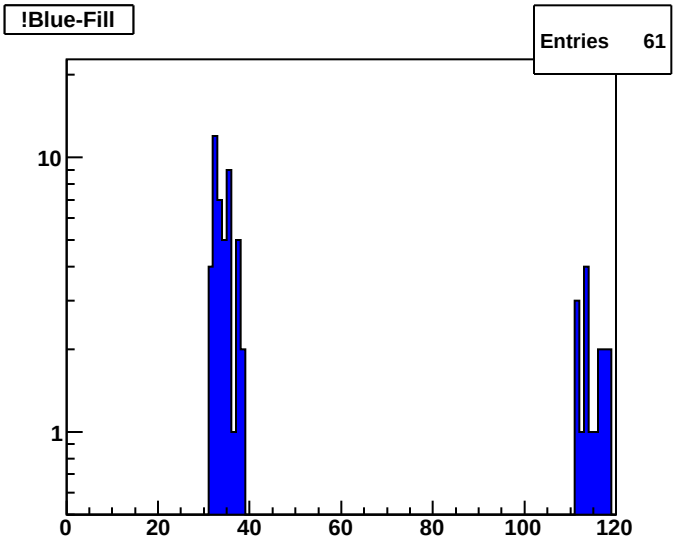

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

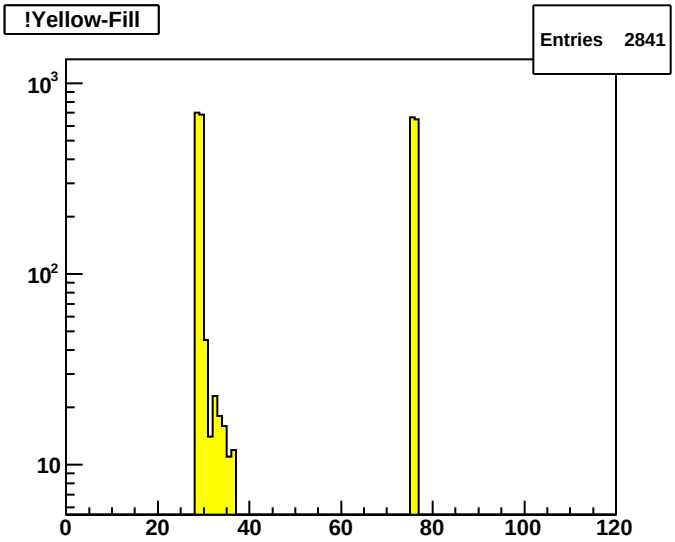

DAQ10k HT bits
Entries 38

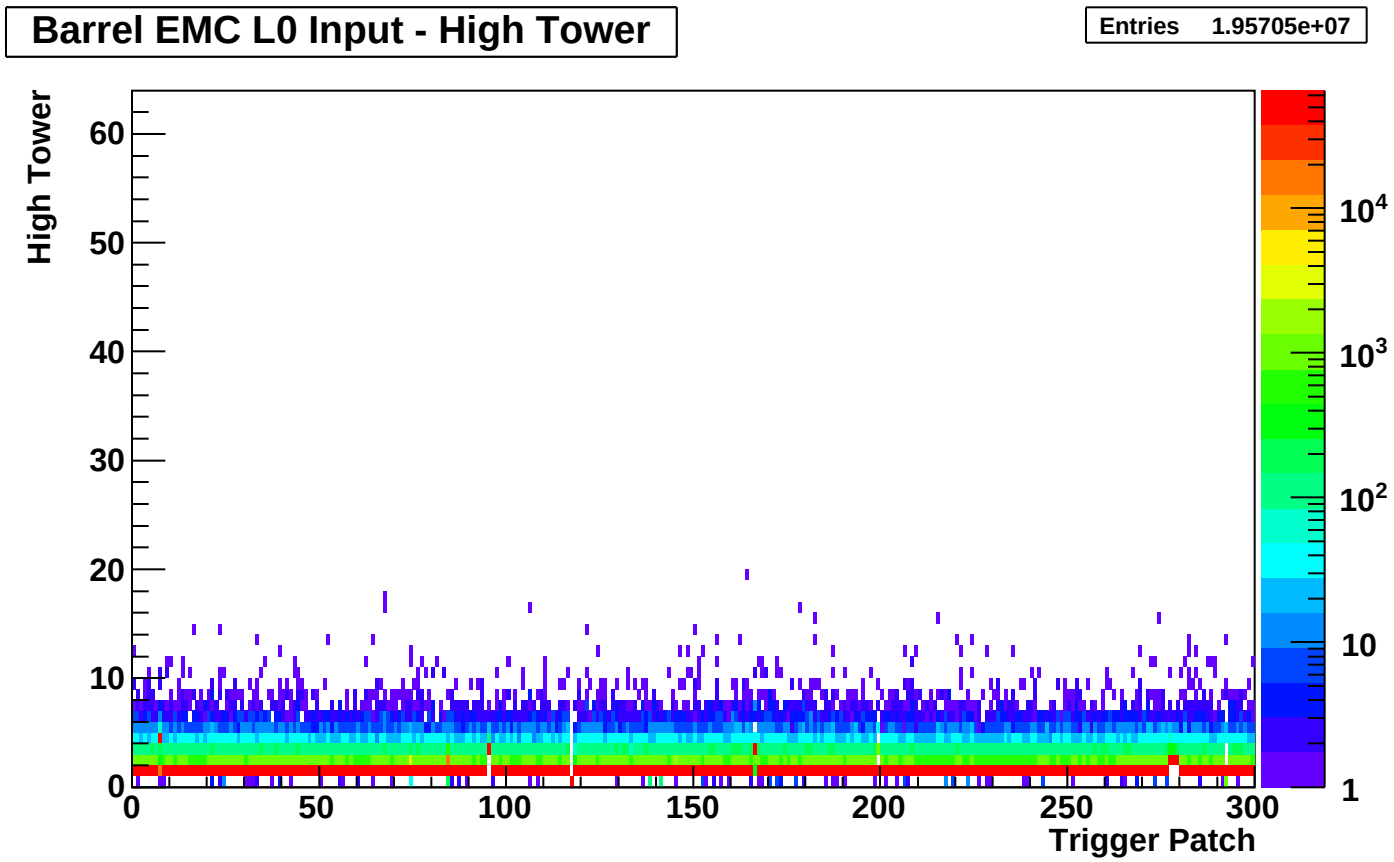

DAQ10k HT bits errors
Entries 0

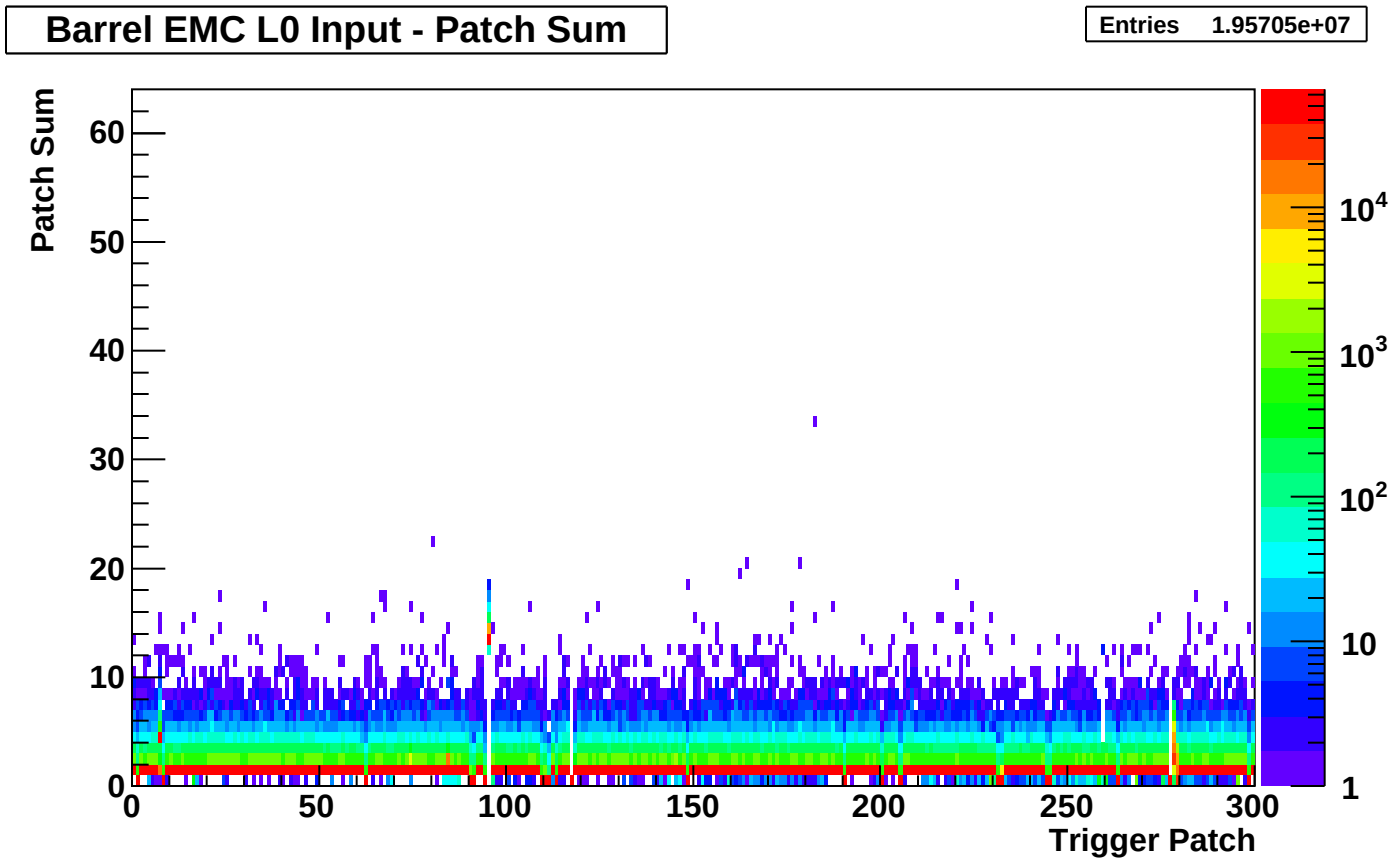

TPC Readout Mask (simulated)
Entries 66

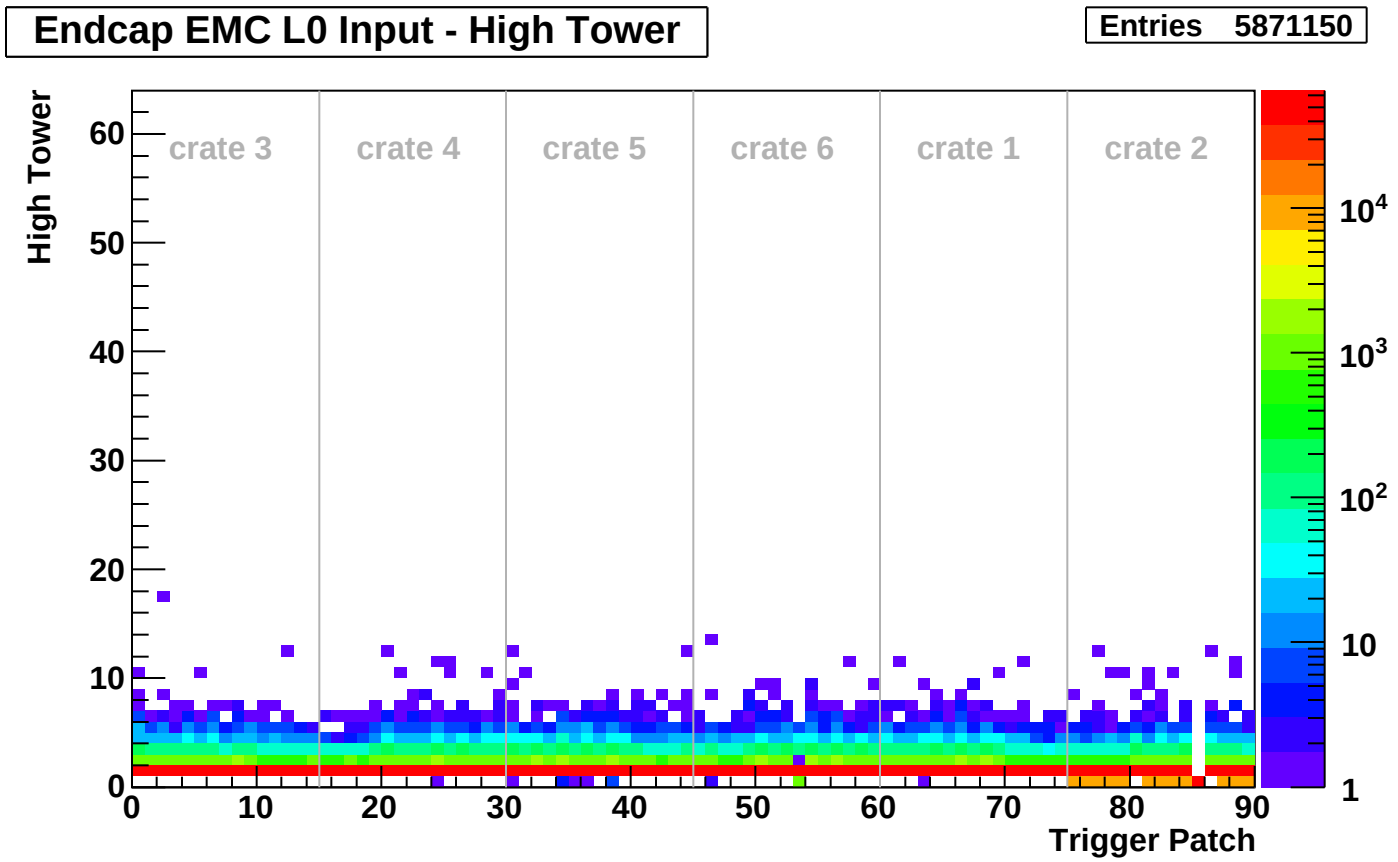

BunchId7Bit (all events)
Entries 65235

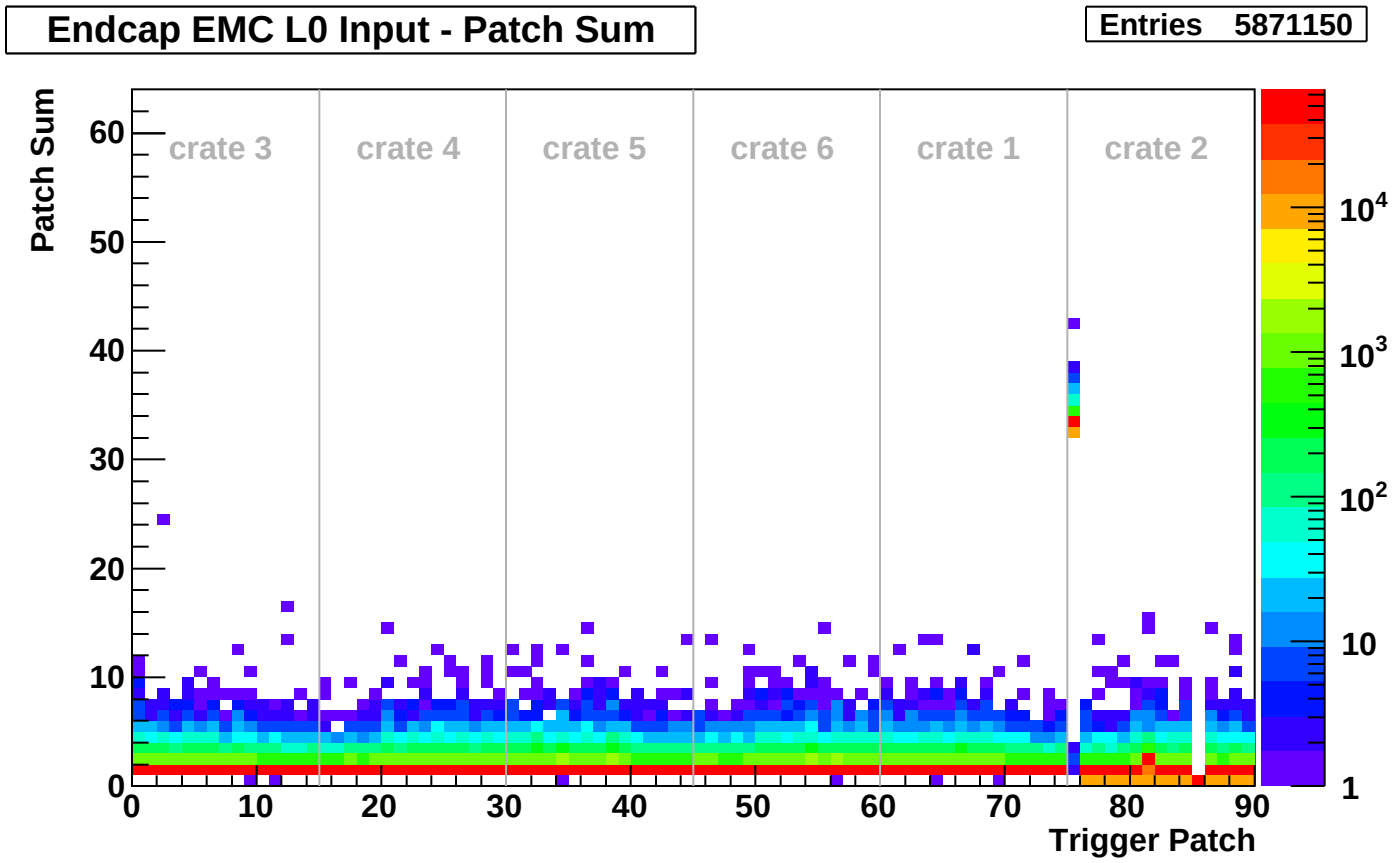

!Blue-Fill
Entries 61

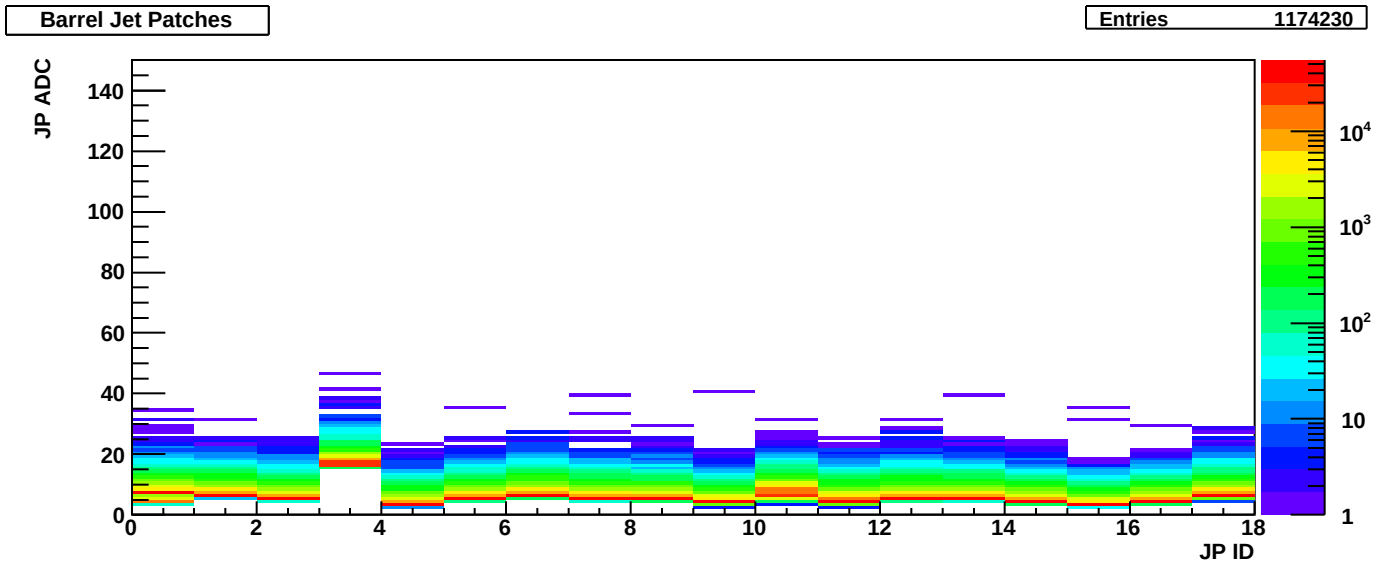

!Yellow-Fill
Entries 2841

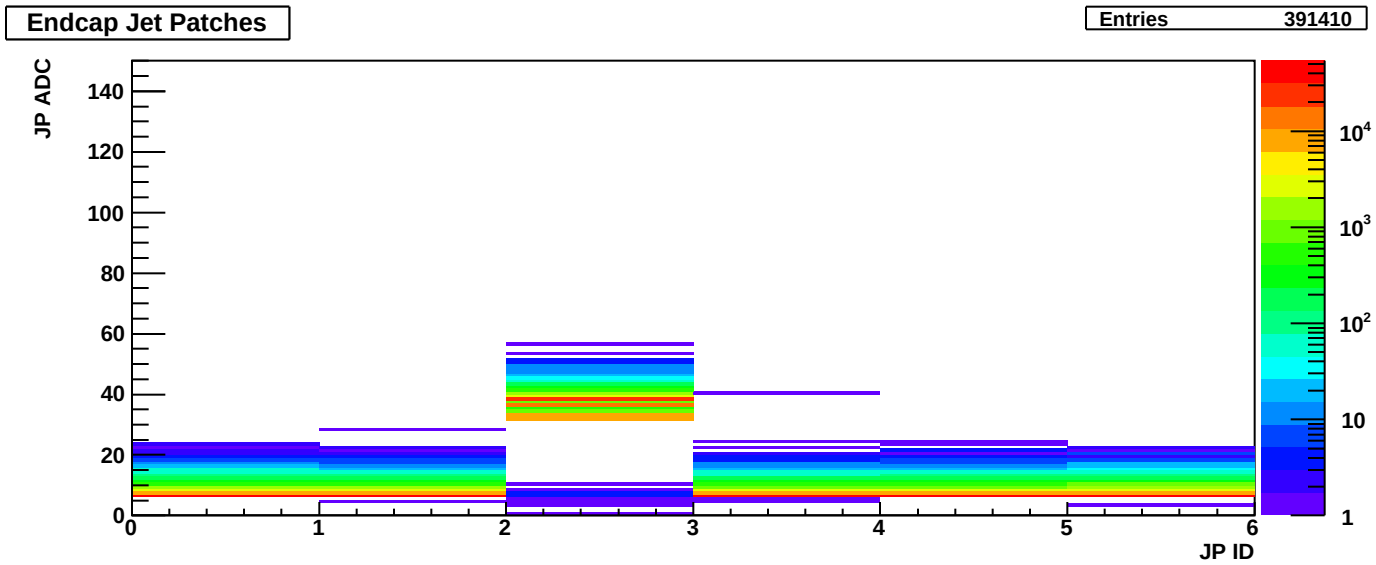

Barrel EMC L0 Input - High Tower
Entries 1.95705e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

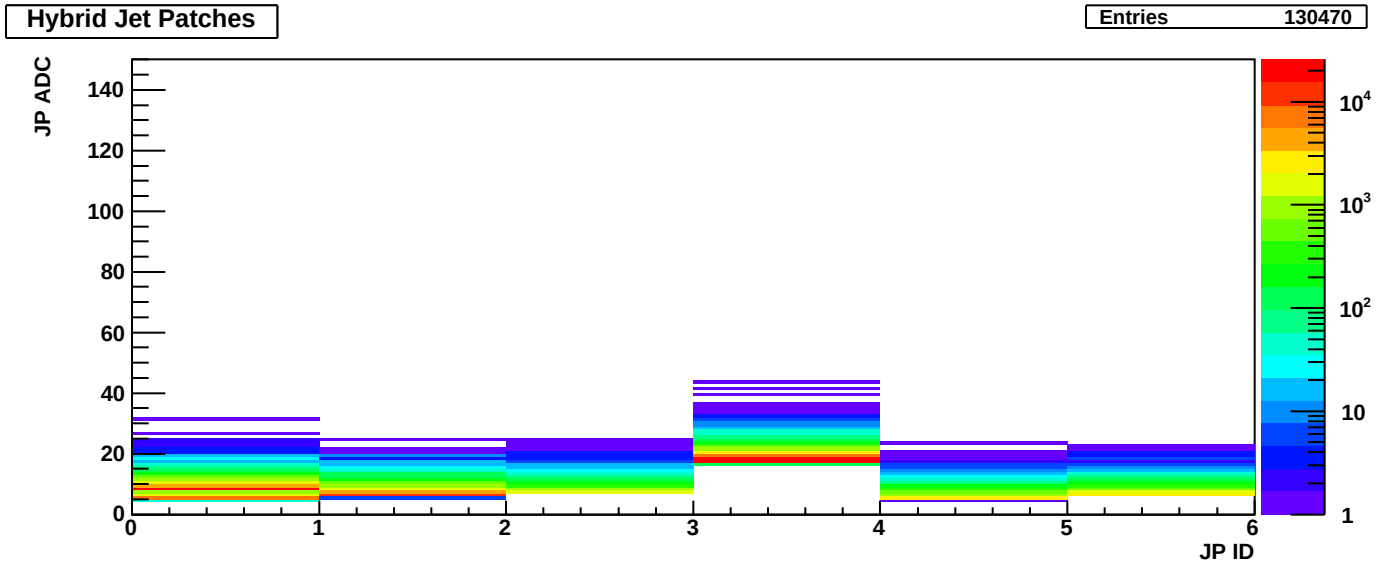

Barrel EMC L0 Input - Patch Sum
Entries 1.95705e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

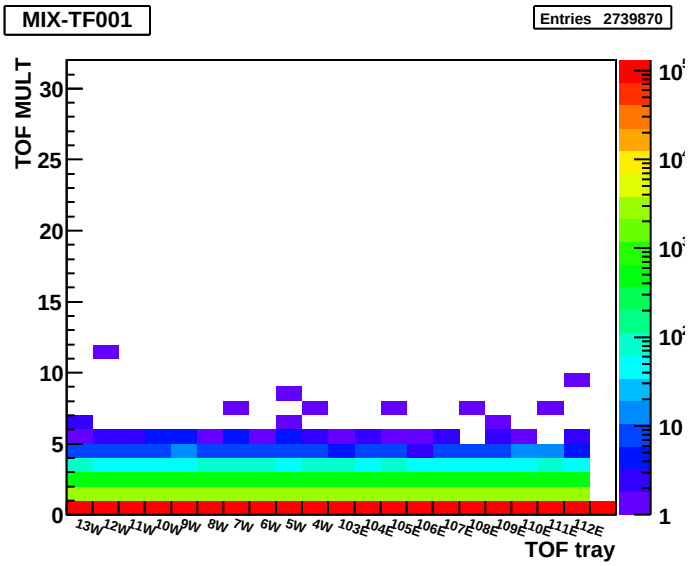

Endcap EMC L0 Input - High Tower
Entries 5871150
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

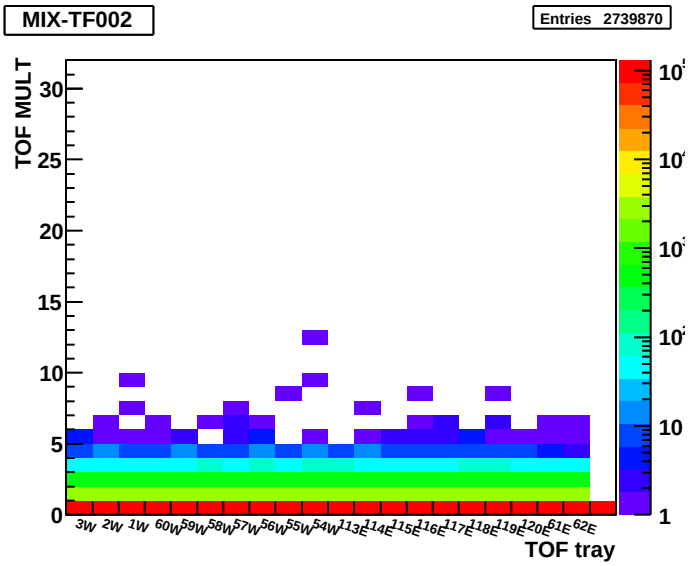

Endcap EMC L0 Input - Patch Sum
Entries 5871150
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

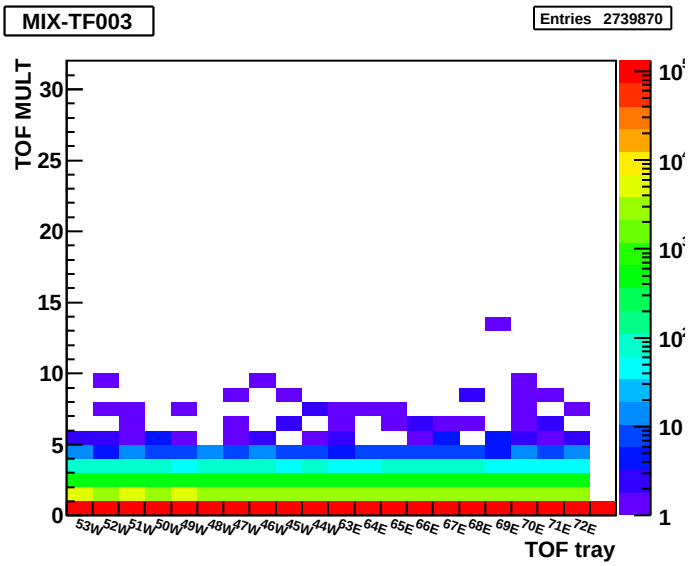

Barrel Jet Patches
Entries 1174230
JP ADC
JP ID

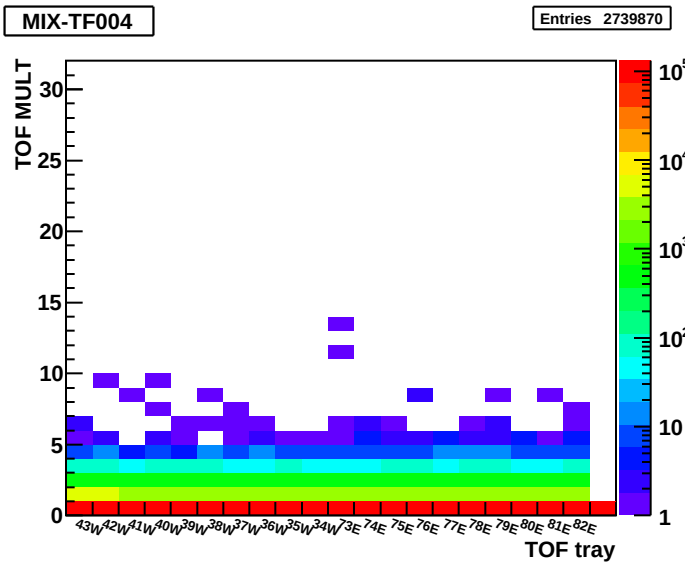

Endcap Jet Patches
Entries 391410
JP ADC
JP ID

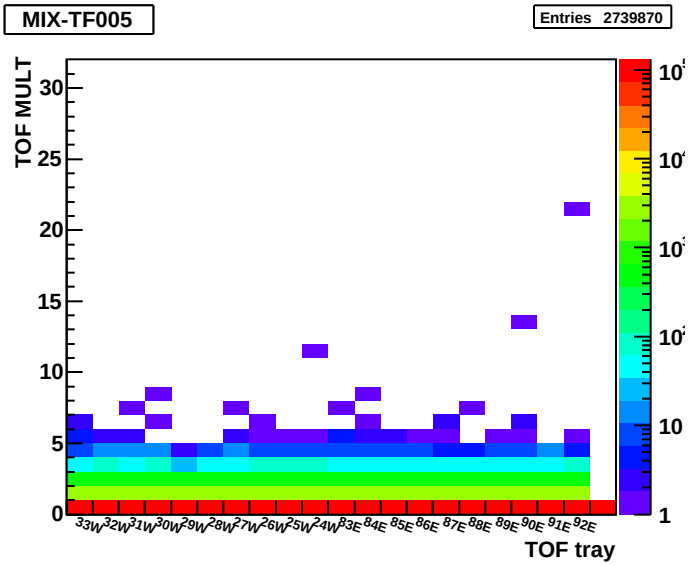

Hybrid Jet Patches
Entries 130470
JP ADC
JP ID

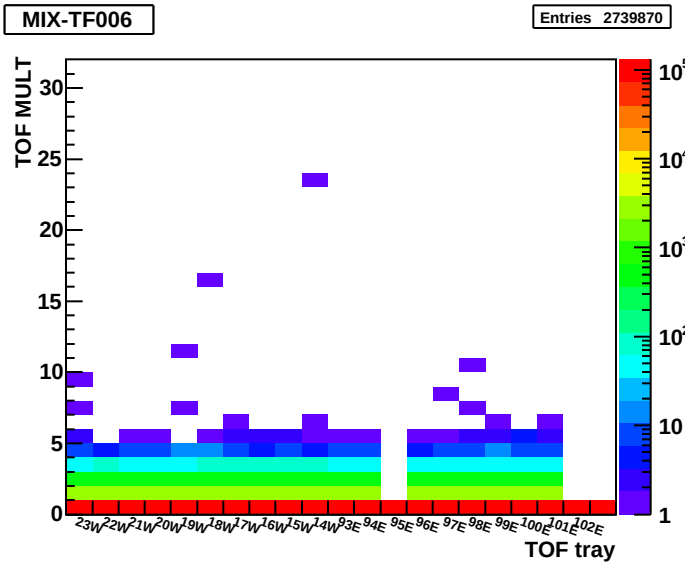

MIX-TF001
Entries 2739870
TOF MULT
TOF tray

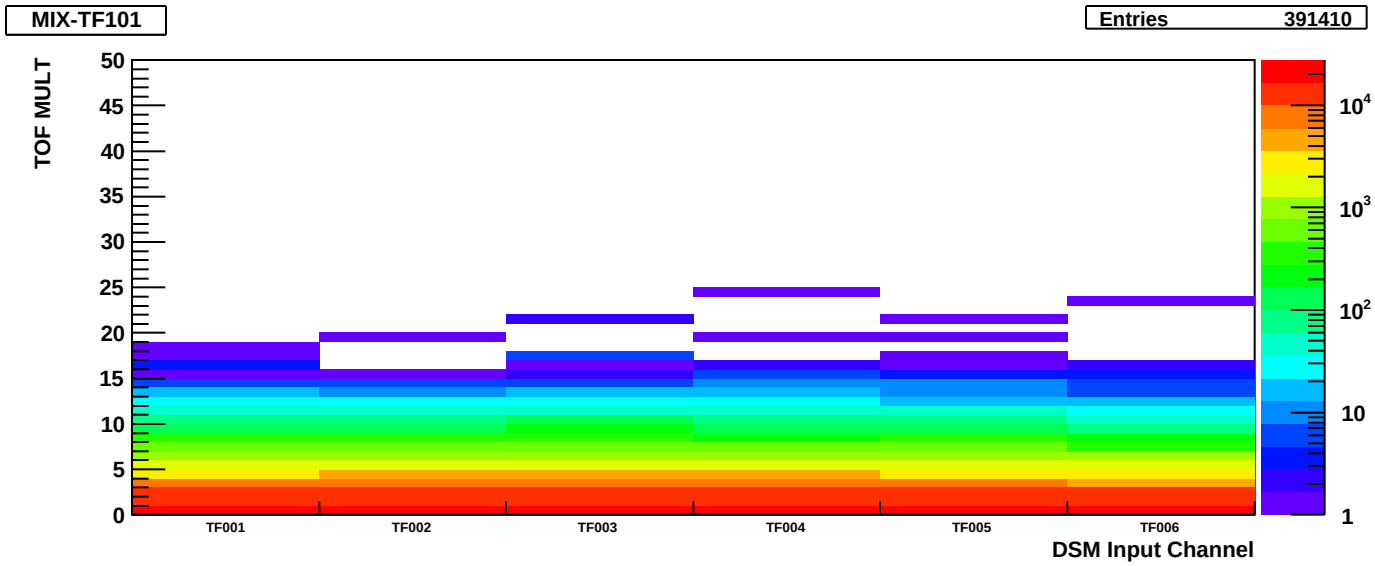

MIX-TF002
Entries 2739870
TOF MULT
TOF tray

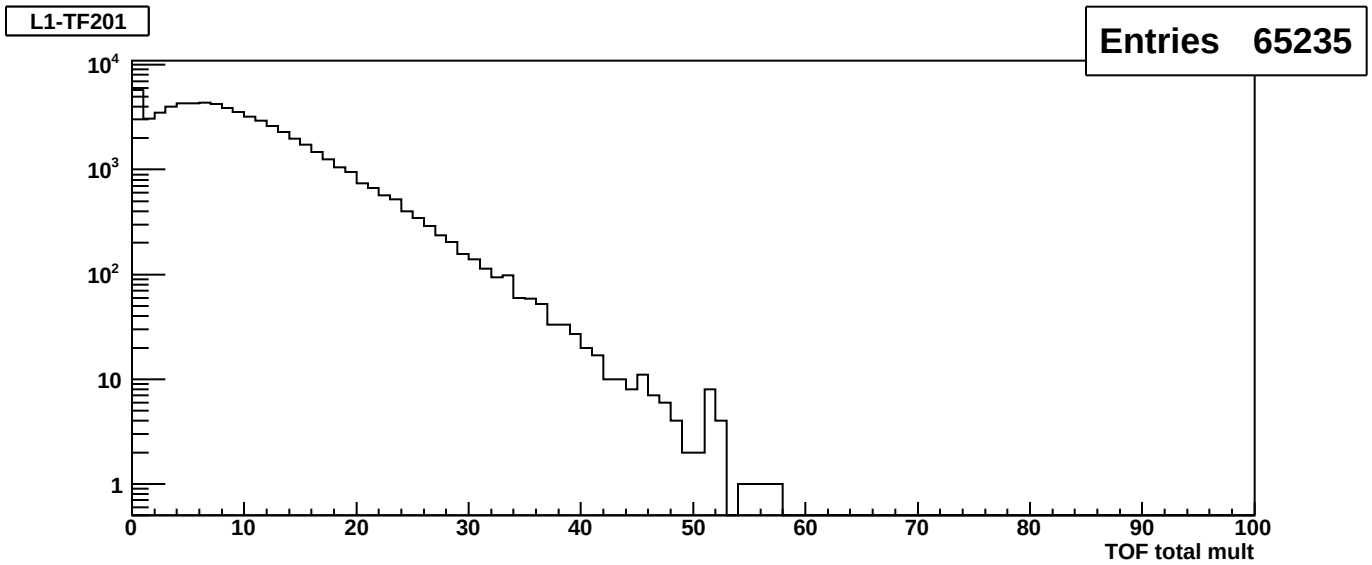

MIX-TF003
Entries 2739870
TOF MULT
TOF tray

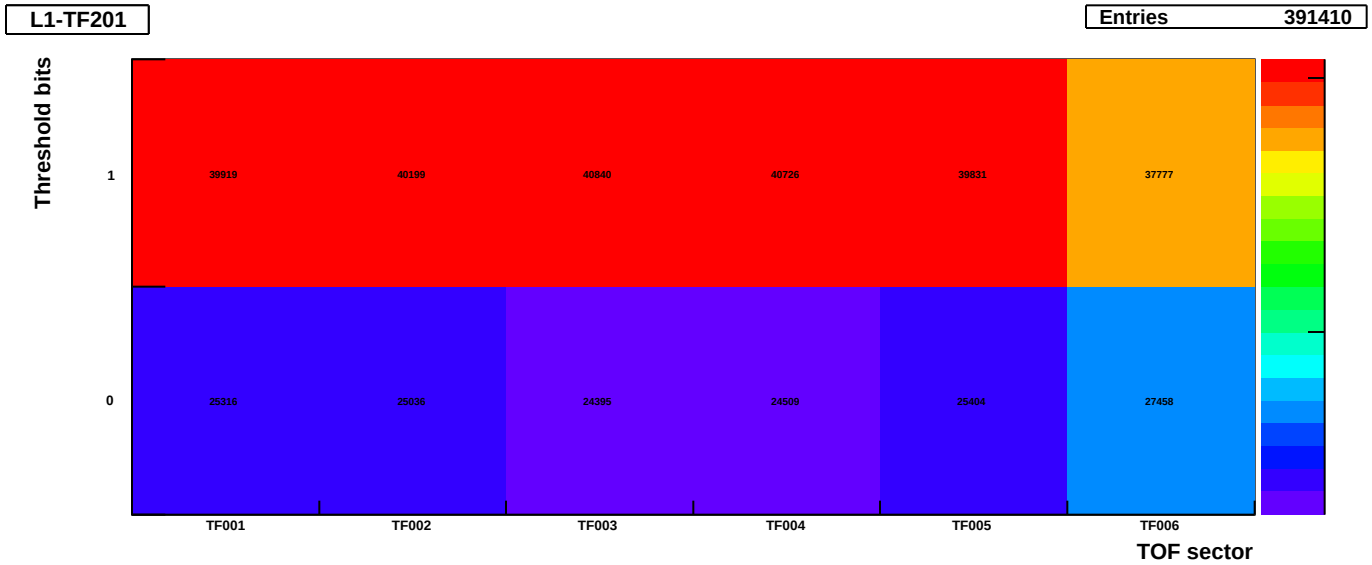

MIX-TF004
Entries 2739870
TOF MULT
TOF tray

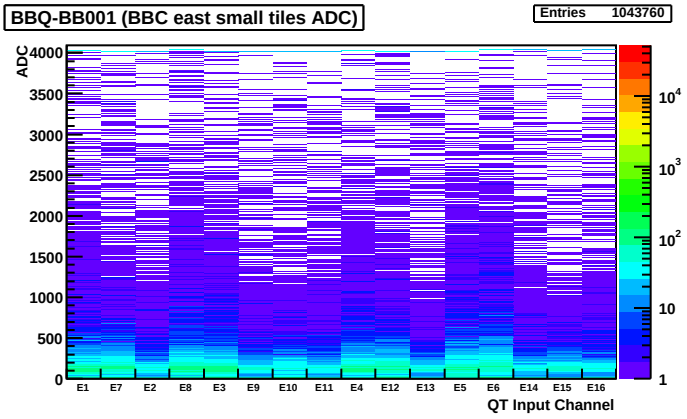

MIX-TF005
Entries 2739870
TOF MULT
TOF tray

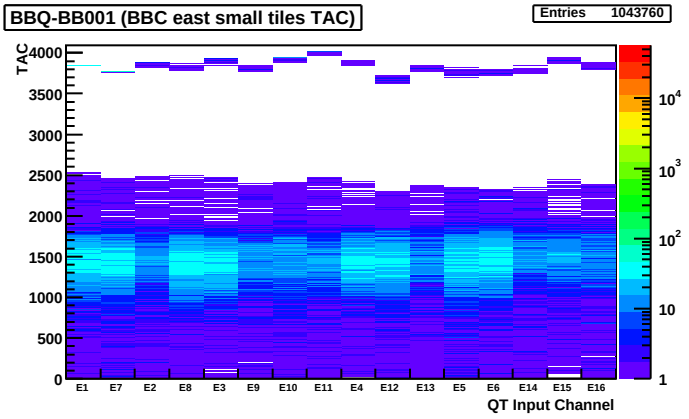

MIX-TF006
Entries 2739870
TOF MULT
TOF tray

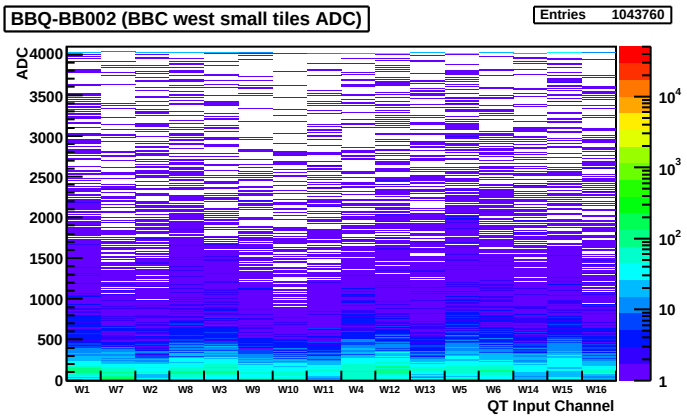

MIX-TF101
Entries 391410
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

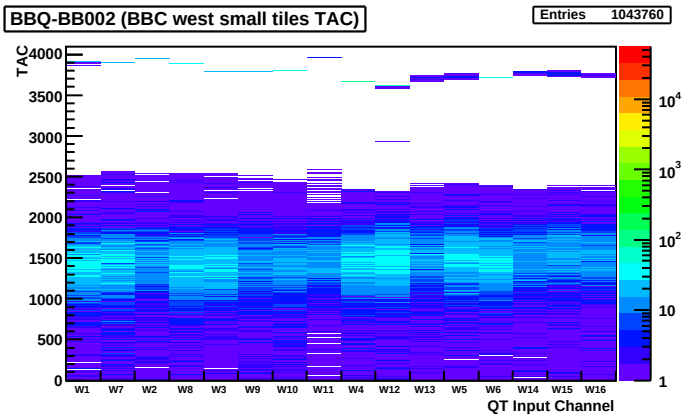

L1-TF201
Entries 65235
TOF total mult

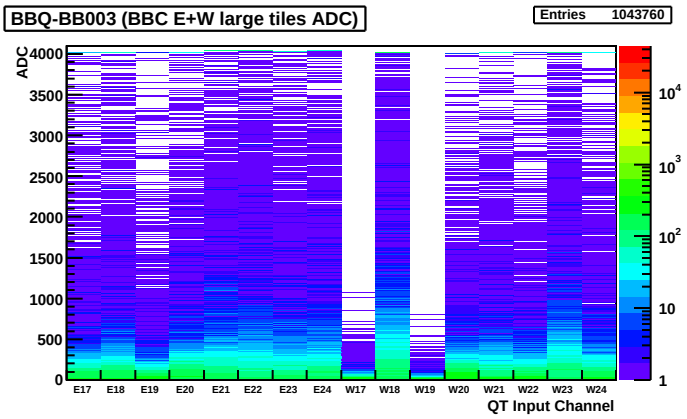

L1-TF201
Entries 391410
Threshold bits
39919
40199
40840
40726
39831
37777
25316
25036
24395
24509
25404
27458
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

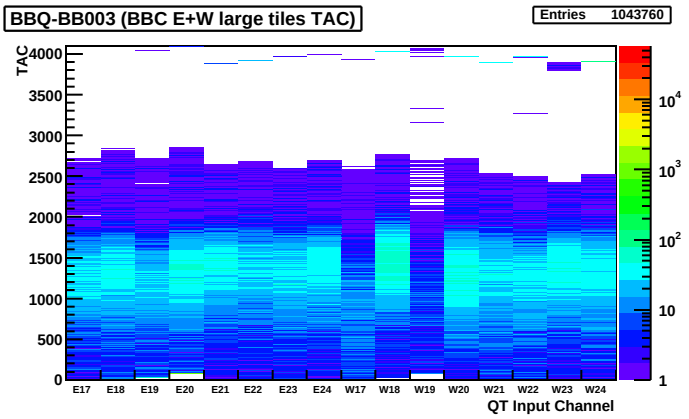

BBQ-BB001 (BBC east small tiles ADC)
Entries 1043760
ADC
QT Input Channel

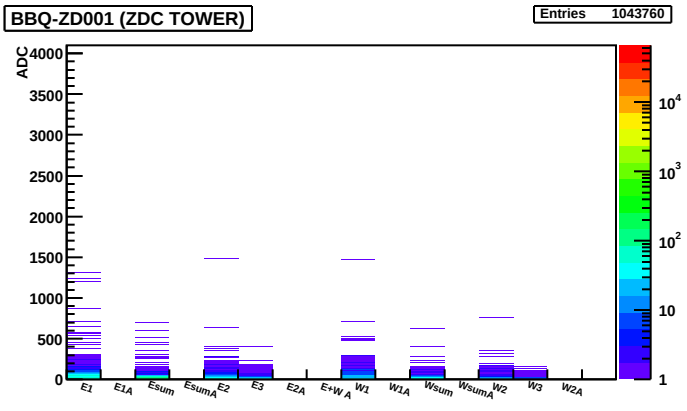

BBQ-BB001 (BBC east small tiles TAC)
Entries 1043760
TAC
QT Input Channel

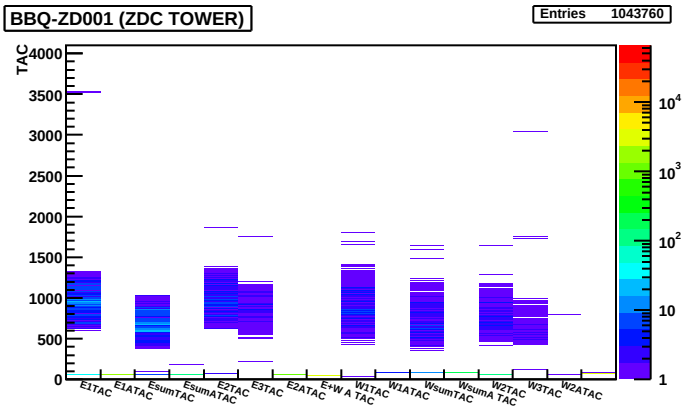

BBQ-BB002 (BBC west small tiles ADC)
Entries 1043760
ADC
QT Input Channel

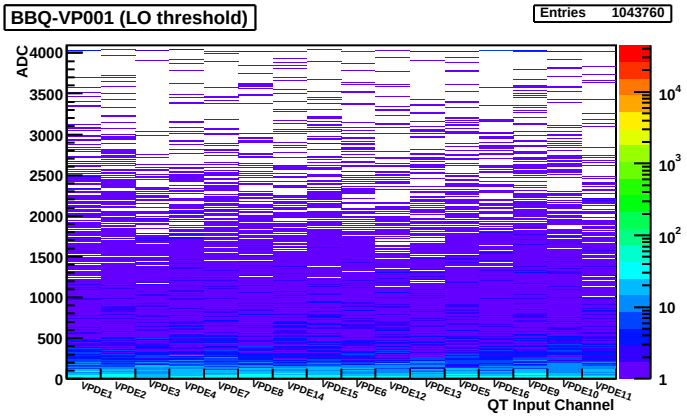

BBQ-BB002 (BBC west small tiles TAC)
Entries 1043760
TAC
QT Input Channel

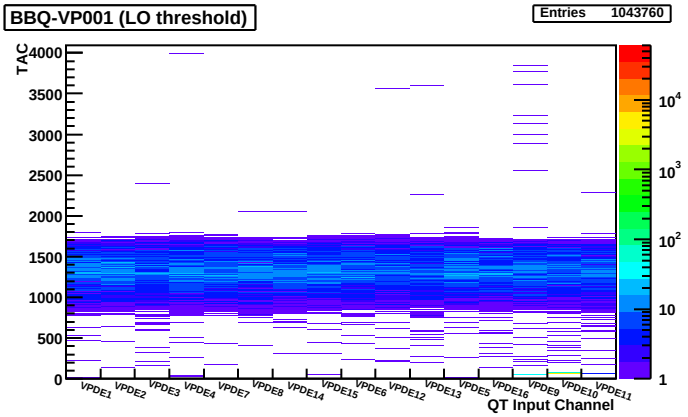

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1043760
ADC
QT Input Channel

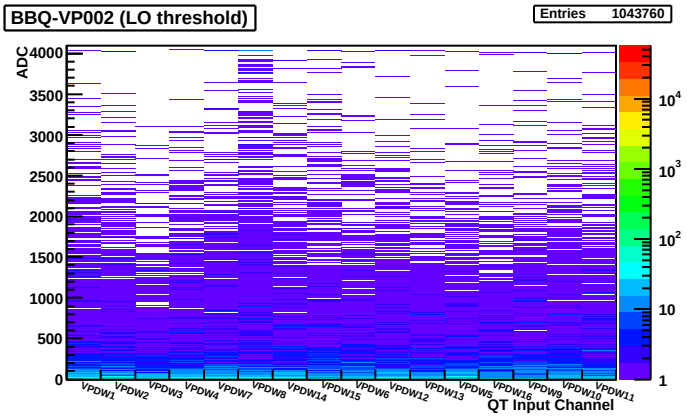

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1043760
TAC
QT Input Channel

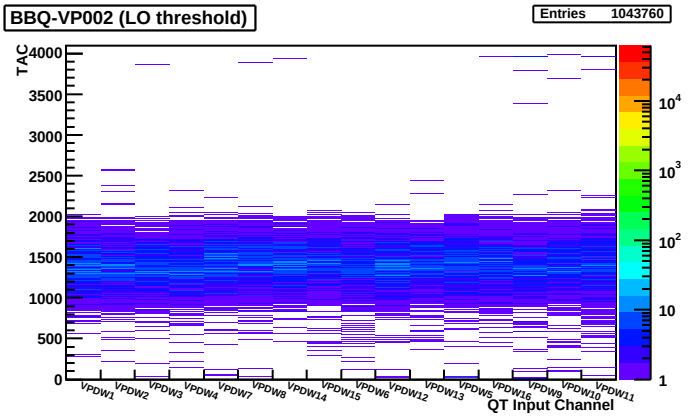

BBQ-ZD001 (ZDC TOWER)
Entries 1043760
ADC

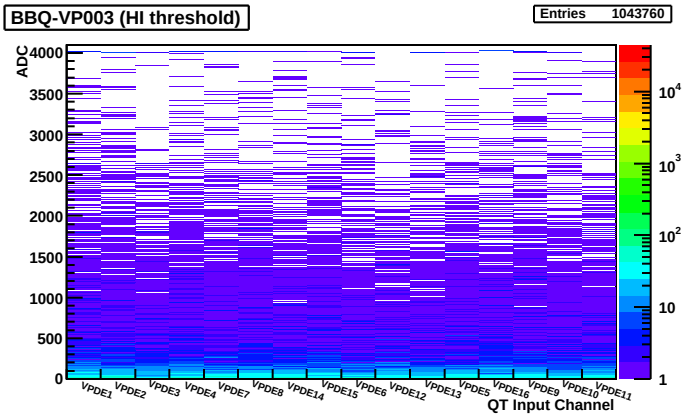

BBQ-ZD001 (ZDC TOWER)
Entries 1043760
TAC

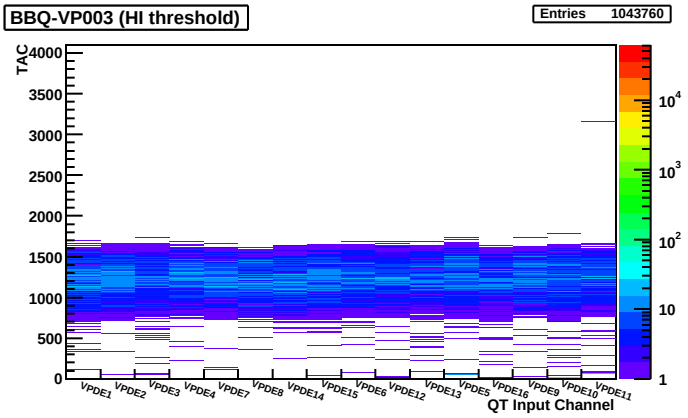

BBQ-VP001 (LO threshold)
Entries 1043760
ADC
QT Input Channel

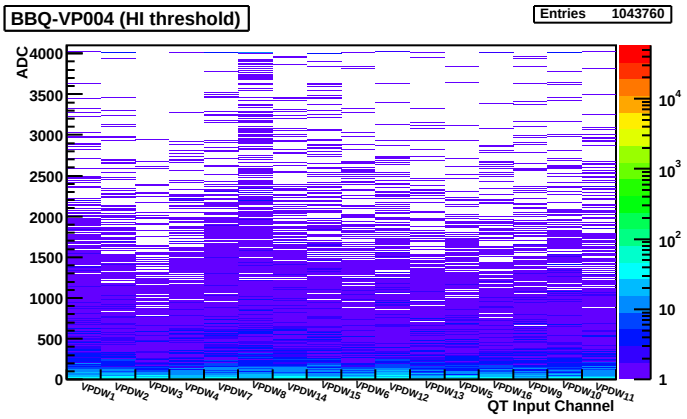

BBQ-VP001 (LO threshold)
Entries 1043760
TAC
QT Input Channel

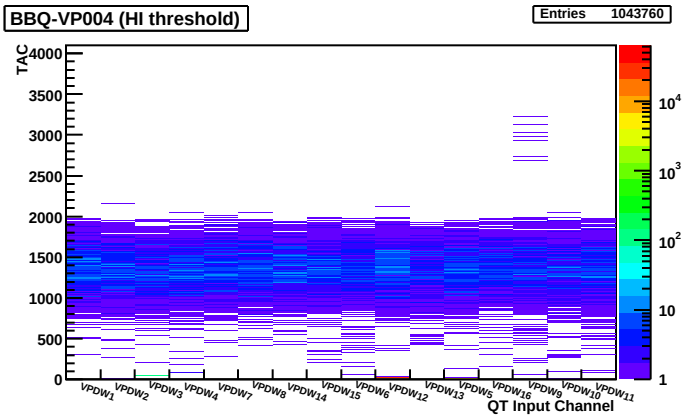

BBQ-VP002 (LO threshold)
Entries 1043760
ADC
QT Input Channel

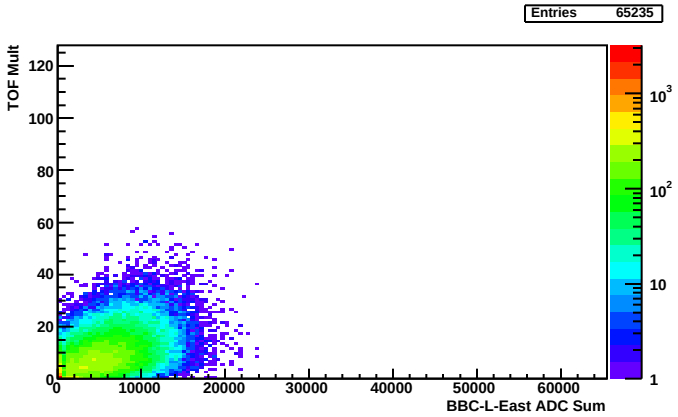

BBQ-VP002 (LO threshold)
Entries 1043760
TAC
QT Input Channel

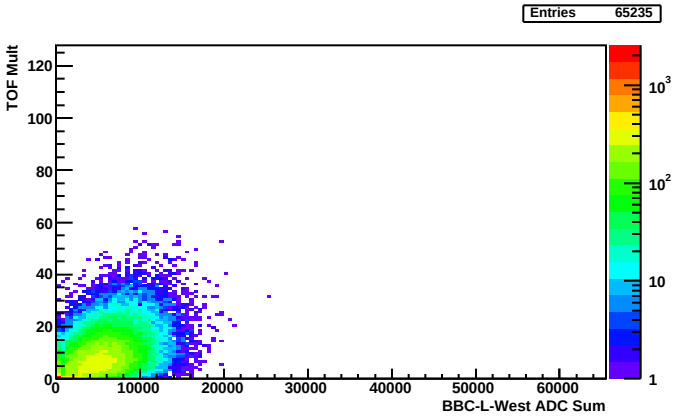

BBQ-VP003 (HI threshold)
Entries 1043760
ADC
QT Input Channel

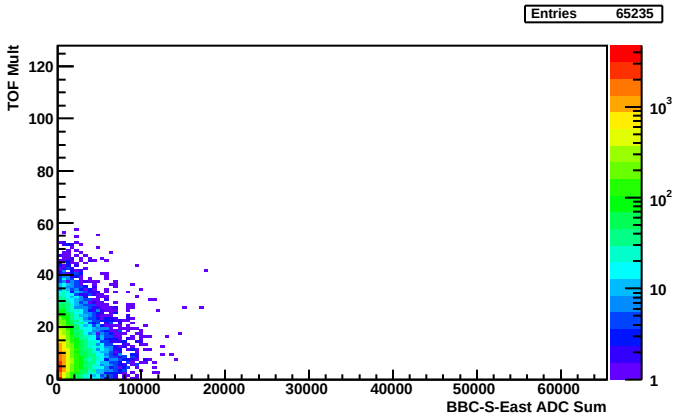

BBQ-VP003 (HI threshold)
Entries 1043760
TAC
QT Input Channel

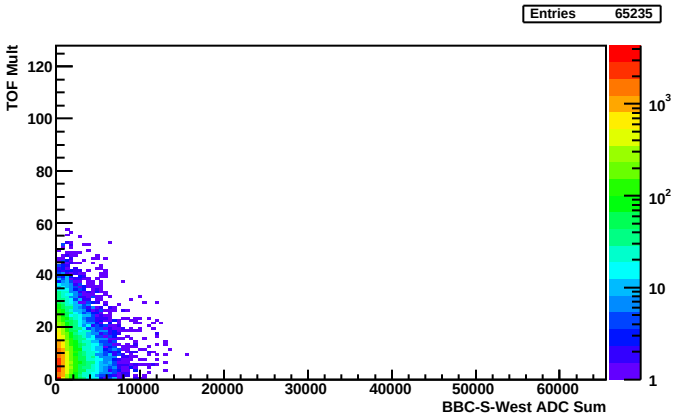

BBQ-VP004 (HI threshold)
Entries 1043760
ADC
QT Input Channel

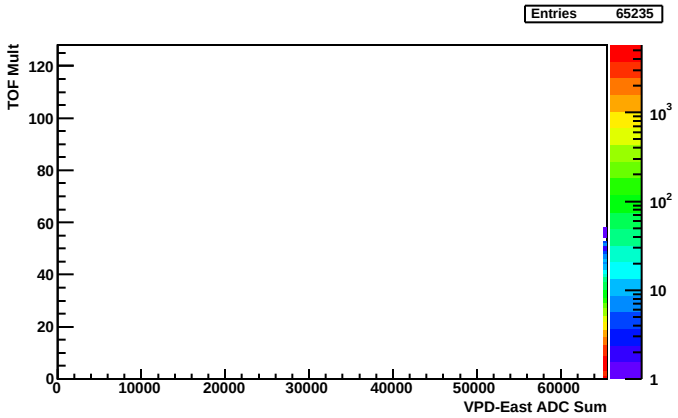

BBQ-VP004 (HI threshold)
Entries 1043760
TAC
QT Input Channel

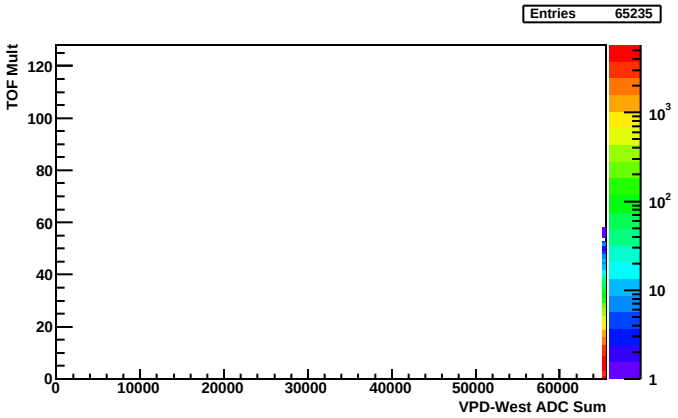

Entries 65235
TOF Mult
BBC-L-East ADC Sum

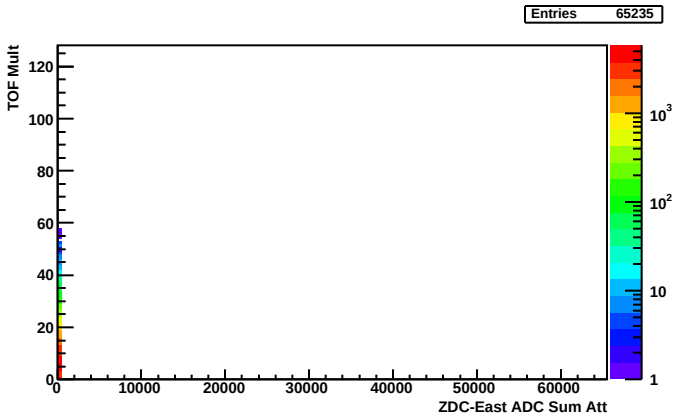

Entries 65235
TOF Mult
BBC-L-West ADC Sum

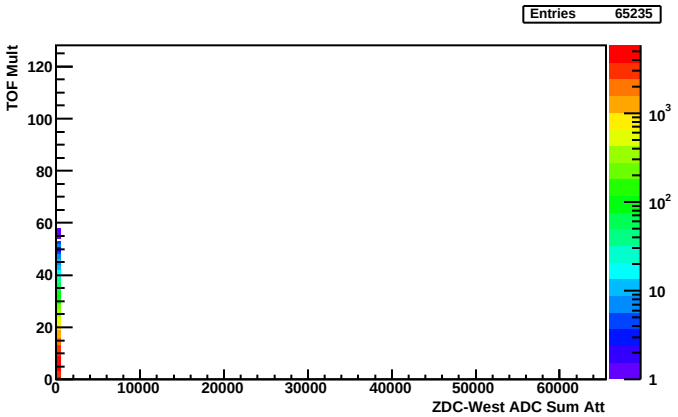

Entries 65235
TOF Mult
BBC-S-East ADC Sum

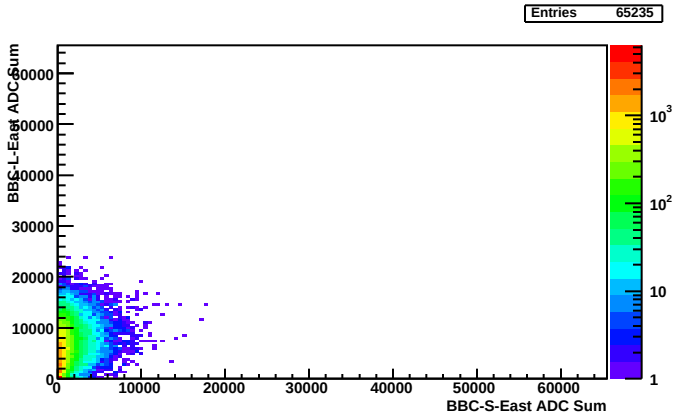

Entries 65235
TOF Mult
BBC-S-West ADC Sum

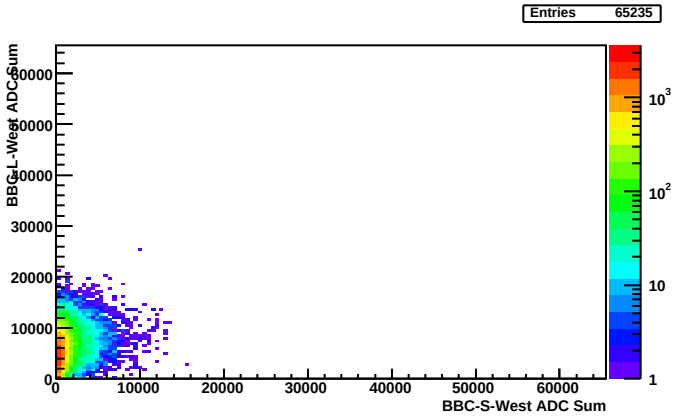

Entries 65235
TOF Mult
VPD-East ADC Sum

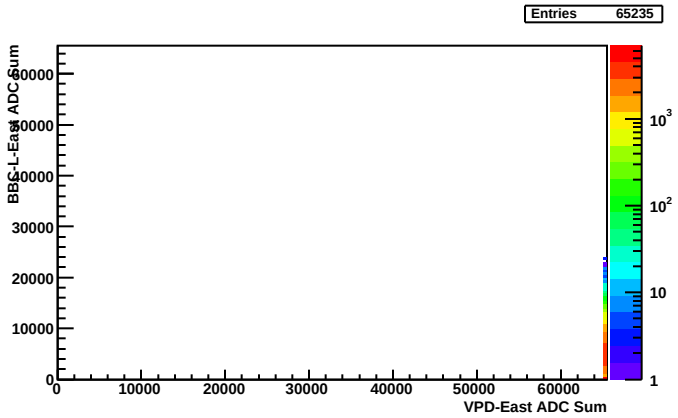

Entries 65235
TOF Mult
VPD-West ADC Sum

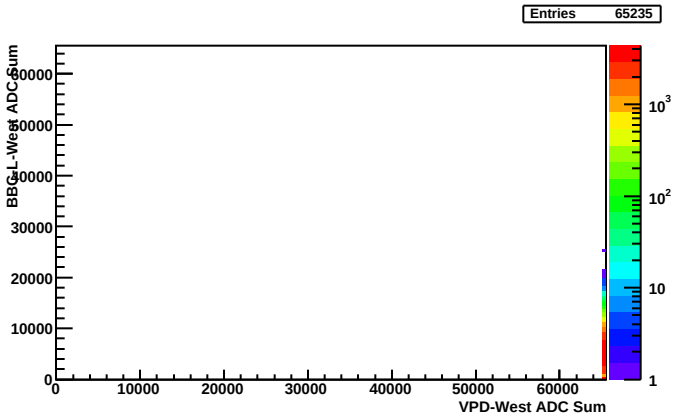

Entries 65235
TOF Mult
ZDC-East ADC Sum Att

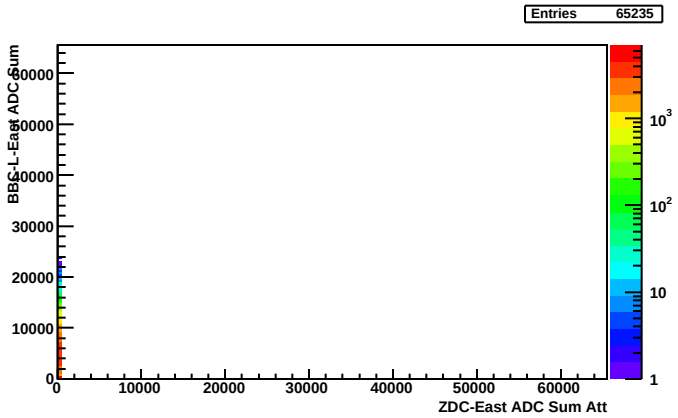

Entries 65235
TOF Mult
ZDC-West ADC Sum Att

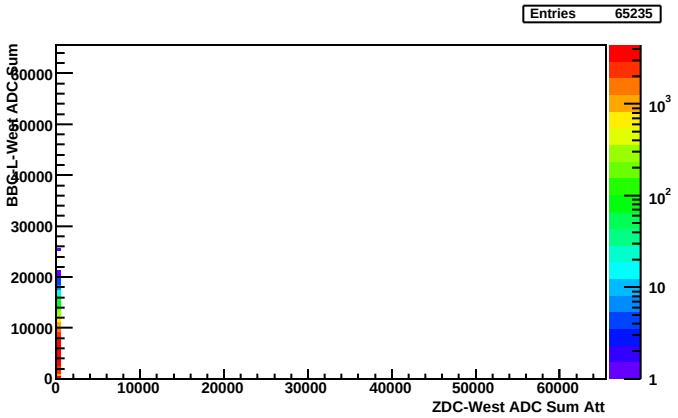

Entries 65235
BBC-L-East ADC Sum
BBC-S-East ADC Sum

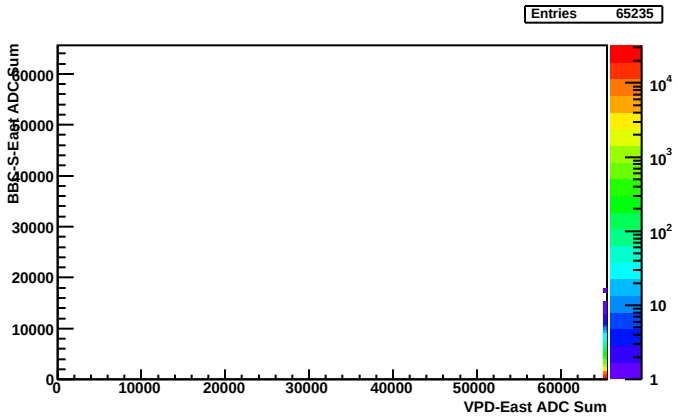

Entries 65235
BBC-L-West ADC Sum
BBC-S-West ADC Sum

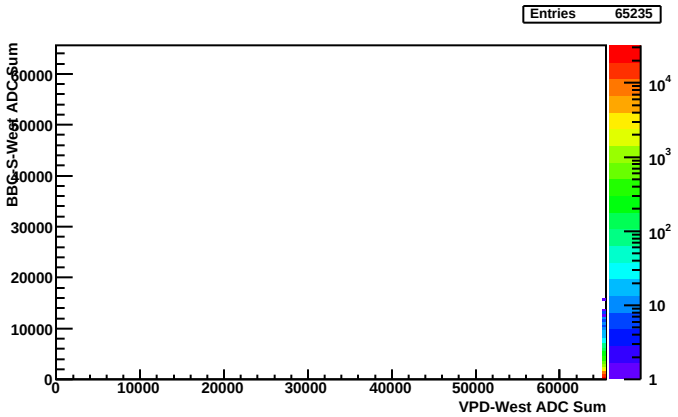

Entries 65235
BBC-L-East ADC Sum
VPD-East ADC Sum

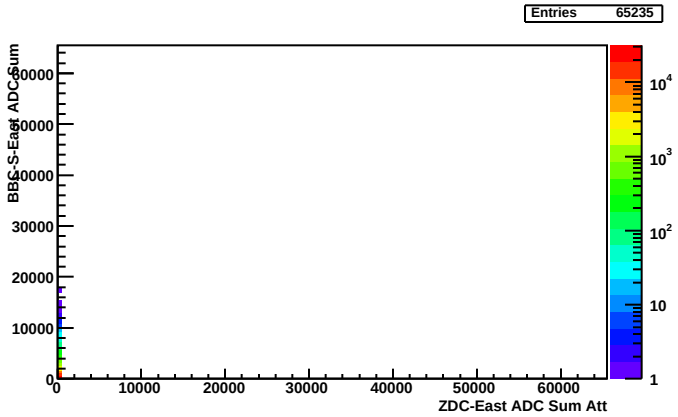

Entries 65235
BBC-L-West ADC Sum
VPD-West ADC Sum

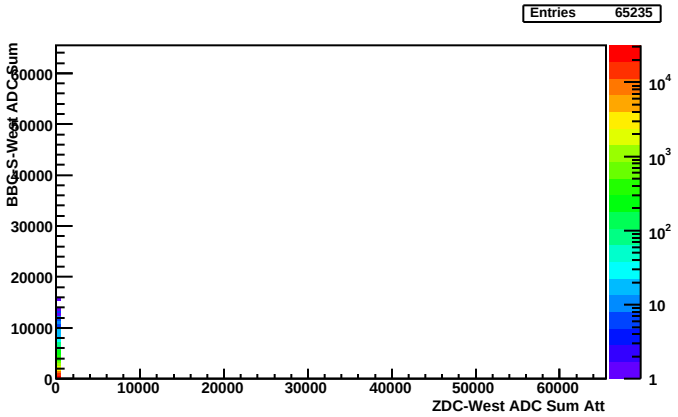

Entries 65235
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

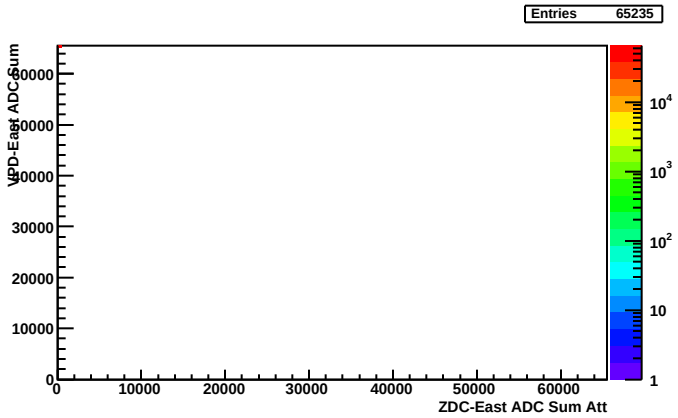

Entries 65235
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

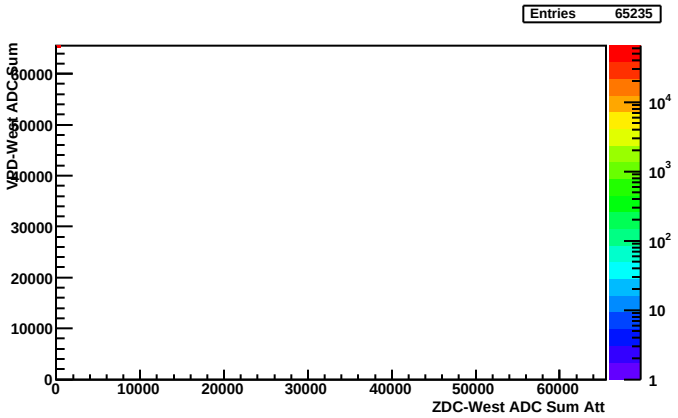

Entries 65235
BBC-S-East ADC Sum
VPD-East ADC Sum

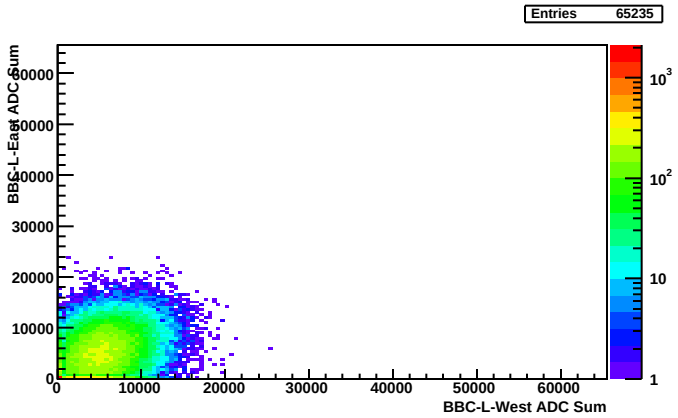

Entries 65235
BBC-S-West ADC Sum
VPD-West ADC Sum

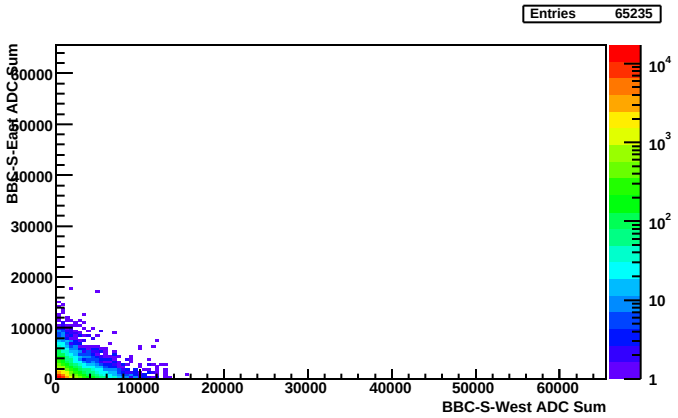

Entries 65235
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

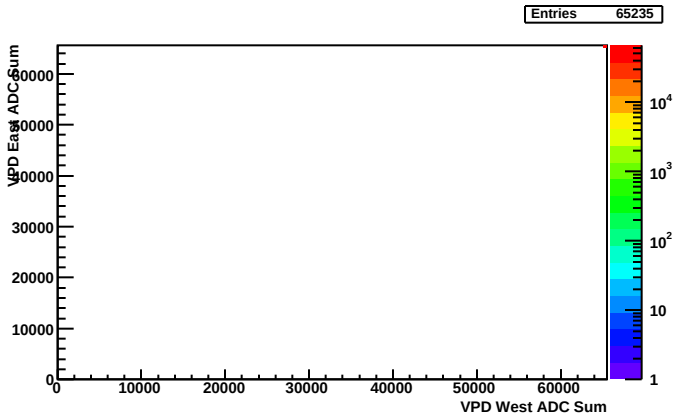

Entries 65235
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

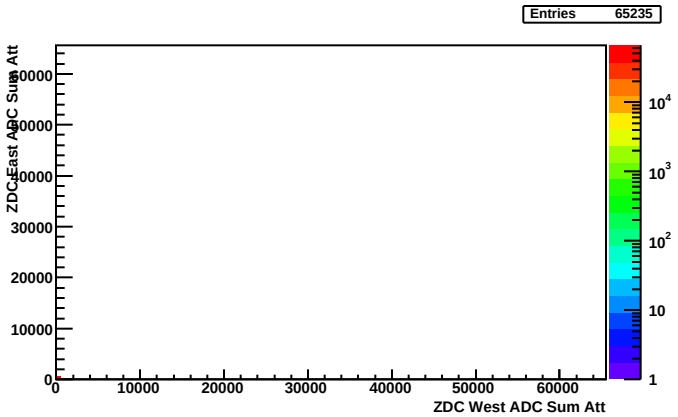

Entries 65235
VPD-East ADC Sum
ZDC-East ADC Sum Att

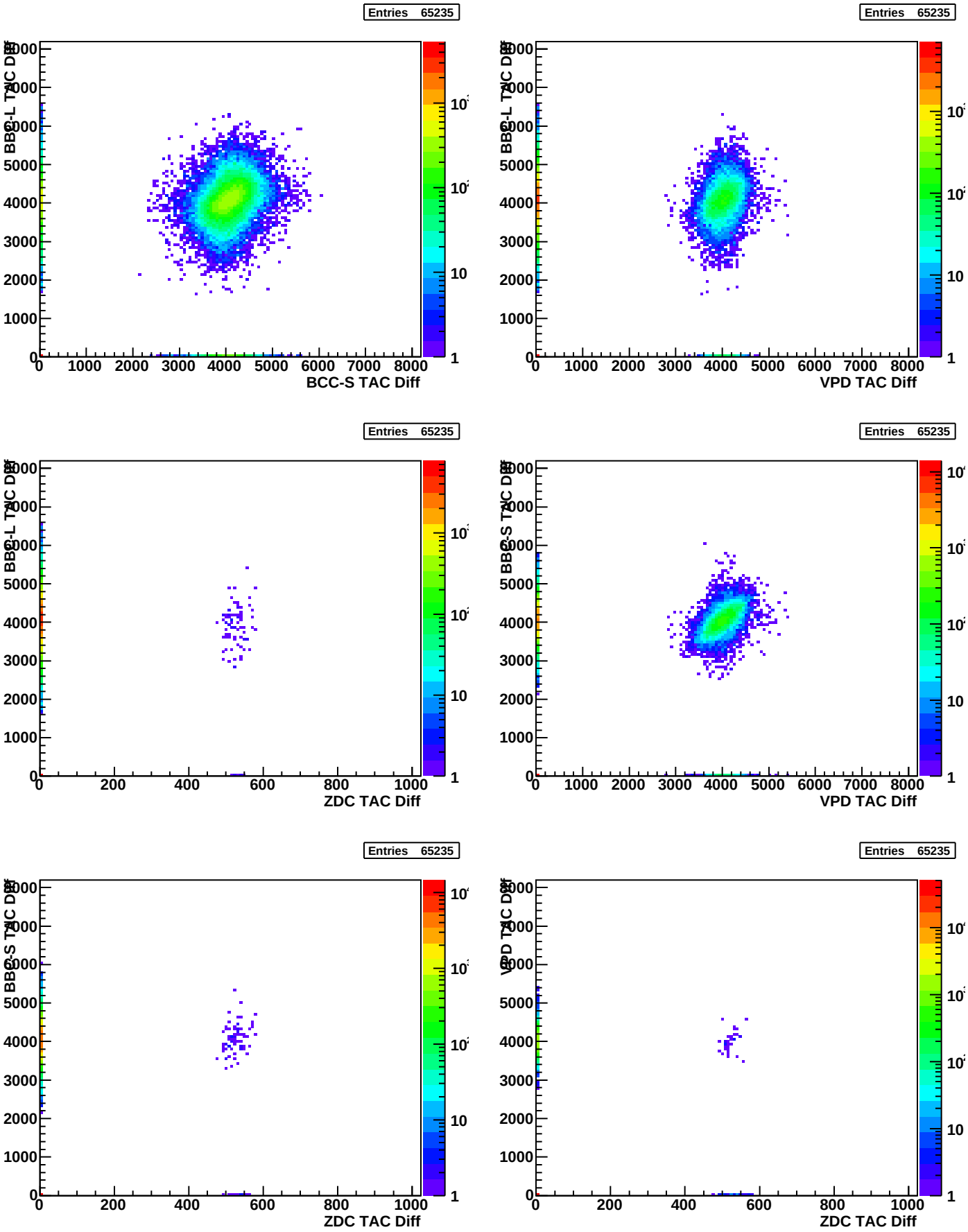

Entries 65235
VPD-West ADC Sum
ZDC-West ADC Sum Att

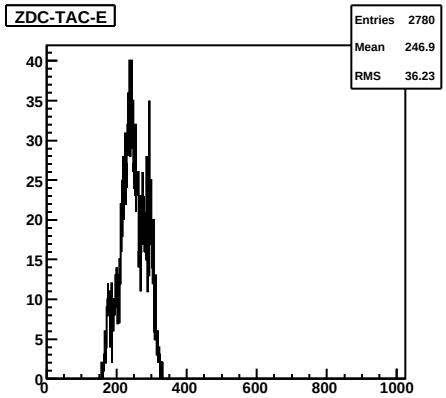

Entries 65235
BBC-L-East ADC Sum
BBC-L-West ADC Sum

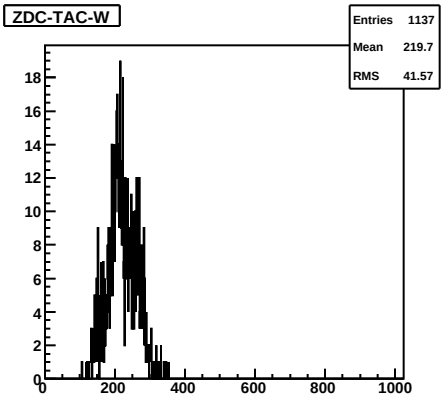

Entries 65235
BBC-S-East ADC Sum
BBC-S-West ADC Sum

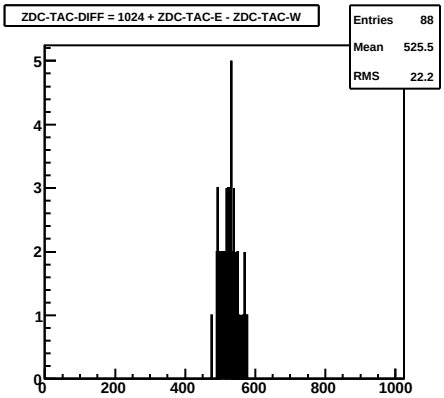

Entries 65235
VPD East ADC Sum
VPD West ADC Sum

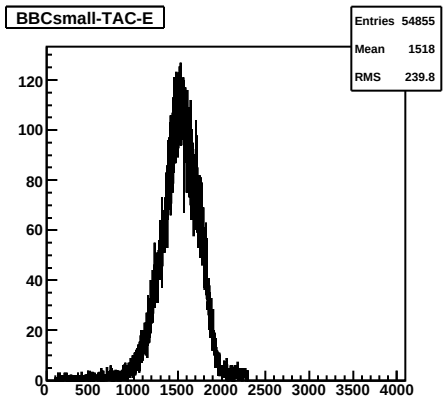

Entries 65235
ZDC East ADC Sum Att
ZDC West ADC Sum Att

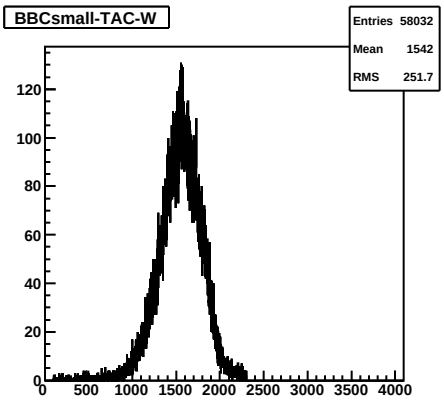

Entries 65235
BBC-L TAC Diff
BCC-S TAC Diff
Entries 65235
BBC-L TAC Diff
VPD TAC Diff
Entries 65235
BBC-L TAC Diff
ZDC TAC Diff
Entries 65235
BBC-S TAC Diff
VPD TAC Diff
Entries 65235
BBC-S TAC Diff
ZDC TAC Diff
Entries 65235
VPD TAC Diff
ZDC TAC Diff

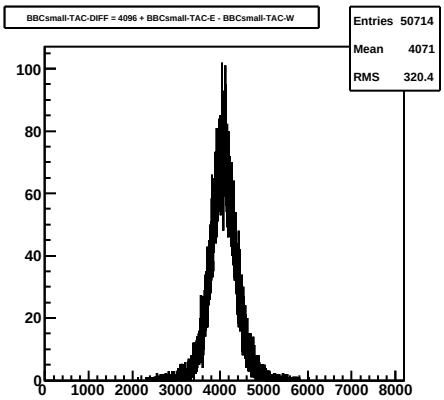

ZDC-TAC-E
Entries 2780
Mean 246.9
RMS 36.23

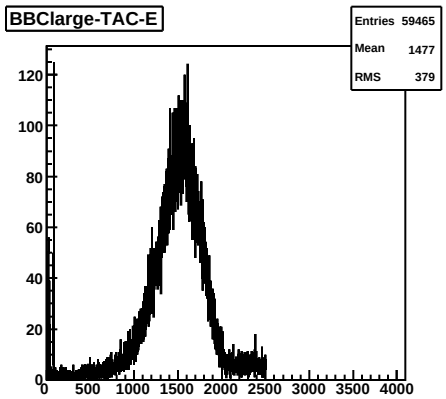

ZDC-TAC-W
Entries 1137
Mean 219.7
RMS 41.57

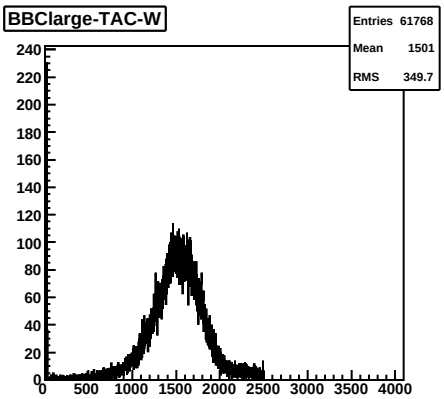

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 88
Mean 525.5
RMS 22.2

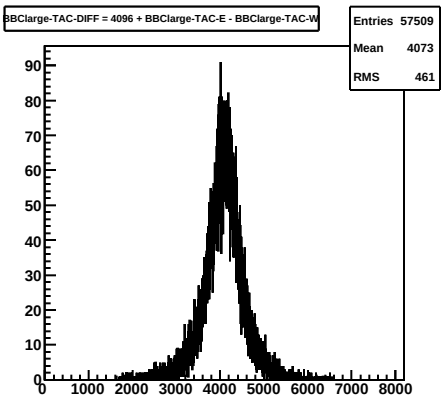

BBCsmall-TAC-E
Entries 54855
Mean 1518
RMS 239.8

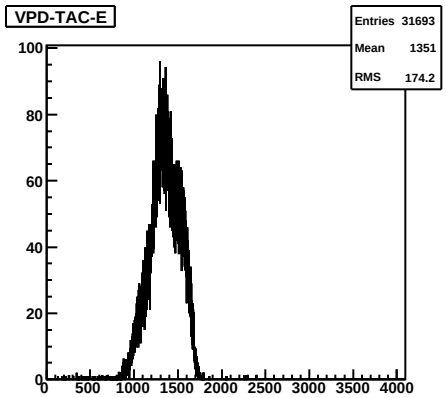

BBCsmall-TAC-W
Entries 58032
Mean 1542
RMS 251.7

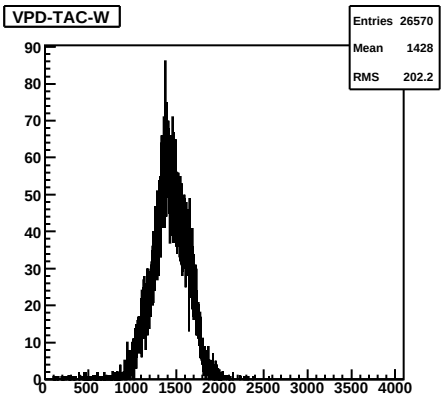

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 50714
Mean 4071
RMS 320.4

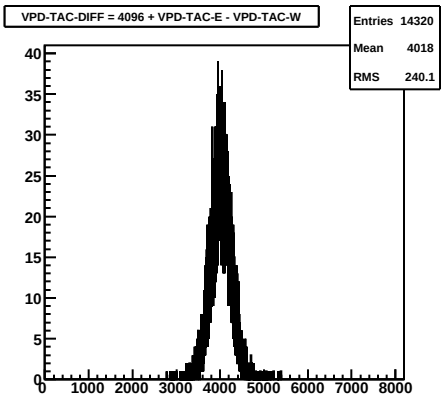

BBClarge-TAC-E
Entries 59465
Mean 1477
RMS 379

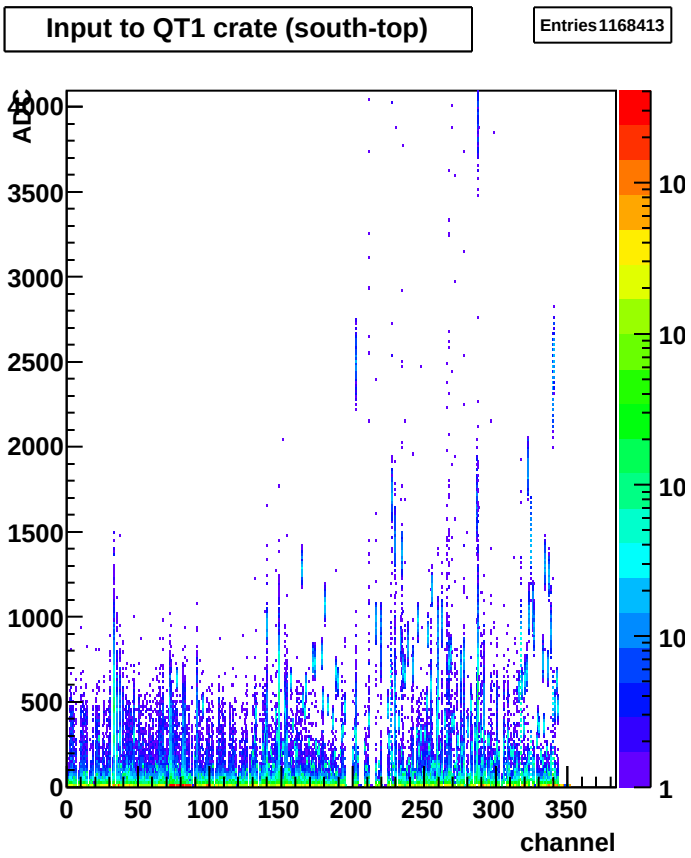

BBClarge-TAC-W
Entries 61768
Mean 1501
RMS 349.7

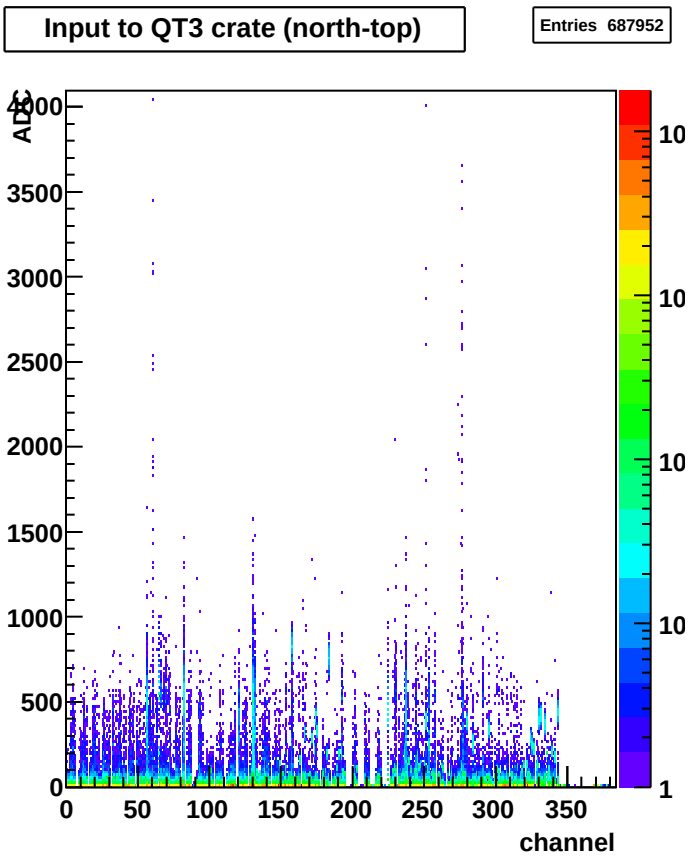

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 57509
Mean 4073
RMS 461

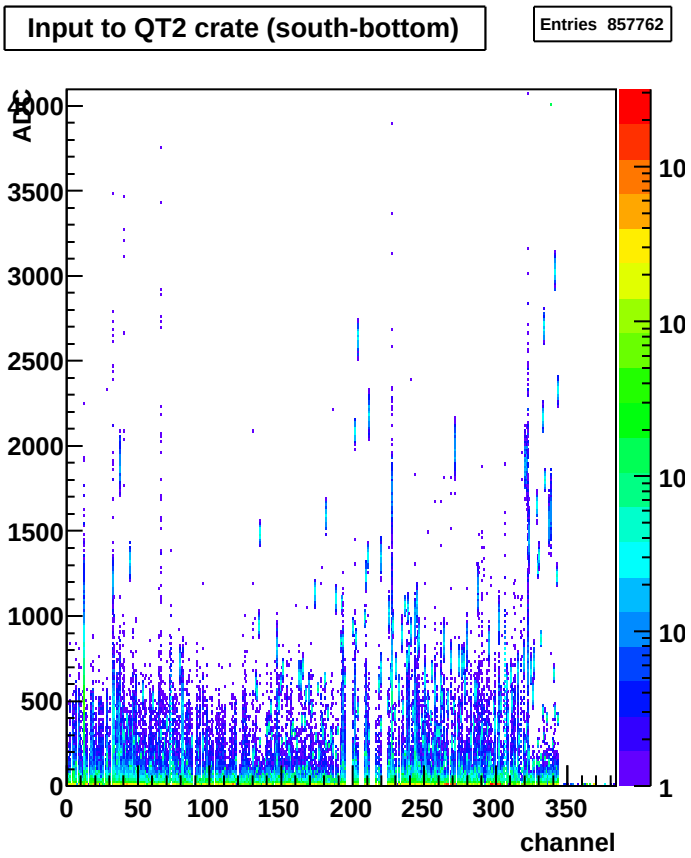

VPD-TAC-E
Entries 31693
Mean 1351
RMS 174.2

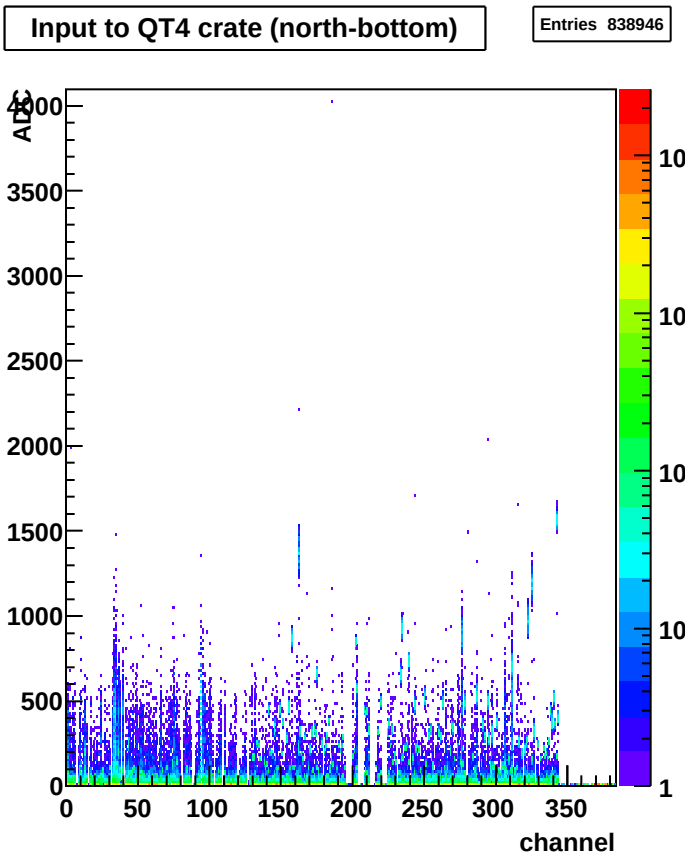

VPD-TAC-W
Entries 26570
Mean 1428
RMS 202.2

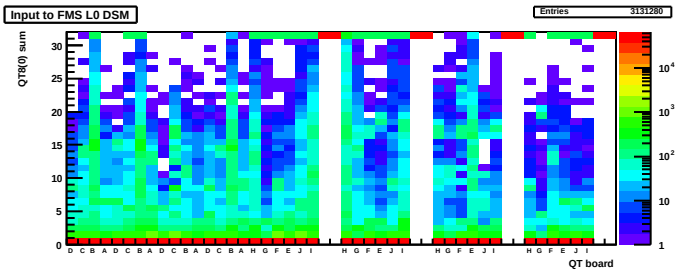

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 14320
Mean 4018
RMS 240.1

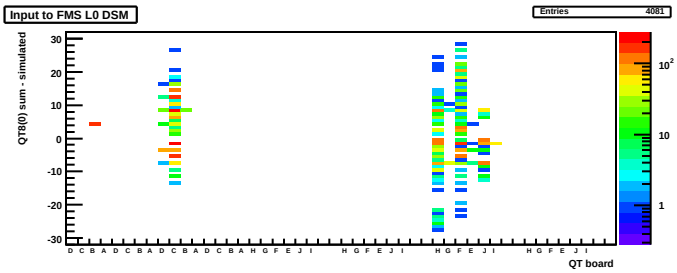

Input to QT1 crate (south-top)
Entries 1168413
ADC
channel

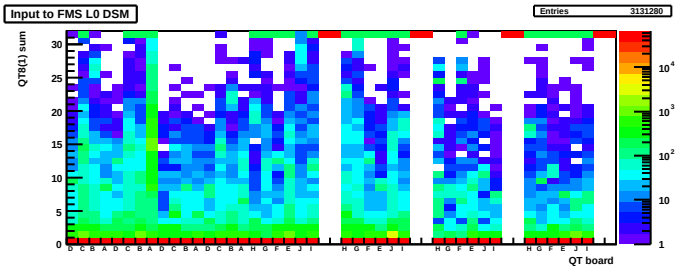

Input to QT3 crate (north-top)
Entries 687952
ADC
channel

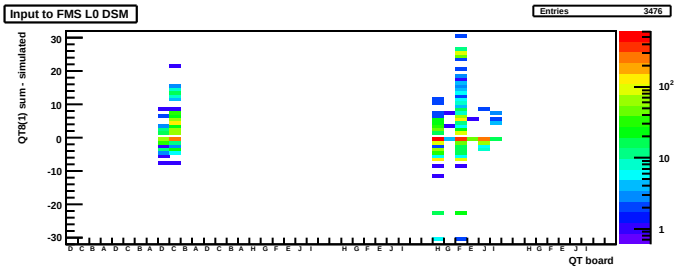

Input to QT2 crate (south-bottom)
Entries 857762
ADC
channel

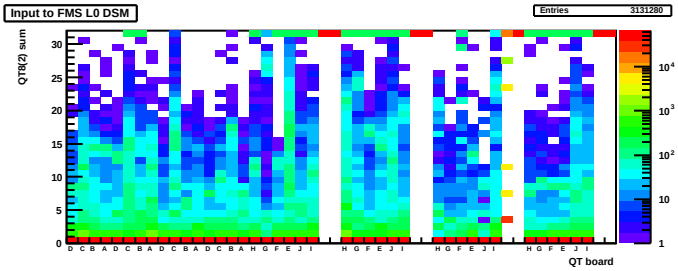

Input to QT4 crate (north-bottom)
Entries 838946
ADC
channel

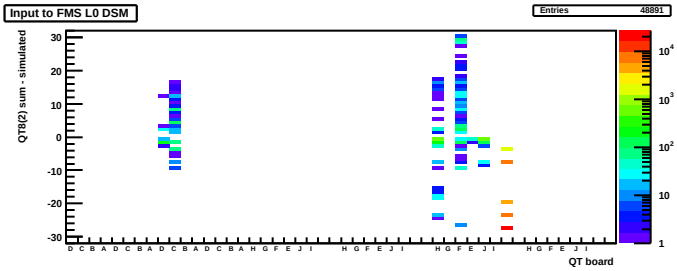

Input to FMS L0 DSM
Entries 3131280
QT8(0) sum
QT board

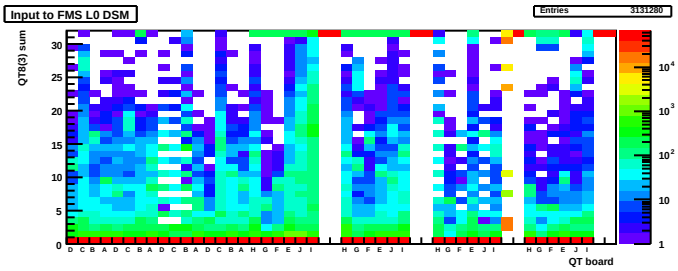

Input to FMS L0 DSM
Entries 4081
QT8(0) sum - simulated
QT board

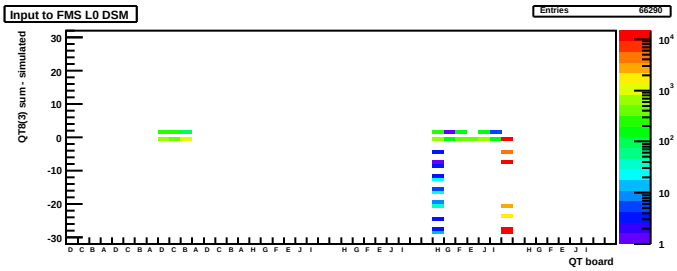

Input to FMS L0 DSM
Entries 3131280
QT8(1) sum
QT board

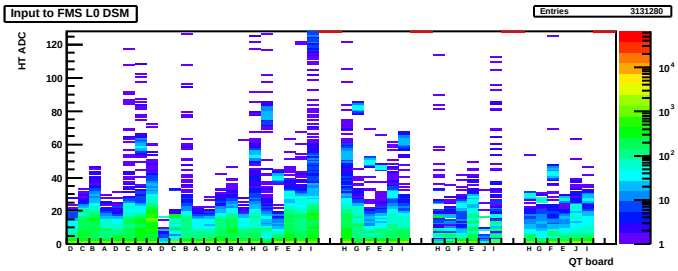

Input to FMS L0 DSM
Entries 3476
QT8(1) sum - simulated
QT board

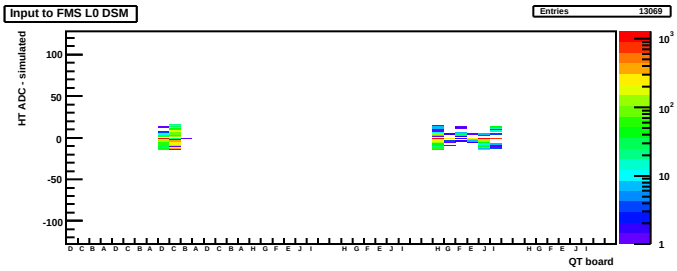

Input to FMS L0 DSM
Entries 3131280
QT8(2) sum
QT board

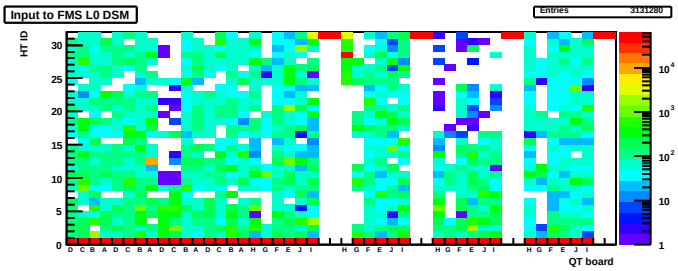

Input to FMS L0 DSM
Entries 48891
QT8(2) sum - simulated
QT board

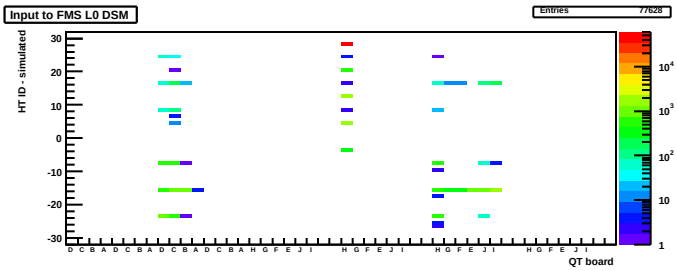

Input to FMS L0 DSM
Entries 3131280
QT8(3) sum
QT board

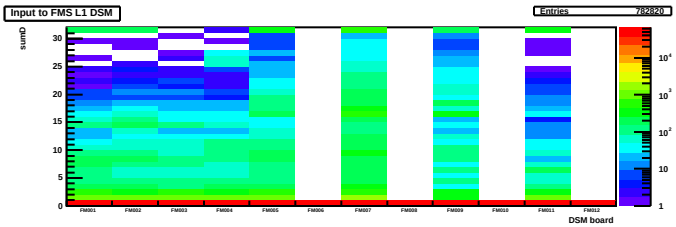

Input to FMS L0 DSM
Entries 66290
QT8(3) sum - simulated
QT board

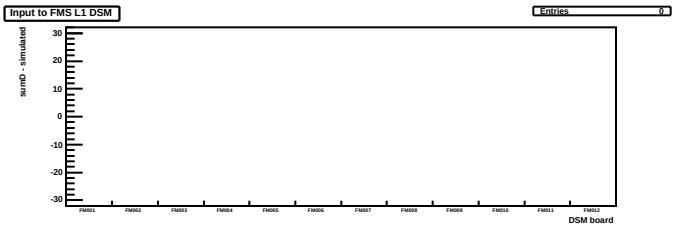

Input to FMS L0 DSM
Entries 3131280
HT ADC
QT board

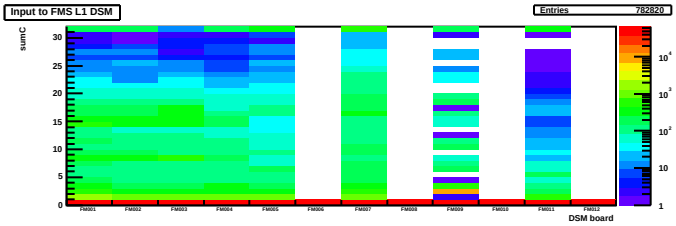

Input to FMS L0 DSM
Entries 13069
HT ADC - simulated
QT board

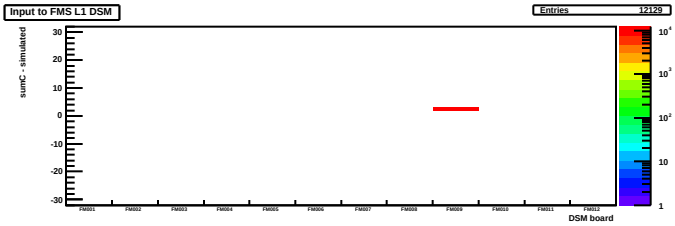

Input to FMS L0 DSM
Entries 3131280
HT ID
QT board

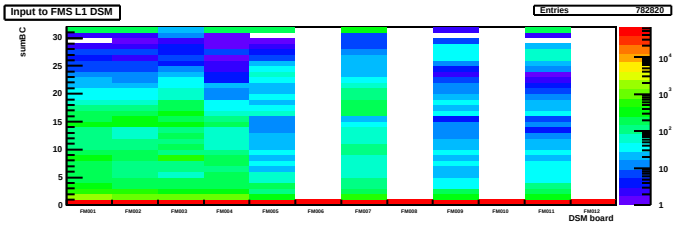

Input to FMS L0 DSM
Entries 77628
HT ID - simulated
QT board

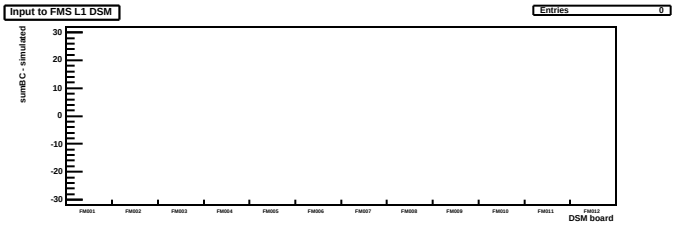

Input to FMS L1 DSM
Entries 782820
sumD
DSM board

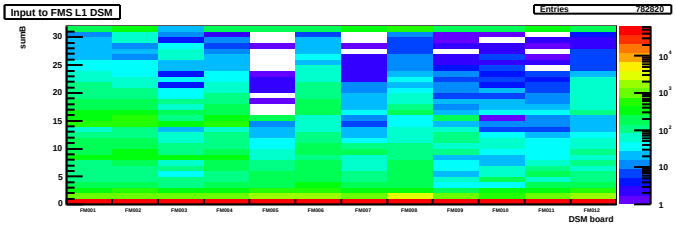

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

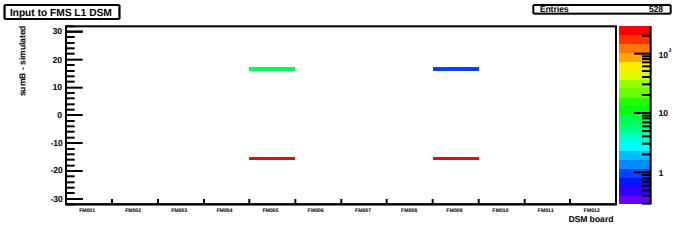

Input to FMS L1 DSM
Entries 782820
sumC
DSM board

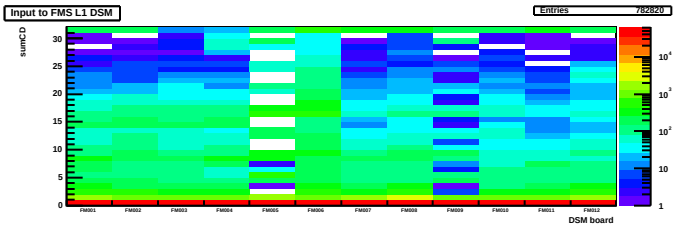

Input to FMS L1 DSM
Entries 12129
sumC - simulated
DSM board

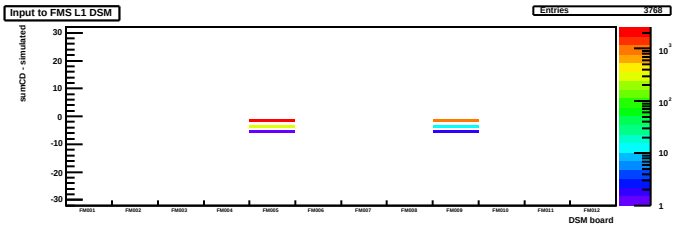

Input to FMS L1 DSM
Entries 782820
sumBC
DSM board

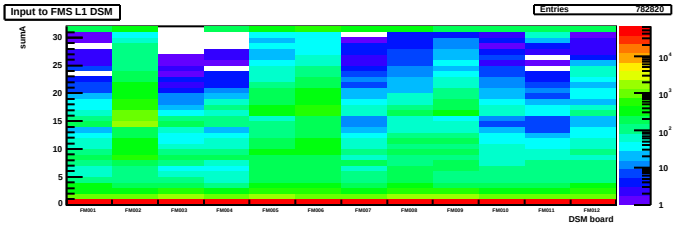

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

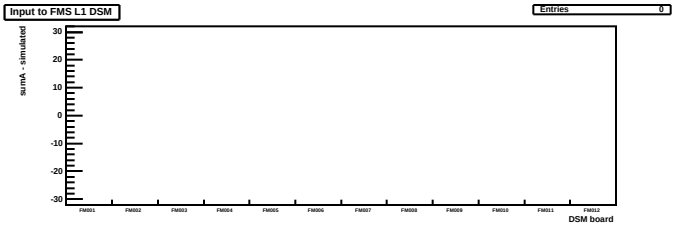

Input to FMS L1 DSM
Entries 782820
sumB
DSM board

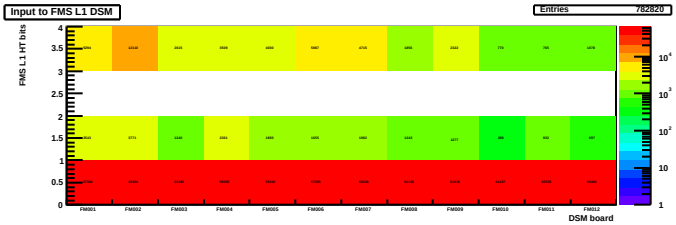

Input to FMS L1 DSM
Entries 528
sumB - simulated
DSM board

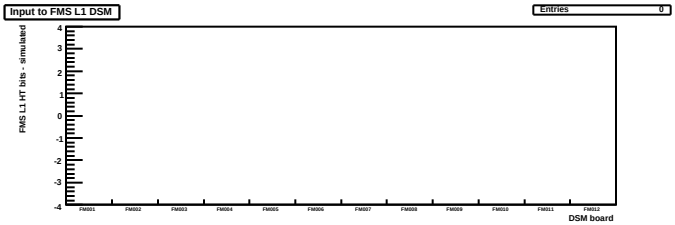

Input to FMS L1 DSM
Entries 782820
sumCD
DSM board

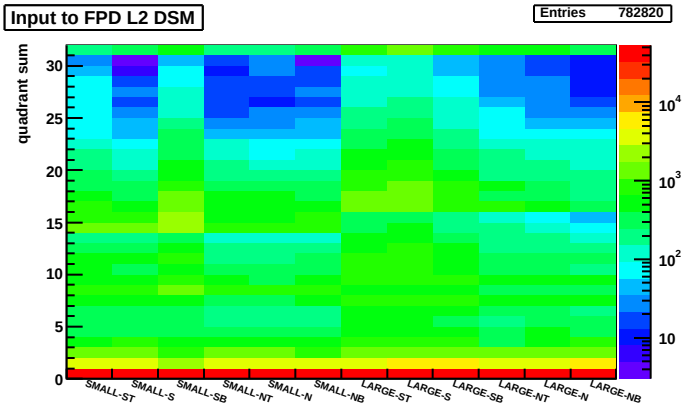

Input to FMS L1 DSM
Entries 3768
sumCD - simulated
DSM board

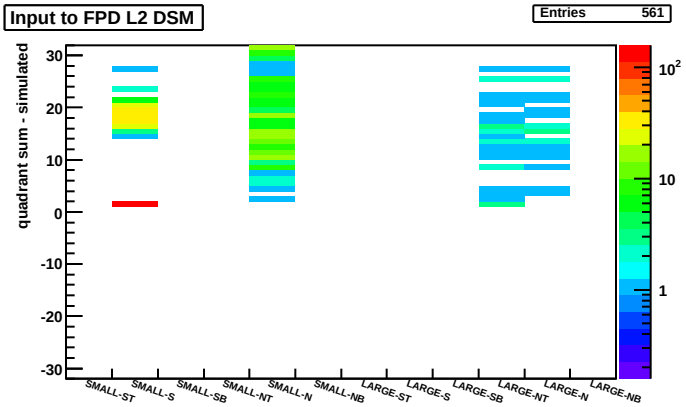

Input to FMS L1 DSM
Entries 782820
sumA
DSM board

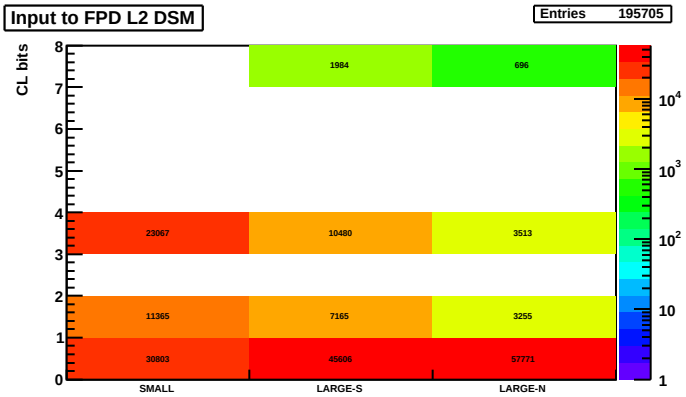

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

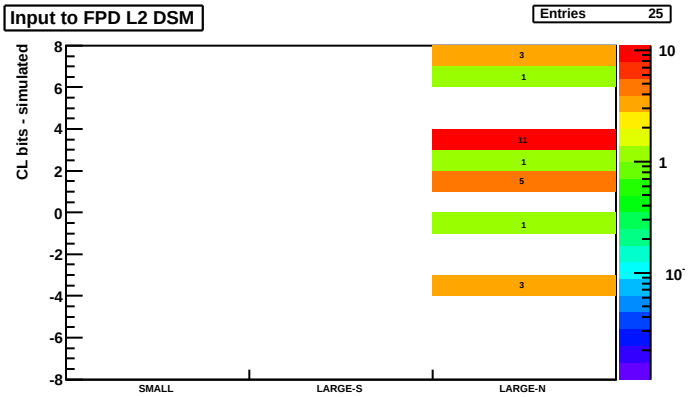

Input to FMS L1 DSM
Entries 782820
FMS L1 HT bits
DSM board

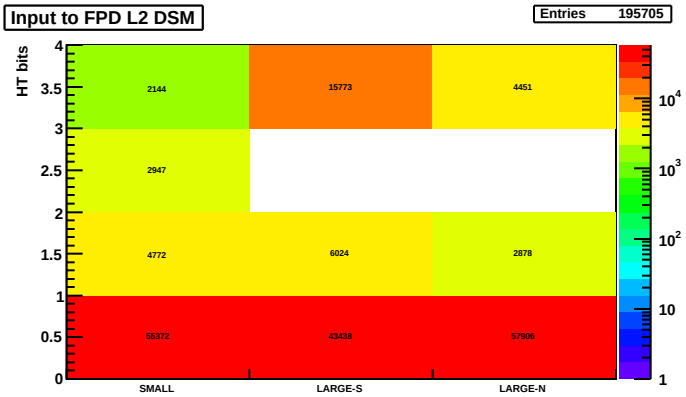

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

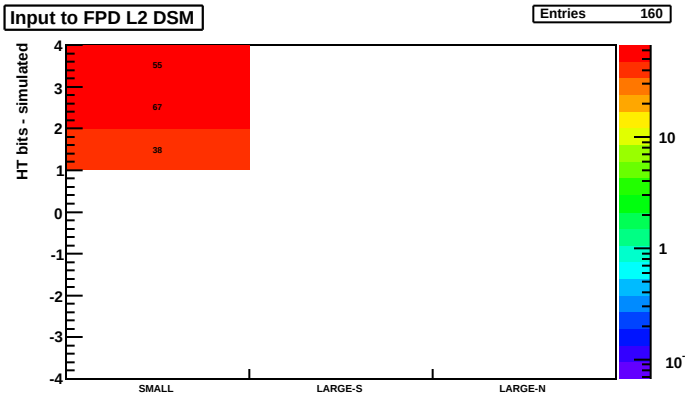

Input to FPD L2 DSM
Entries 782820
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

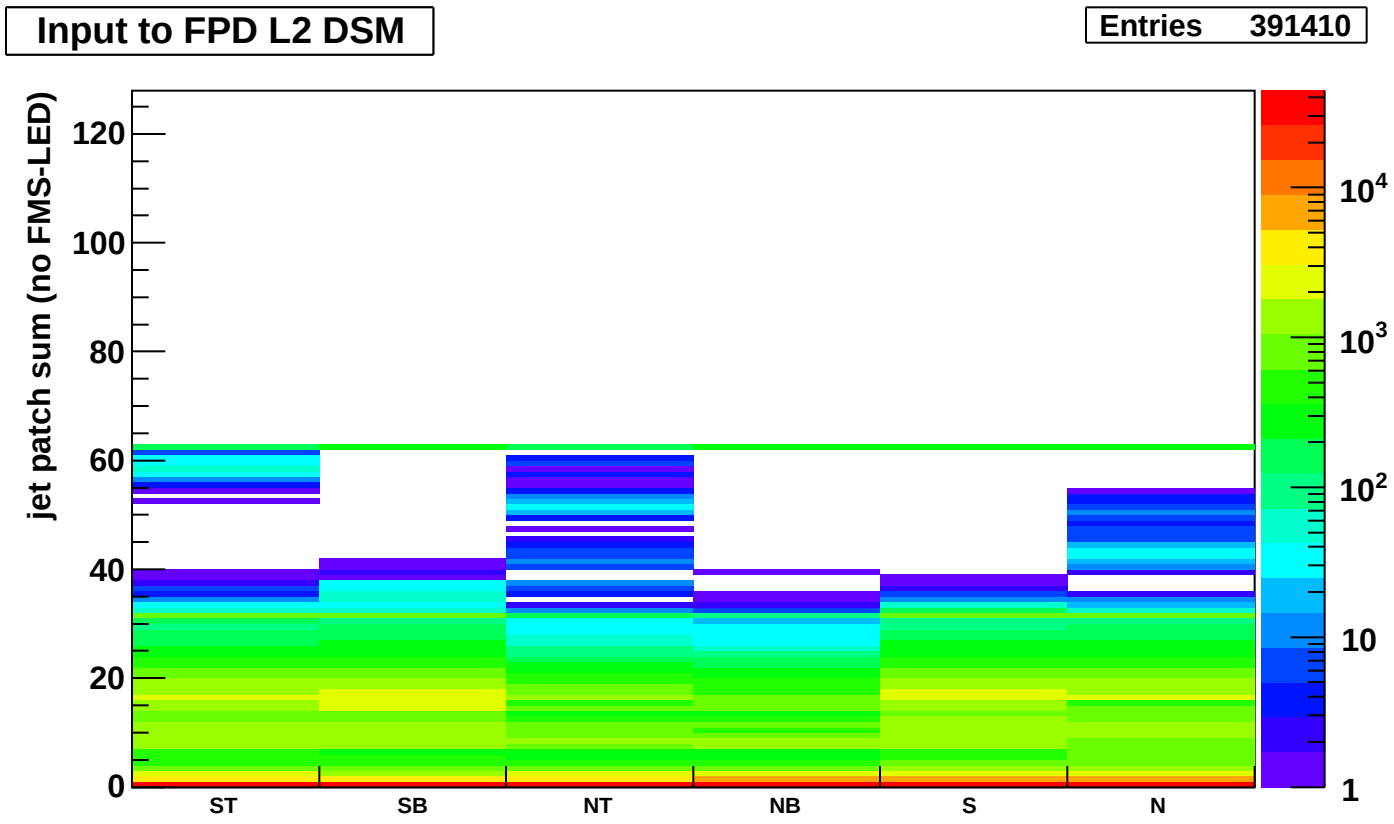

Input to FPD L2 DSM
Entries 561
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

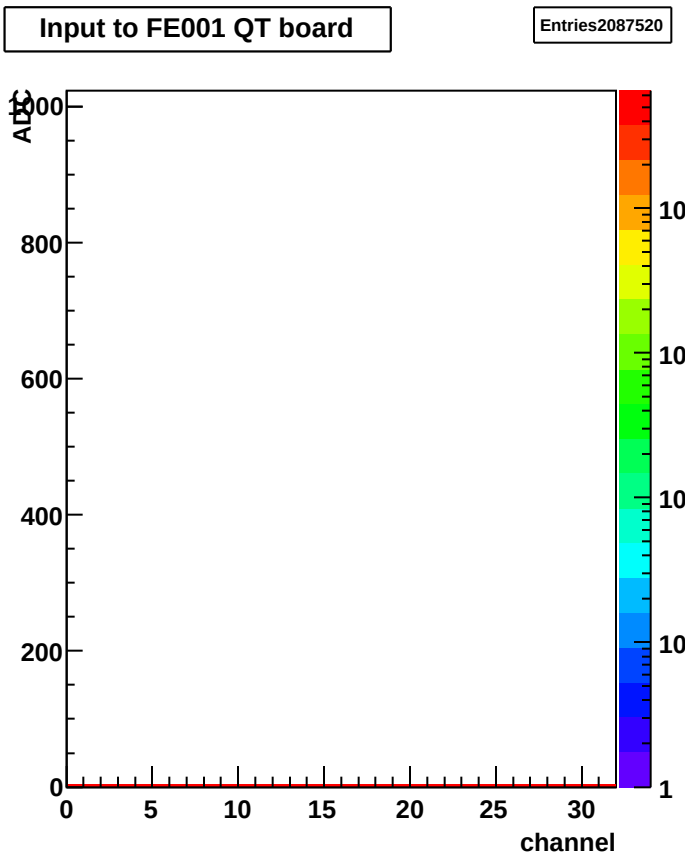

Input to FPD L2 DSM
Entries 195705
CL bits
1984
696
23067
10480
3513
11365
7165
3255
30803
45606
57771
SMALL
LARGE-S
LARGE-N

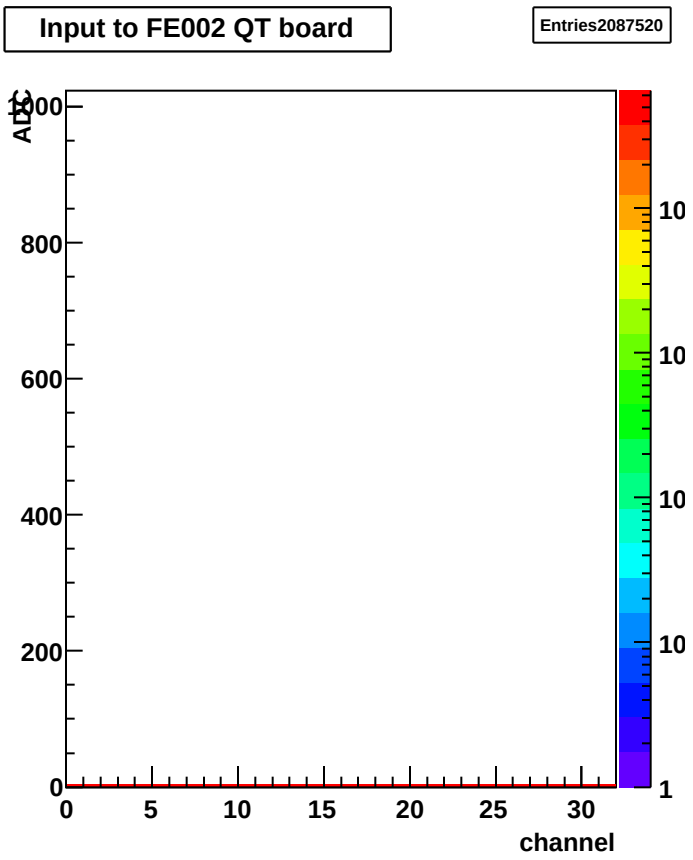

Input to FPD L2 DSM
Entries 25
CL bits - simulated
3
1
11
1
5
1
3
SMALL
LARGE-S
LARGE-N

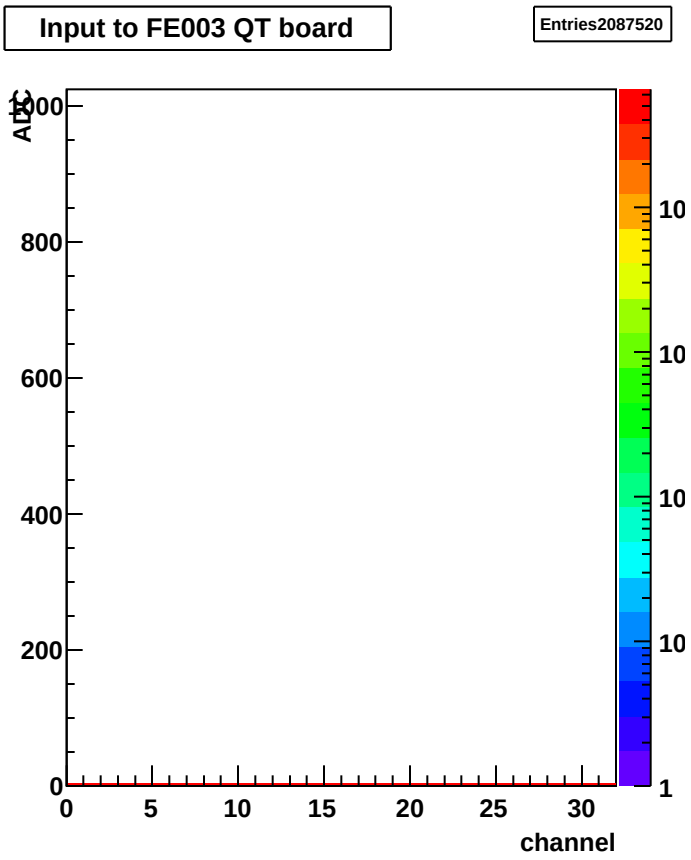

Input to FPD L2 DSM
Entries 195705
HT bits
2144
15773
4451
2947
4772
6024
2878
55372
43438
57906
SMALL
LARGE-S
LARGE-N

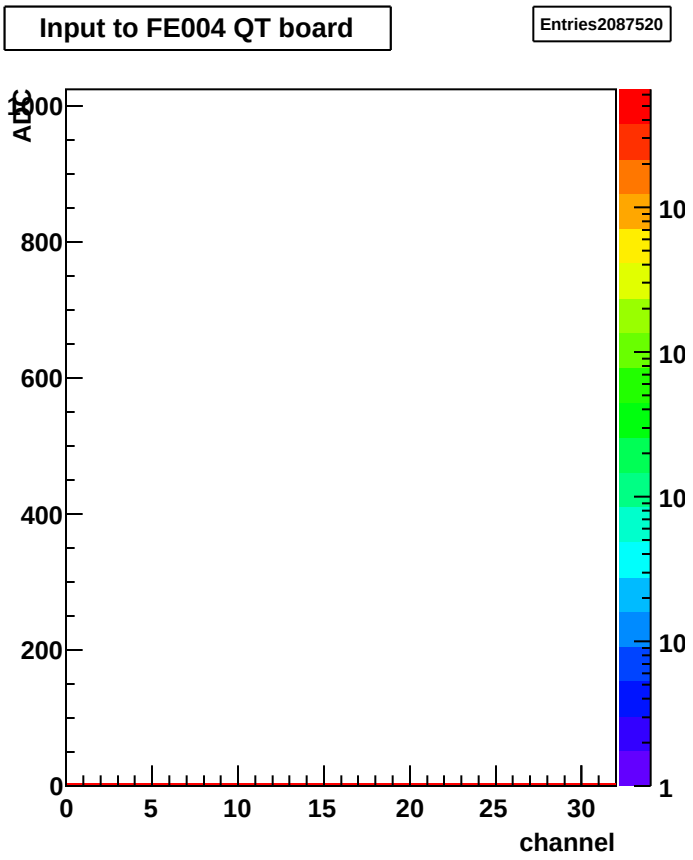

Input to FPD L2 DSM
Entries 160
HT bits - simulated
55
67
38
SMALL
LARGE-S
LARGE-N

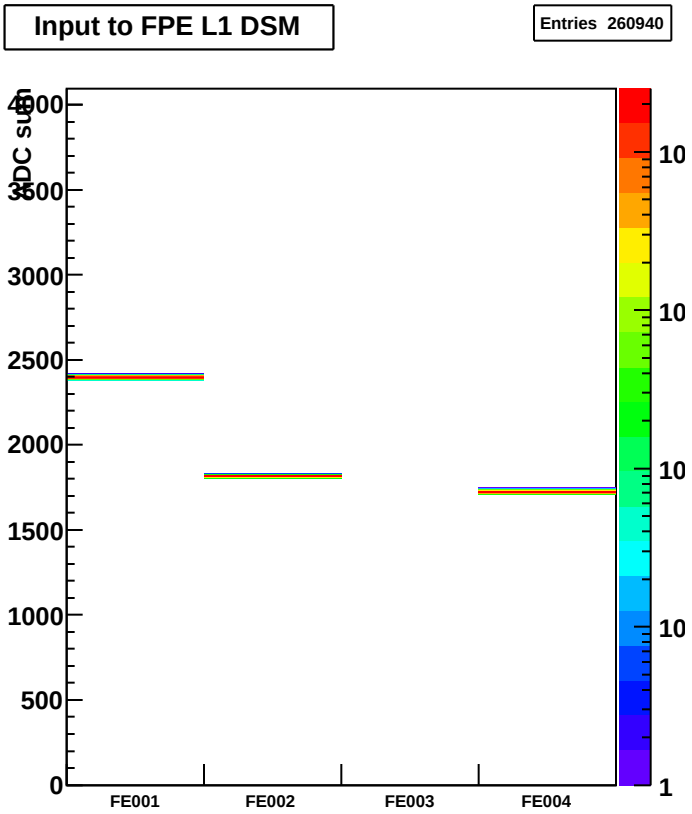

Input to FPD L2 DSM
Entries 391410
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4


Input to FE001 QT board
Entries2087520
ADC
channel

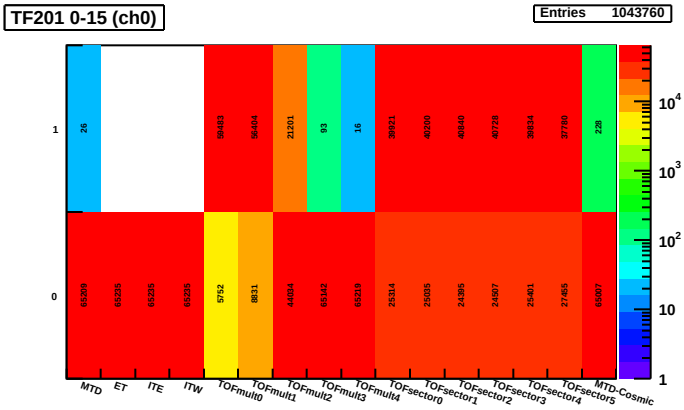

Input to FE002 QT board
Entries2087520
ADC
channel


Input to FE003 QT board
Entries2087520
ADC
channel

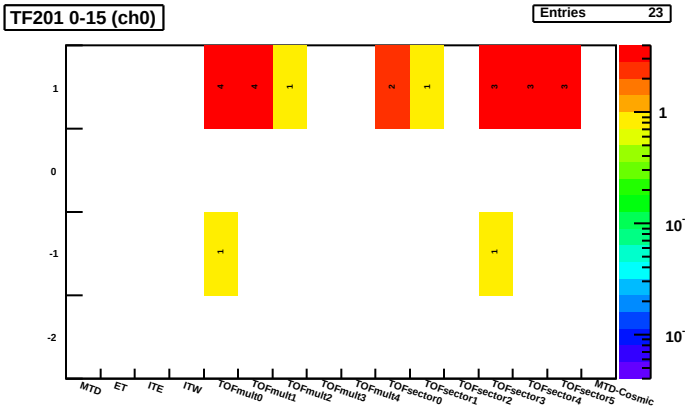

Input to FE004 QT board
Entries2087520
ADC
channel


Input to FPE L1 DSM
Entries 260940
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

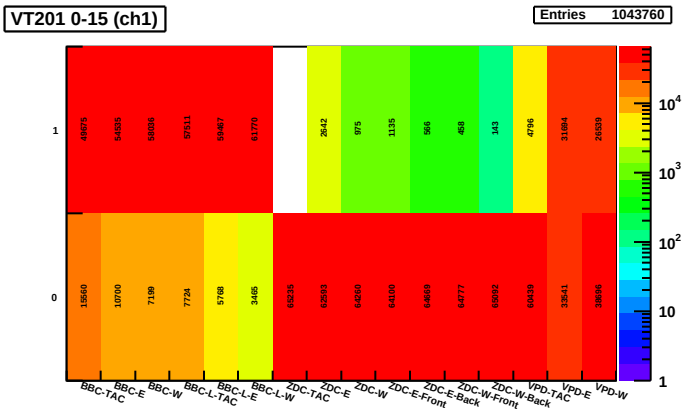
TF201 0-15 (ch0)



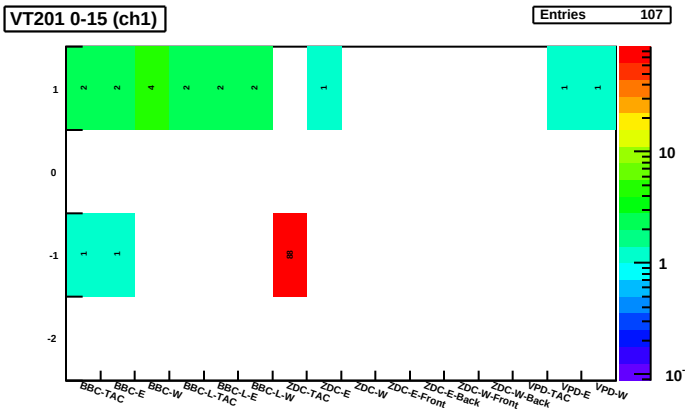
TF201 0-15 (ch0)



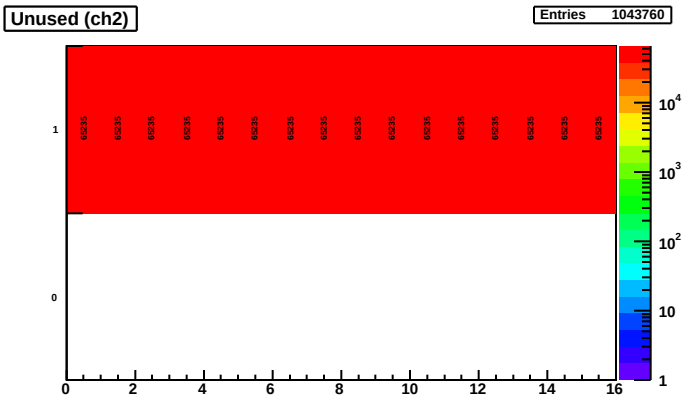
VT201 0-15 (ch1)

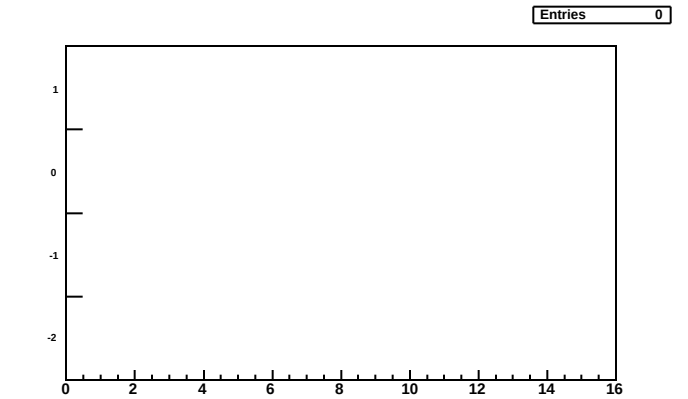


VT201 0-15 (ch1)

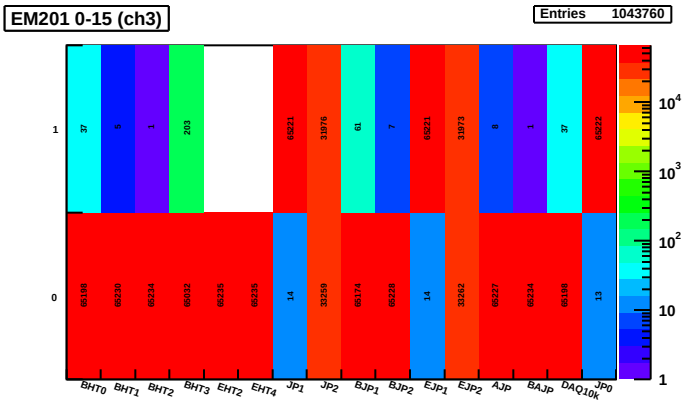


Unused (ch2)

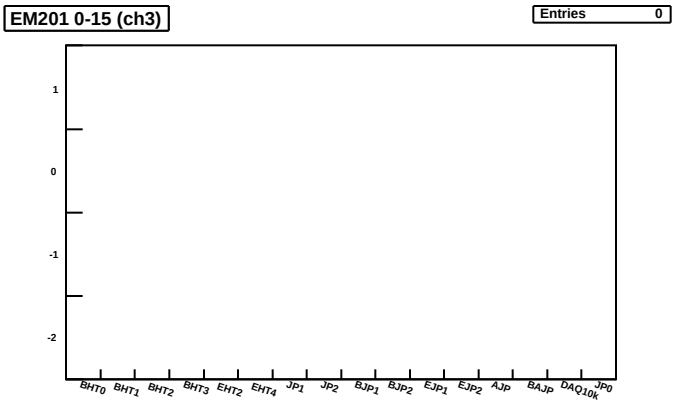




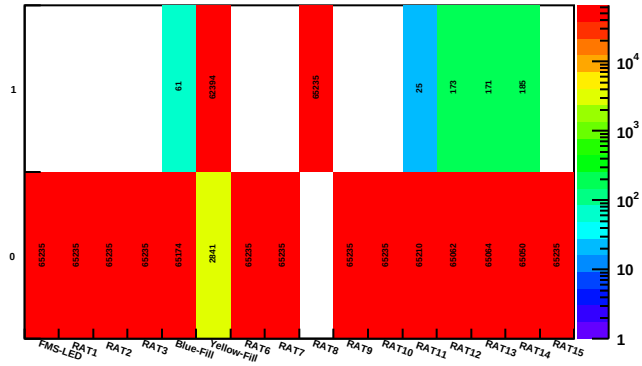
EM201 0-15 (ch3)

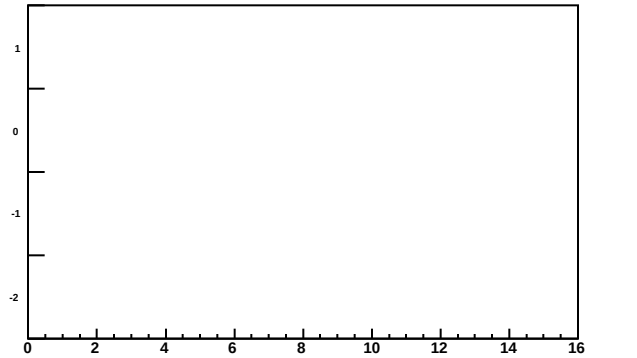


EM201 0-15 (ch3)

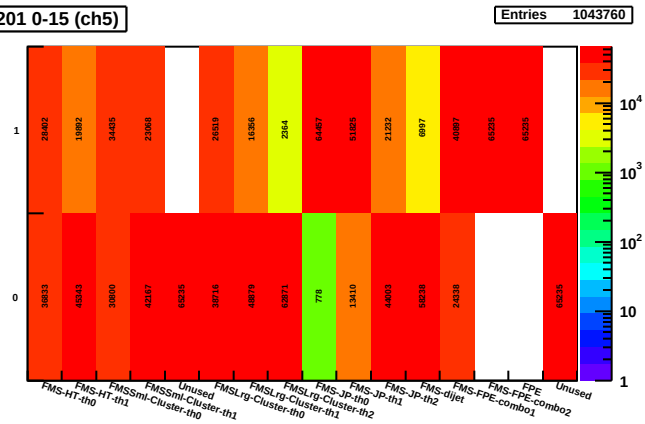


RAT board (ch4)





FP201 0-15 (ch5)



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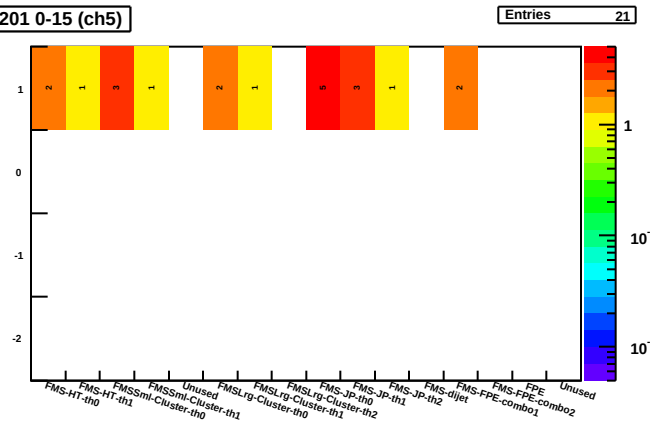
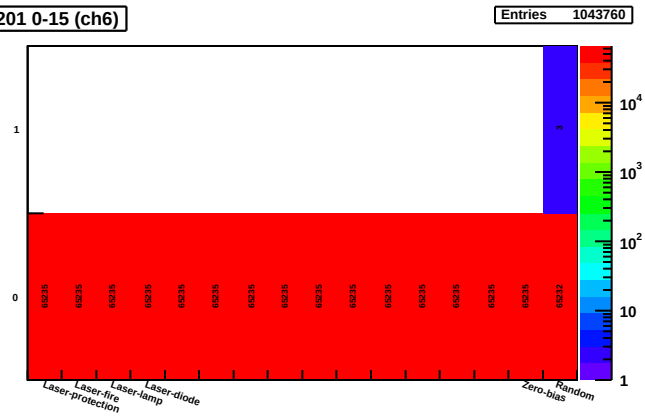
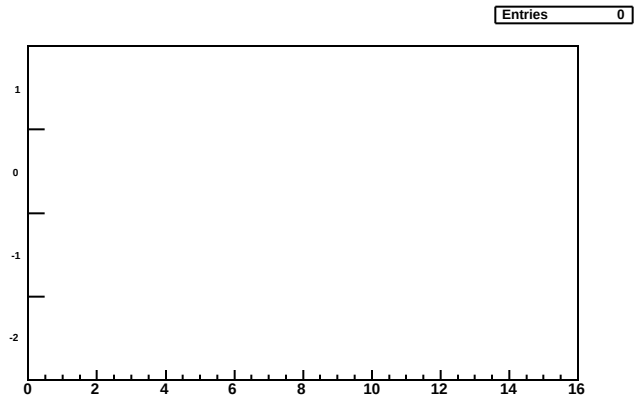
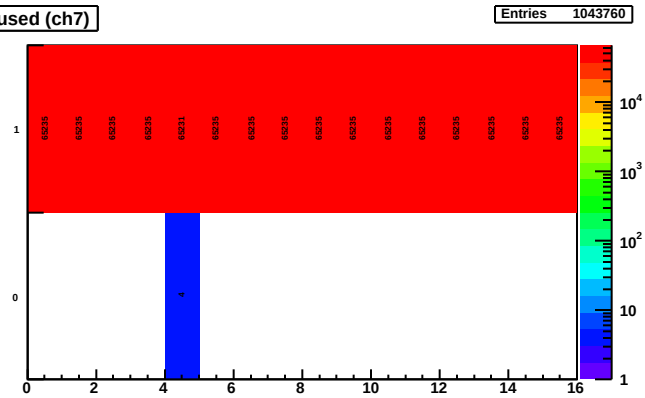
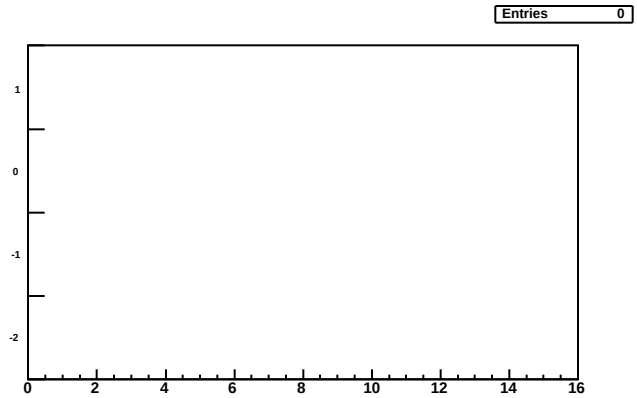


ST201 0-15 (ch6)





Unused (ch7)