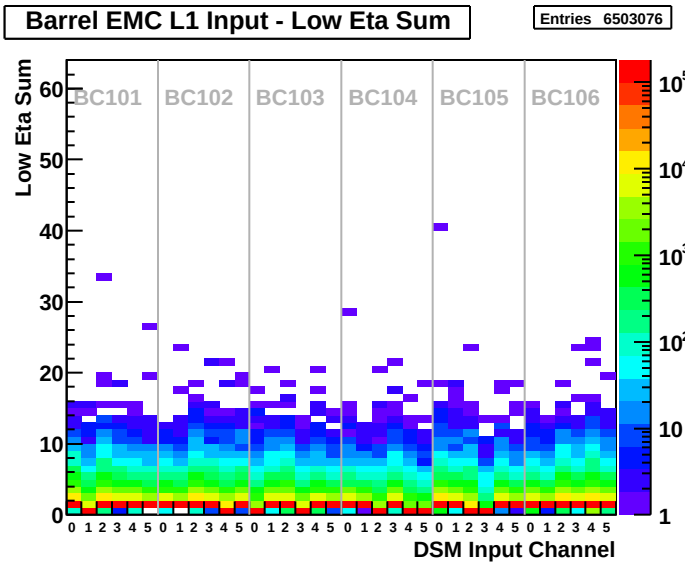
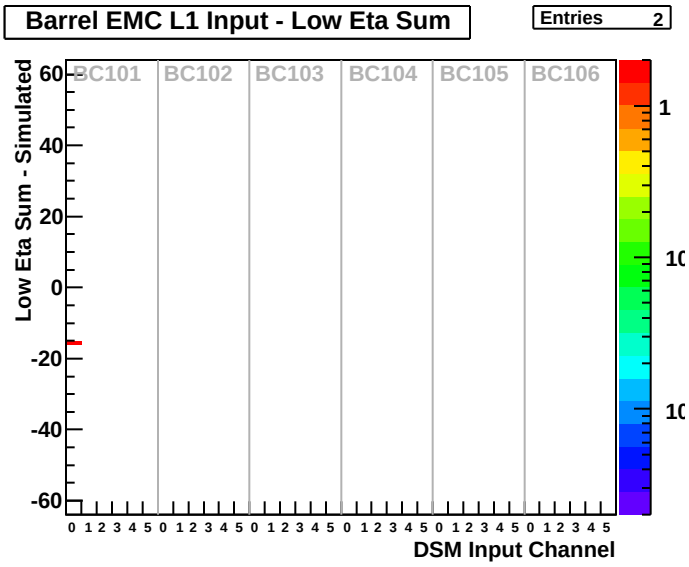
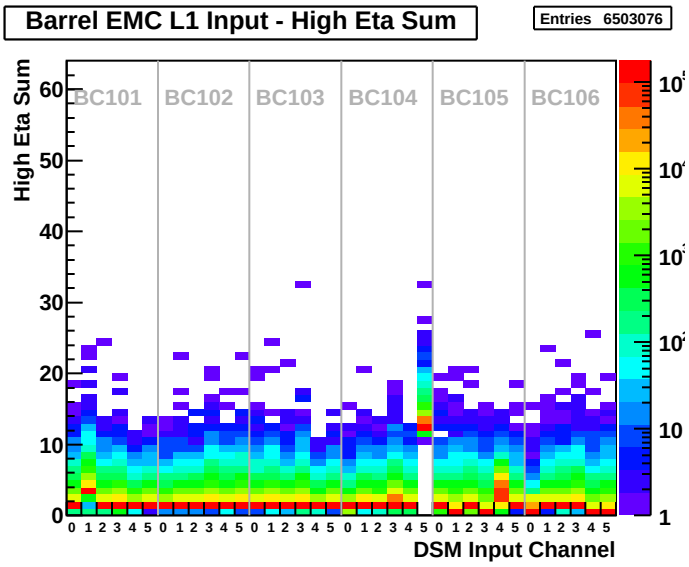
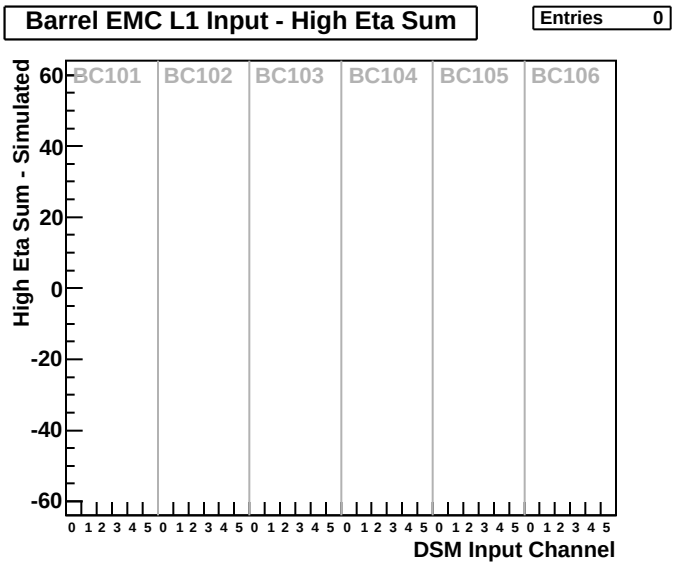
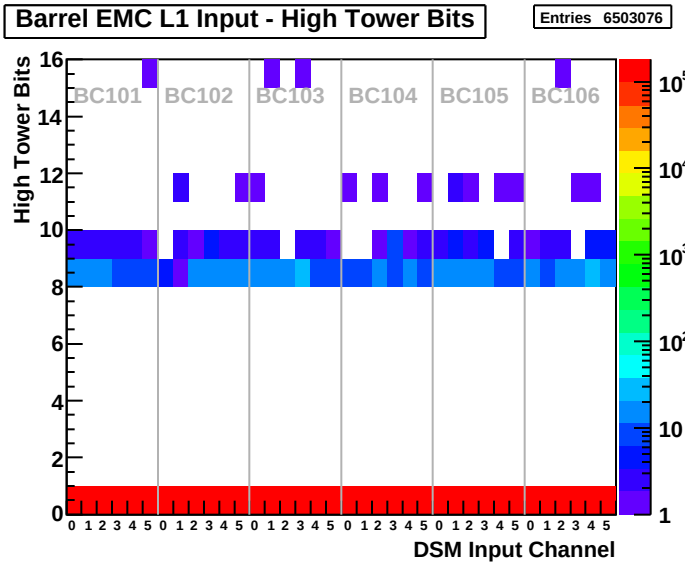
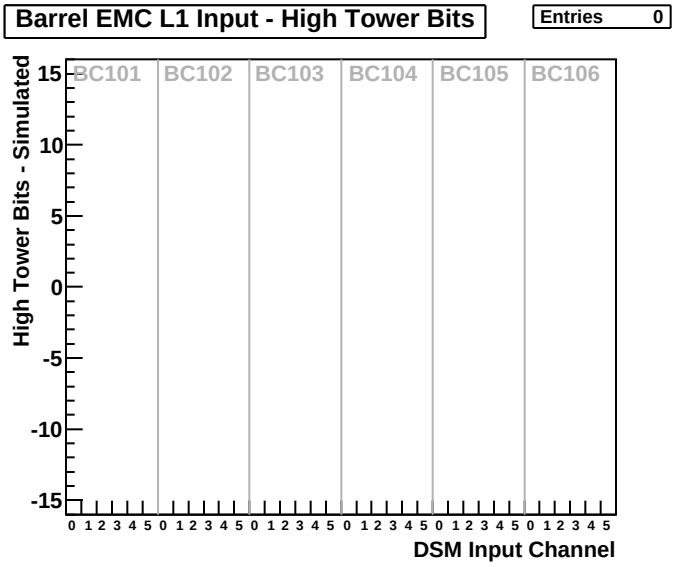

Barrel EMC L1 Input - Low Eta Sum
Entries 6503076
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

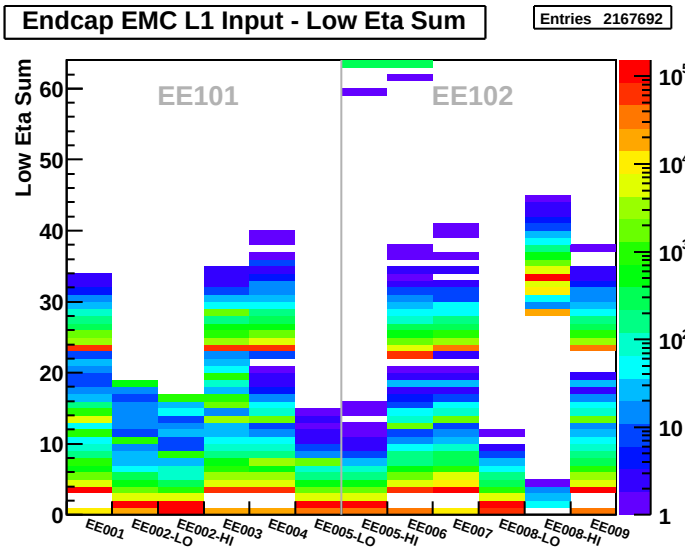

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

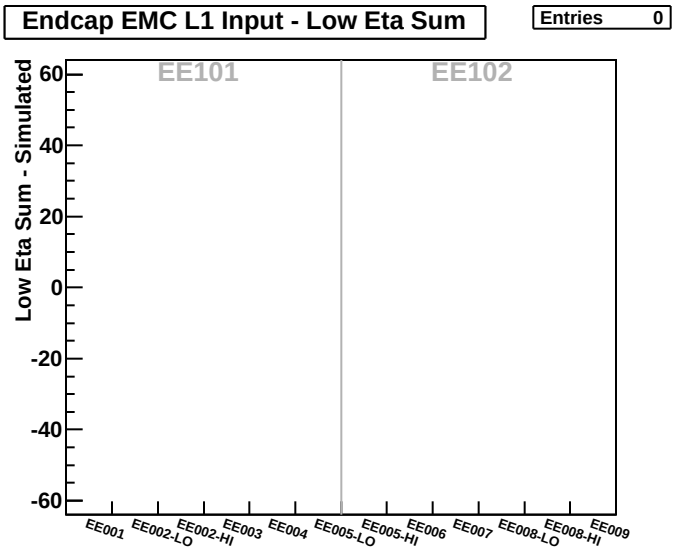

Barrel EMC L1 Input - High Eta Sum
Entries 6503076
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

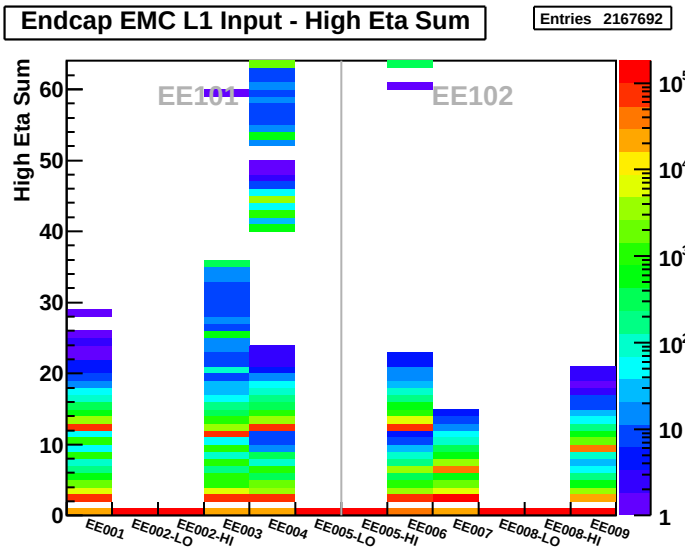

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

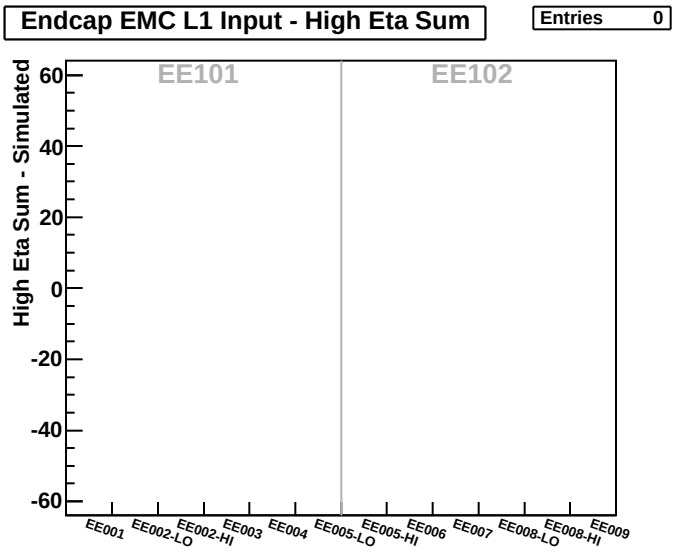

Barrel EMC L1 Input - High Tower Bits
Entries 6503076
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

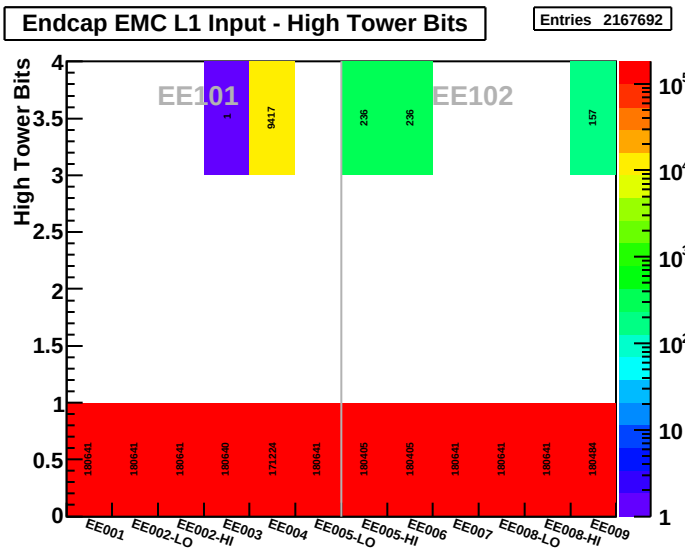

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

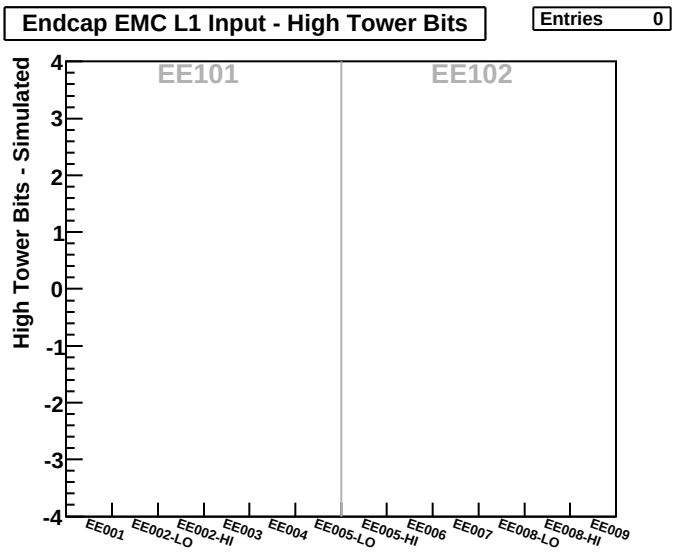

Endcap EMC L1 Input - Low Eta Sum
Entries 2167692
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

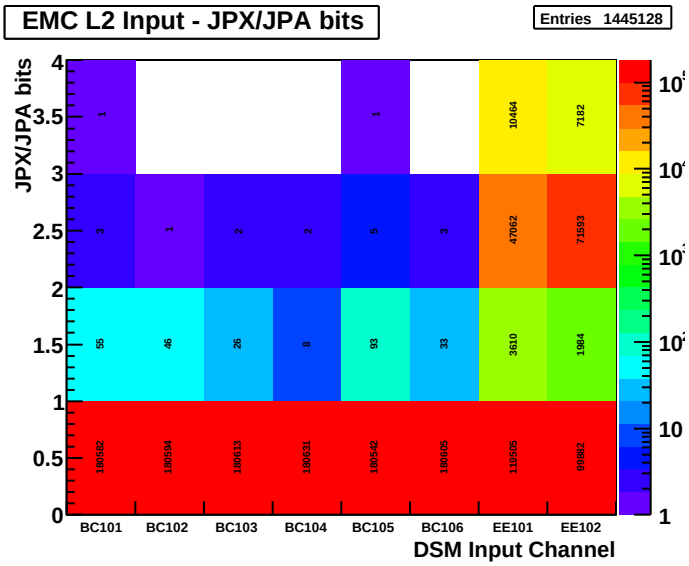

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

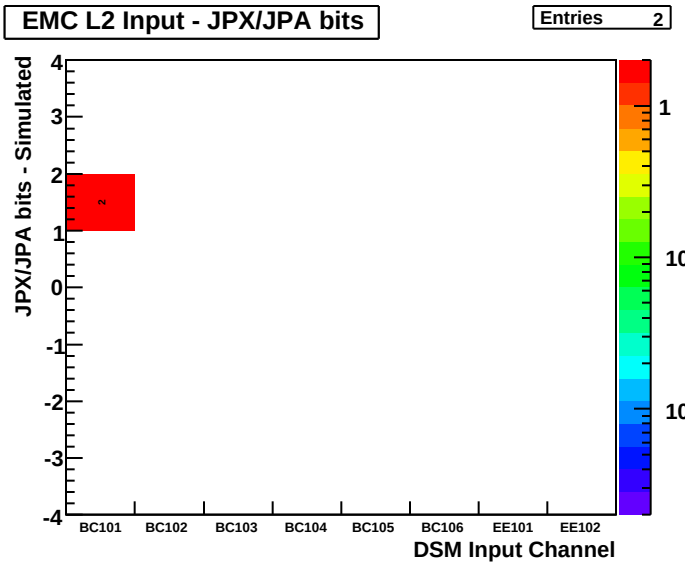

Endcap EMC L1 Input - High Eta Sum
Entries 2167692
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

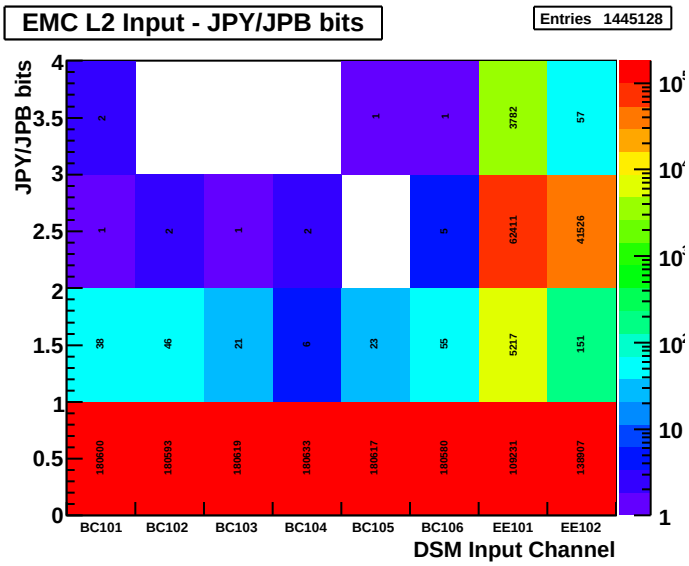

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

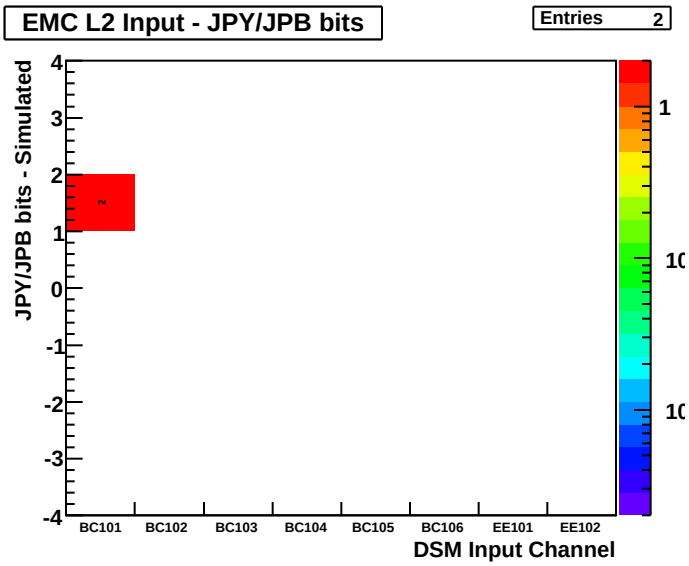

Endcap EMC L1 Input - High Tower Bits
Entries 2167692
High Tower Bits
EE101
EE102
1
9417
236
236
157
180641
180641
180641
180640
171224
180641
180405
180405
180641
180641
180641
180494
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

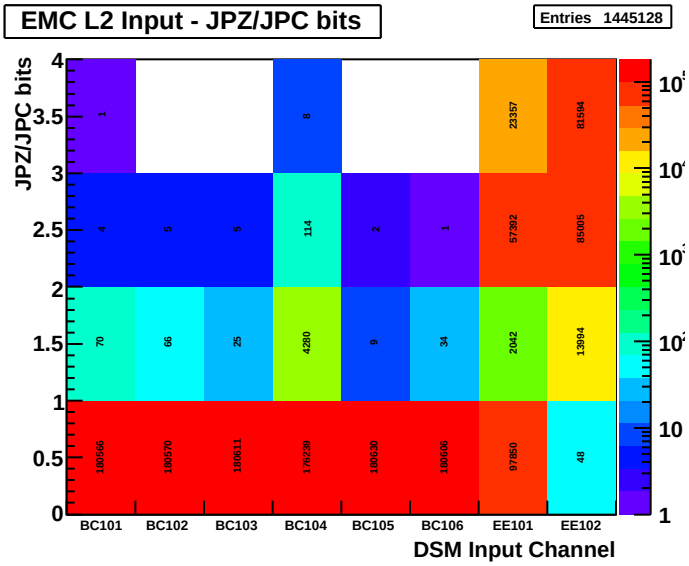

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

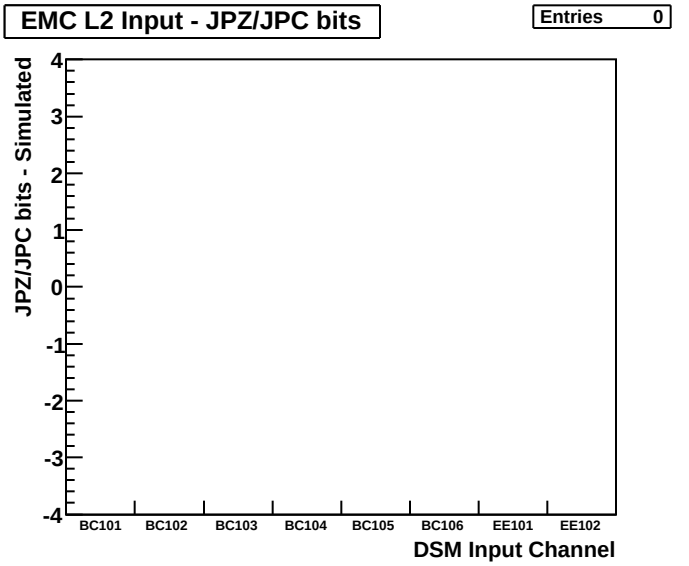

EMC L2 Input - JPX/JPA bits
Entries 1445128
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

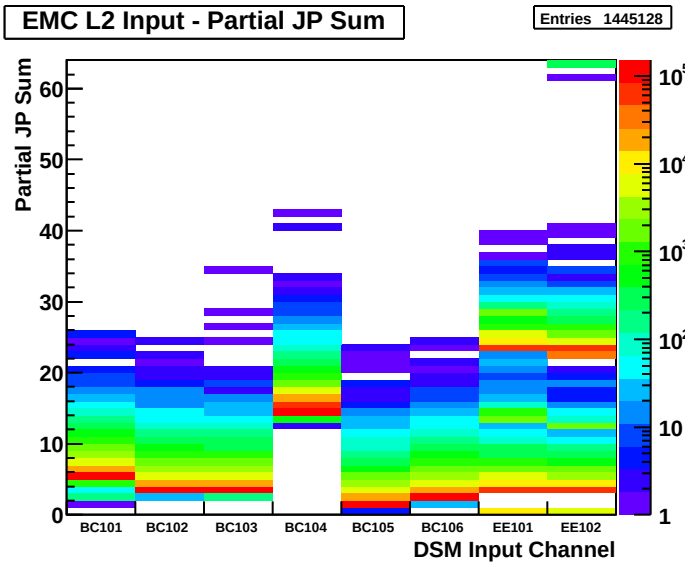

EMC L2 Input - JPX/JPA bits
Entries 2
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

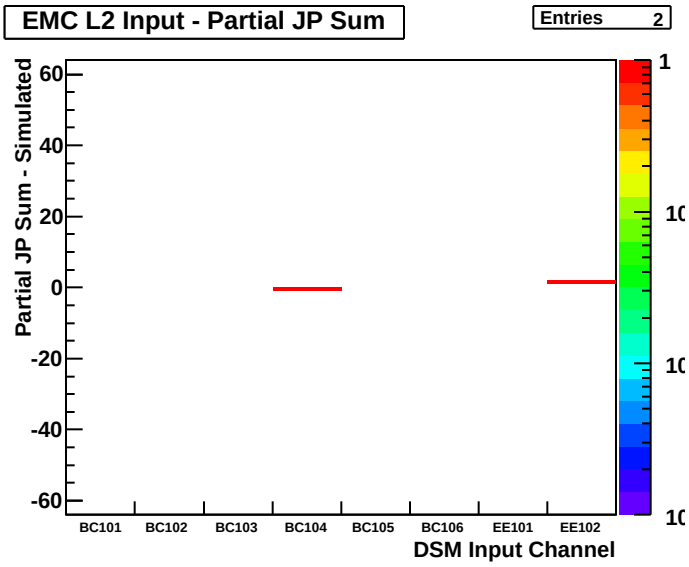

EMC L2 Input - JPY/JPB bits
Entries 1445128
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

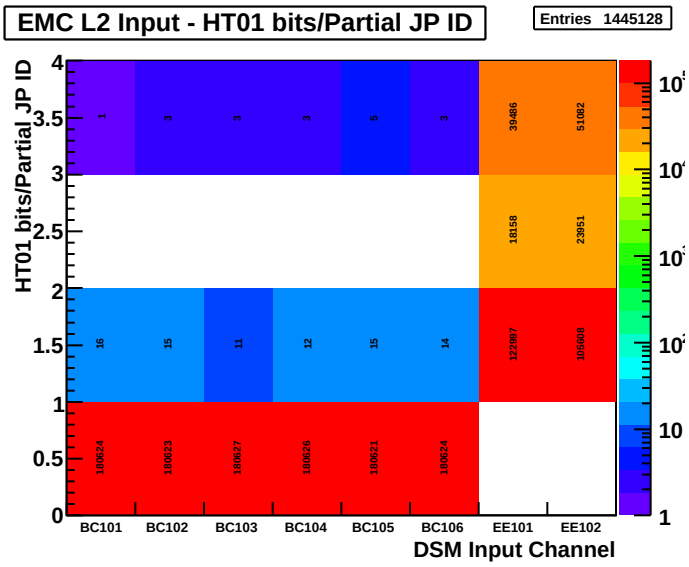

EMC L2 Input - JPY/JPB bits
Entries 2
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

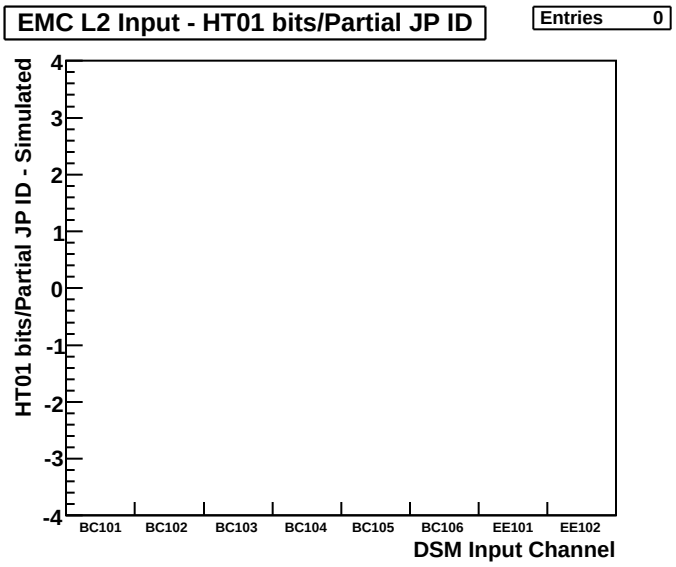

EMC L2 Input - JPZ/JPC bits
Entries 1445128
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

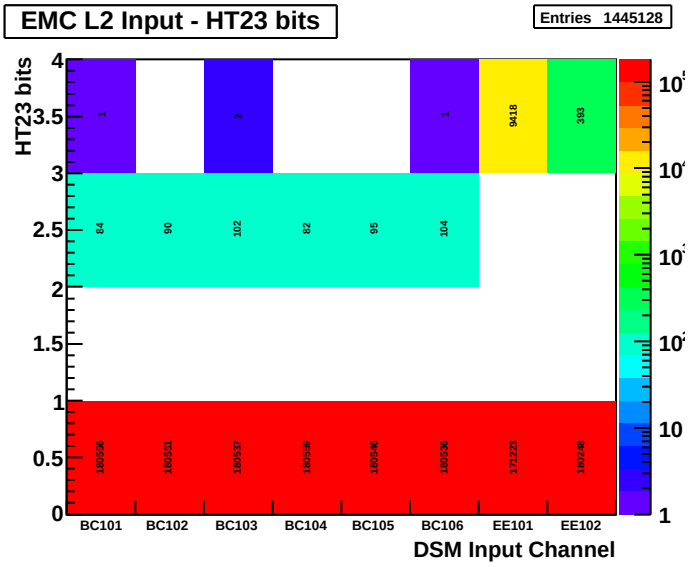

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

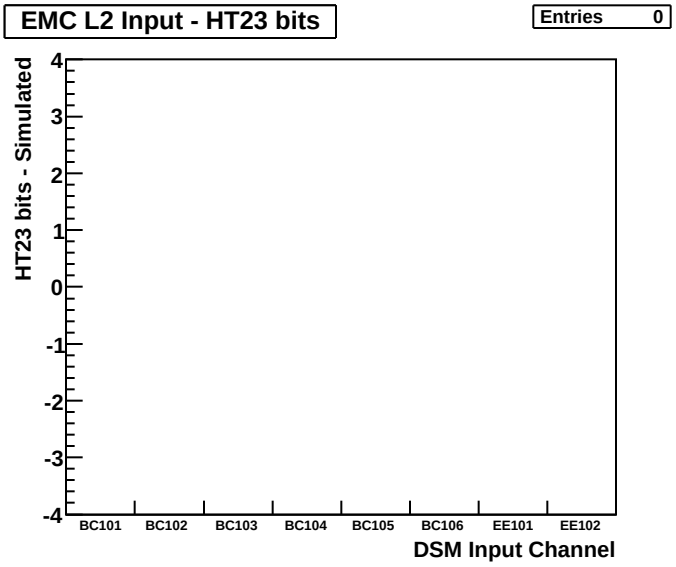

EMC L2 Input - Partial JP Sum
Entries 1445128
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

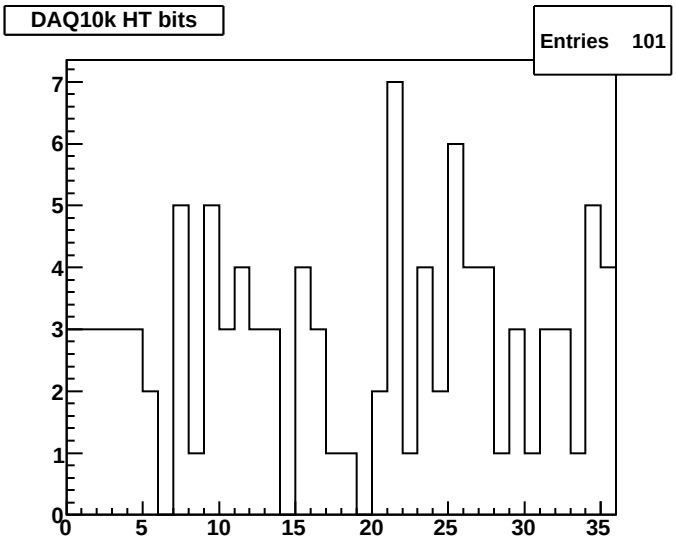

EMC L2 Input - Partial JP Sum
Entries 2
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

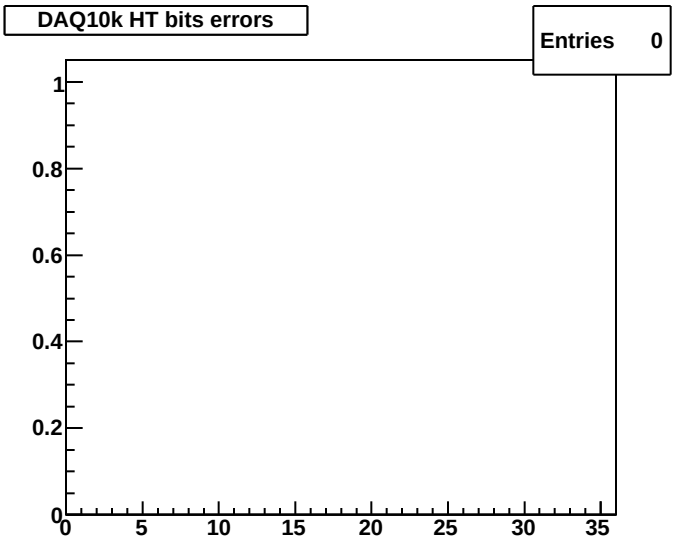

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1445128
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

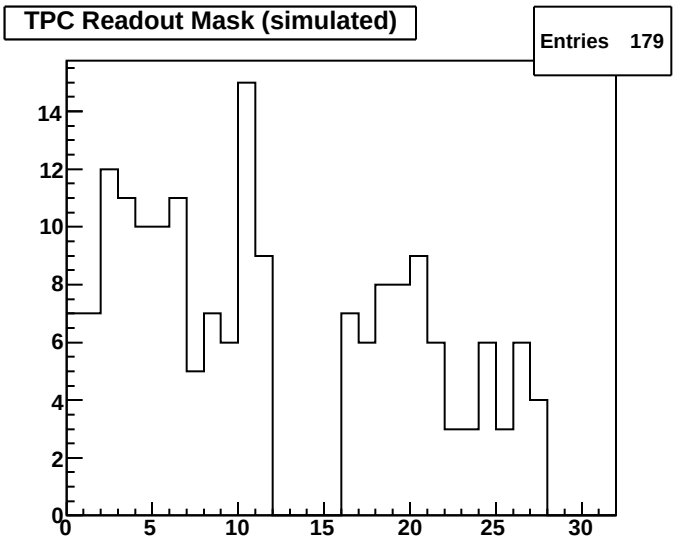

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

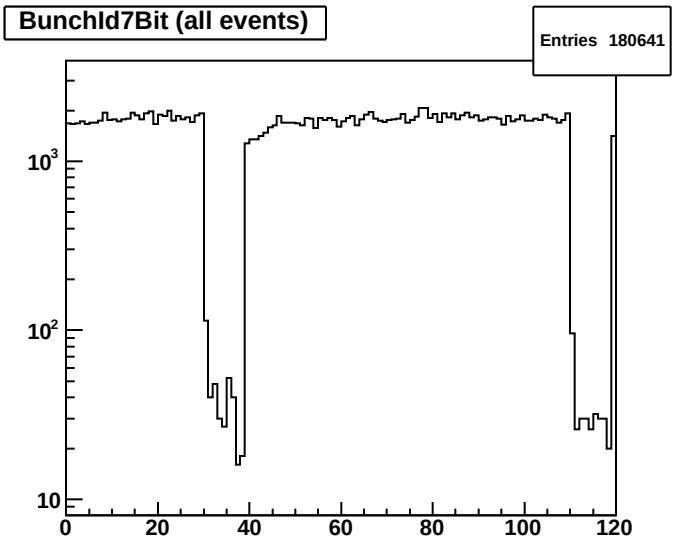

EMC L2 Input - HT23 bits
Entries 1445128
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

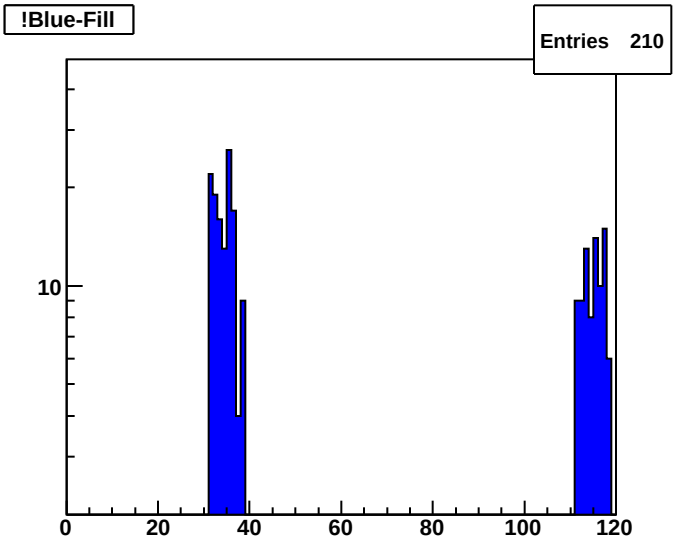

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

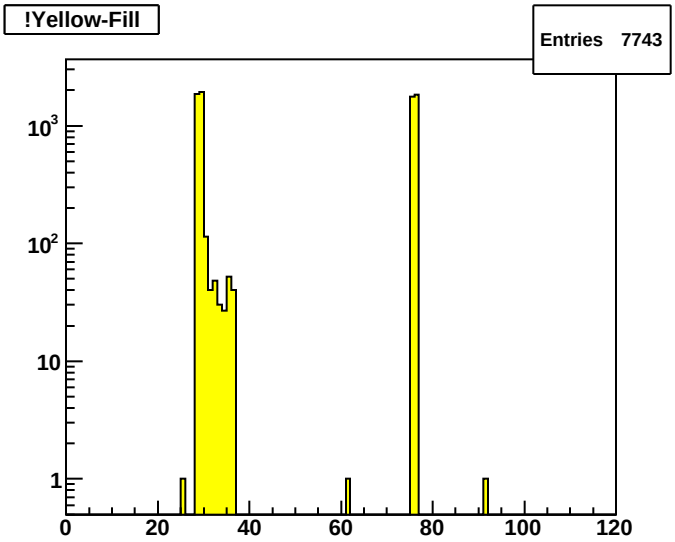

DAQ10k HT bits
Entries 101

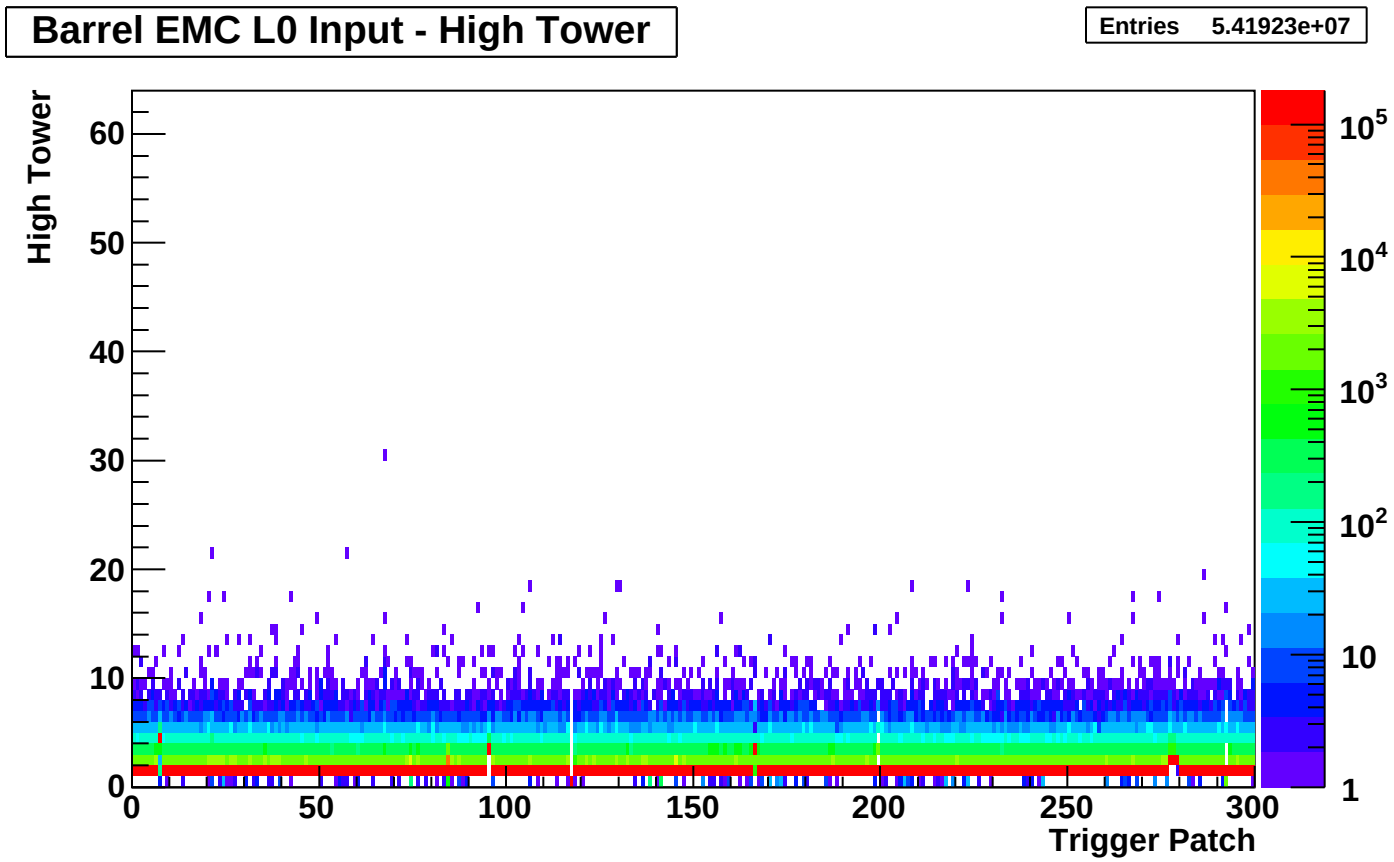

DAQ10k HT bits errors
Entries 0

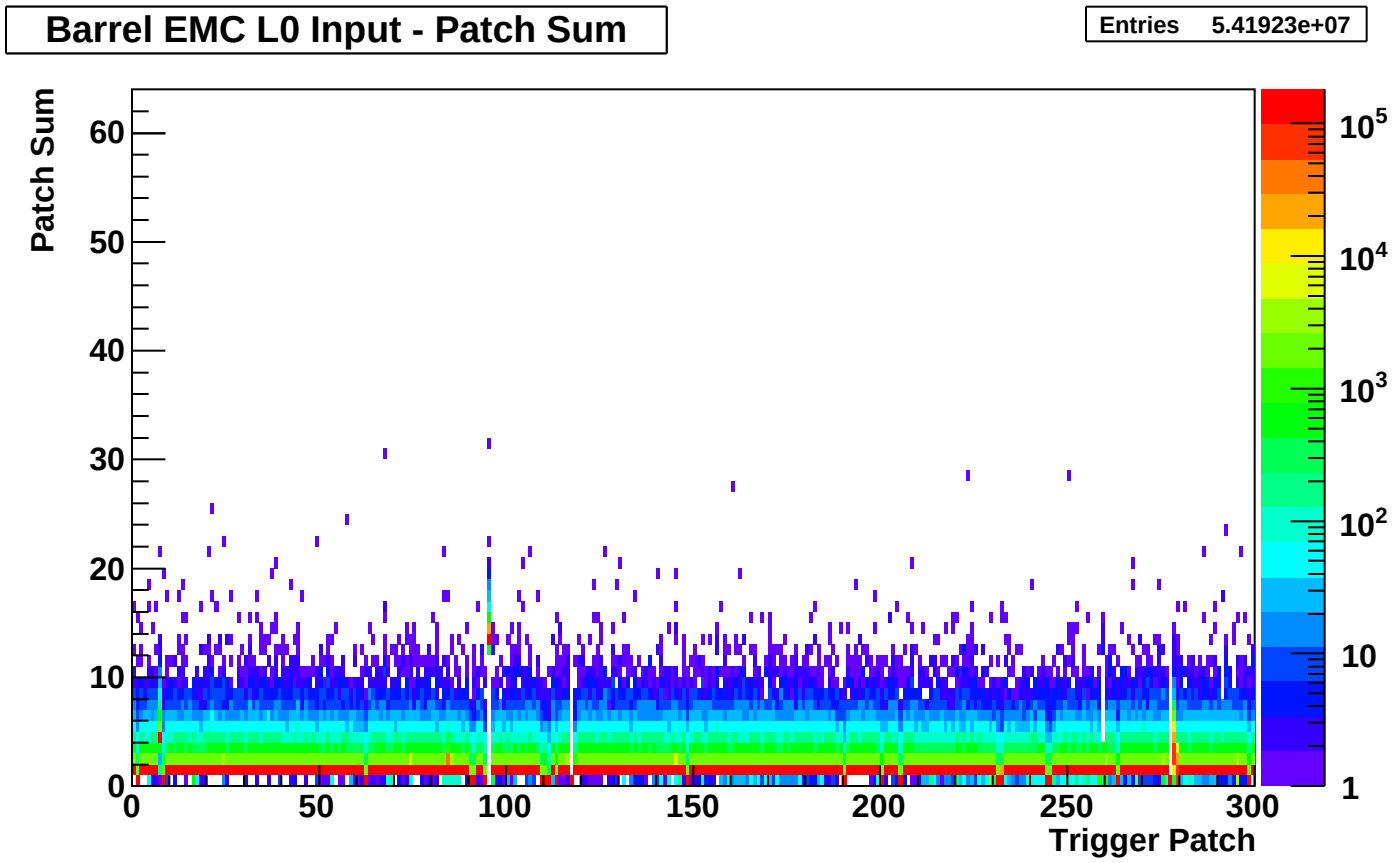

TPC Readout Mask (simulated)
Entries 179

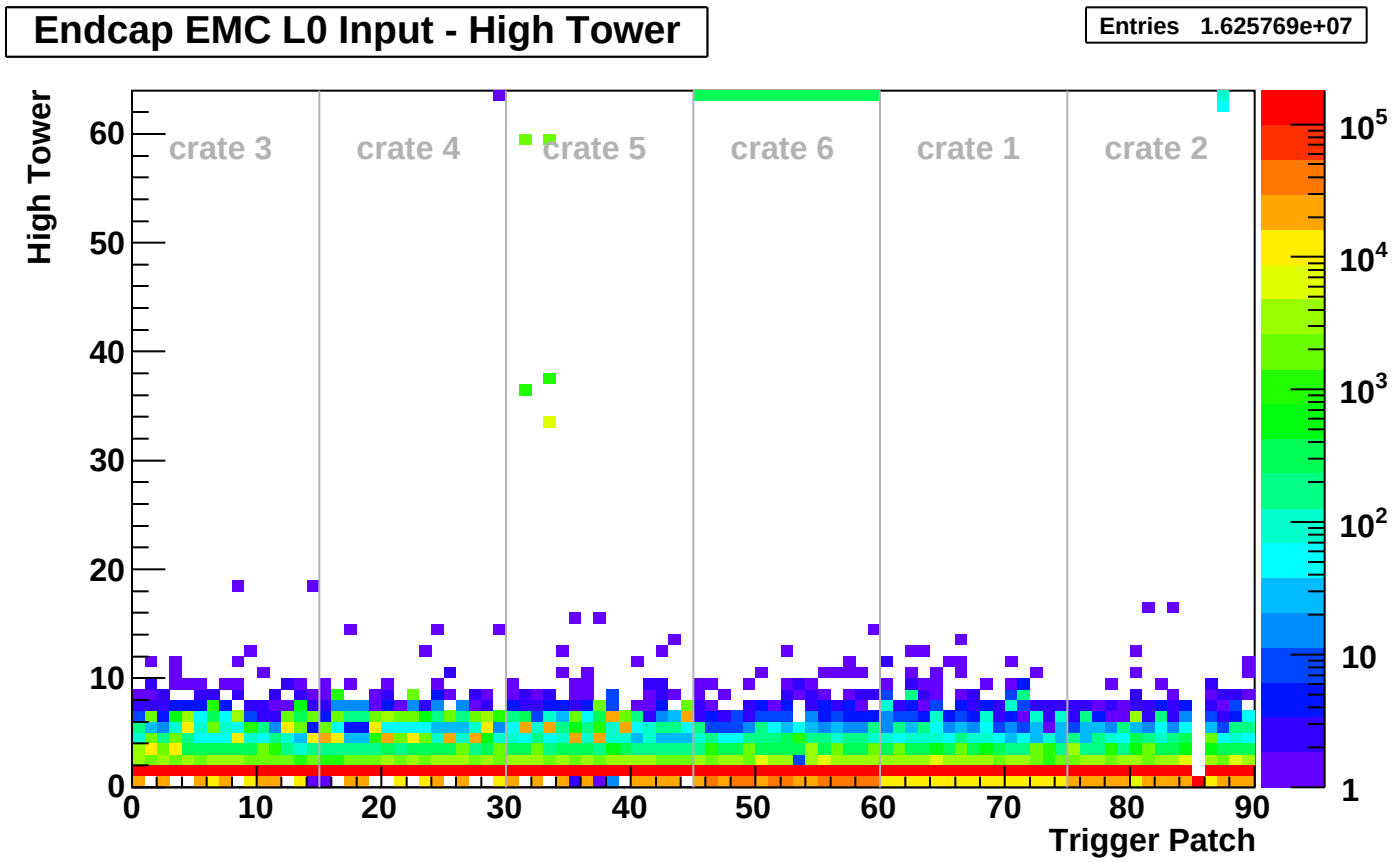

BunchId7Bit (all events)
Entries 180641

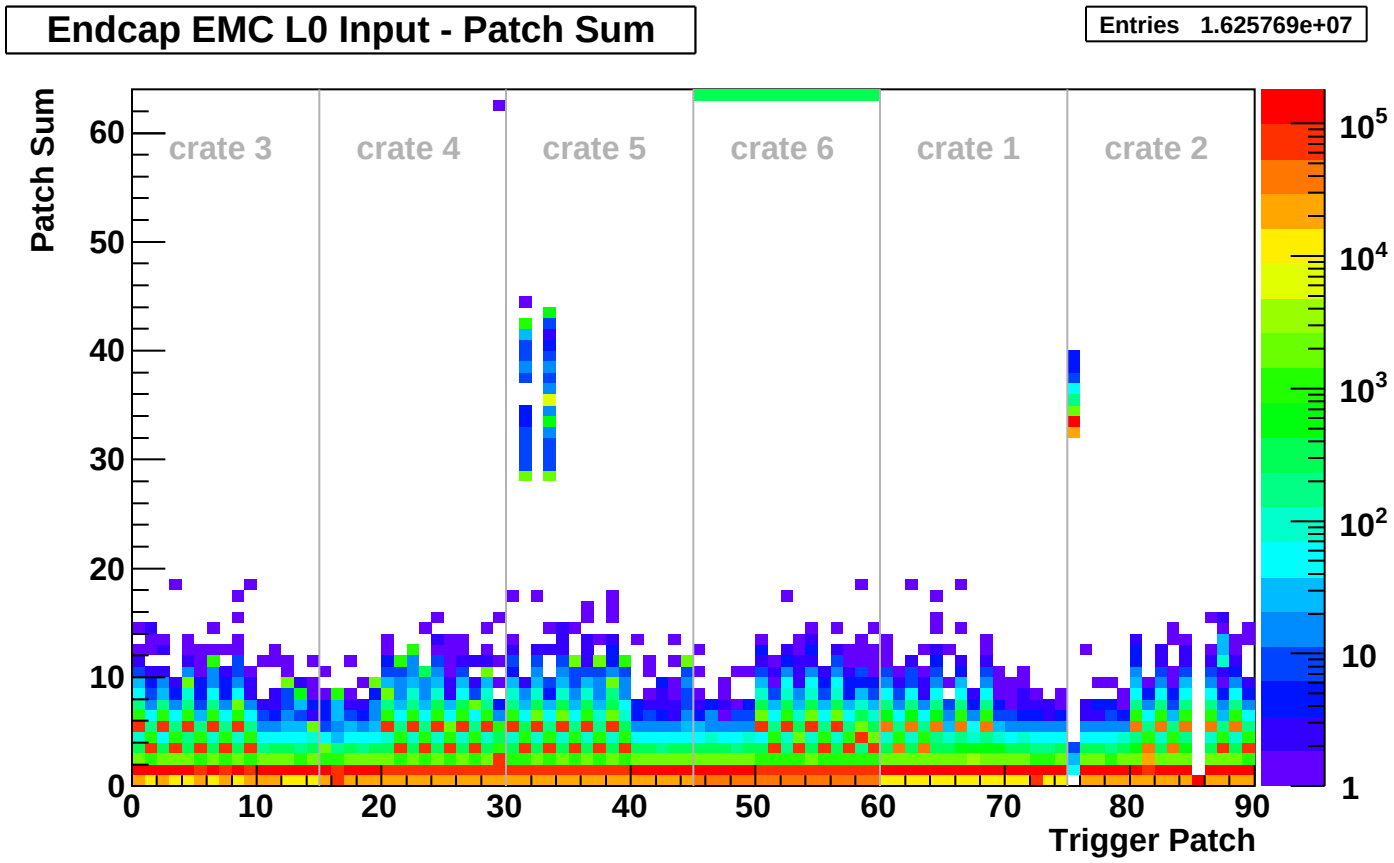

!Blue-Fill
Entries 210

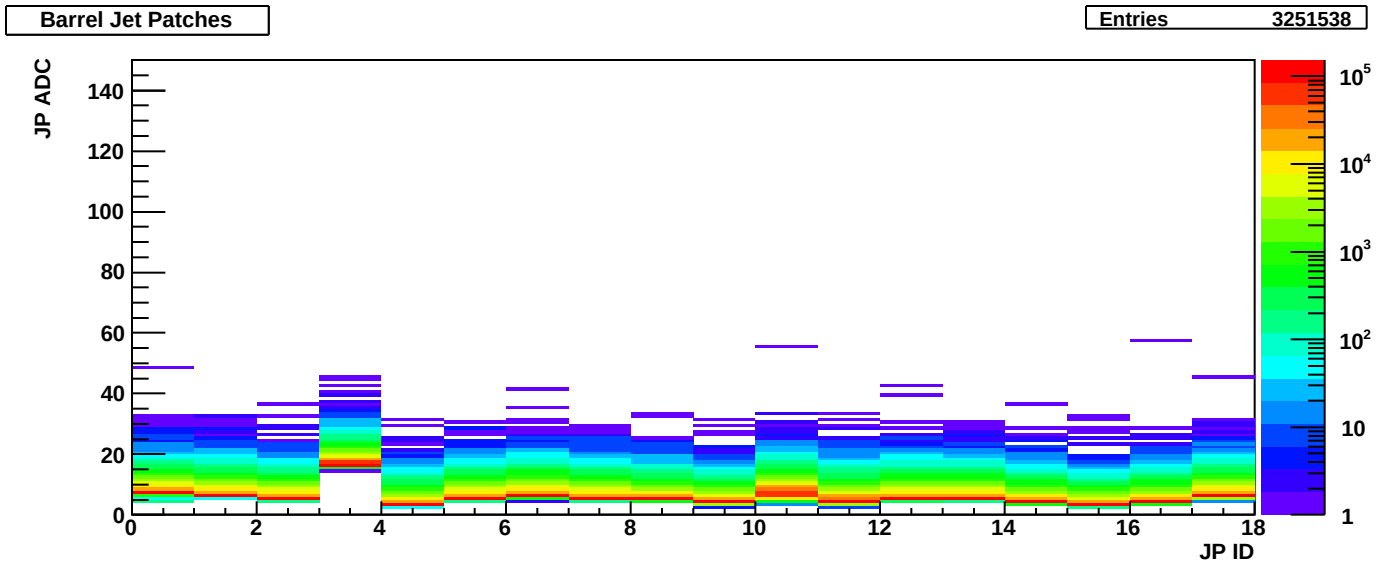

!Yellow-Fill
Entries 7743

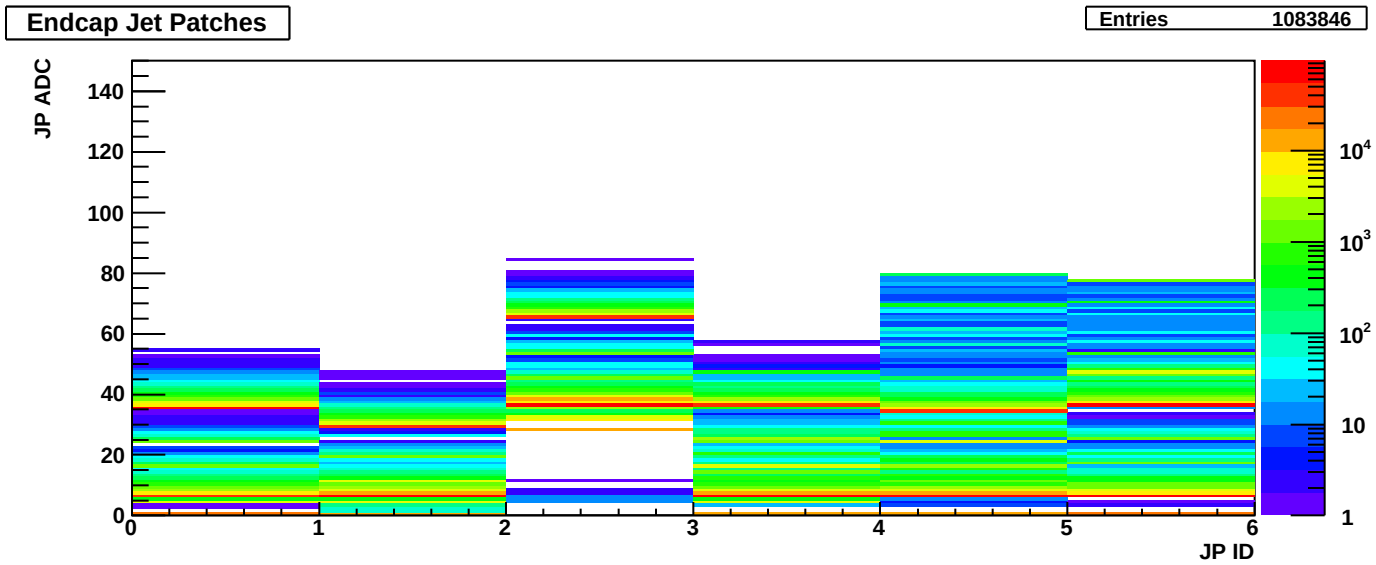

Barrel EMC L0 Input - High Tower
Entries 5.41923e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

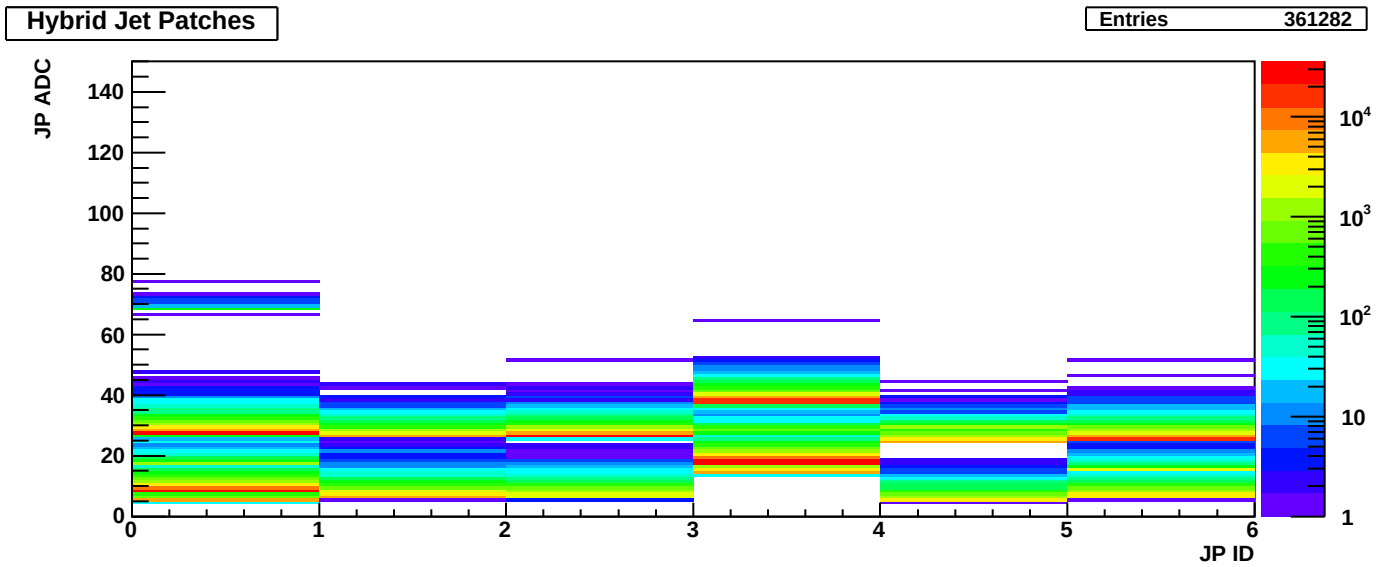

Barrel EMC L0 Input - Patch Sum
Entries 5.41923e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

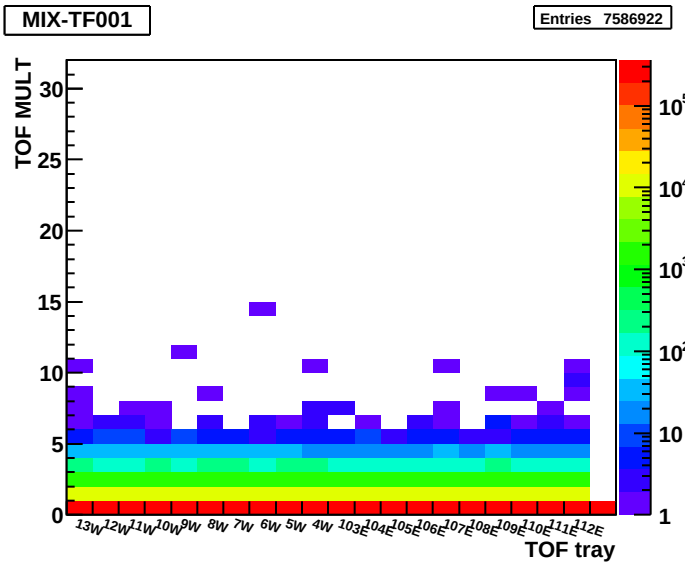

Endcap EMC L0 Input - High Tower
Entries 1.625769e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

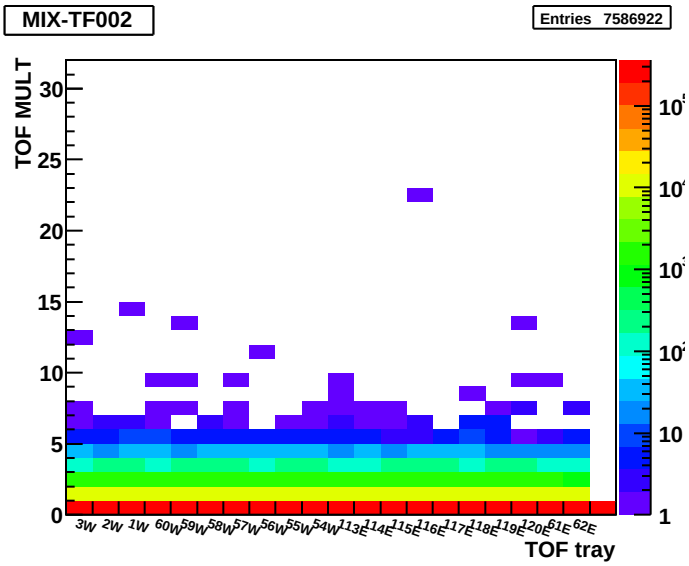

Endcap EMC L0 Input - Patch Sum
Entries 1.625769e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

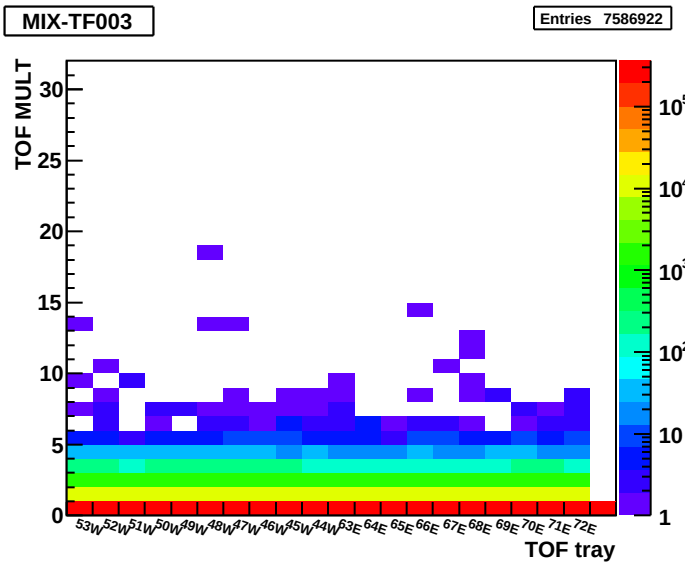

Barrel Jet Patches
Entries 3251538
JP ADC
JP ID

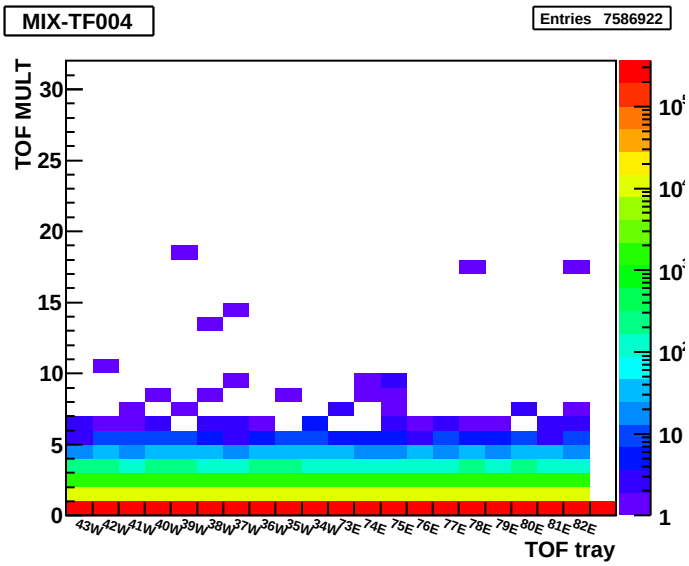

Endcap Jet Patches
Entries 1083846
JP ADC
JP ID

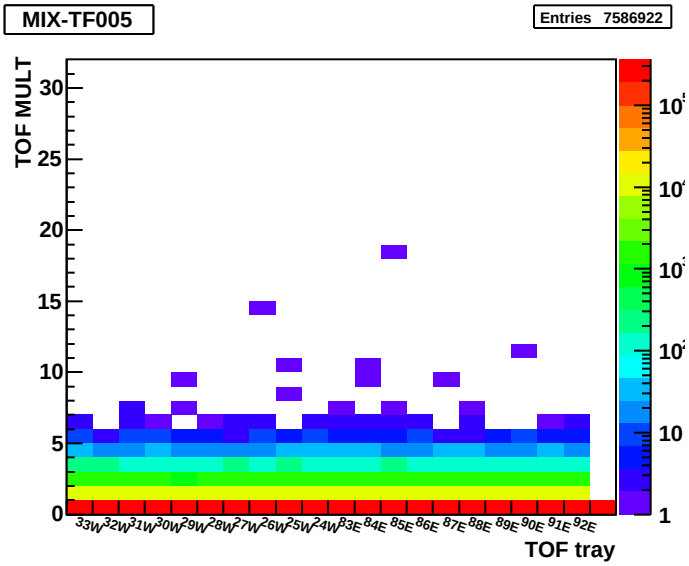

Hybrid Jet Patches
Entries 361282
JP ADC
JP ID

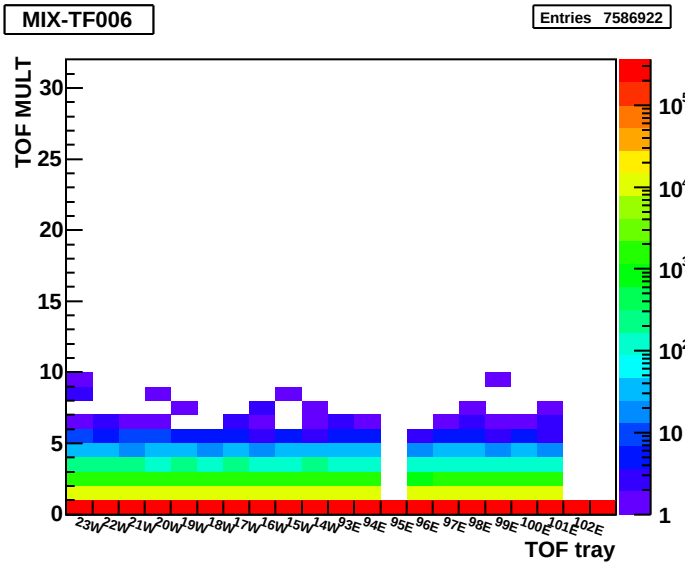

MIX-TF001
Entries 7586922
TOF MULT
TOF tray

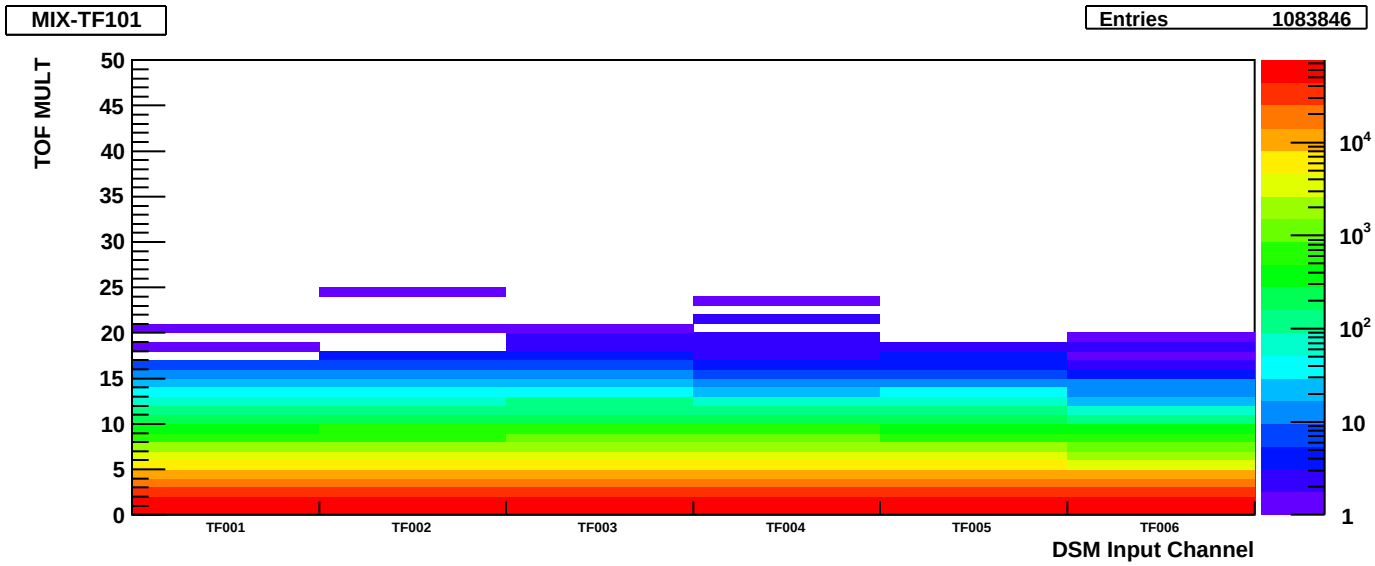

MIX-TF002
Entries 7586922
TOF MULT
TOF tray

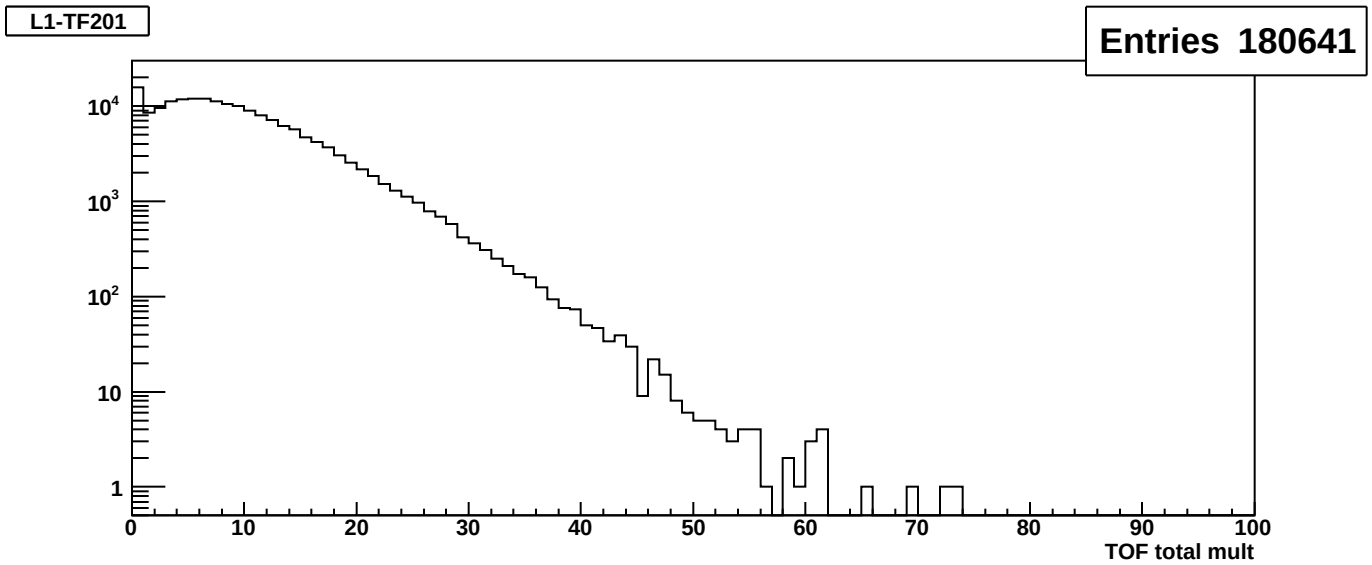

MIX-TF003
Entries 7586922
TOF MULT
TOF tray

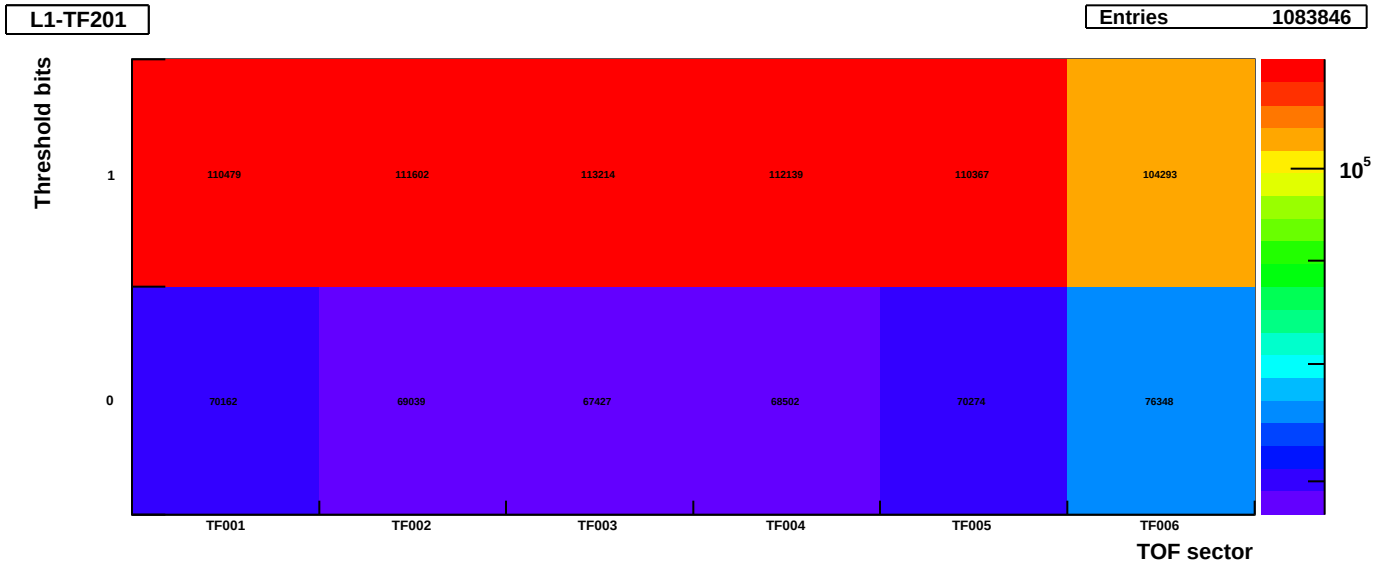

MIX-TF004
Entries 7586922
TOF MULT
TOF tray

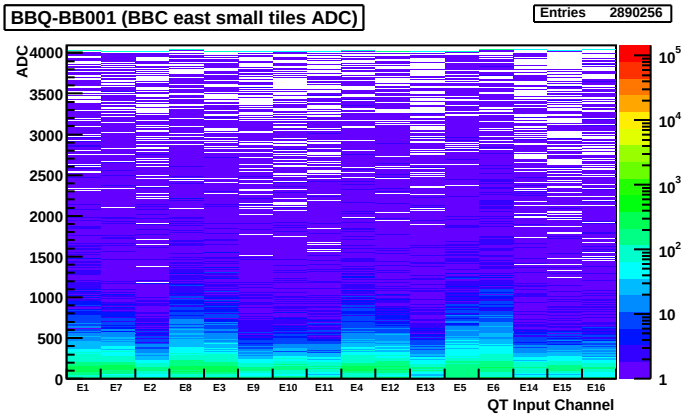

MIX-TF005
Entries 7586922
TOF MULT
TOF tray

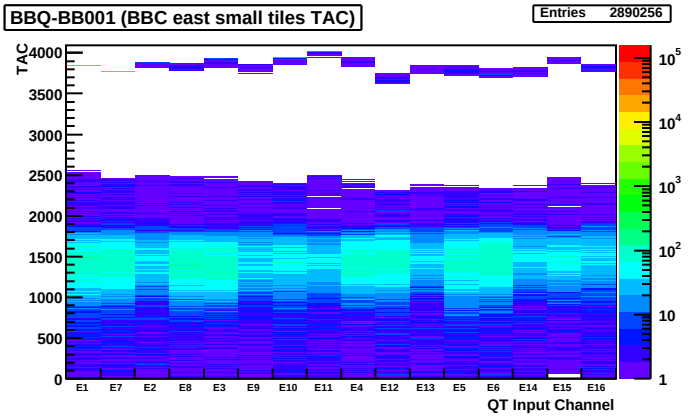

MIX-TF006
Entries 7586922
TOF MULT
TOF tray

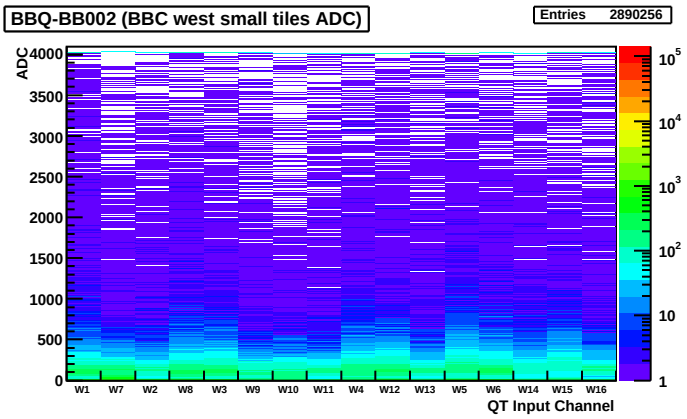

MIX-TF101
Entries 1083846
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

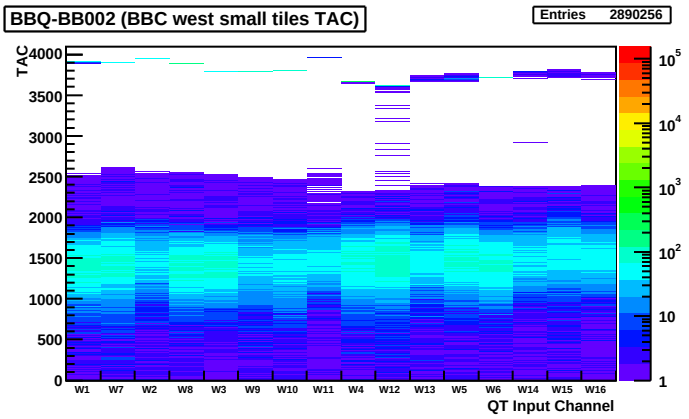

L1-TF201
Entries 180641
TOF total mult

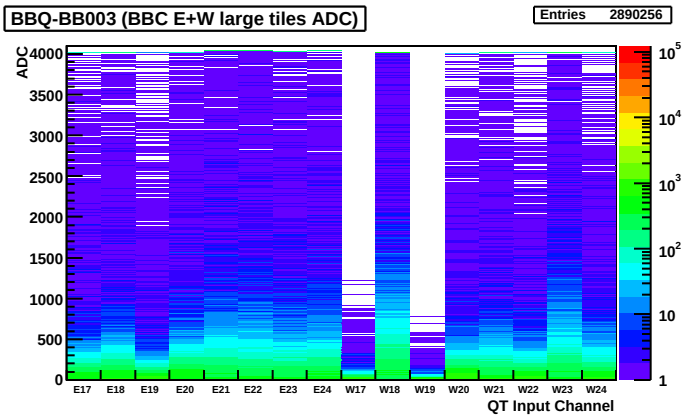

L1-TF201
Entries 1083846
Threshold bits
110479
111602
113214
112139
110367
104293
70162
69039
67427
68502
70274
76348
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

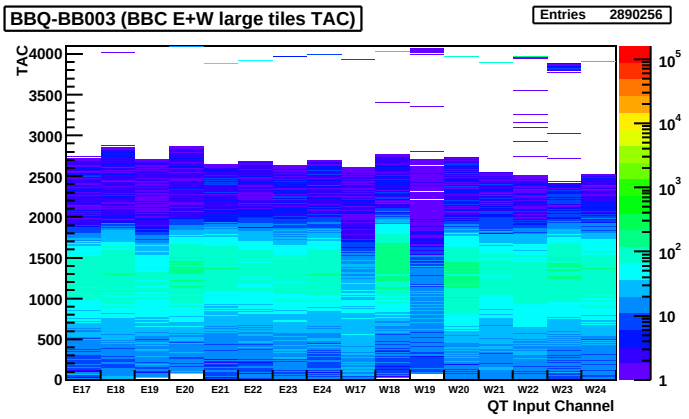

BBQ-BB001 (BBC east small tiles ADC)
Entries 2890256
ADC
QT Input Channel

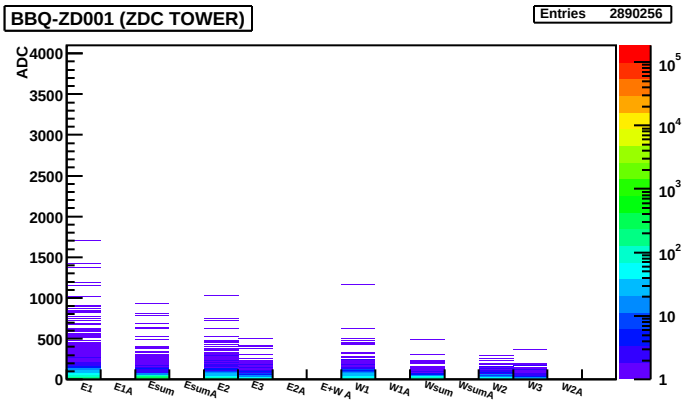

BBQ-BB001 (BBC east small tiles TAC)
Entries 2890256
TAC
QT Input Channel

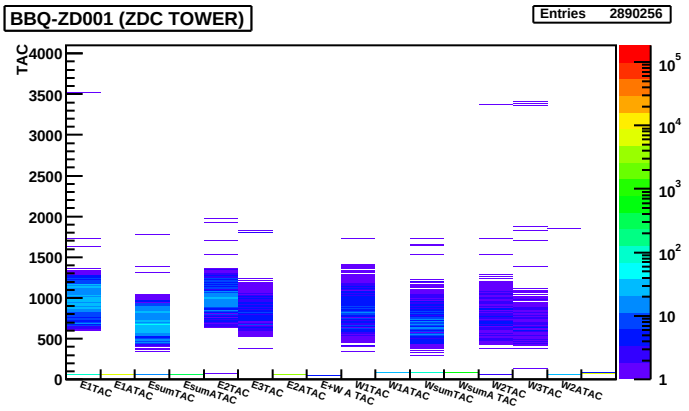

BBQ-BB002 (BBC west small tiles ADC)
Entries 2890256
ADC
QT Input Channel

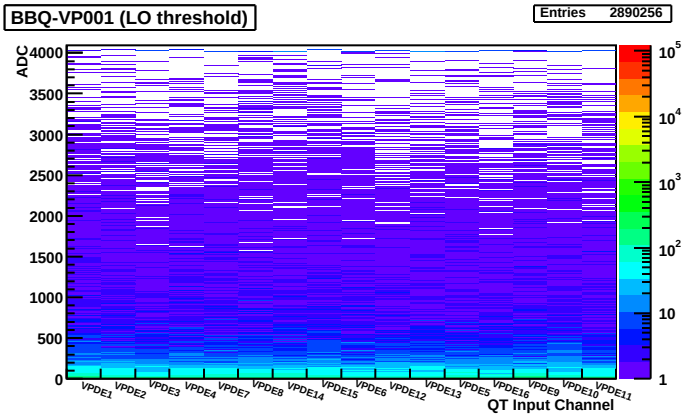

BBQ-BB002 (BBC west small tiles TAC)
Entries 2890256
TAC
QT Input Channel

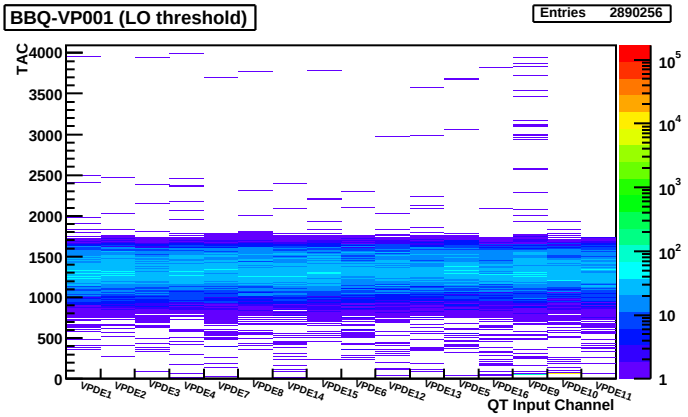

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2890256
ADC
QT Input Channel

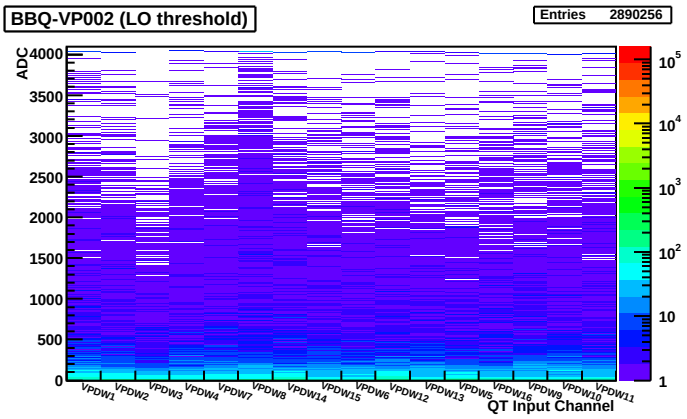

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2890256
TAC
QT Input Channel

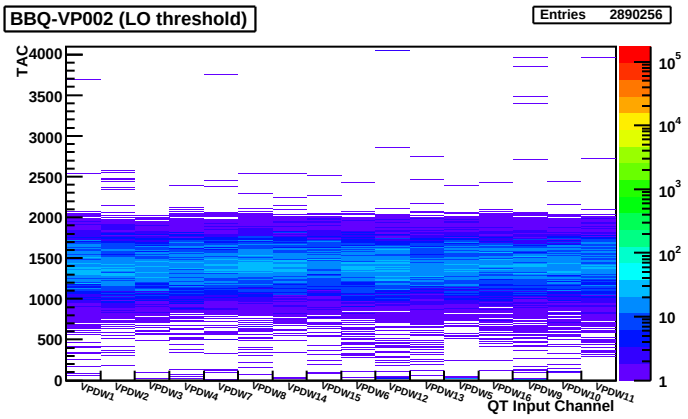

BBQ-ZD001 (ZDC TOWER)
Entries 2890256
ADC

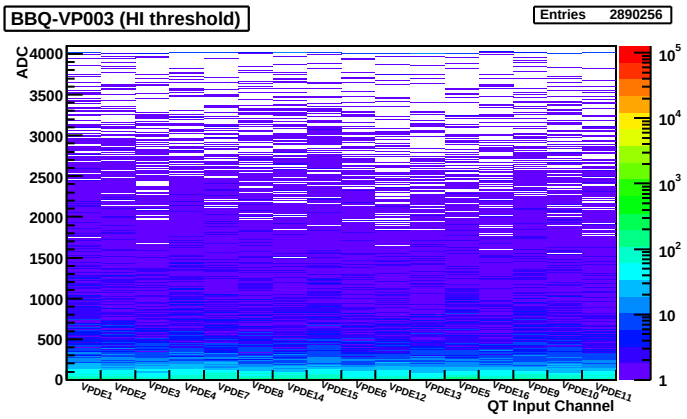

BBQ-ZD001 (ZDC TOWER)
Entries 2890256
TAC

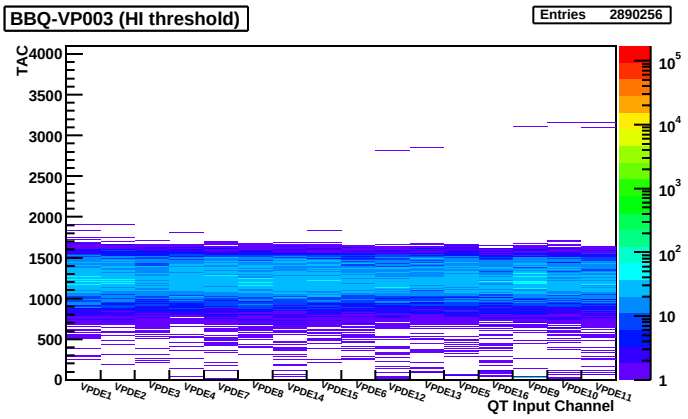

BBQ-VP001 (LO threshold)
Entries 2890256
ADC
QT Input Channel

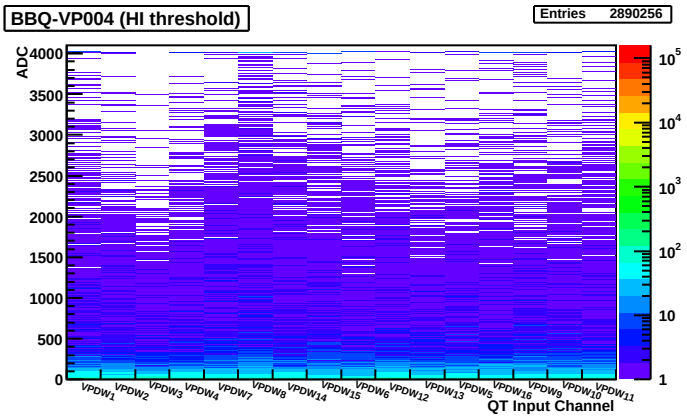

BBQ-VP001 (LO threshold)
Entries 2890256
TAC
QT Input Channel

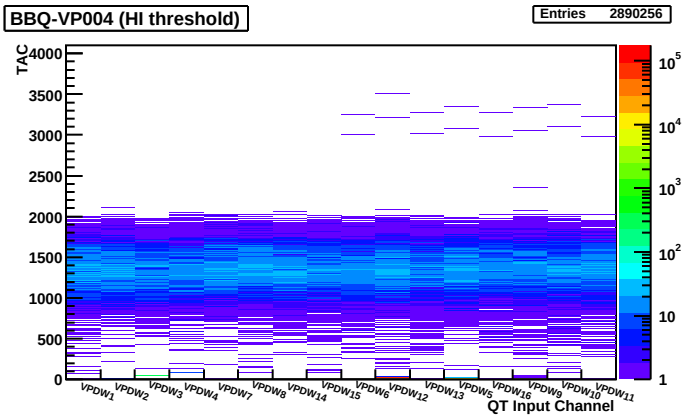

BBQ-VP002 (LO threshold)
Entries 2890256
ADC
QT Input Channel

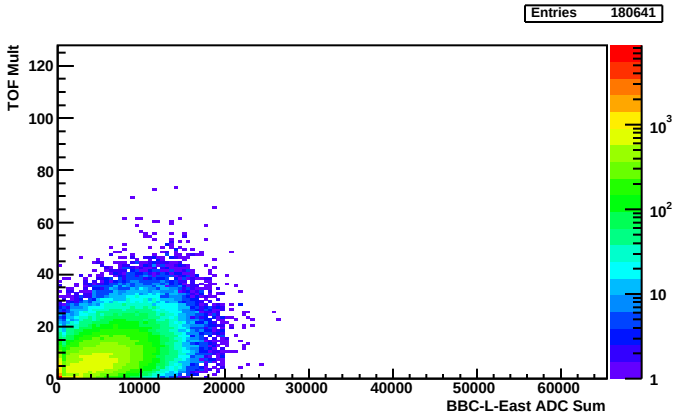

BBQ-VP002 (LO threshold)
Entries 2890256
TAC
QT Input Channel

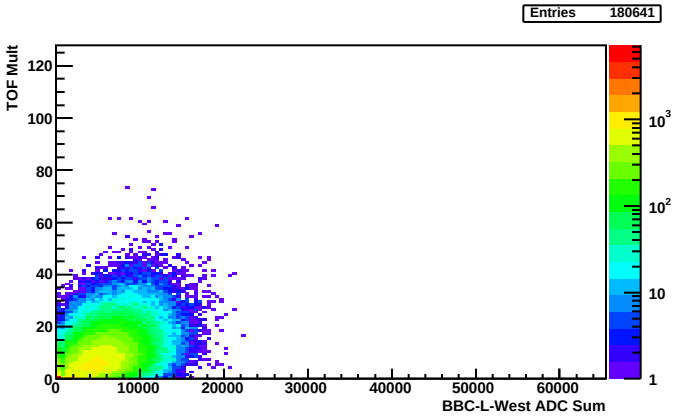

BBQ-VP003 (HI threshold)
Entries 2890256
ADC
QT Input Channel

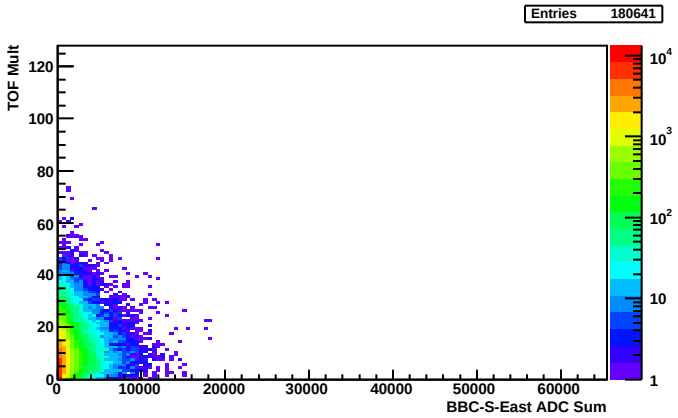

BBQ-VP003 (HI threshold)
Entries 2890256
TAC
QT Input Channel

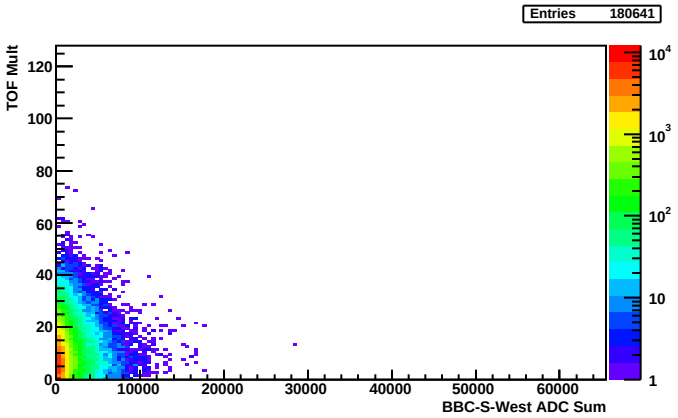

BBQ-VP004 (HI threshold)
Entries 2890256
ADC
QT Input Channel

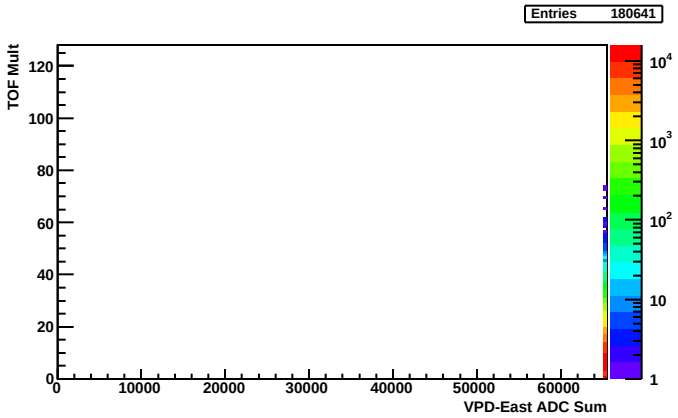

BBQ-VP004 (HI threshold)
Entries 2890256
TAC
QT Input Channel

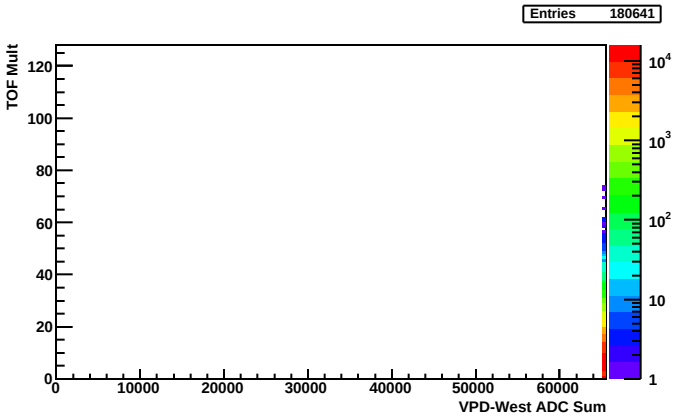

Entries 180641
TOF Mult
BBC-L-East ADC Sum

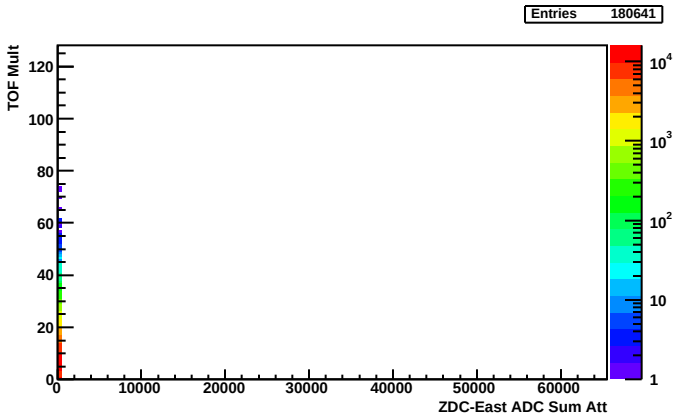

Entries 180641
TOF Mult
BBC-L-West ADC Sum

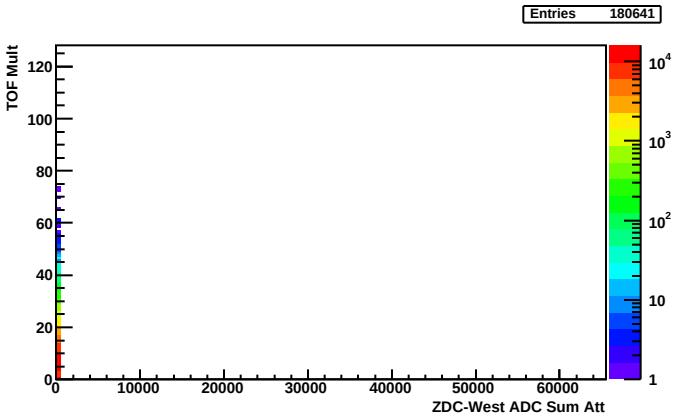

Entries 180641
TOF Mult
BBC-S-East ADC Sum

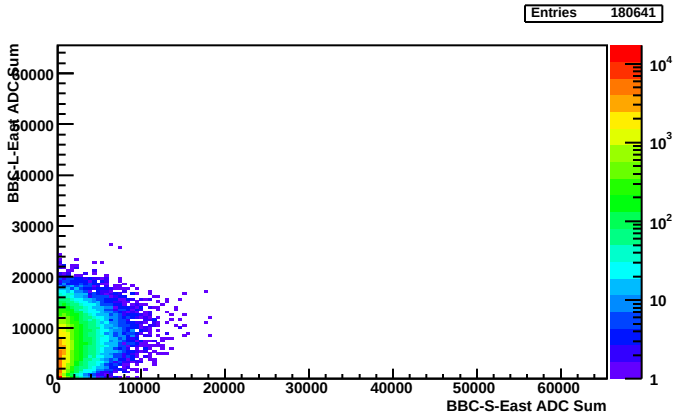

Entries 180641
TOF Mult
BBC-S-West ADC Sum

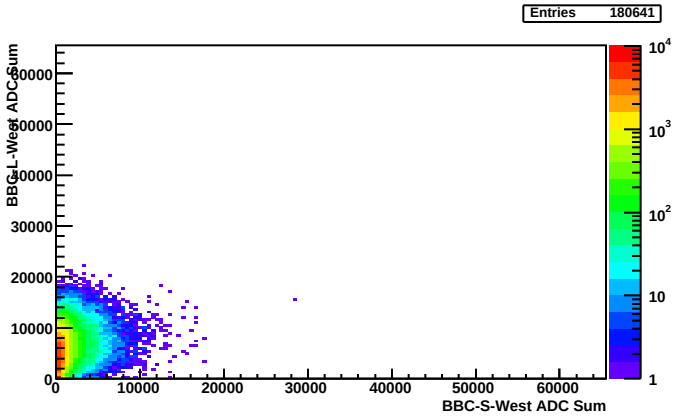

Entries 180641
TOF Mult
VPD-East ADC Sum

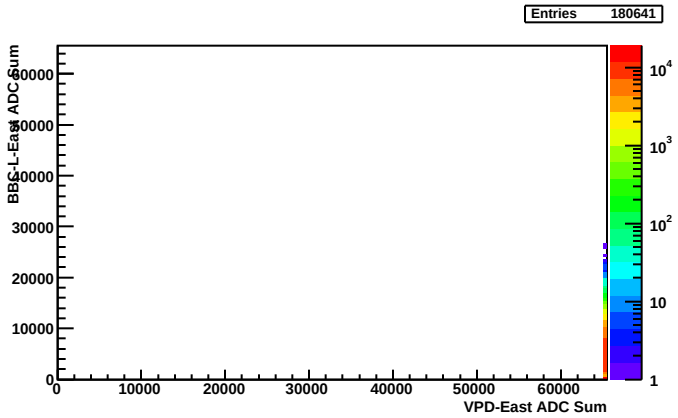

Entries 180641
TOF Mult
VPD-West ADC Sum

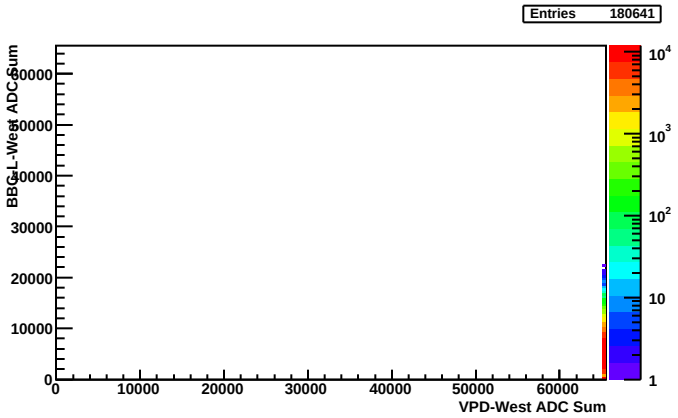

Entries 180641
TOF Mult
ZDC-East ADC Sum Att

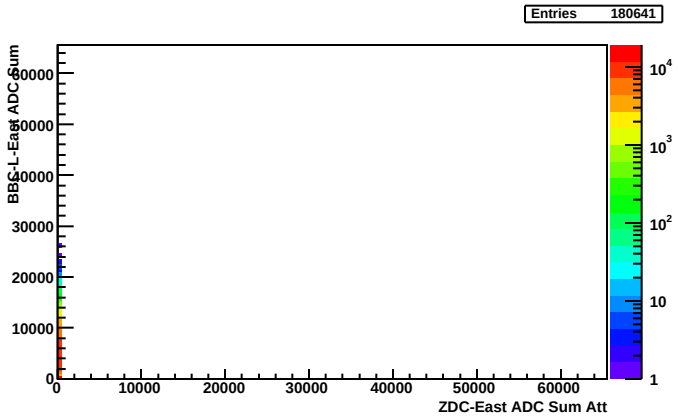

Entries 180641
TOF Mult
ZDC-West ADC Sum Att

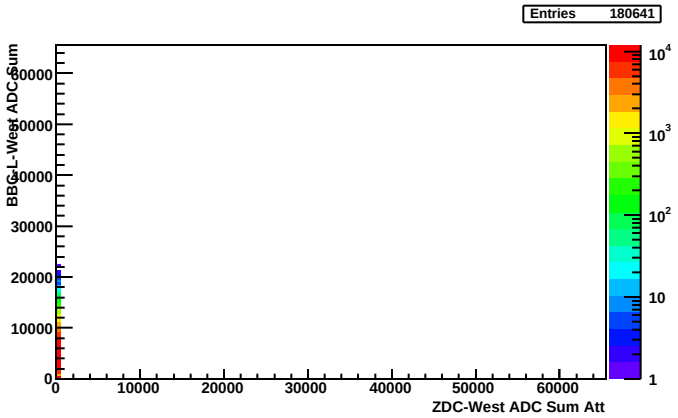

Entries 180641
BBC-L-East ADC Sum
BBC-S-East ADC Sum

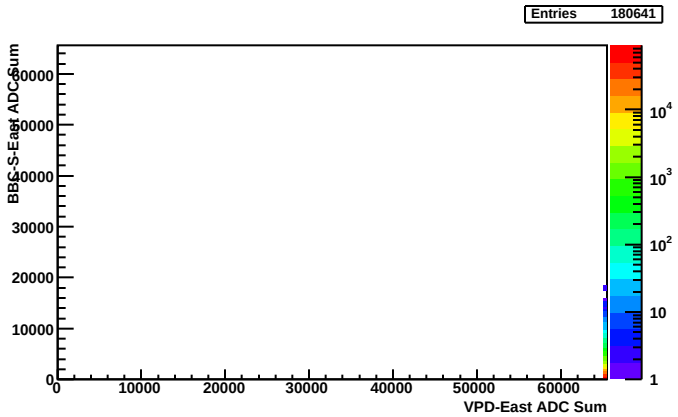

Entries 180641
BBC-L-West ADC Sum
BBC-S-West ADC Sum

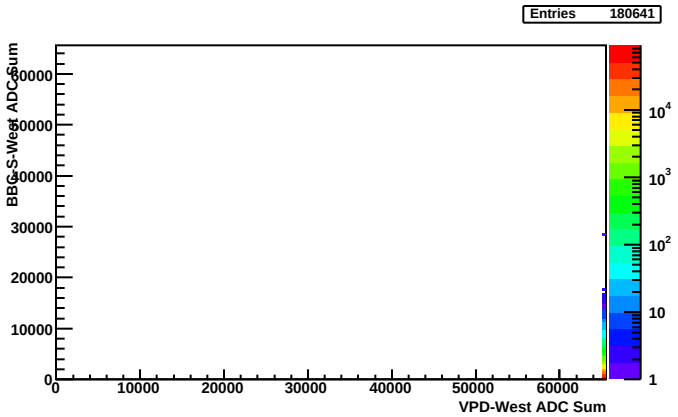

Entries 180641
BBC-L-East ADC Sum
VPD-East ADC Sum

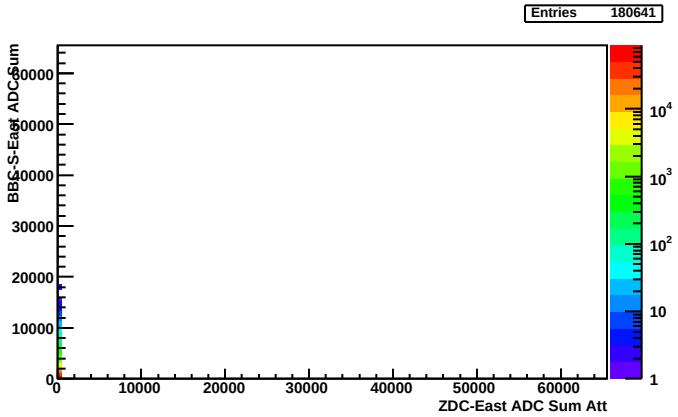

Entries 180641
BBC-L-West ADC Sum
VPD-West ADC Sum

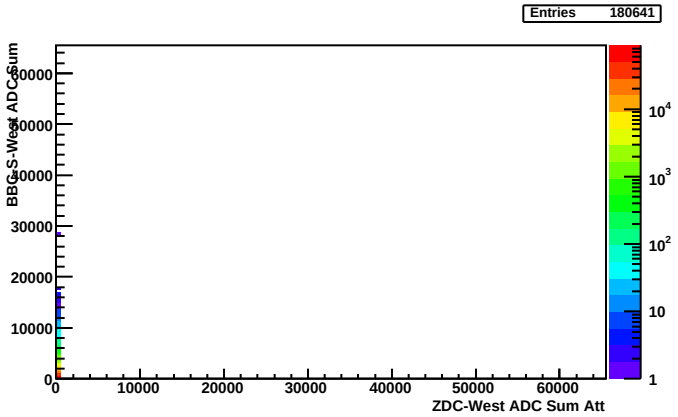

Entries 180641
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

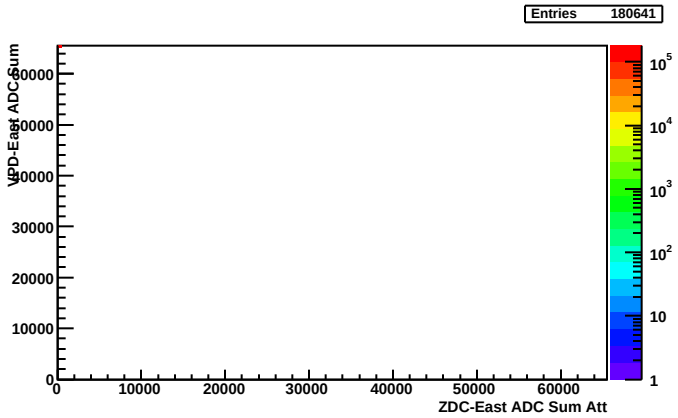

Entries 180641
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

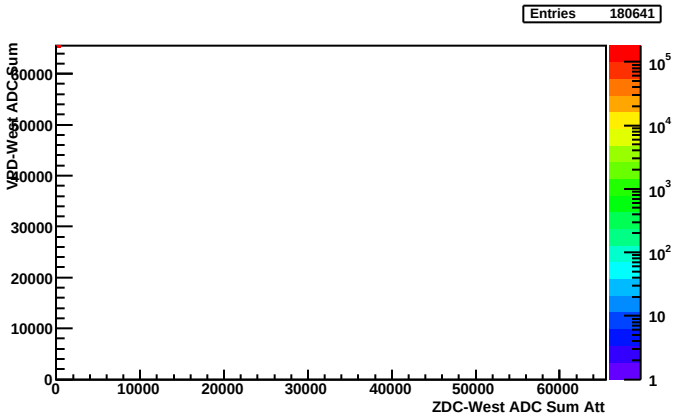

Entries 180641
BBC-S-East ADC Sum
VPD-East ADC Sum

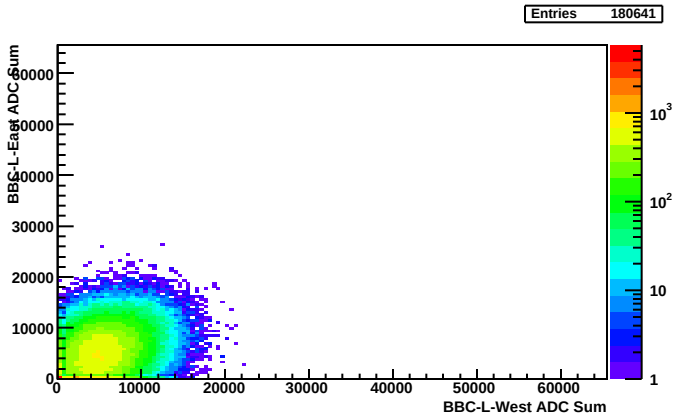

Entries 180641
BBC-S-West ADC Sum
VPD-West ADC Sum

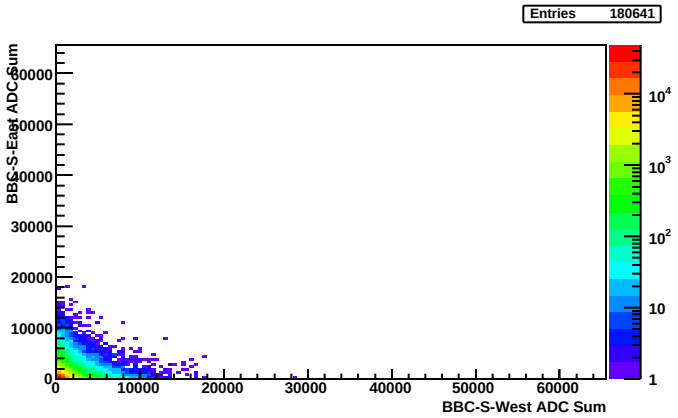

Entries 180641
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

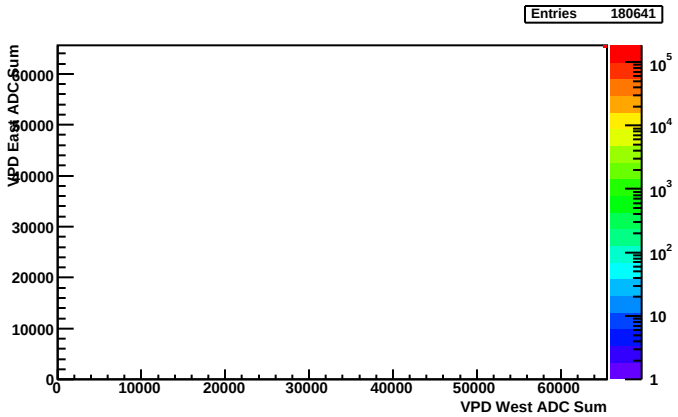

Entries 180641
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

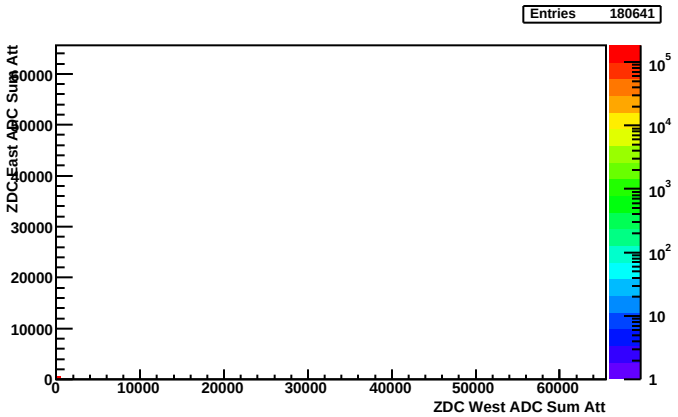

Entries 180641
VPD-East ADC Sum
ZDC-East ADC Sum Att

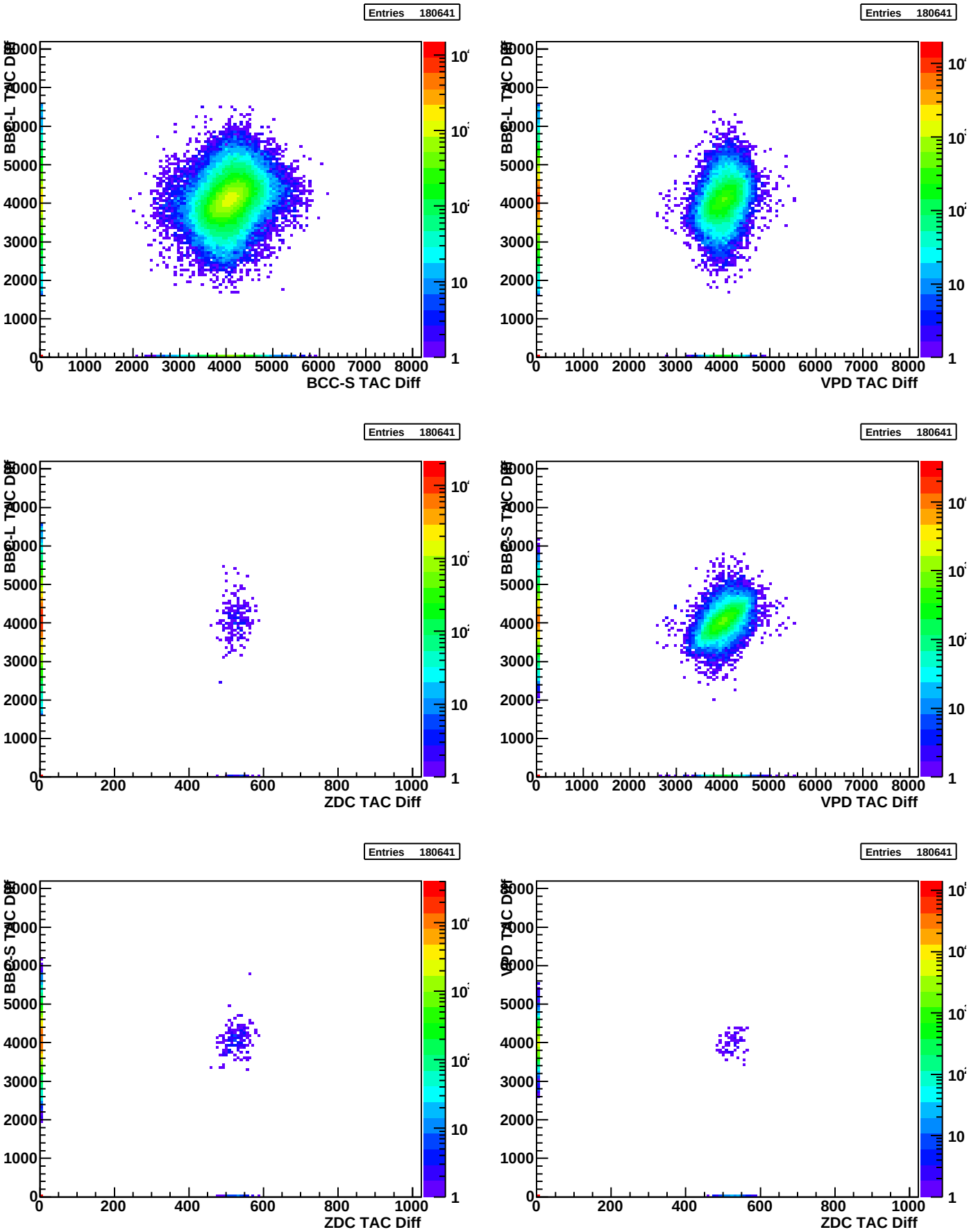

Entries 180641
VPD-West ADC Sum
ZDC-West ADC Sum Att

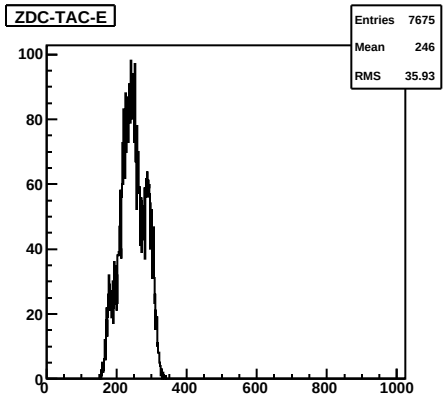

Entries 180641
BBC-L-East ADC Sum
BBC-L-West ADC Sum

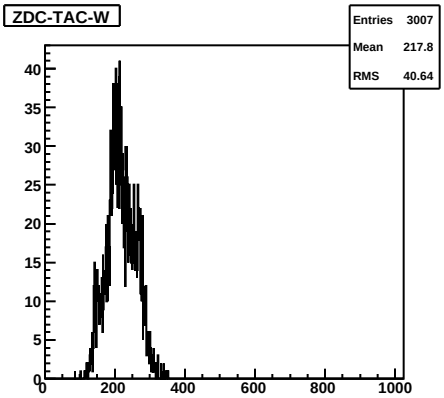

Entries 180641
BBC-S-East ADC Sum
BBC-S-West ADC Sum

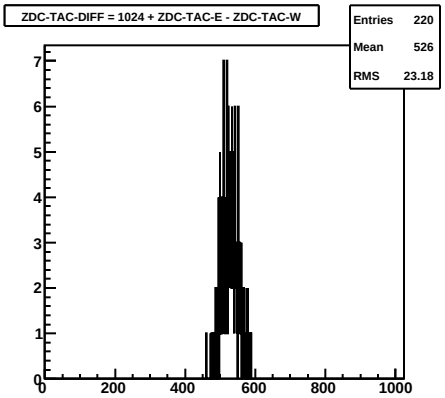

Entries 180641
VPD East ADC Sum
VPD West ADC Sum

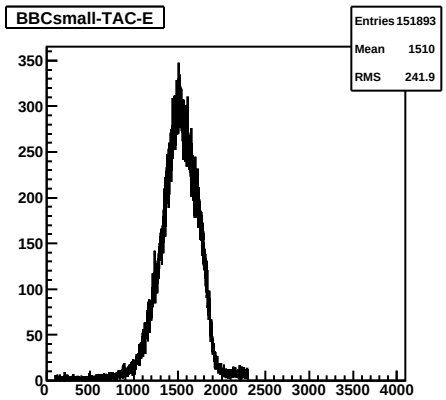

Entries 180641
ZDC East ADC Sum Att
ZDC West ADC Sum Att

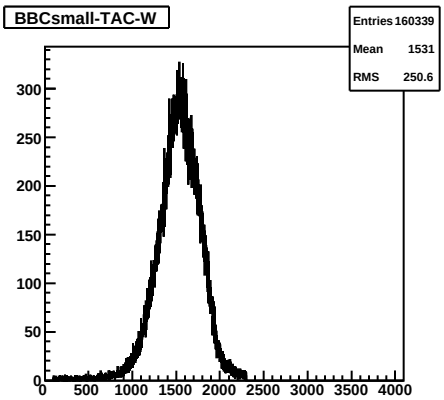

Entries 180641
BBC-L TAC Diff
BCC-S TAC Diff
Entries 180641
BBC-L TAC Diff
VPD TAC Diff
Entries 180641
BBC-L TAC Diff
ZDC TAC Diff
Entries 180641
BBC-S TAC Diff
VPD TAC Diff
Entries 180641
BBC-S TAC Diff
ZDC TAC Diff
Entries 180641
VPD TAC Diff
ZDC TAC Diff

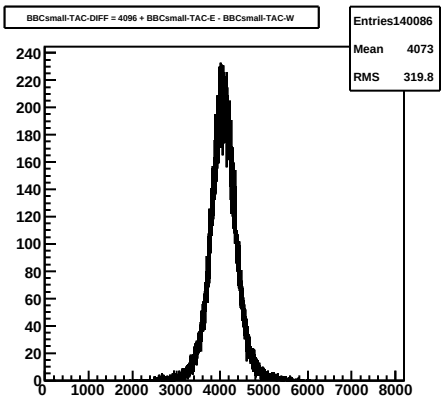

ZDC-TAC-E
Entries 7675
Mean 246
RMS 35.93

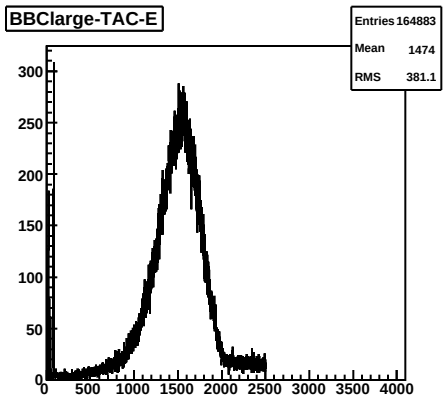

ZDC-TAC-W
Entries 3007
Mean 217.8
RMS 40.64

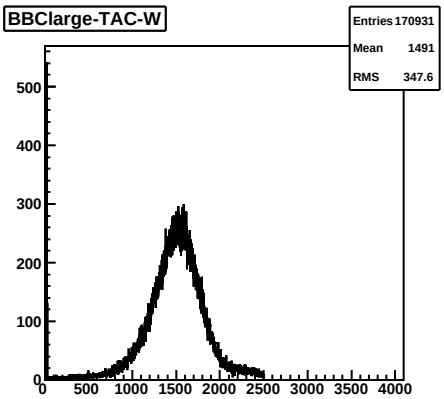

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 220
Mean 526
RMS 23.18

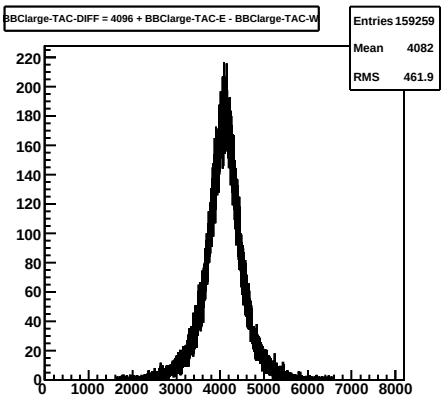

BBCsmall-TAC-E
Entries 151893
Mean 1510
RMS 241.9

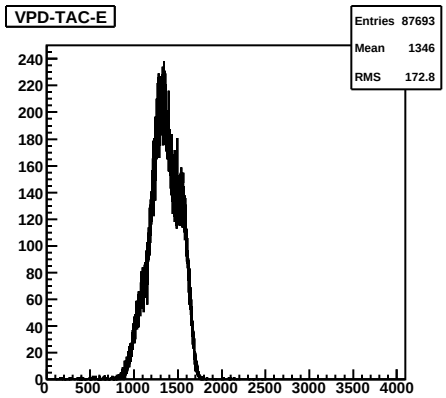

BBCsmall-TAC-W
Entries 160339
Mean 1531
RMS 250.6

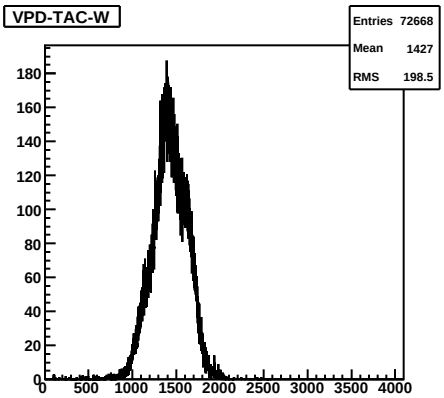

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 140086
Mean 4073
RMS 319.8

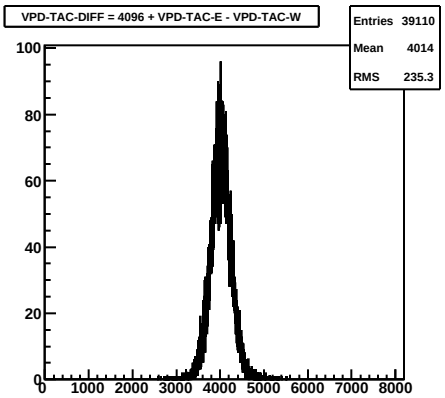

BBClarge-TAC-E
Entries 164883
Mean 1474
RMS 381.1

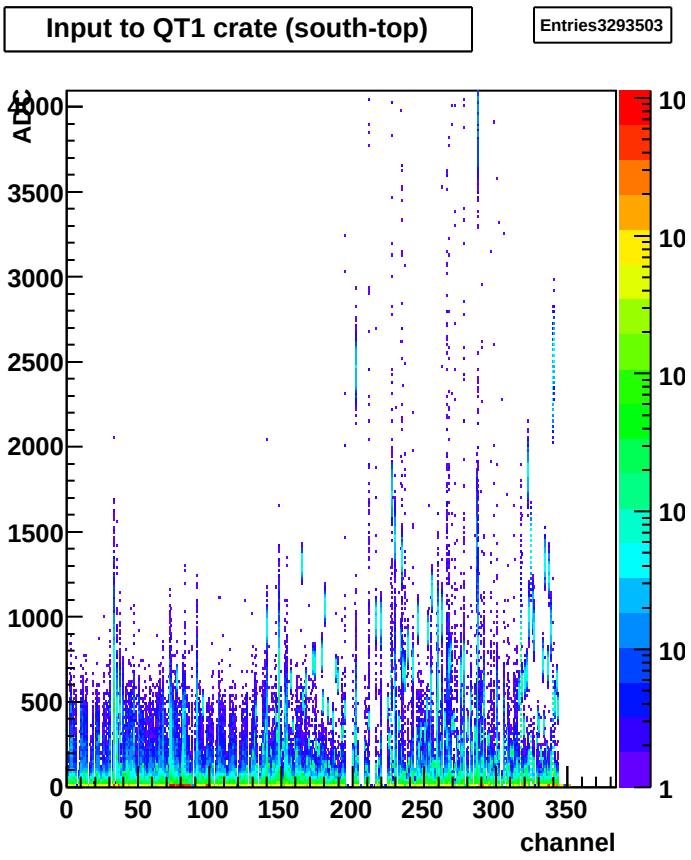

BBClarge-TAC-W
Entries 170931
Mean 1491
RMS 347.6

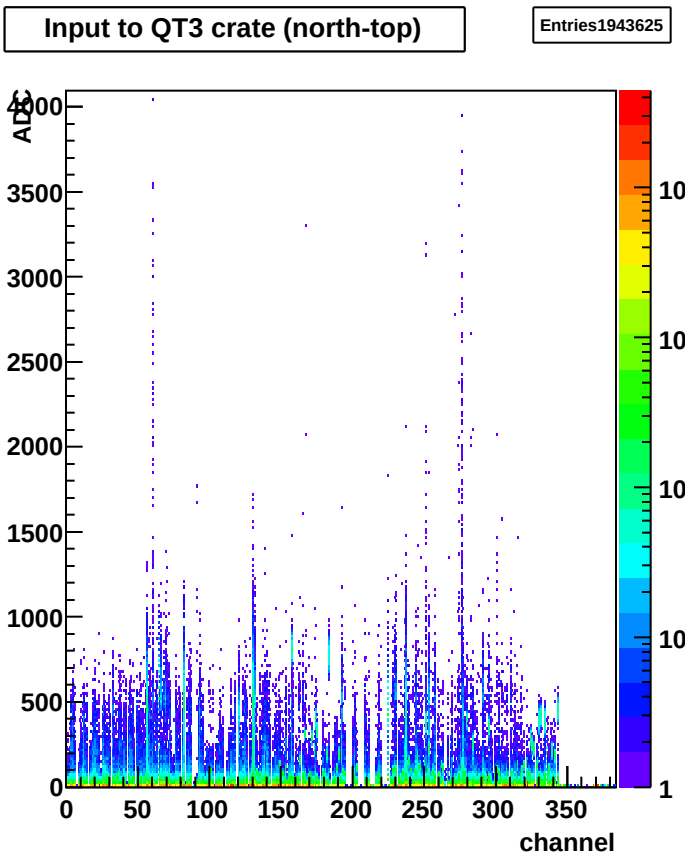

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 159259
Mean 4082
RMS 461.9

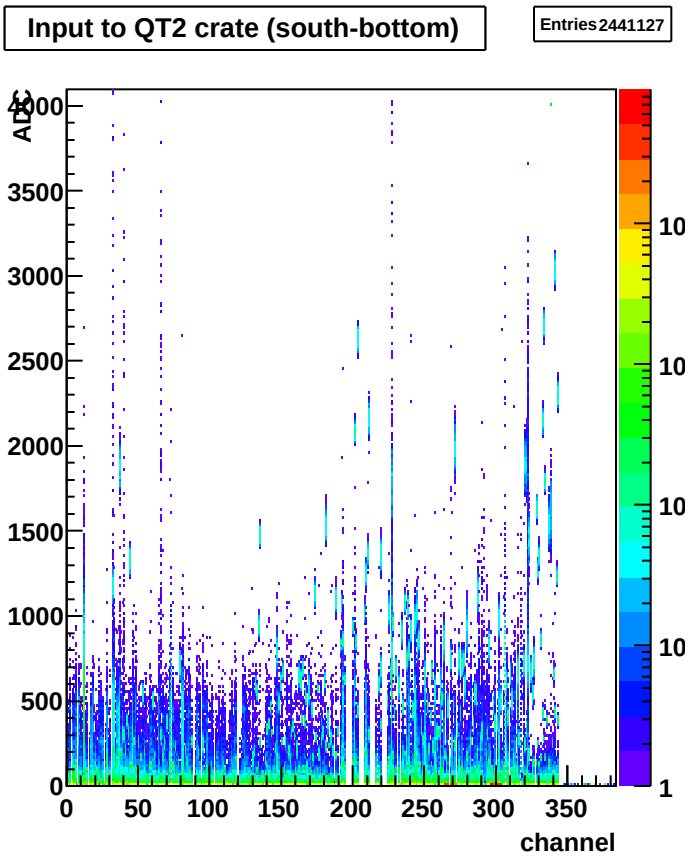

VPD-TAC-E
Entries 87693
Mean 1346
RMS 172.8

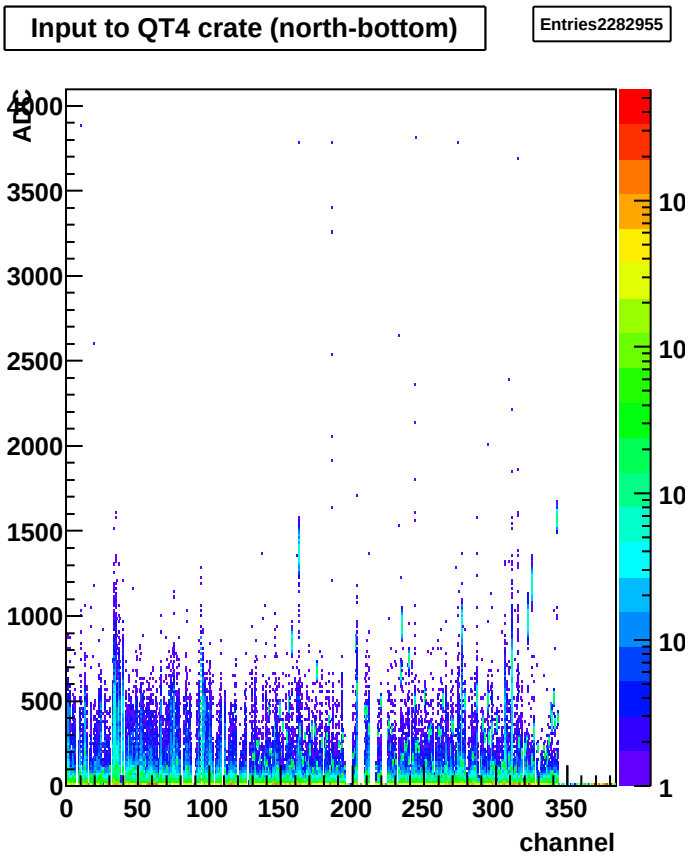

VPD-TAC-W
Entries 72668
Mean 1427
RMS 198.5

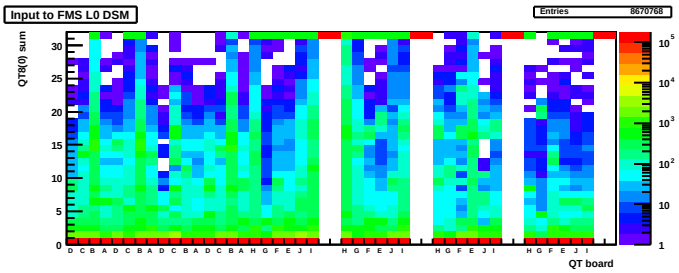

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 39110
Mean 4014
RMS 235.3

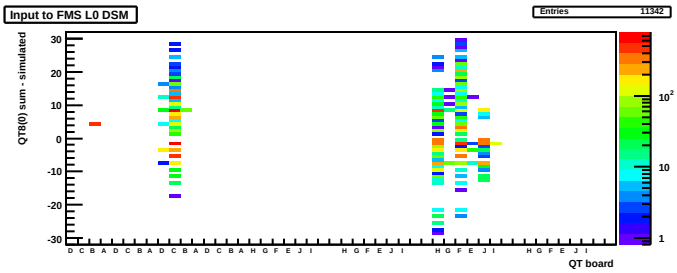

Input to QT1 crate (south-top)
Entries3293503
ADC
channel

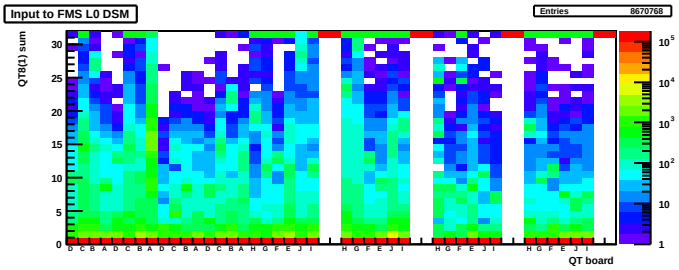

Input to QT3 crate (north-top)
Entries1943625
ADC
channel

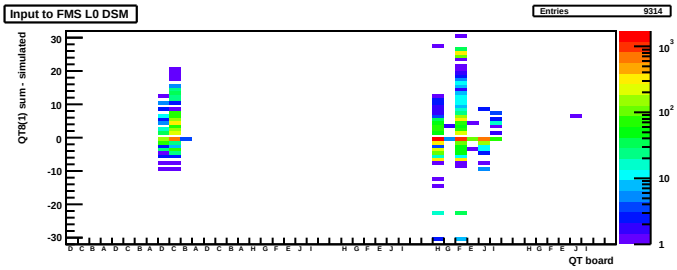

Input to QT2 crate (south-bottom)
Entries2441127
ADC
channel

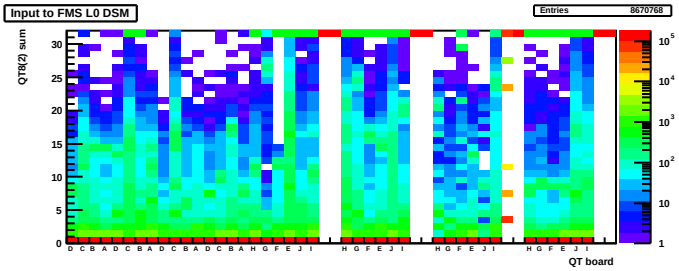

Input to QT4 crate (north-bottom)
Entries2282955
ADC
channel

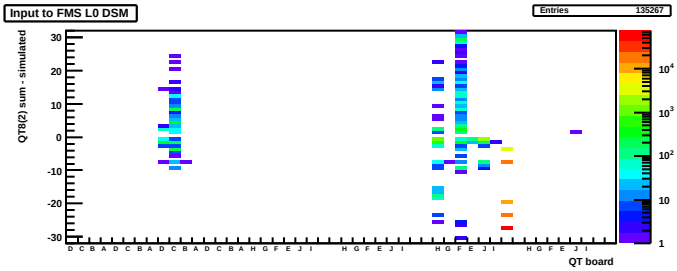

Input to FMS L0 DSM
Entries 8670768
QT8(0) sum
QT board

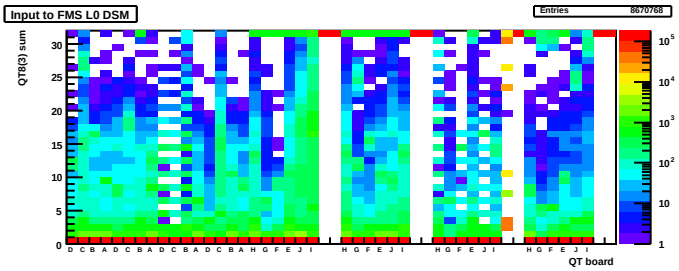

Input to FMS L0 DSM
Entries 11342
QT8(0) sum - simulated
QT board

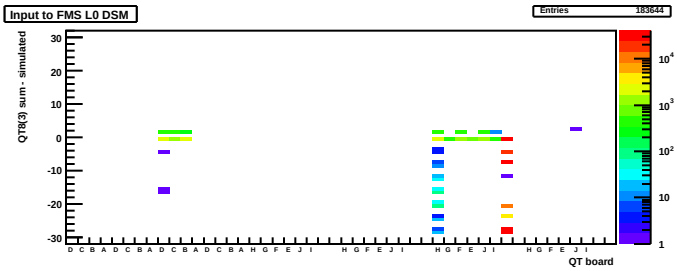

Input to FMS L0 DSM
Entries 8670768
QT8(1) sum
QT board

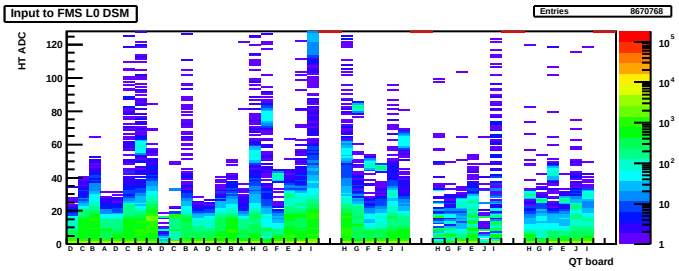

Input to FMS L0 DSM
Entries 9314
QT8(1) sum - simulated
QT board

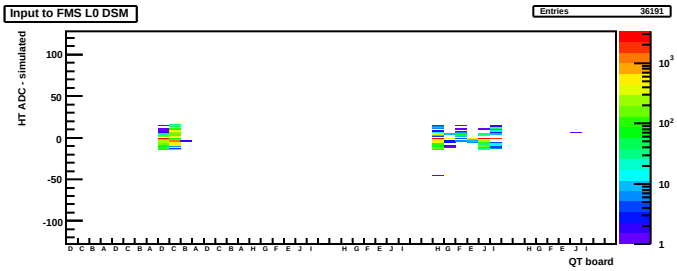

Input to FMS L0 DSM
Entries 8670768
QT8(2) sum
QT board

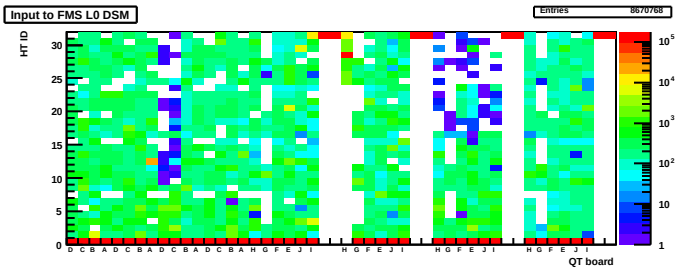

Input to FMS L0 DSM
Entries 135267
QT8(2) sum - simulated
QT board

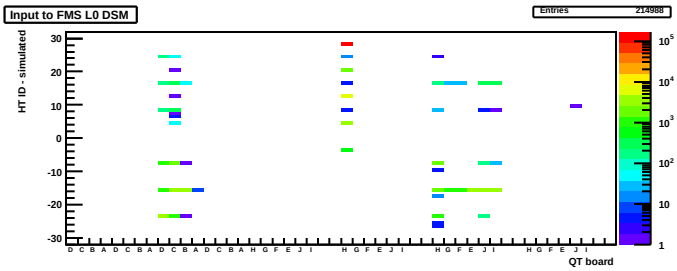

Input to FMS L0 DSM
Entries 8670768
QT8(3) sum
QT board

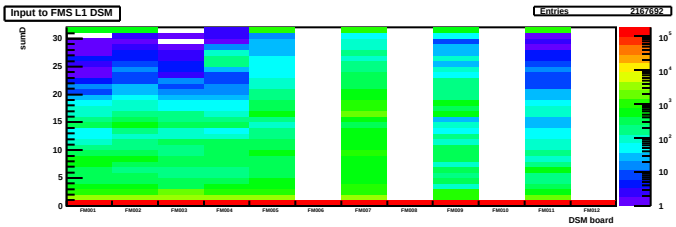

Input to FMS L0 DSM
Entries 183644
QT8(3) sum - simulated
QT board

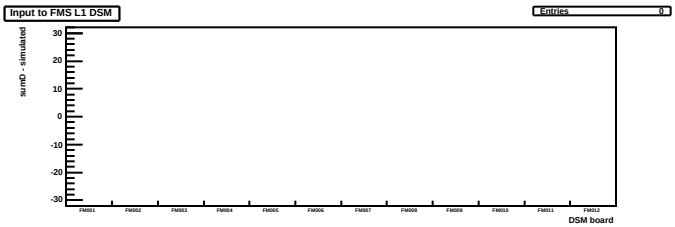

Input to FMS L0 DSM
Entries 8670768
HT ADC
QT board

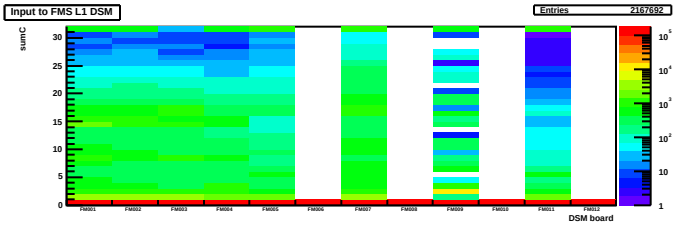

Input to FMS L0 DSM
Entries 36191
HT ADC - simulated
QT board

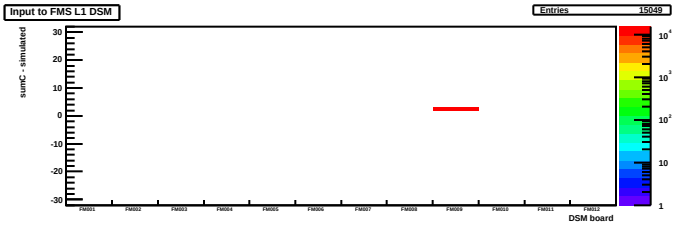

Input to FMS L0 DSM
Entries 8670768
HT ID
QT board

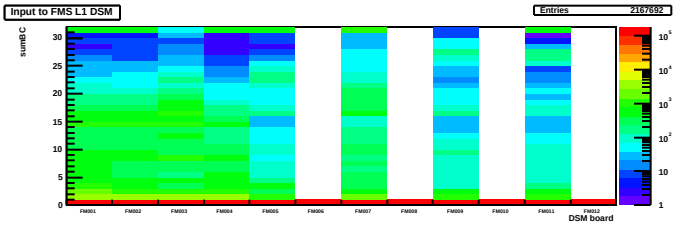

Input to FMS L0 DSM
Entries 214988
HT ID - simulated
QT board

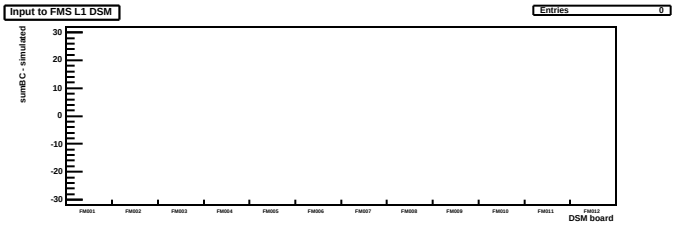

Input to FMS L1 DSM
Entries 2167692
sumD
DSM board

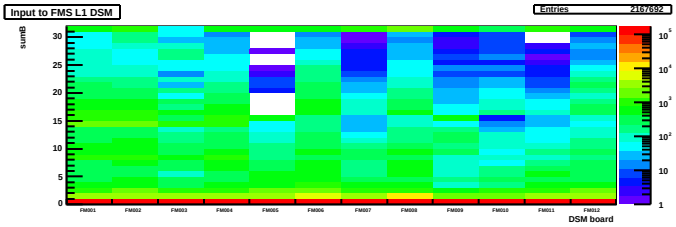

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

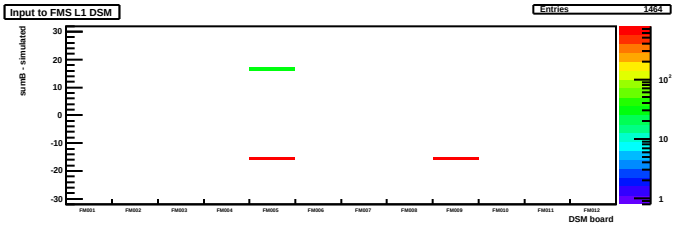

Input to FMS L1 DSM
Entries 2167692
sumC
DSM board

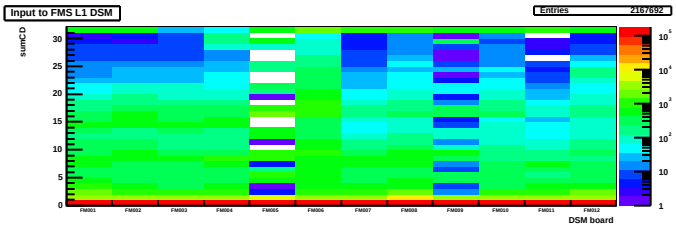

Input to FMS L1 DSM
Entries 15049
sumC - simulated
DSM board

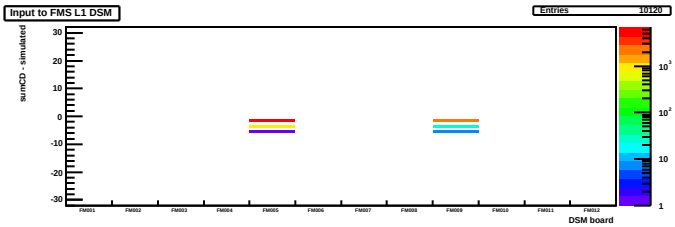

Input to FMS L1 DSM
Entries 2167692
sumBC
DSM board

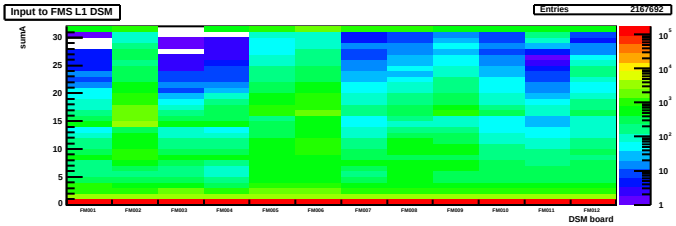

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

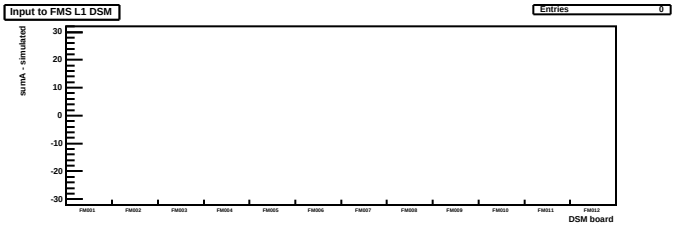

Input to FMS L1 DSM
Entries 2167692
sumB
DSM board

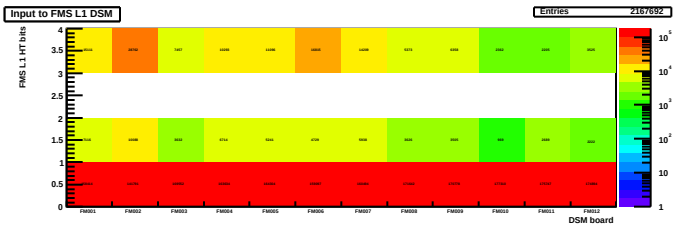

Input to FMS L1 DSM
Entries 1464
sumB - simulated
DSM board

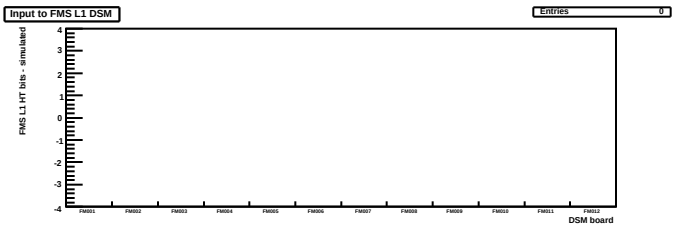

Input to FMS L1 DSM
Entries 2167692
sumCD
DSM board

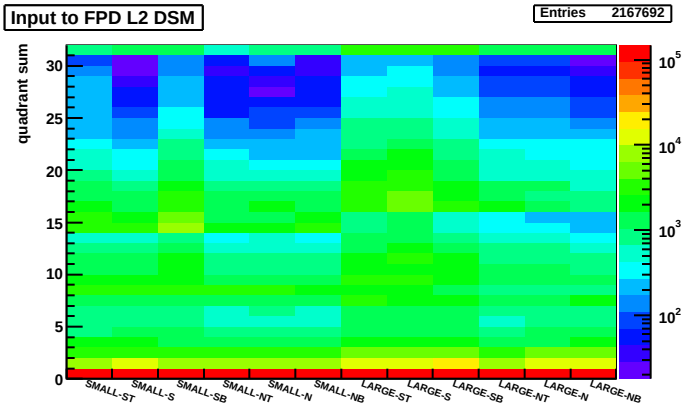

Input to FMS L1 DSM
Entries 10120
sumCD - simulated
DSM board

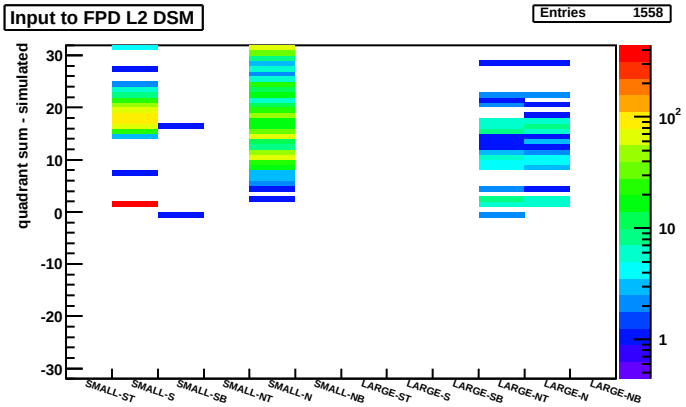

Input to FMS L1 DSM
Entries 2167692
sumA
DSM board

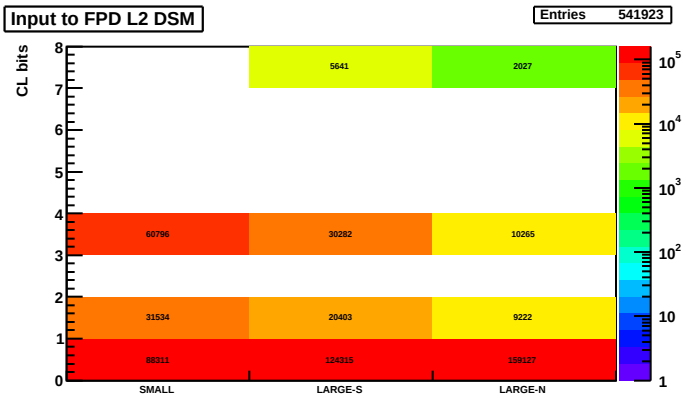

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

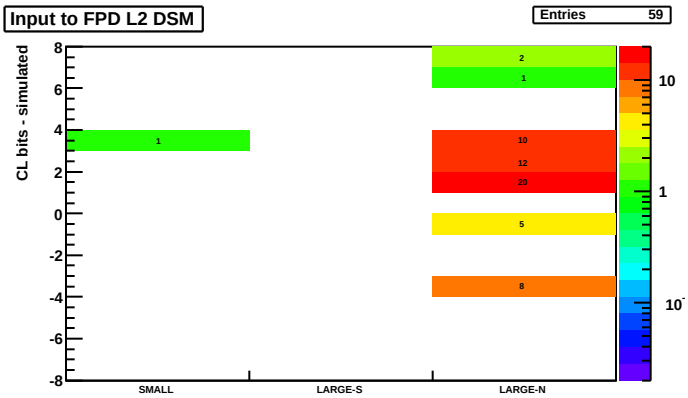

Input to FMS L1 DSM
Entries 2167692
FMS L1 HT bits
DSM board

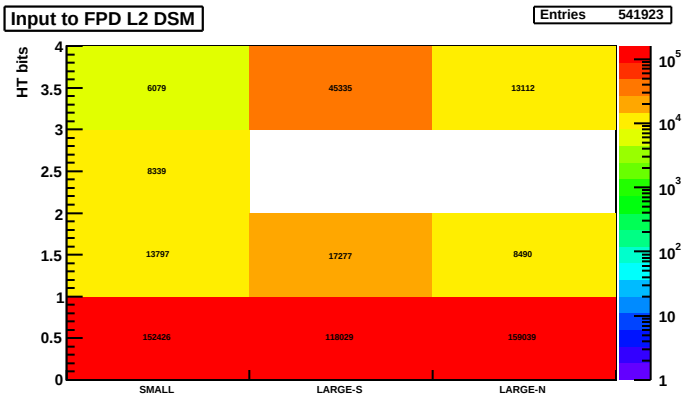

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

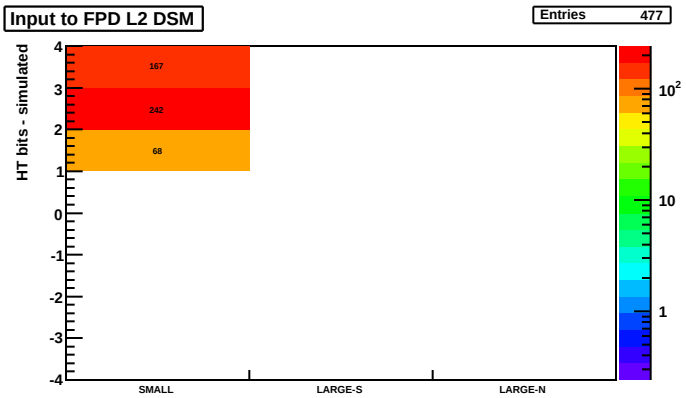

Input to FPD L2 DSM
Entries 2167692
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

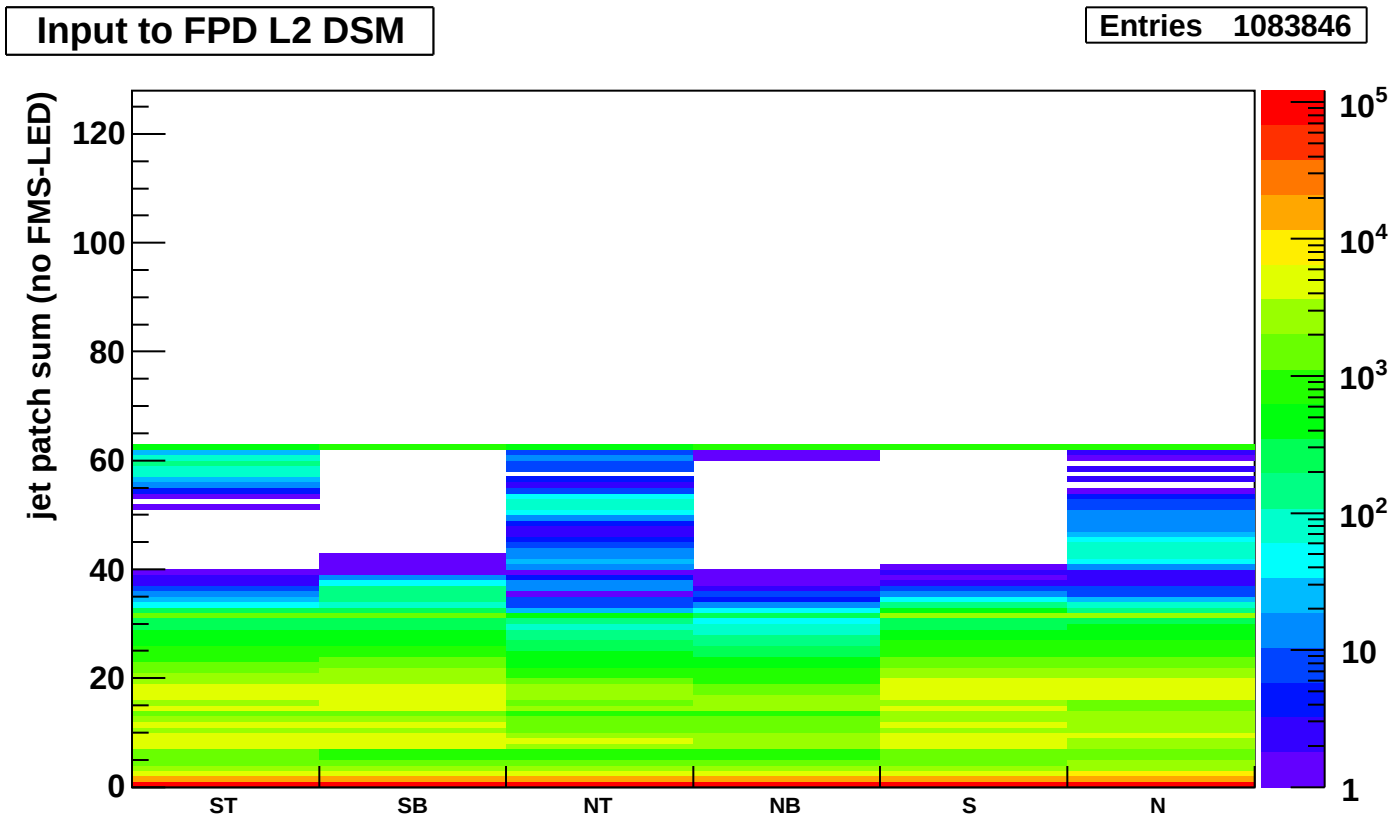

Input to FPD L2 DSM
Entries 1558
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

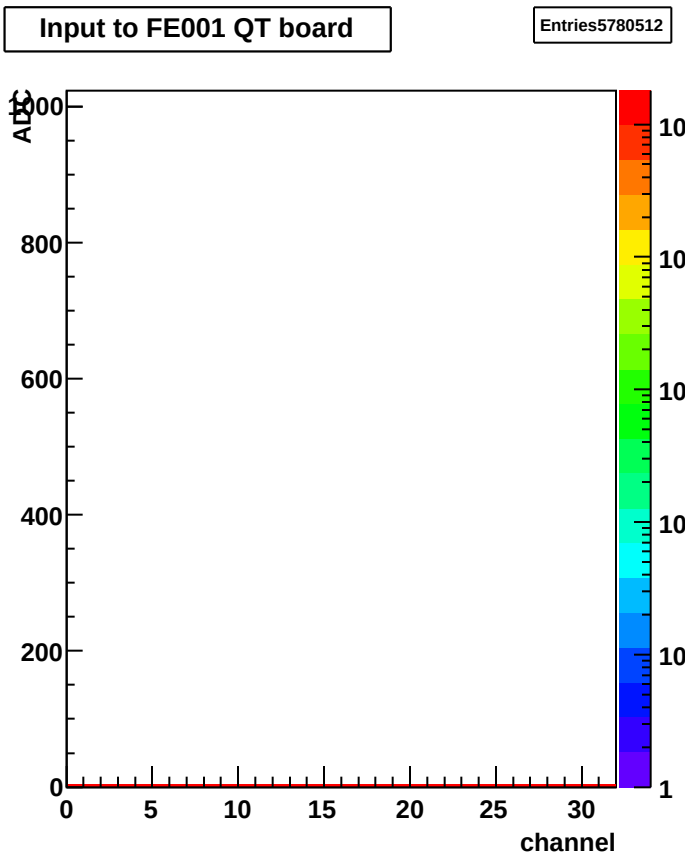

Input to FPD L2 DSM
Entries 541923
CL bits
5641
2027
60796
30282
10265
31534
20403
9222
88311
124315
159127
SMALL
LARGE-S
LARGE-N

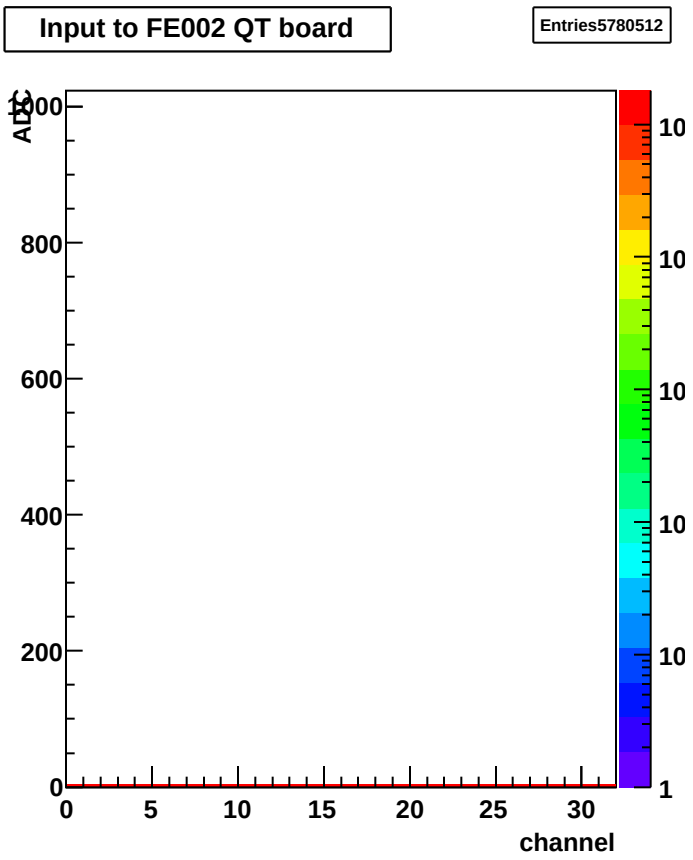

Input to FPD L2 DSM
Entries 59
CL bits - simulated
2
1
1
10
12
20
5
8
SMALL
LARGE-S
LARGE-N

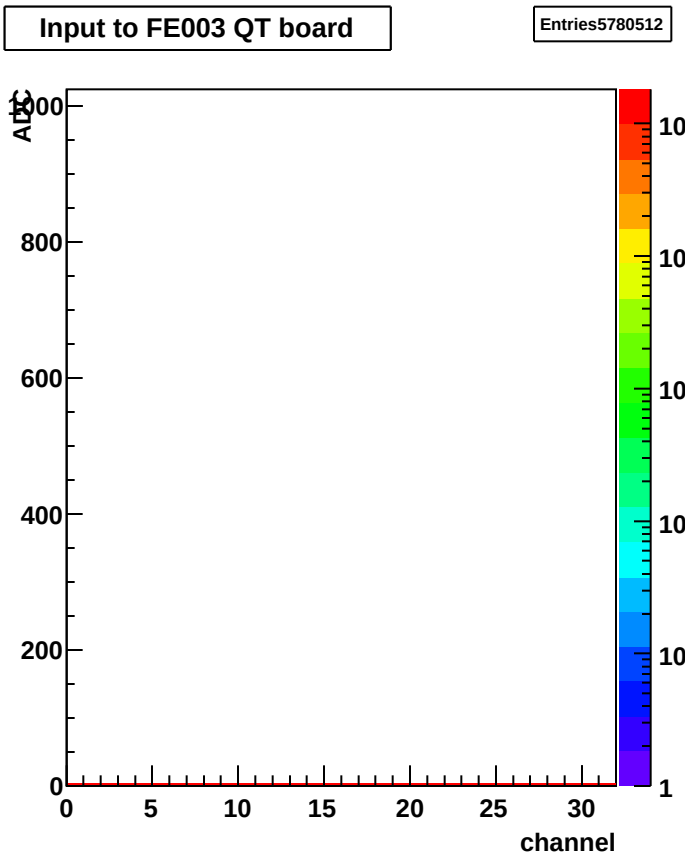

Input to FPD L2 DSM
Entries 541923
HT bits
6079
45335
13112
8339
13797
17277
8490
152426
118029
159039
SMALL
LARGE-S
LARGE-N

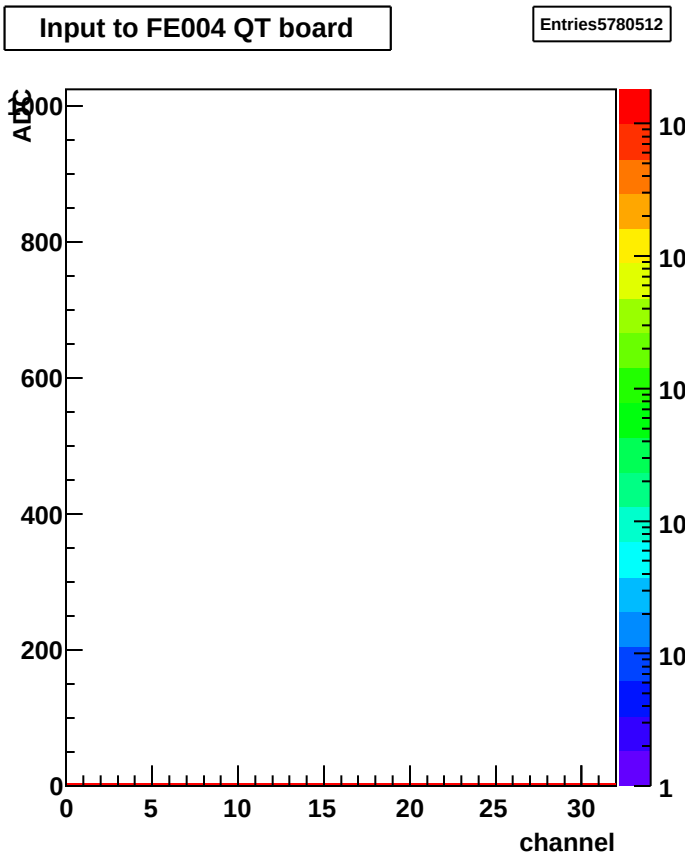

Input to FPD L2 DSM
Entries 477
HT bits - simulated
167
242
68
SMALL
LARGE-S
LARGE-N

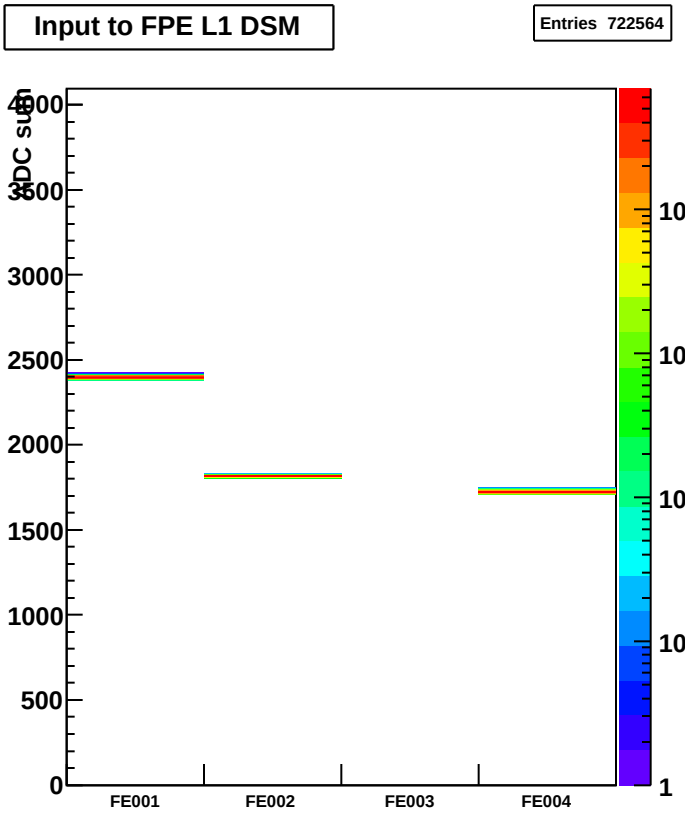

Input to FPD L2 DSM
Entries 1083846
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

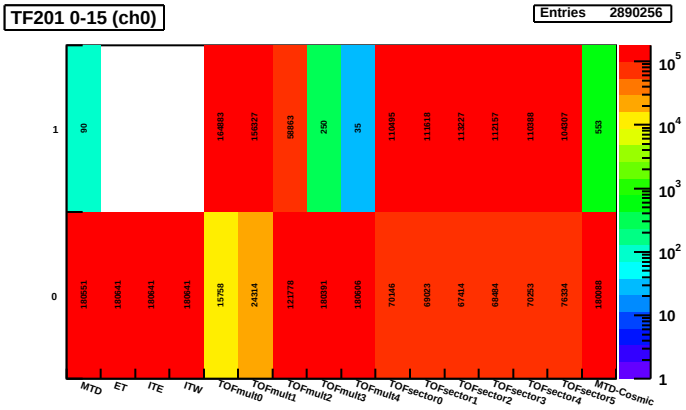

Input to FE001 QT board
Entries5780512
ADC
channel

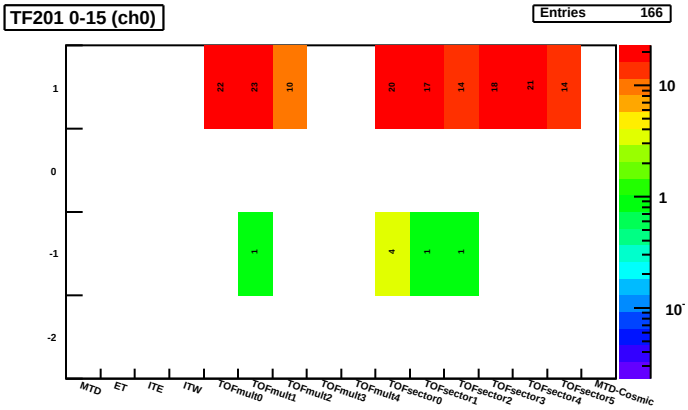

Input to FE002 QT board
Entries5780512
ADC
channel

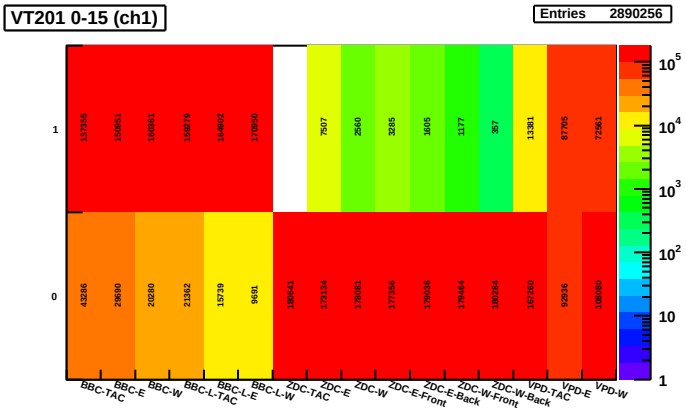

Input to FE003 QT board
Entries5780512
ADC
channel

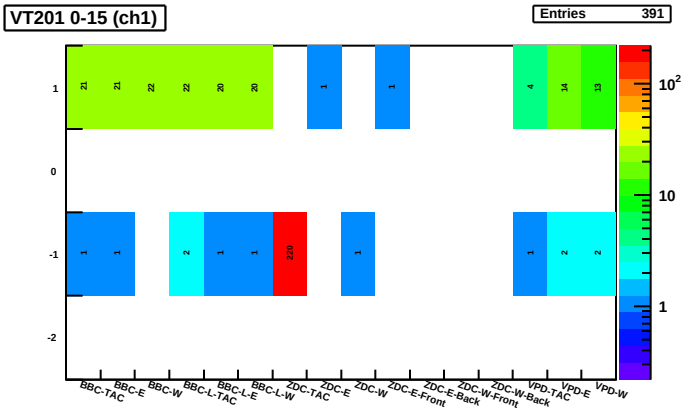

Input to FE004 QT board
Entries5780512
ADC
channel

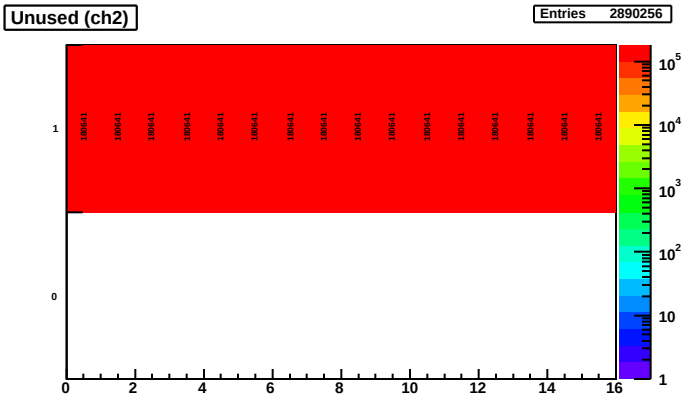

Input to FPE L1 DSM
Entries 722564
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

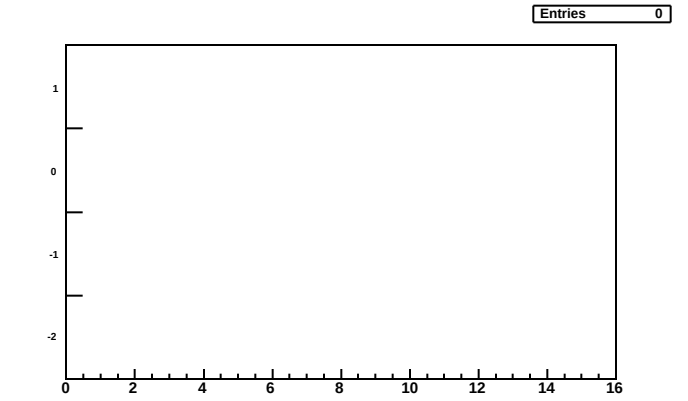

TF201 0-15 (ch0)
Entries 2890256
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

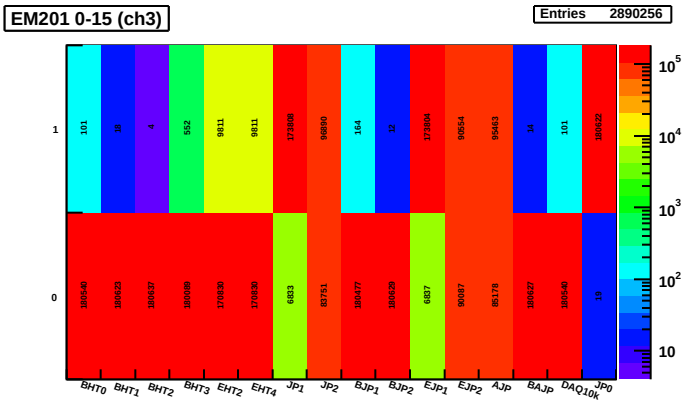

TF201 0-15 (ch0)
Entries 166
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

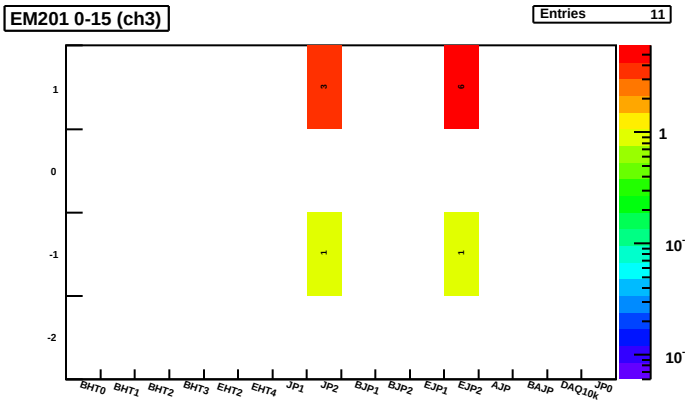

VT201 0-15 (ch1)
Entries 2890256
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 391
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

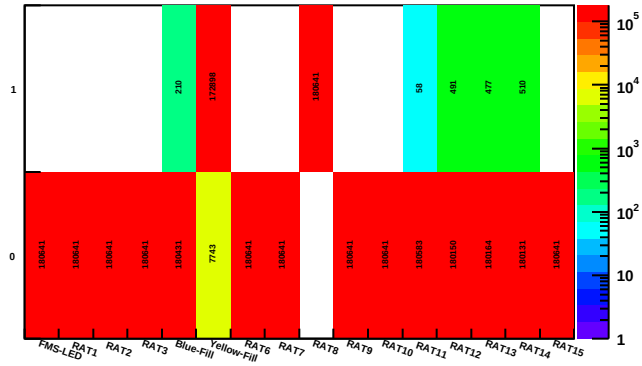

Unused (ch2)
Entries 2890256

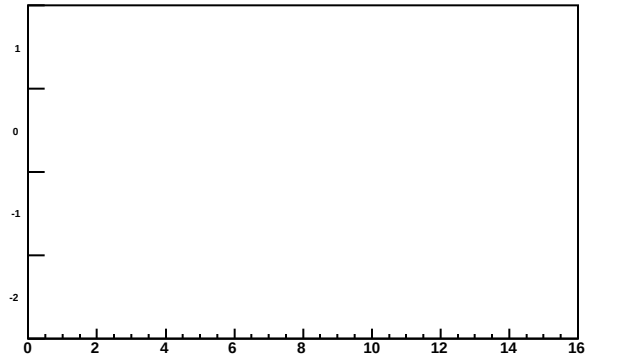

Entries 0


EM201 0-15 (ch3)
Entries 2890256
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

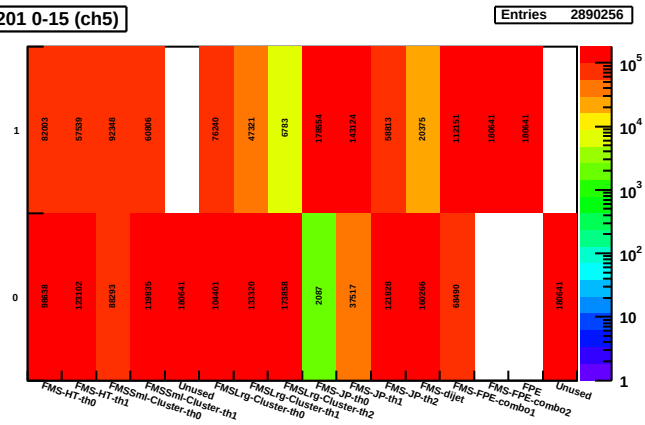

EM201 0-15 (ch3)
Entries 11
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

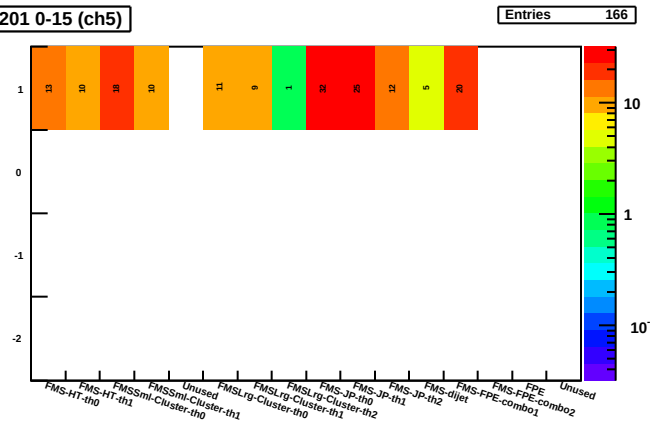




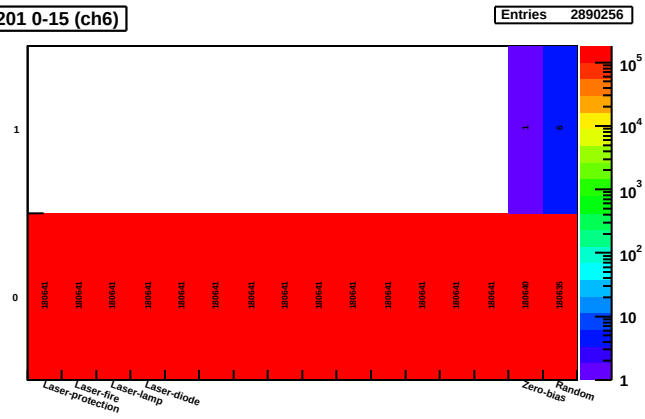
FP201 0-15 (ch5)

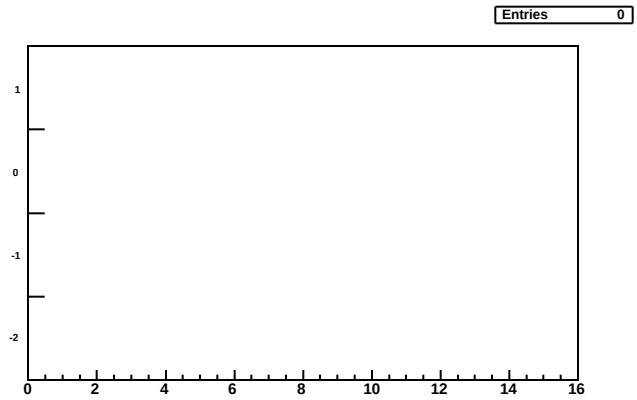
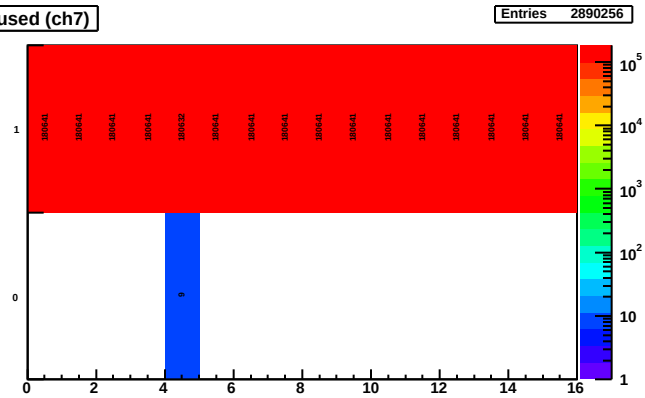
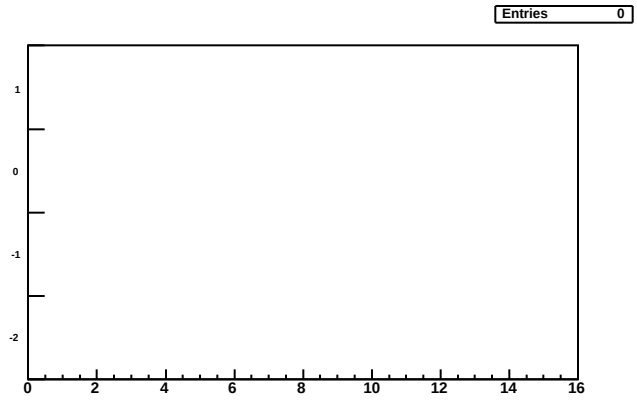


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)