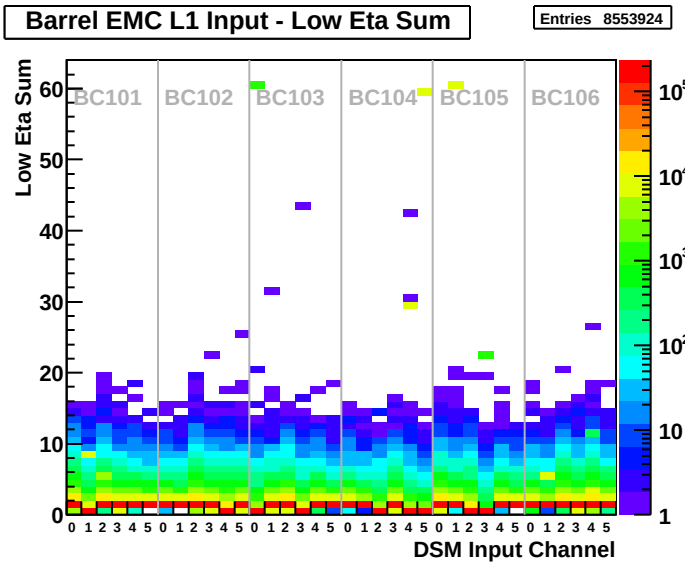
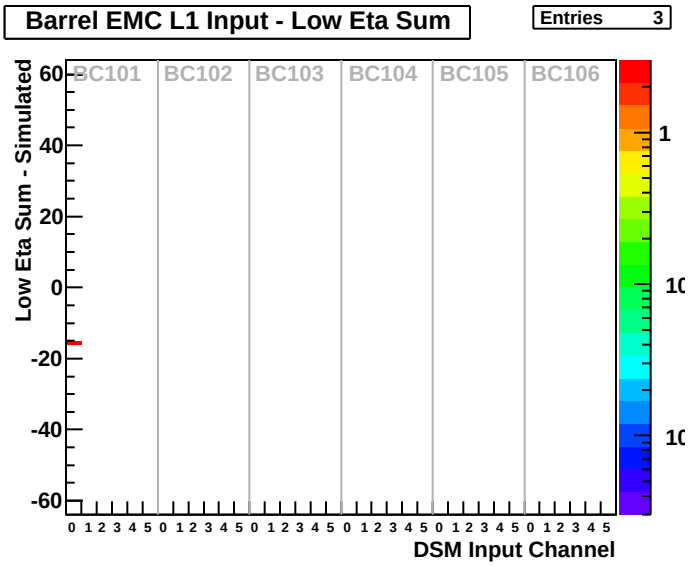
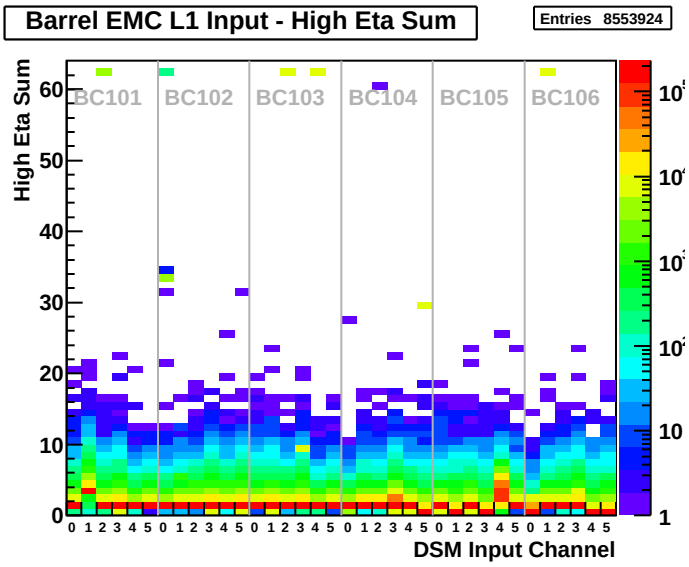
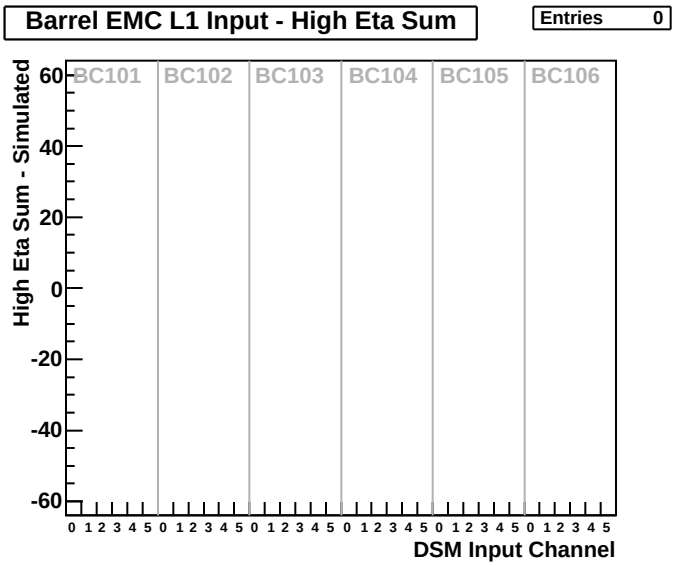
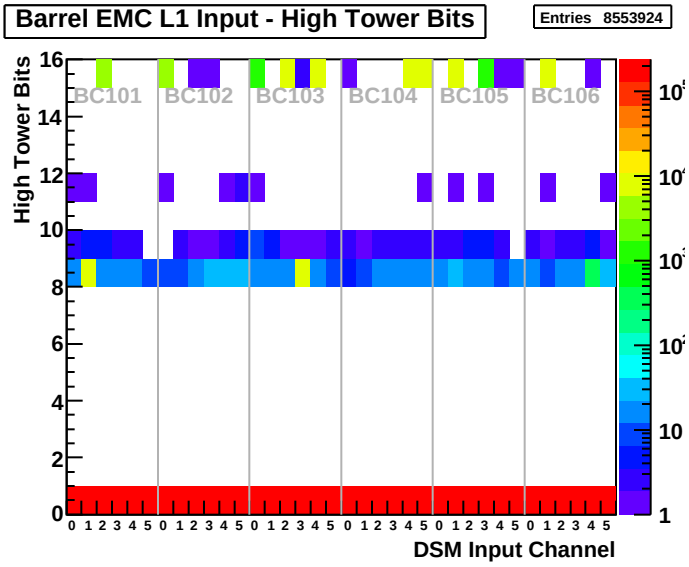
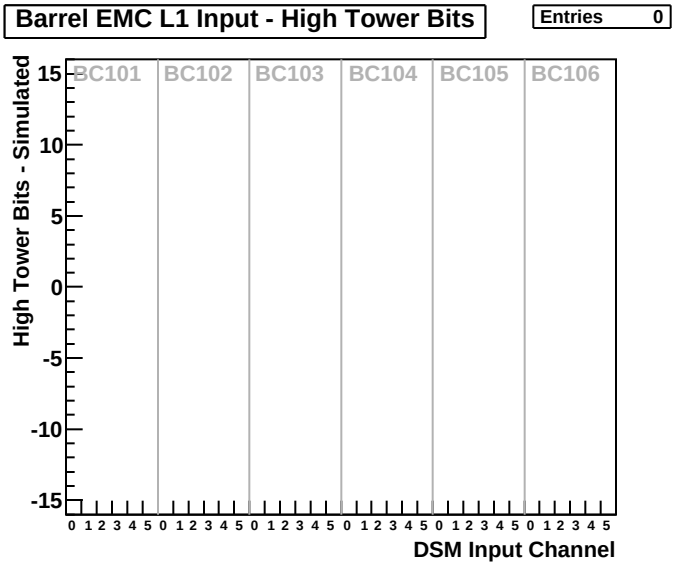

Barrel EMC L1 Input - Low Eta Sum
Entries 8553924
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

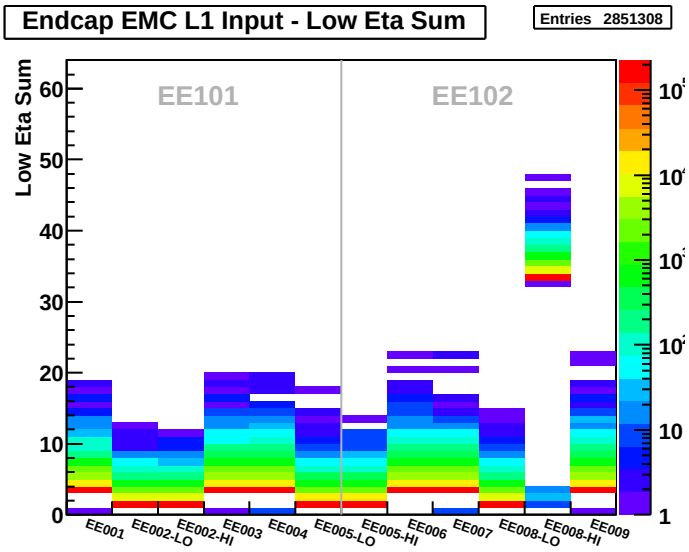

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

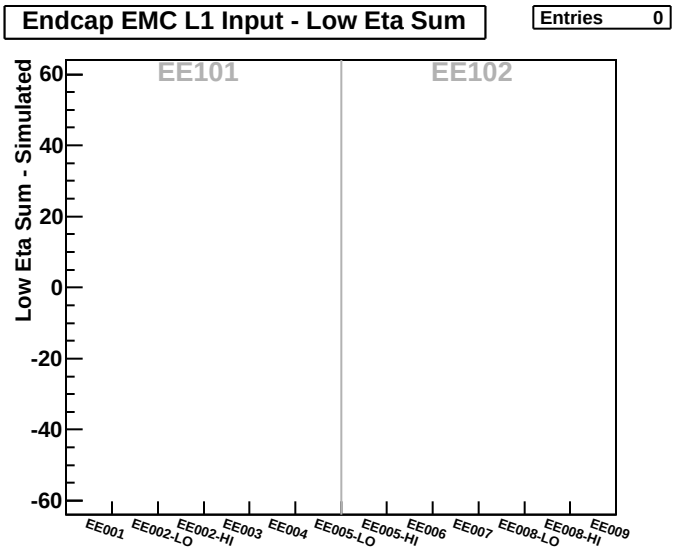

Barrel EMC L1 Input - High Eta Sum
Entries 8553924
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

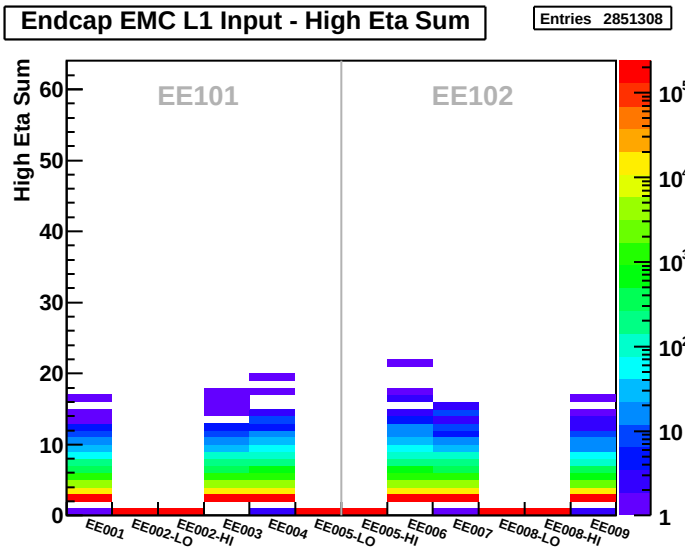

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

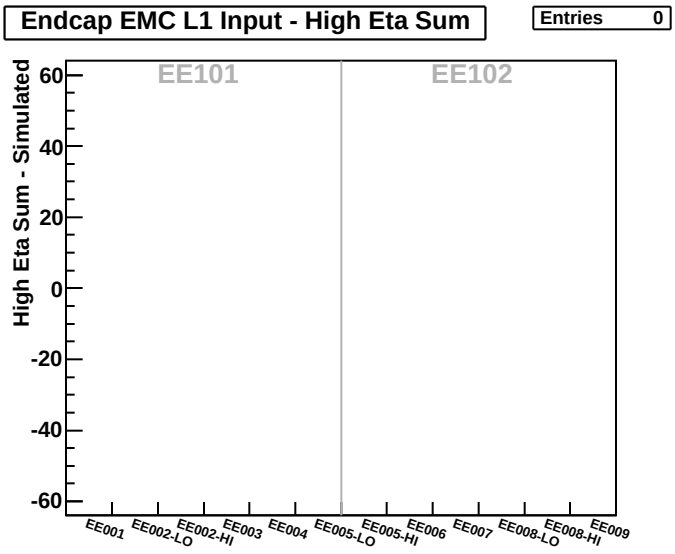

Barrel EMC L1 Input - High Tower Bits
Entries 8553924
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

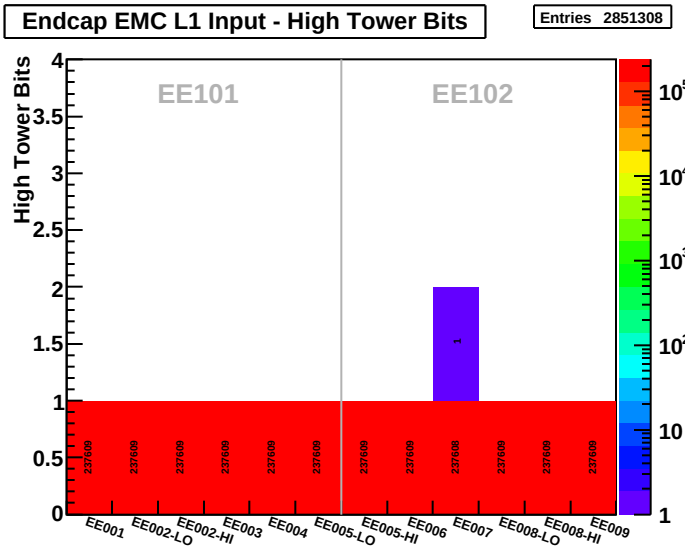

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

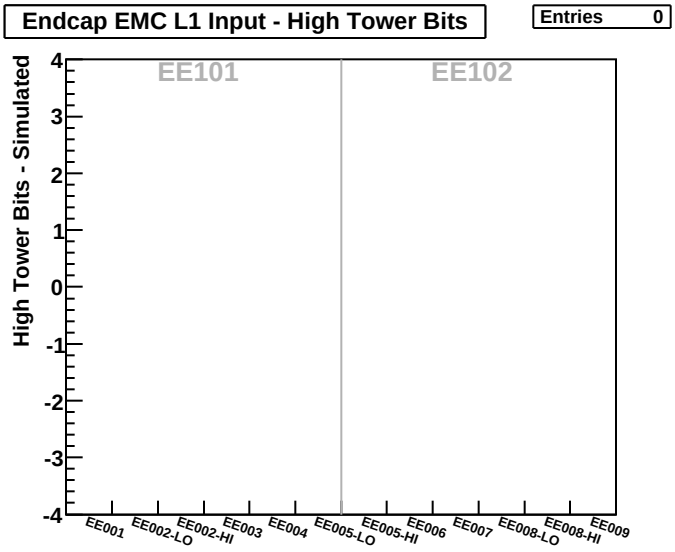

Endcap EMC L1 Input - Low Eta Sum
Entries 2851308
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

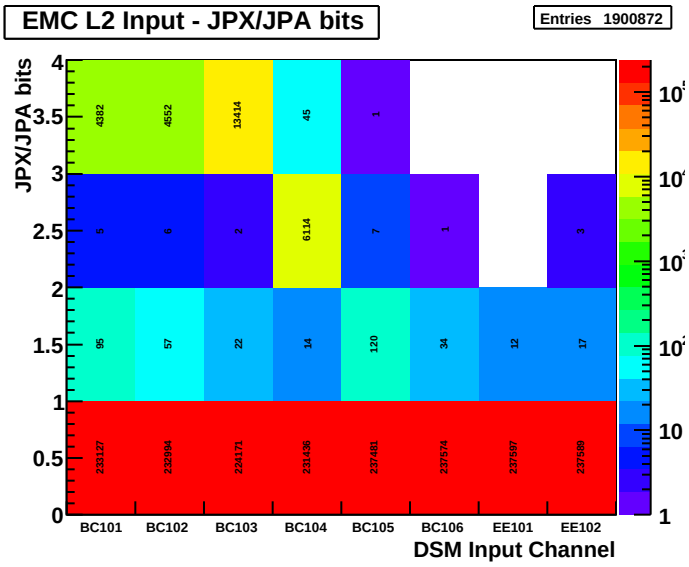

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

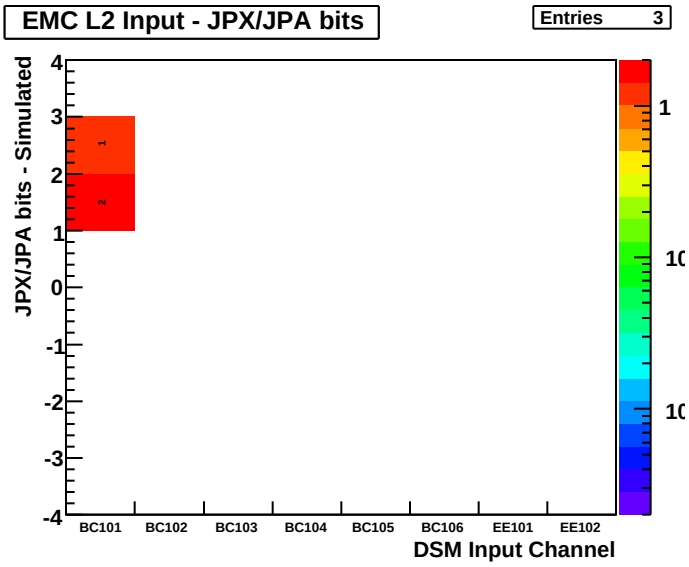

Endcap EMC L1 Input - High Eta Sum
Entries 2851308
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

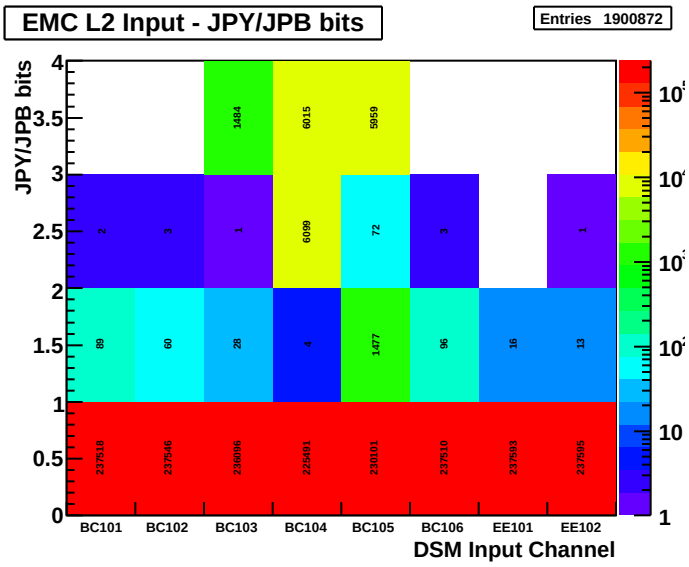

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

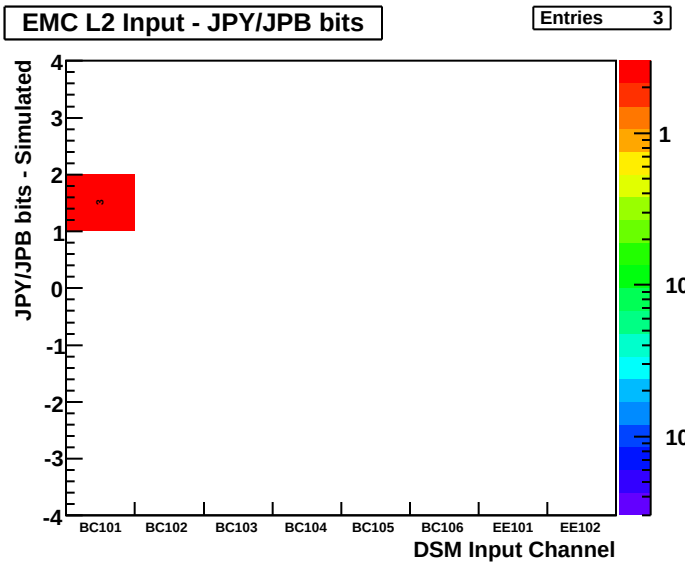

Endcap EMC L1 Input - High Tower Bits
Entries 2851308
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

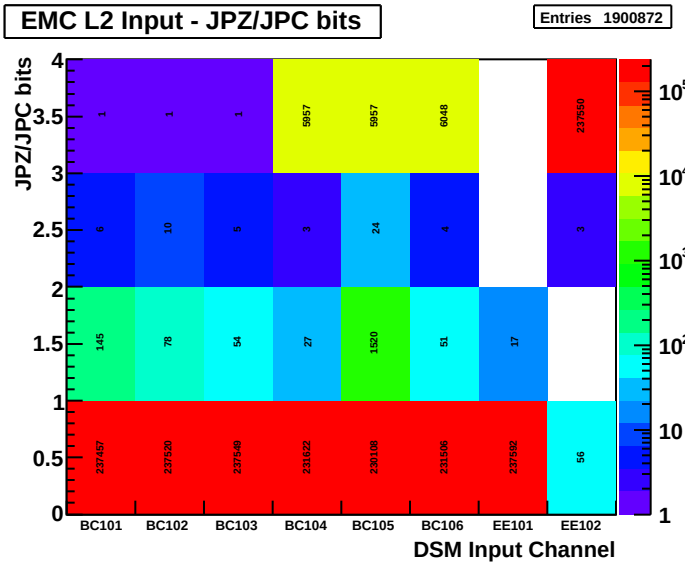

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

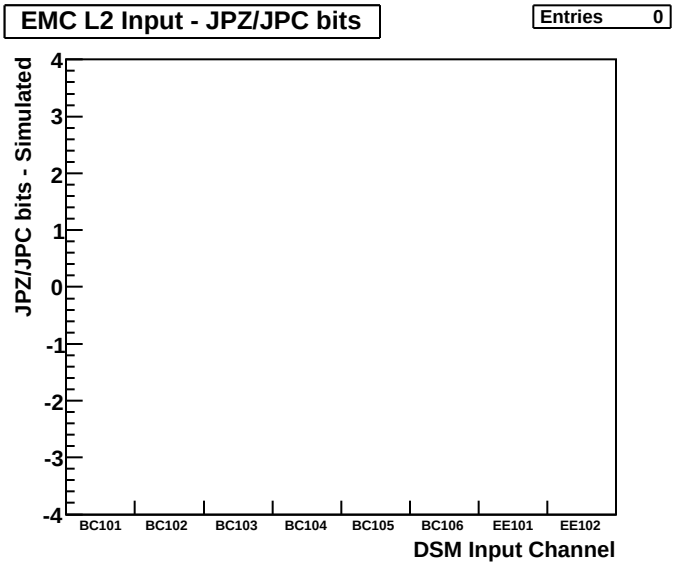

EMC L2 Input - JPX/JPA bits
Entries 1900872
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

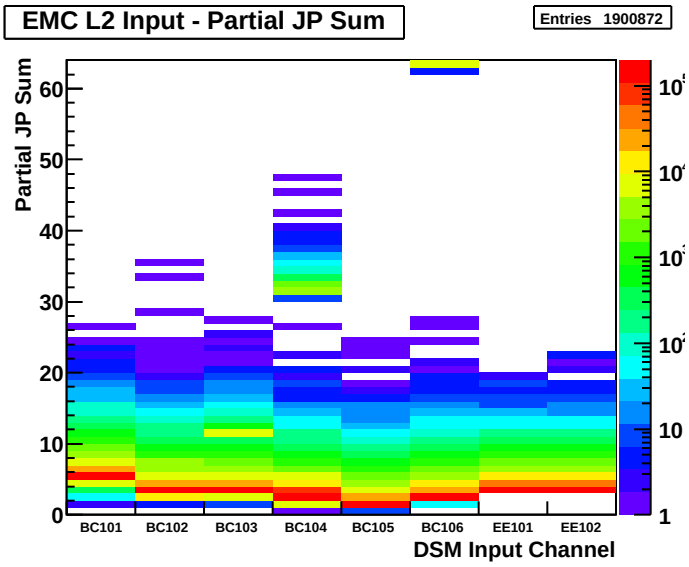

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

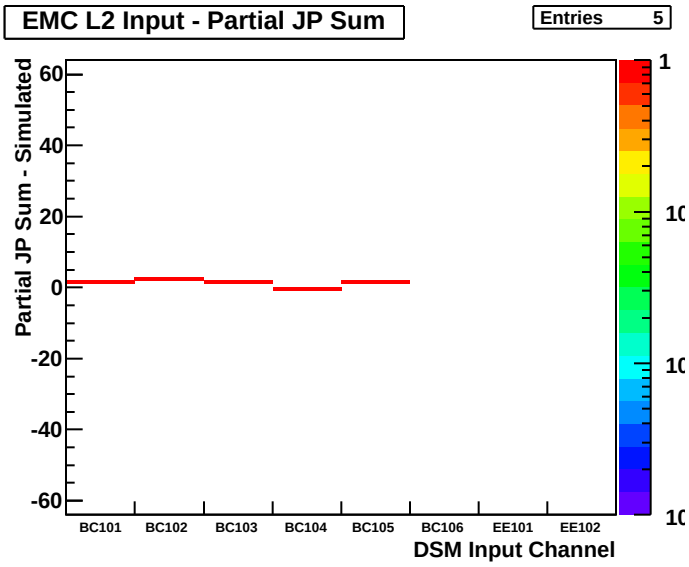

EMC L2 Input - JPY/JPB bits
Entries 1900872
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

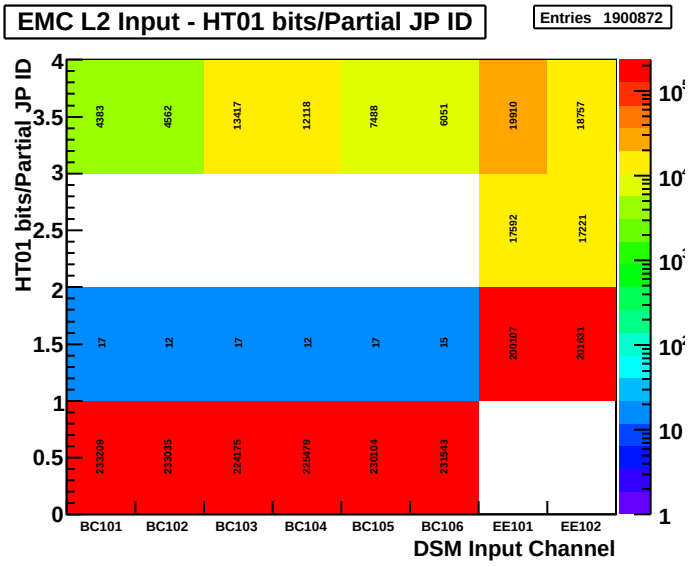

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

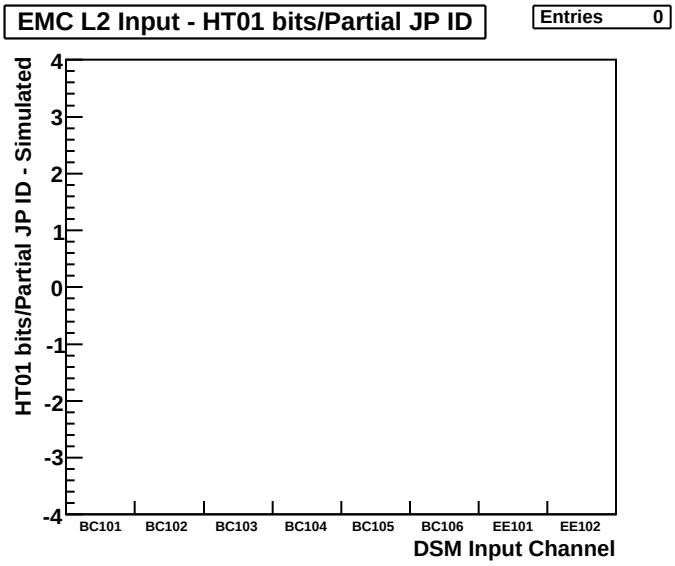

EMC L2 Input - JPZ/JPC bits
Entries 1900872
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

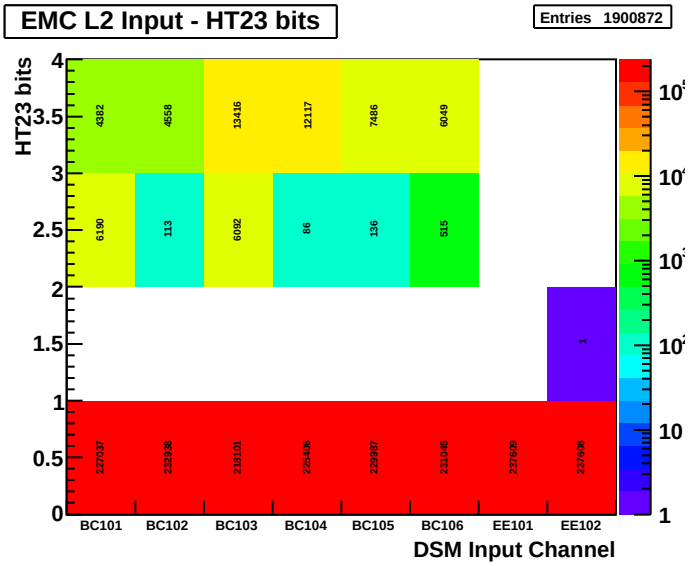

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

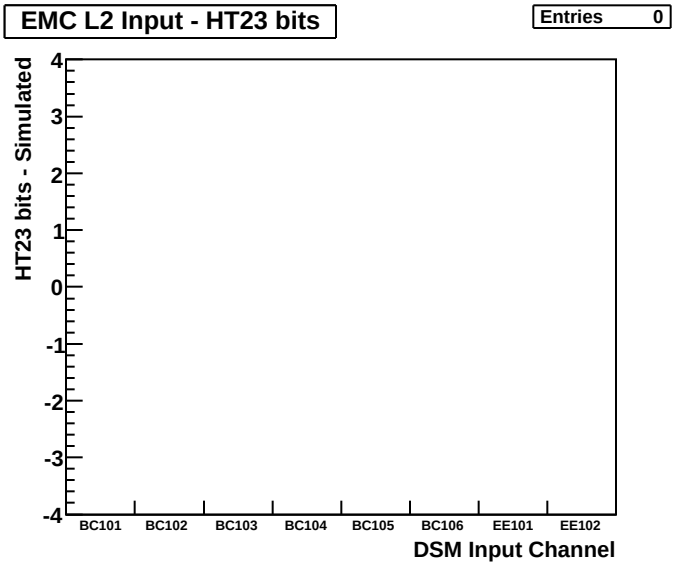

EMC L2 Input - Partial JP Sum
Entries 1900872
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

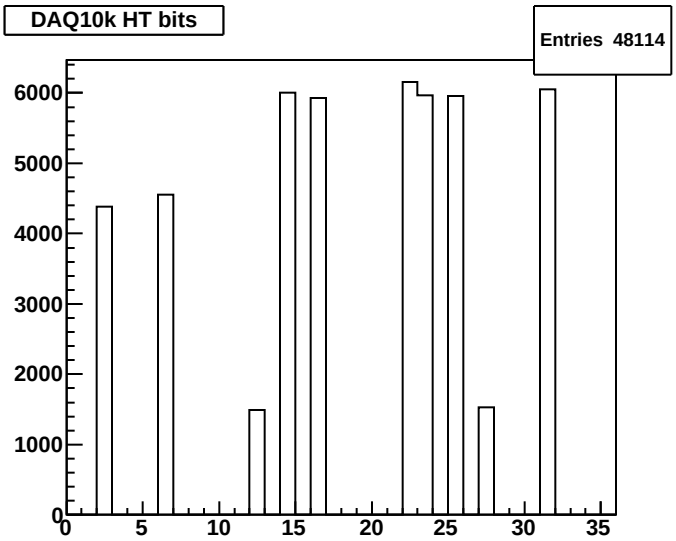

EMC L2 Input - Partial JP Sum
Entries 5
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

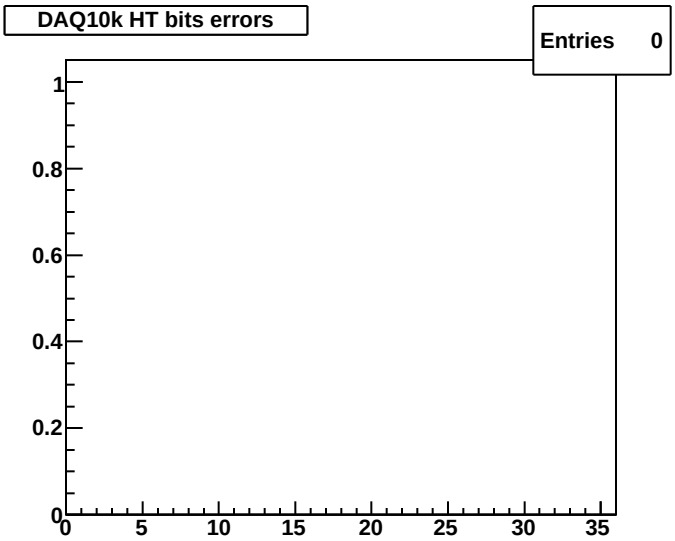

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1900872
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

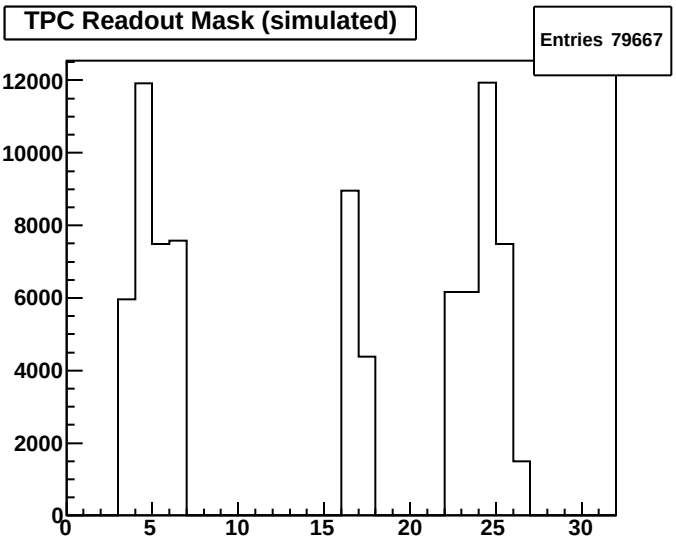

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

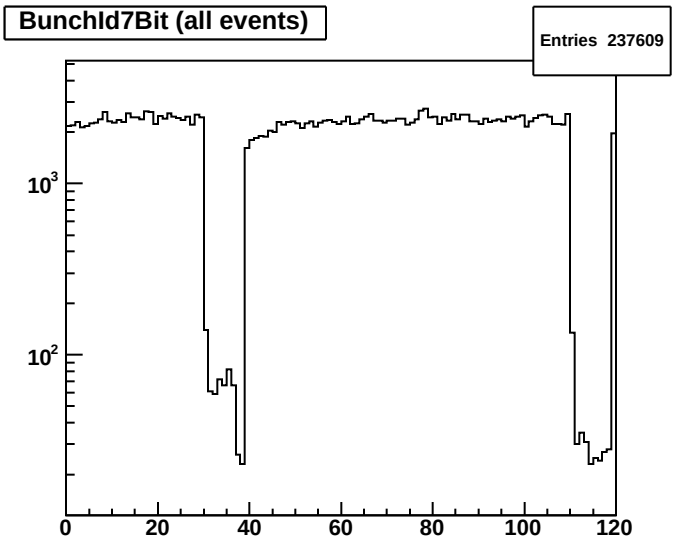

EMC L2 Input - HT23 bits
Entries 1900872
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

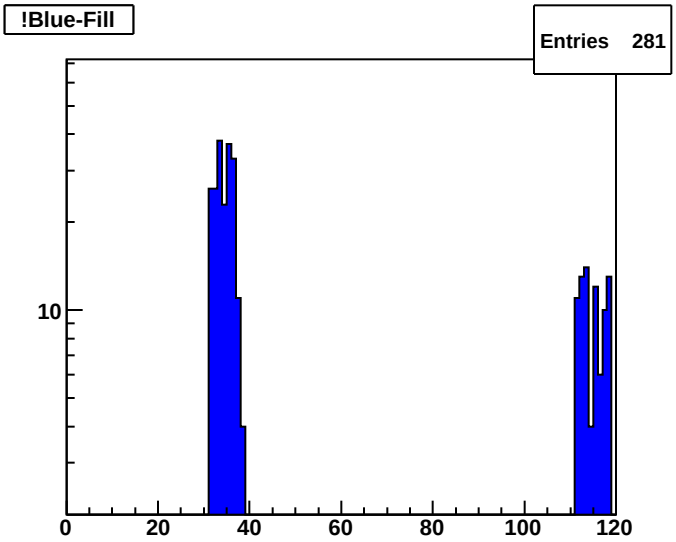

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

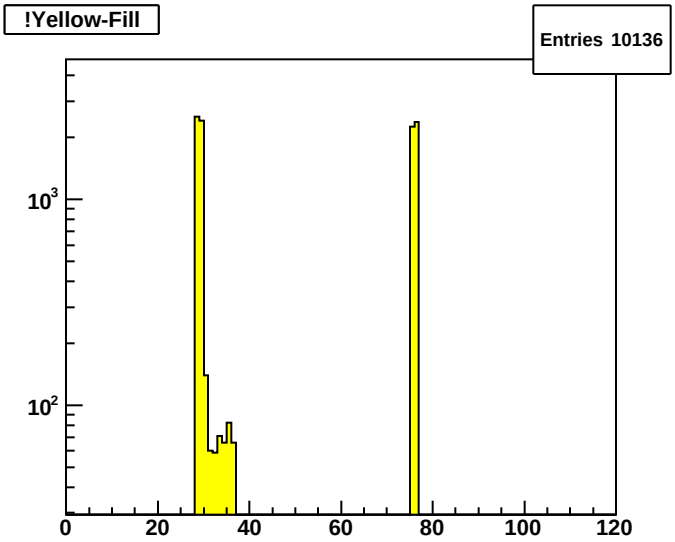

DAQ10k HT bits
Entries 48114

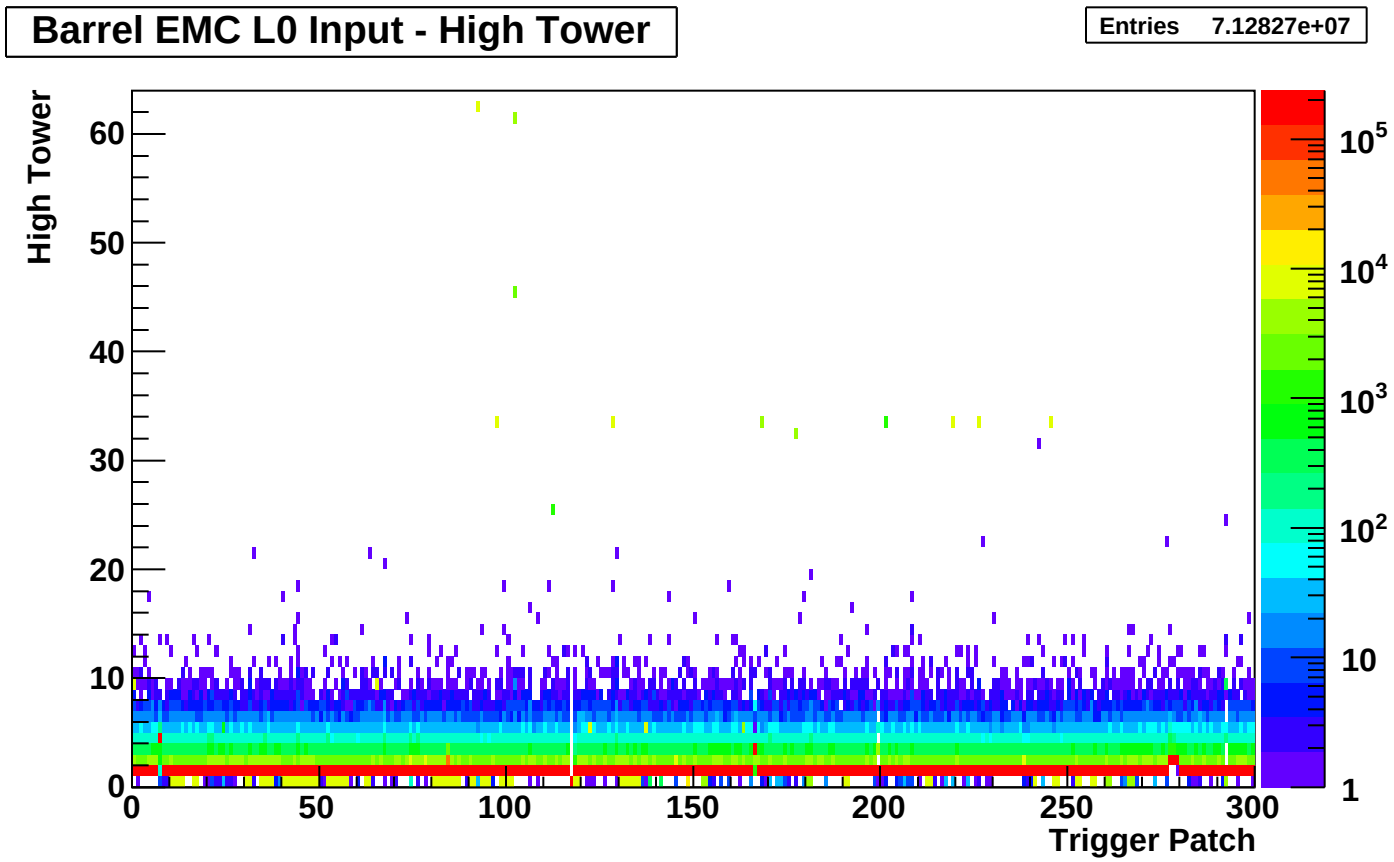

DAQ10k HT bits errors
Entries 0

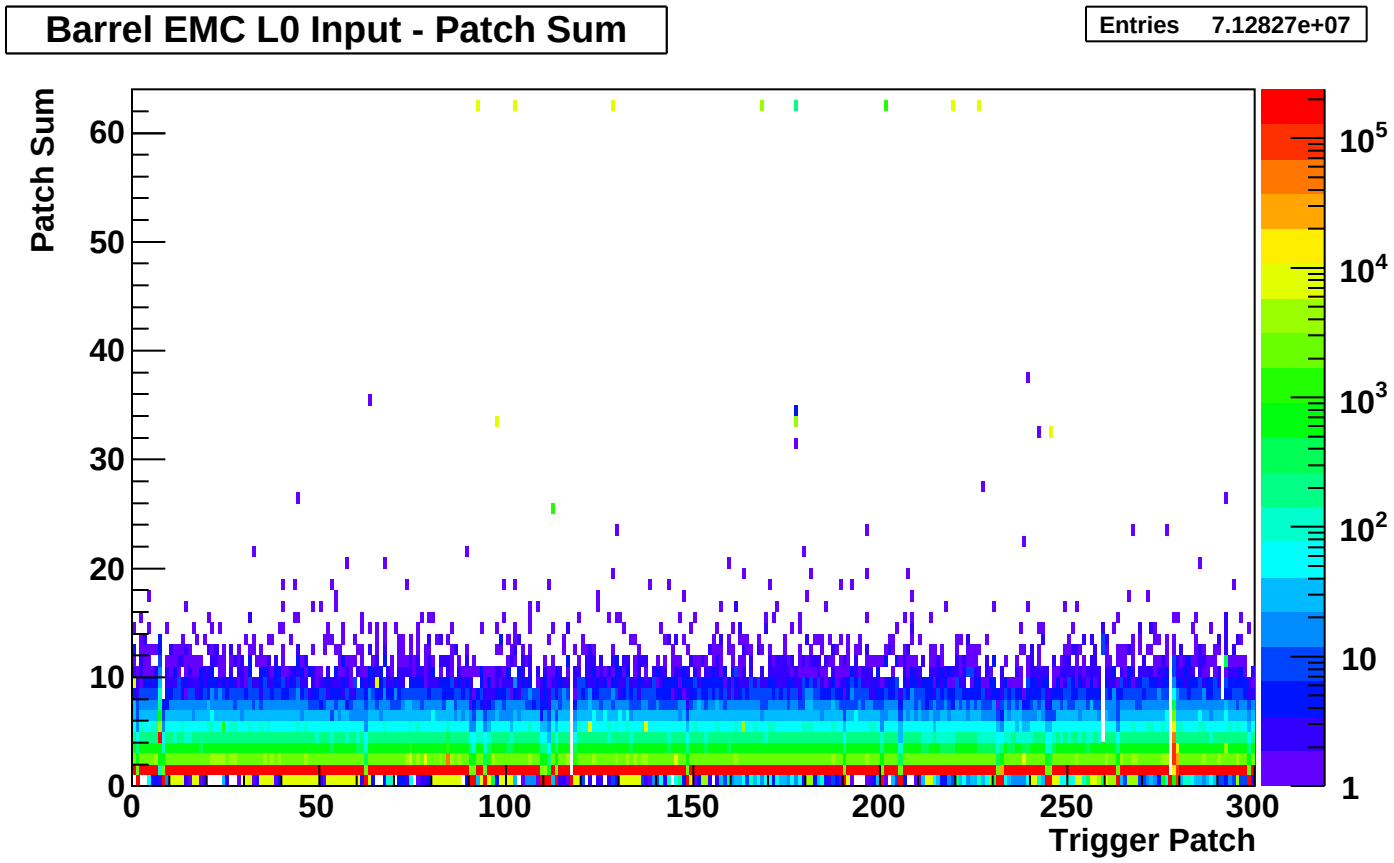

TPC Readout Mask (simulated)
Entries 79667

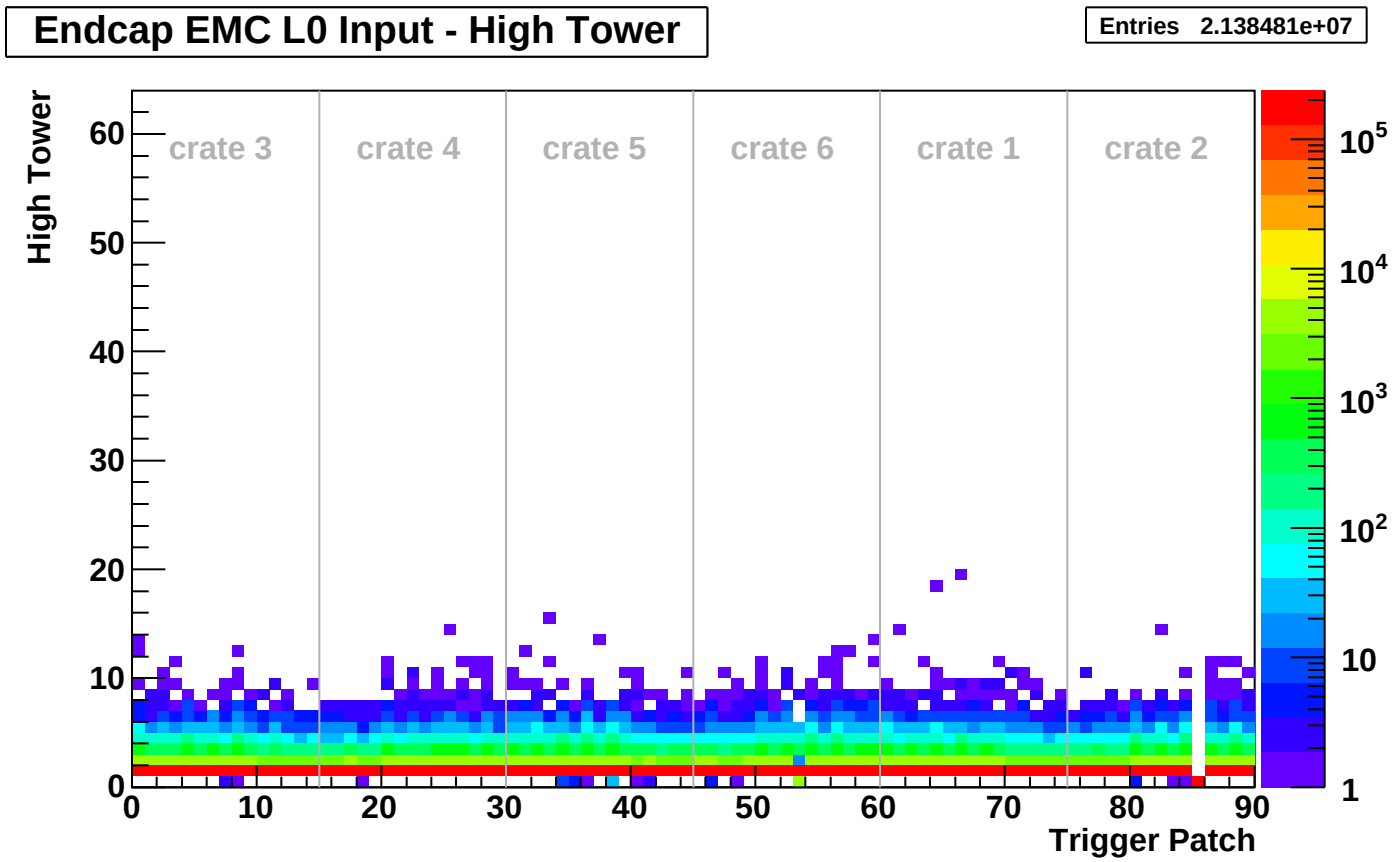

BunchId7Bit (all events)
Entries 237609

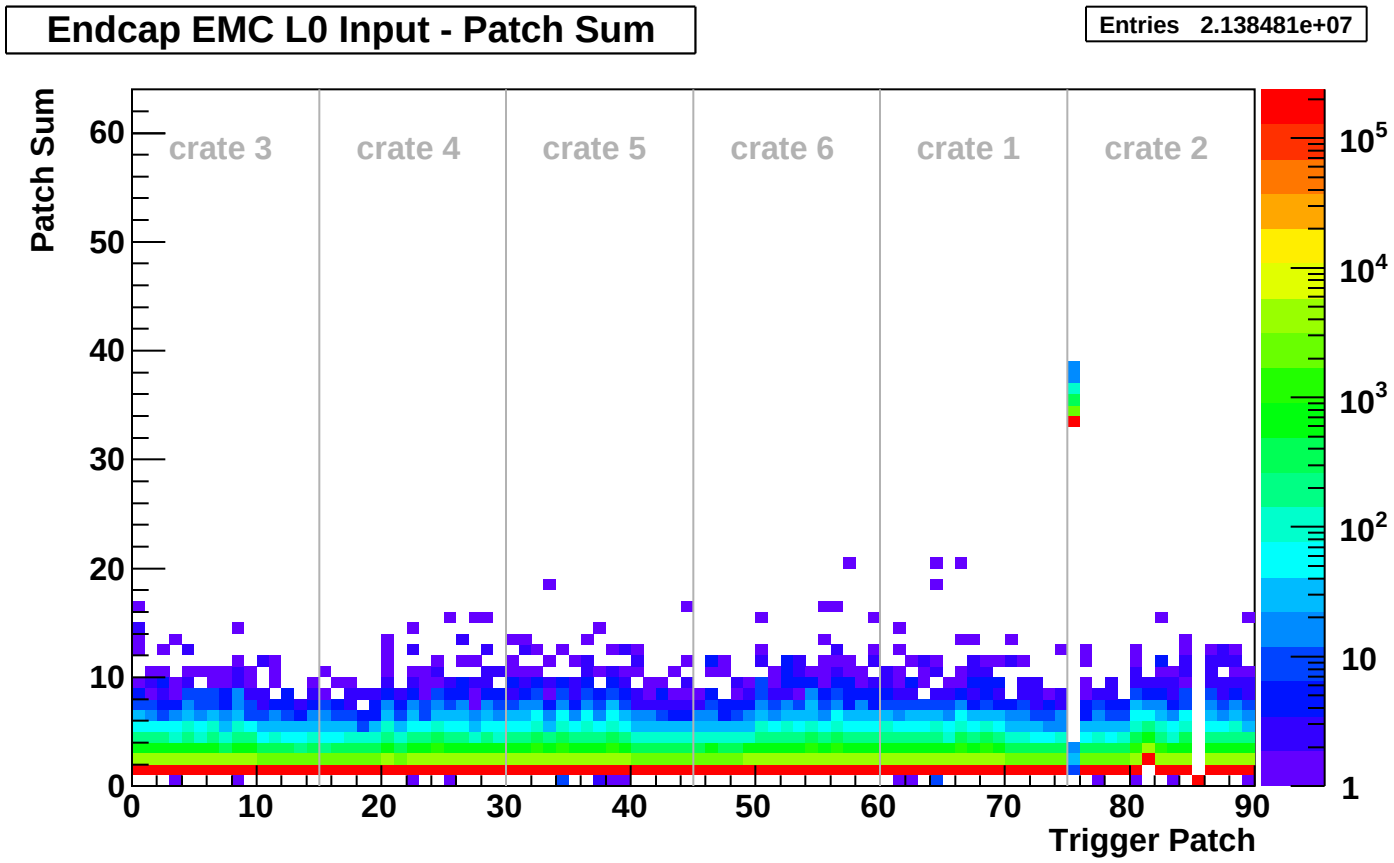

!Blue-Fill
Entries 281

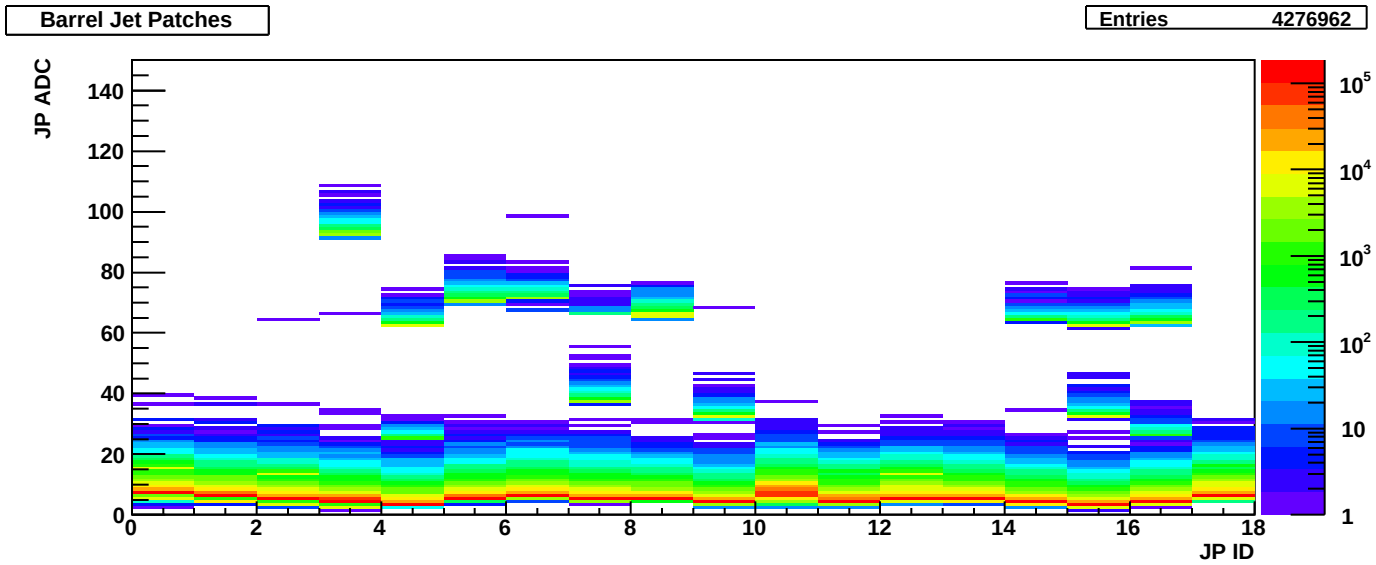

!Yellow-Fill
Entries 10136

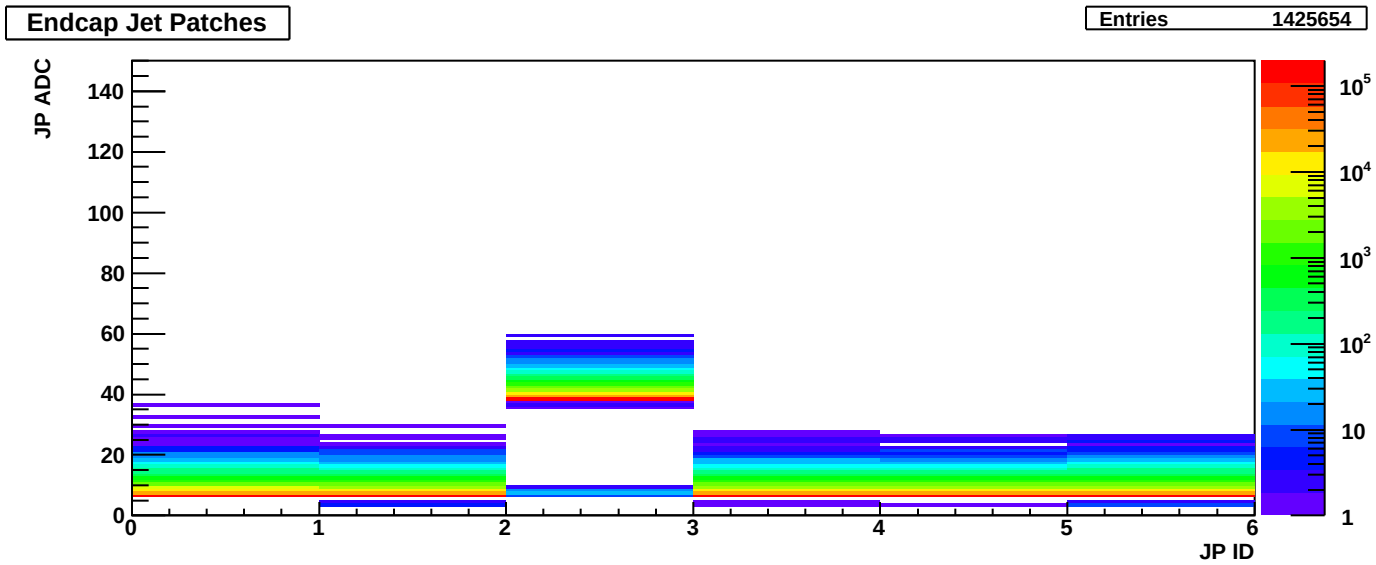

Barrel EMC L0 Input - High Tower
Entries 7.12827e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

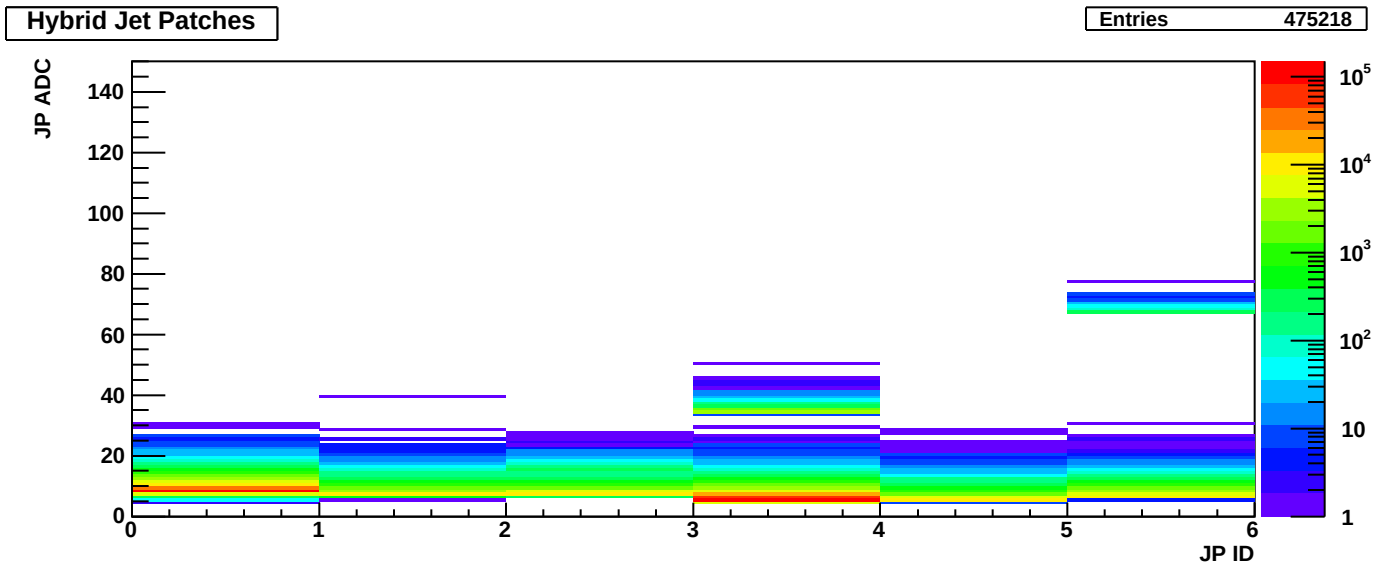

Barrel EMC L0 Input - Patch Sum
Entries 7.12827e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

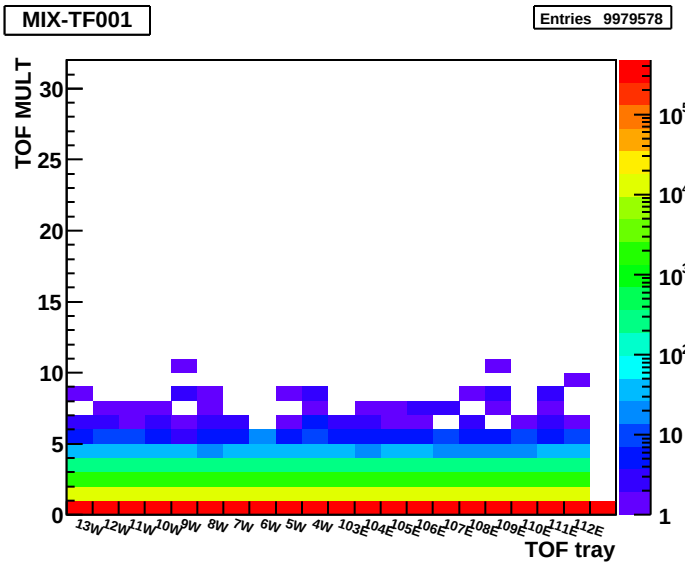

Endcap EMC L0 Input - High Tower
Entries 2.138481e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

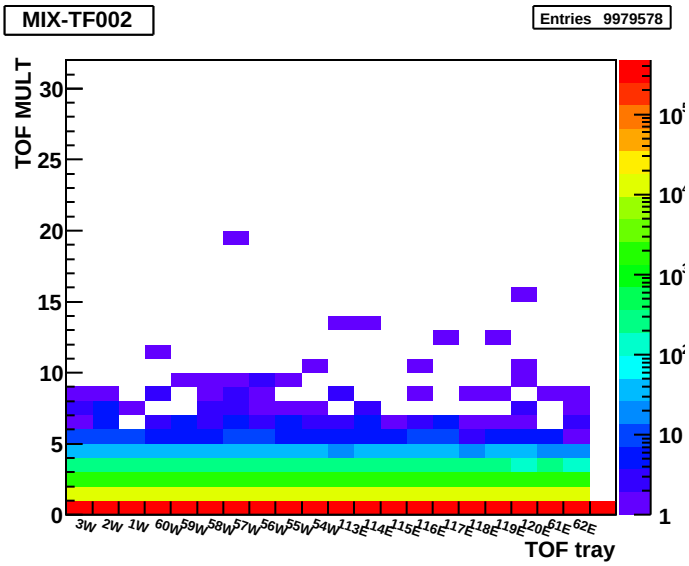

Endcap EMC L0 Input - Patch Sum
Entries 2.138481e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

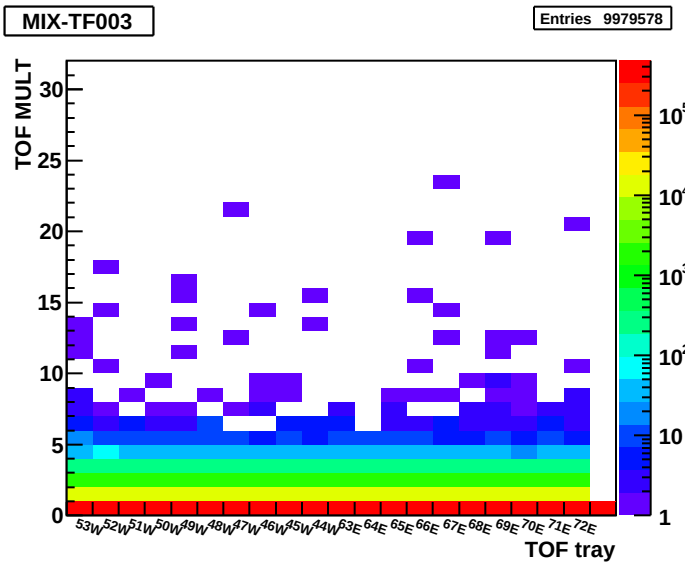

Barrel Jet Patches
Entries 4276962
JP ADC
JP ID

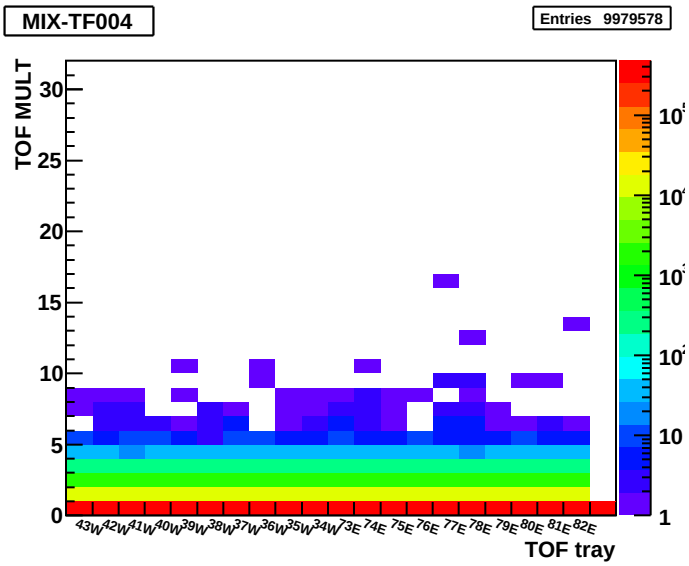

Endcap Jet Patches
Entries 1425654
JP ADC
JP ID

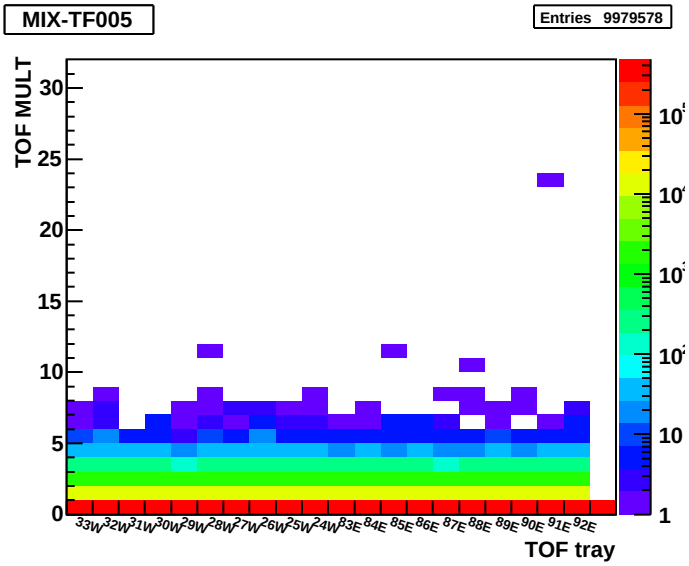

Hybrid Jet Patches
Entries 475218
JP ADC
JP ID

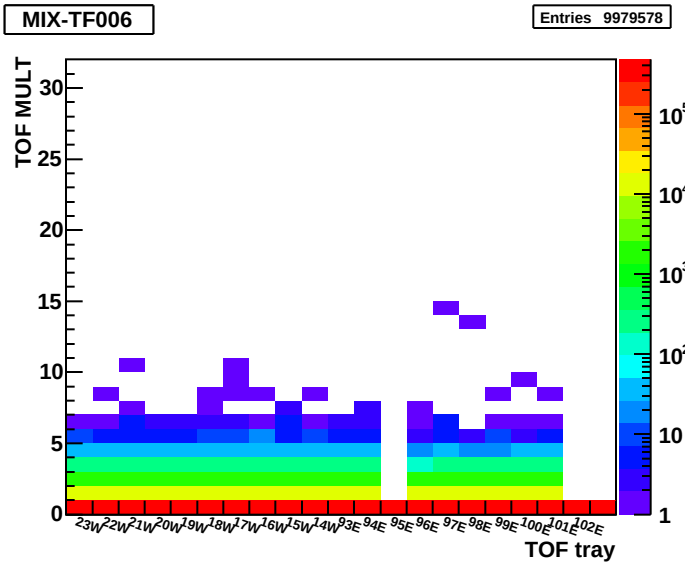

MIX-TF001
Entries 9979578
TOF MULT
TOF tray

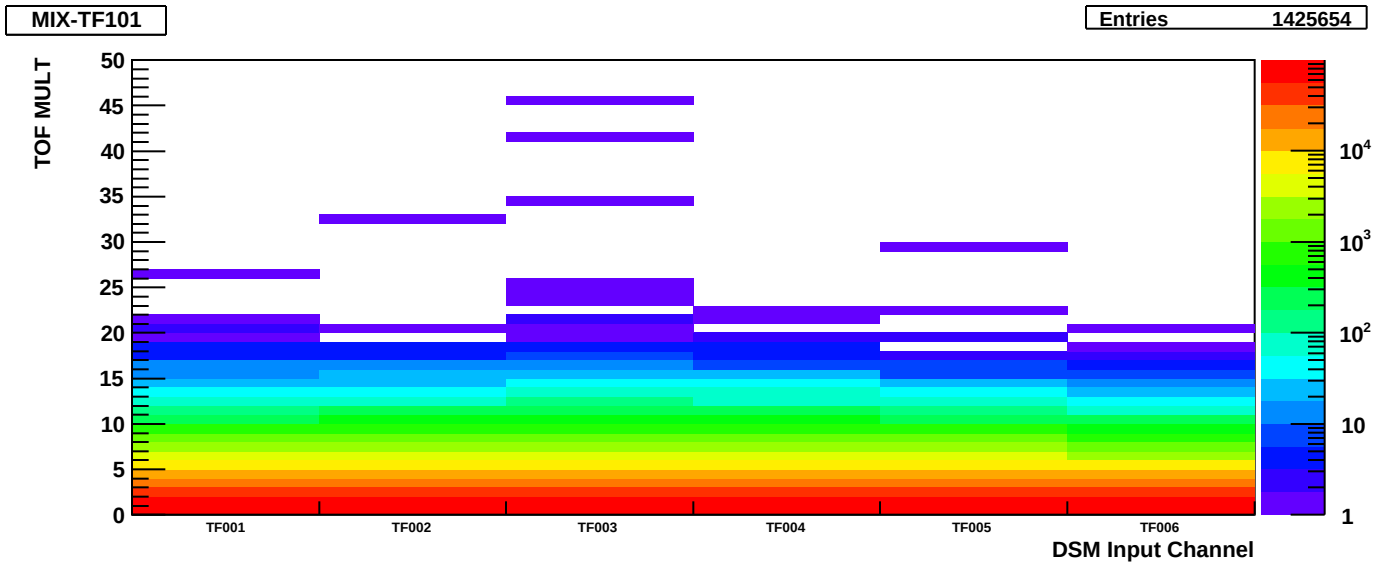

MIX-TF002
Entries 9979578
TOF MULT
TOF tray

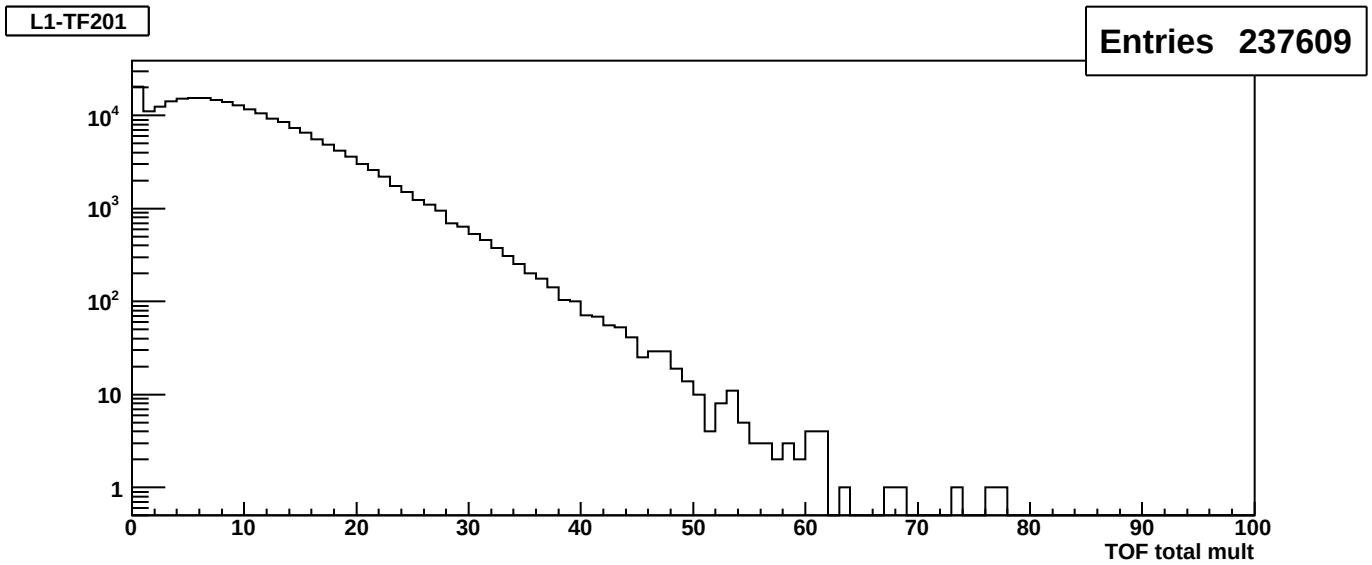

MIX-TF003
Entries 9979578
TOF MULT
TOF tray

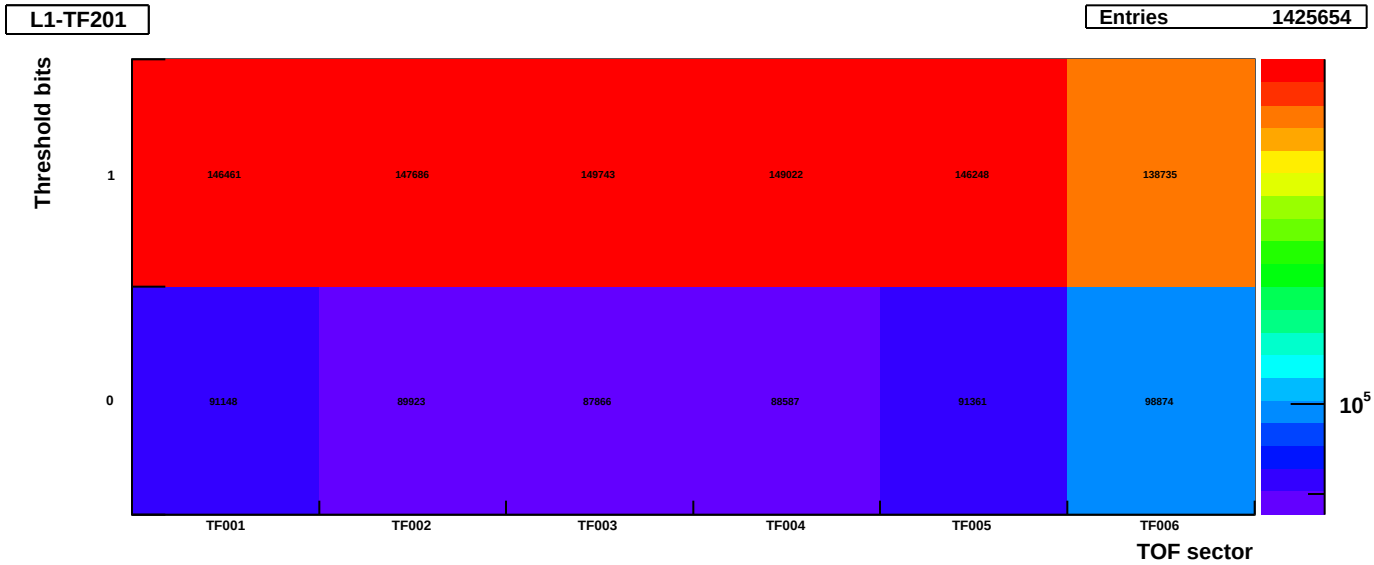

MIX-TF004
Entries 9979578
TOF MULT
TOF tray

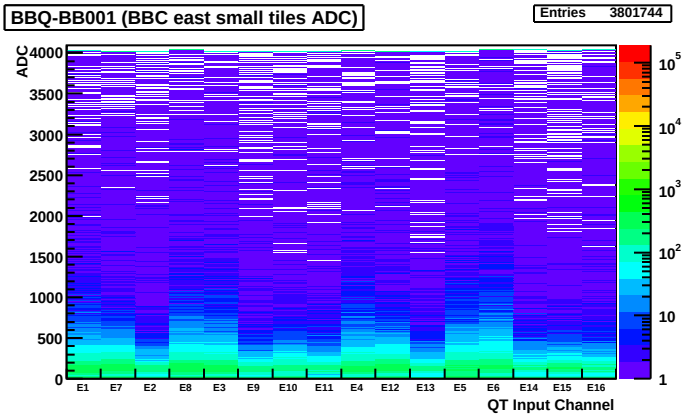

MIX-TF005
Entries 9979578
TOF MULT
TOF tray

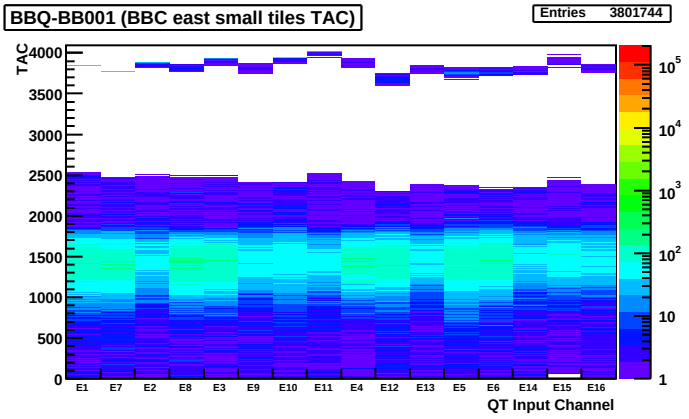

MIX-TF006
Entries 9979578
TOF MULT
TOF tray

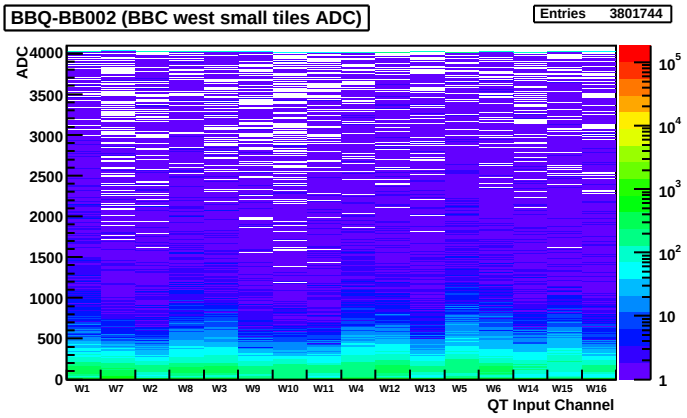

MIX-TF101
Entries 1425654
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

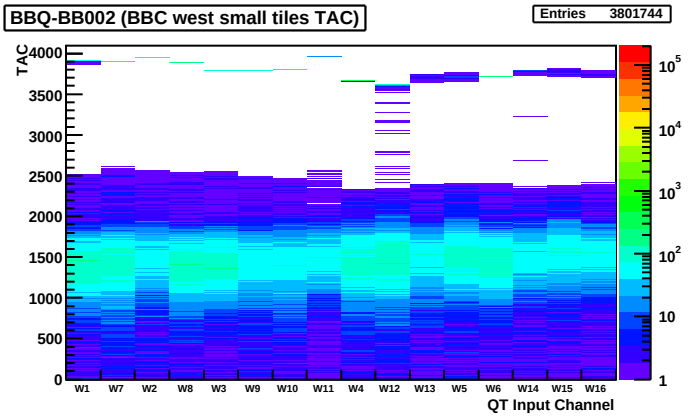

L1-TF201
Entries 237609
TOF total mult

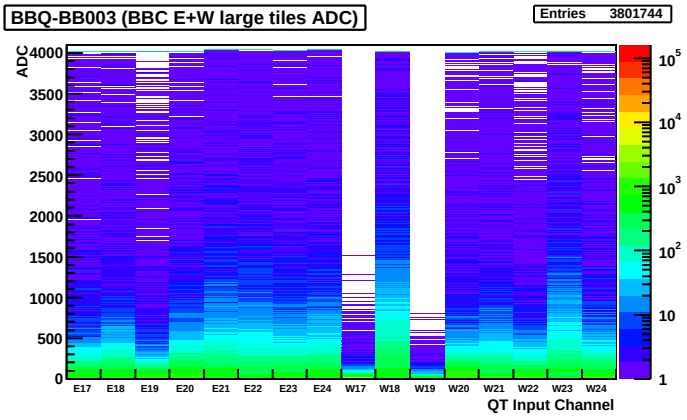

L1-TF201
Entries 1425654
Threshold bits
146461
147686
149743
149022
146248
138735
91148
89923
87866
88587
91361
98874
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

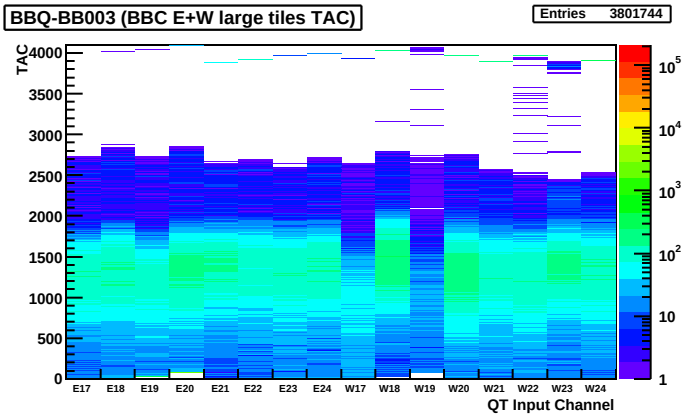

BBQ-BB001 (BBC east small tiles ADC)
Entries 3801744
ADC
QT Input Channel

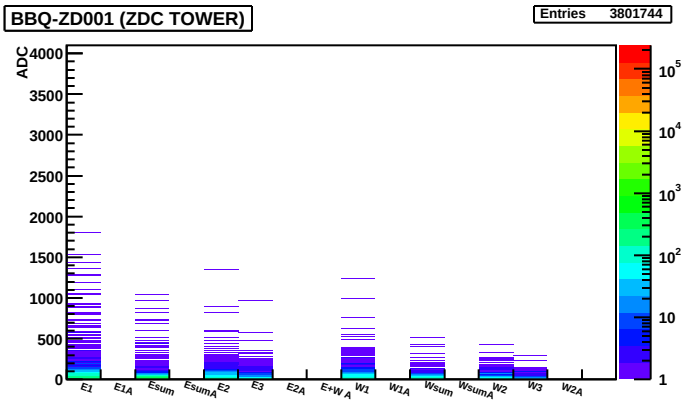

BBQ-BB001 (BBC east small tiles TAC)
Entries 3801744
TAC
QT Input Channel

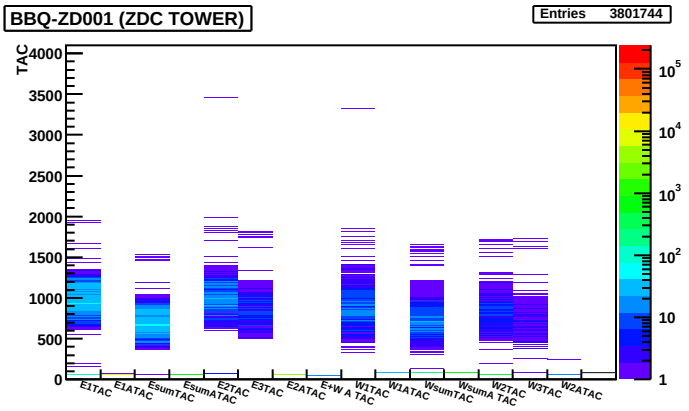

BBQ-BB002 (BBC west small tiles ADC)
Entries 3801744
ADC
QT Input Channel

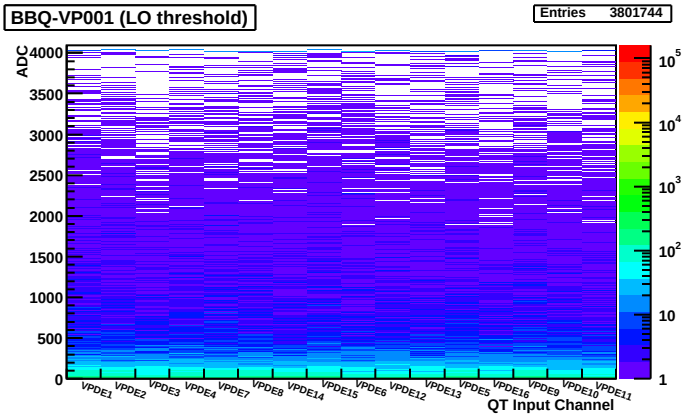

BBQ-BB002 (BBC west small tiles TAC)
Entries 3801744
TAC
QT Input Channel

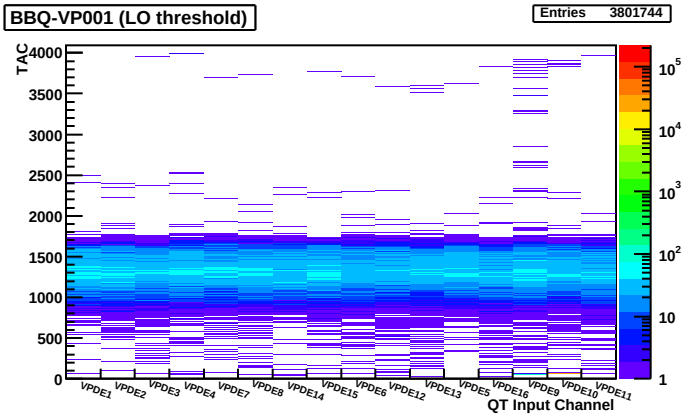

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3801744
ADC
QT Input Channel

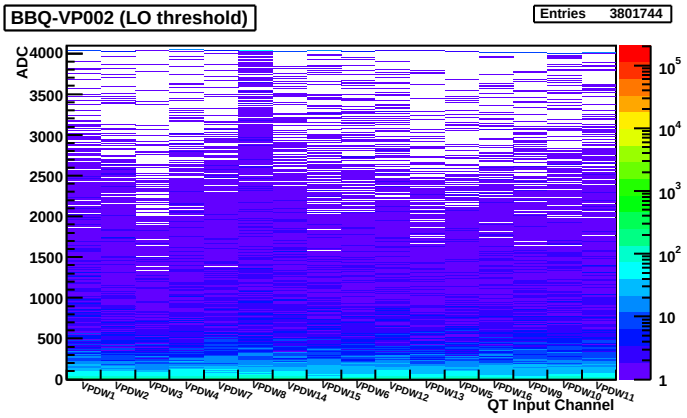

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3801744
TAC
QT Input Channel

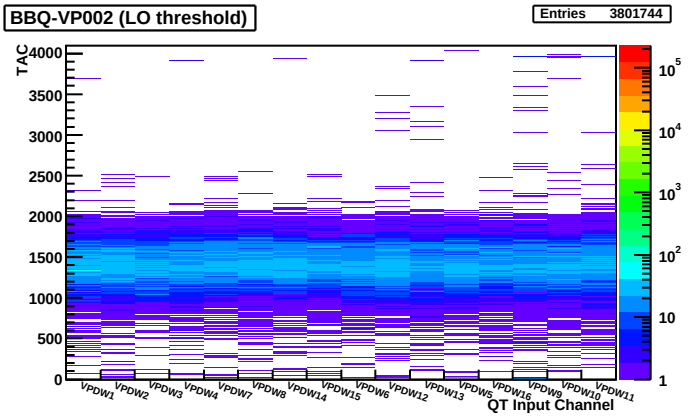

BBQ-ZD001 (ZDC TOWER)
Entries 3801744
ADC

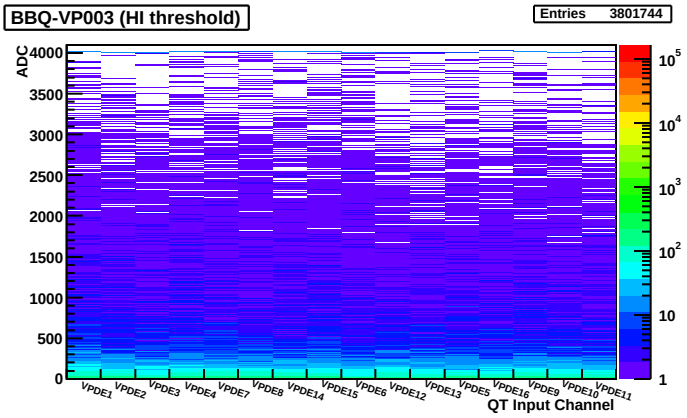

BBQ-ZD001 (ZDC TOWER)
Entries 3801744
TAC

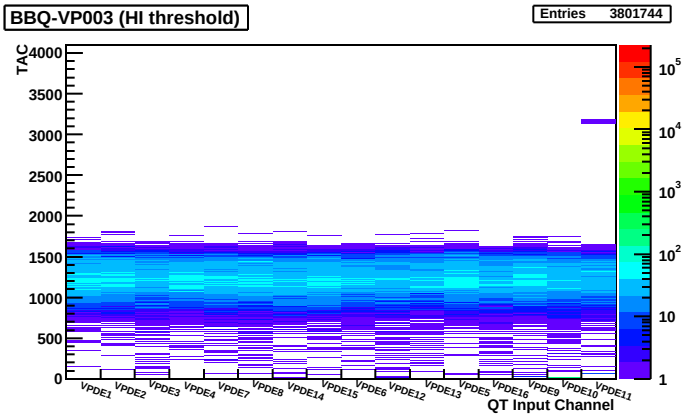

BBQ-VP001 (LO threshold)
Entries 3801744
ADC
QT Input Channel

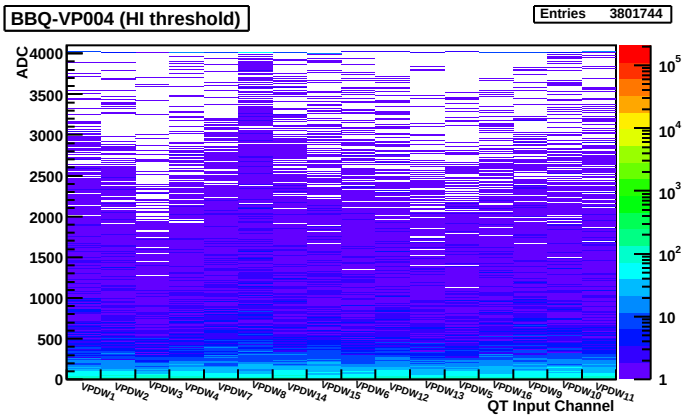

BBQ-VP001 (LO threshold)
Entries 3801744
TAC
QT Input Channel

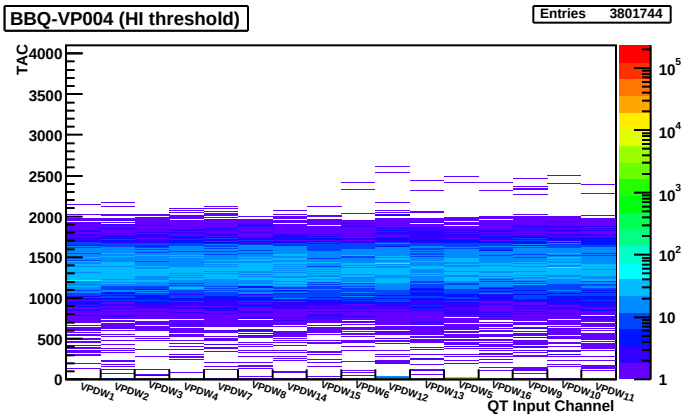

BBQ-VP002 (LO threshold)
Entries 3801744
ADC
QT Input Channel

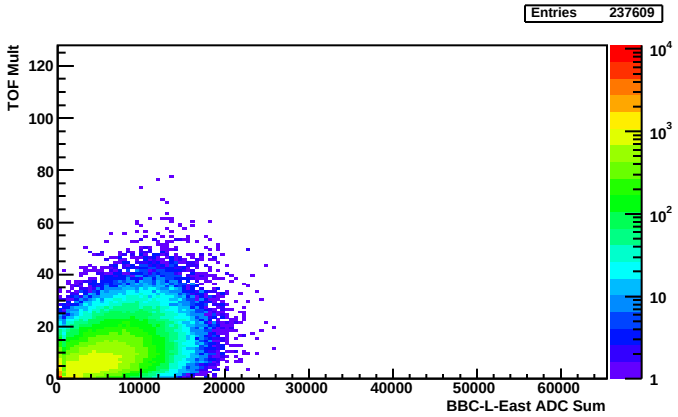

BBQ-VP002 (LO threshold)
Entries 3801744
TAC
QT Input Channel

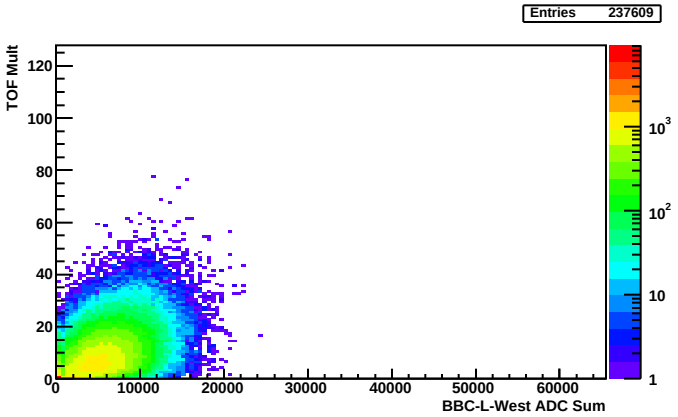

BBQ-VP003 (HI threshold)
Entries 3801744
ADC
QT Input Channel

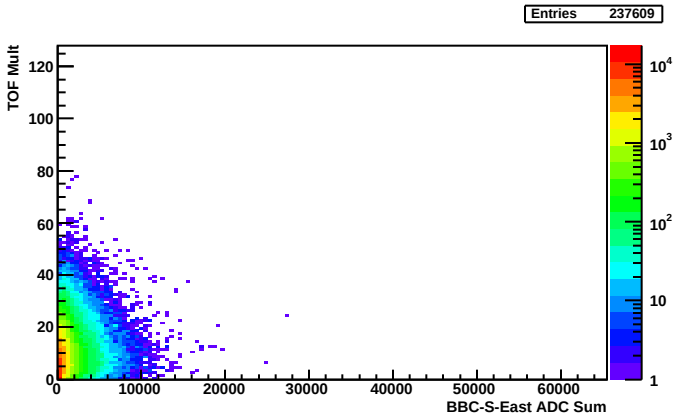

BBQ-VP003 (HI threshold)
Entries 3801744
TAC
QT Input Channel

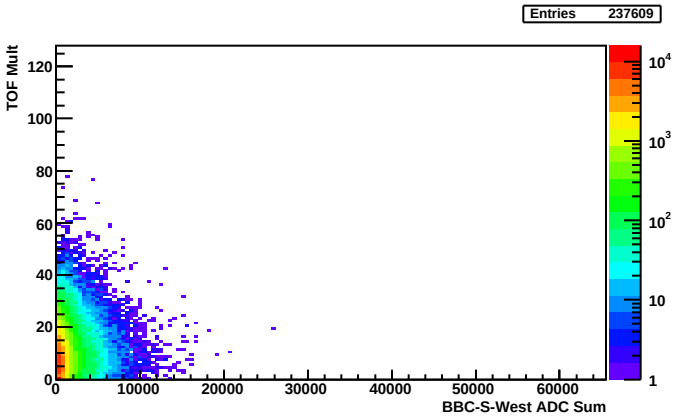

BBQ-VP004 (HI threshold)
Entries 3801744
ADC
QT Input Channel

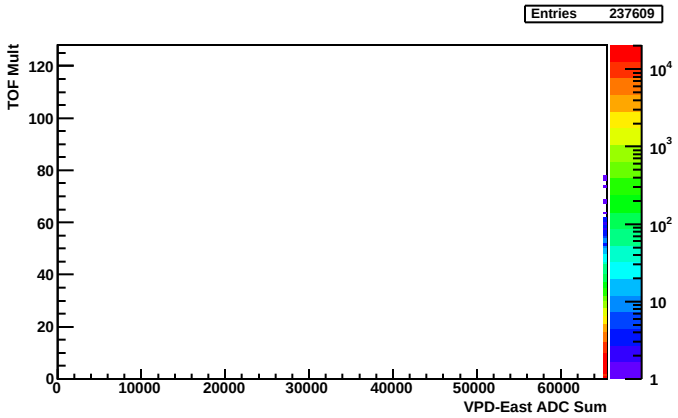

BBQ-VP004 (HI threshold)
Entries 3801744
TAC
QT Input Channel

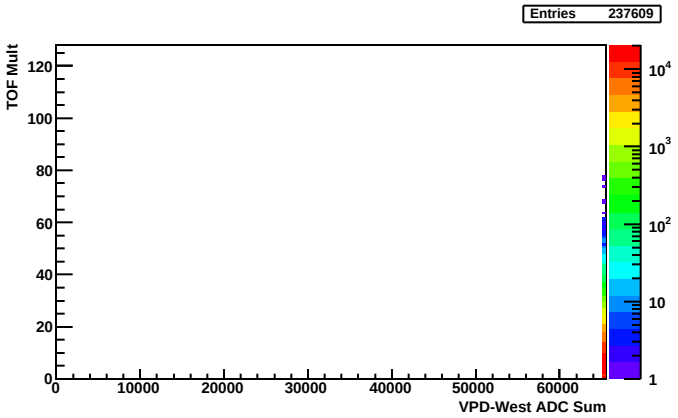

Entries 237609
TOF Mult
BBC-L-East ADC Sum

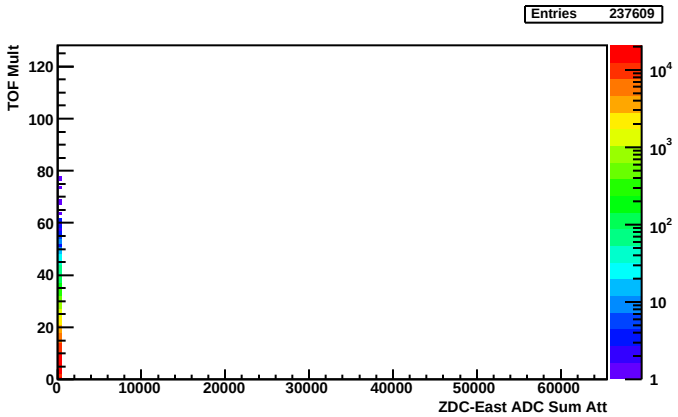

Entries 237609
TOF Mult
BBC-L-West ADC Sum

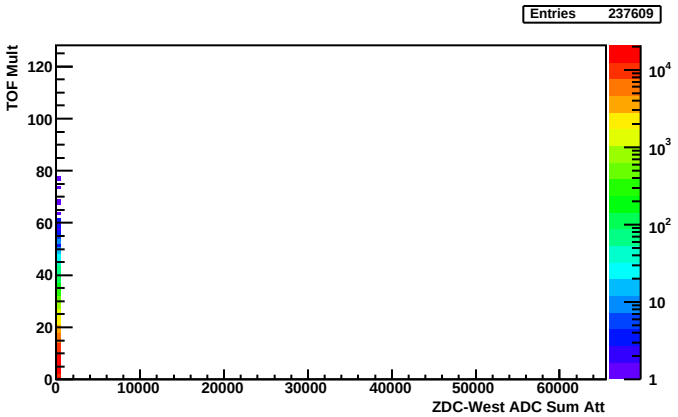

Entries 237609
TOF Mult
BBC-S-East ADC Sum

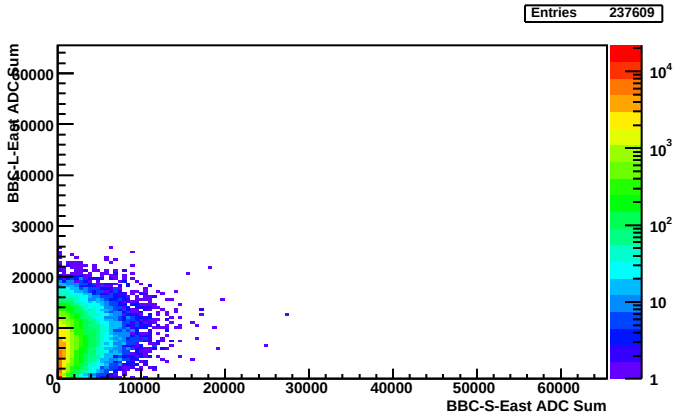

Entries 237609
TOF Mult
BBC-S-West ADC Sum

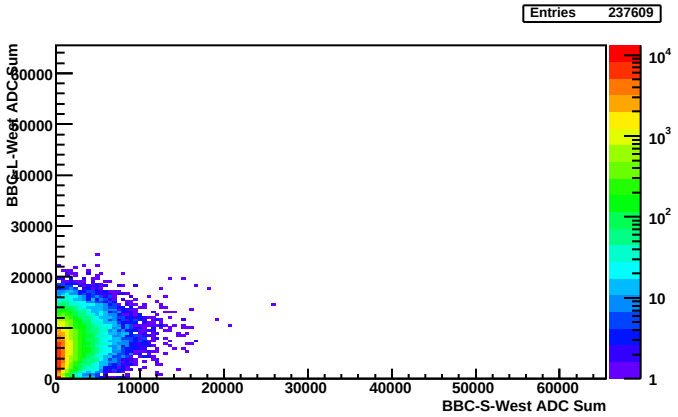

Entries 237609
TOF Mult
VPD-East ADC Sum

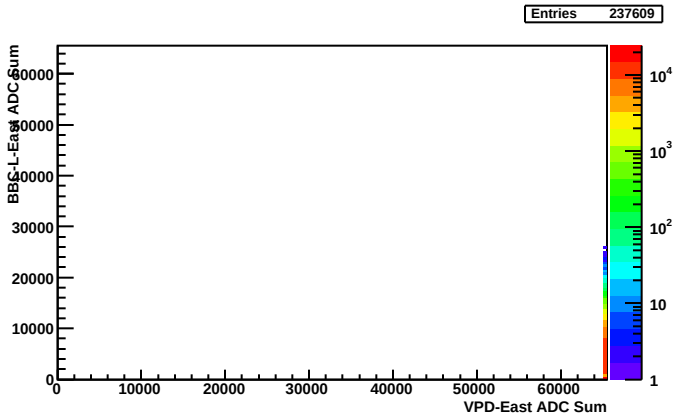

Entries 237609
TOF Mult
VPD-West ADC Sum

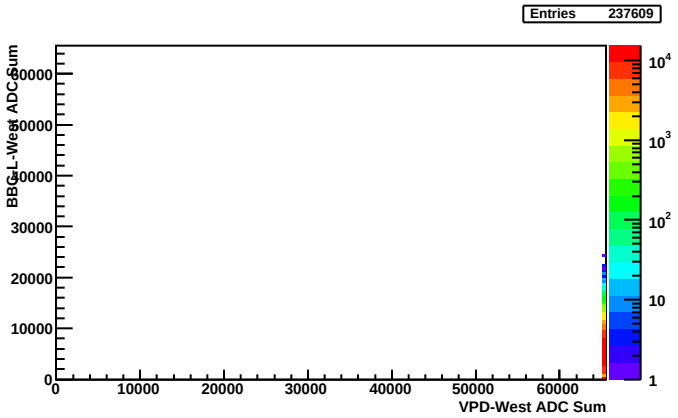

Entries 237609
TOF Mult
ZDC-East ADC Sum Att

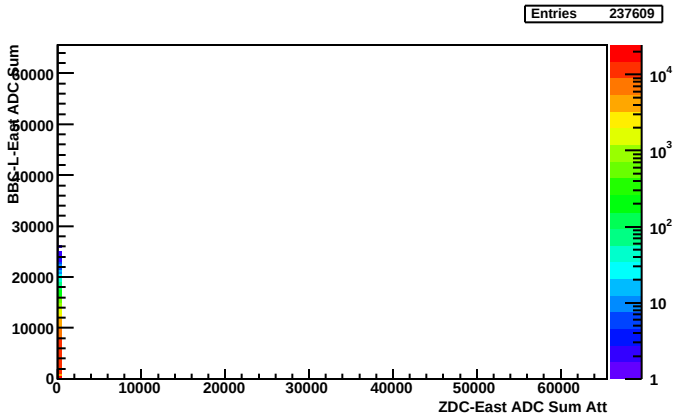

Entries 237609
TOF Mult
ZDC-West ADC Sum Att

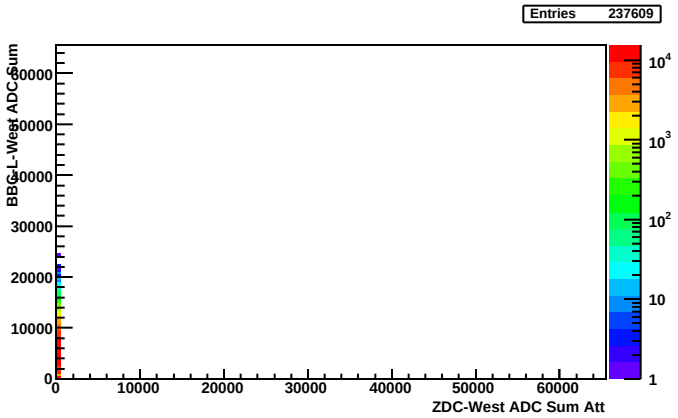

Entries 237609
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 237609
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 237609
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 237609
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 237609
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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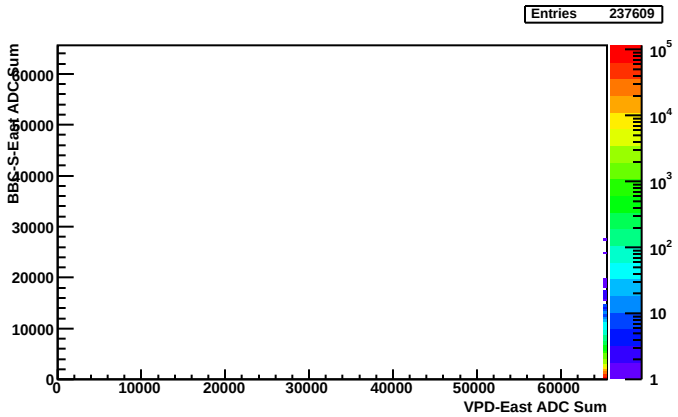
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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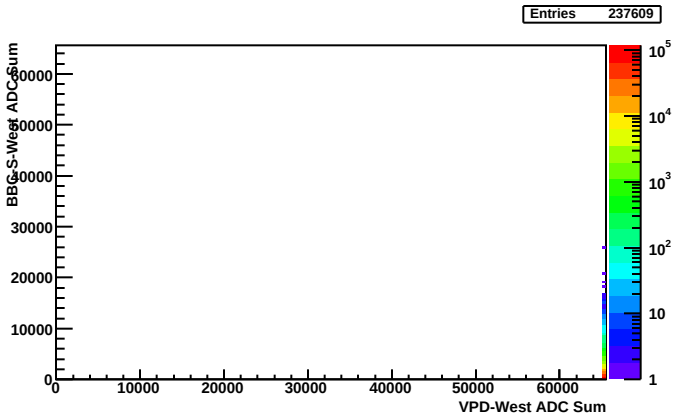
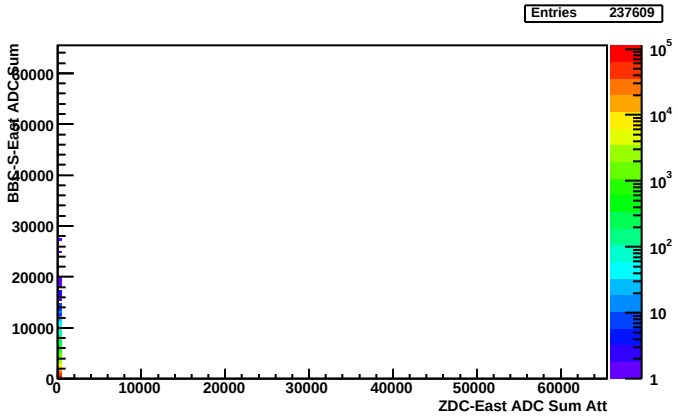
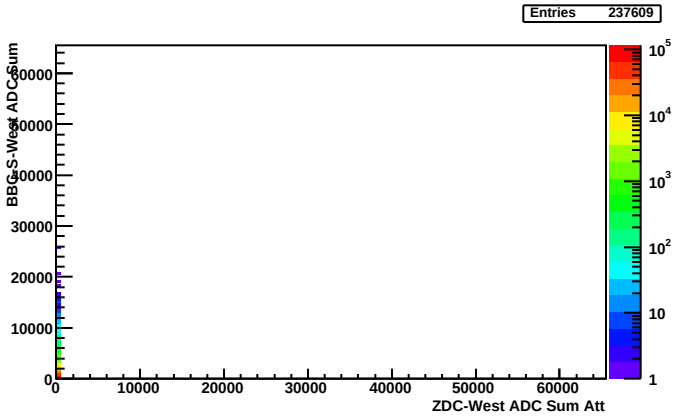
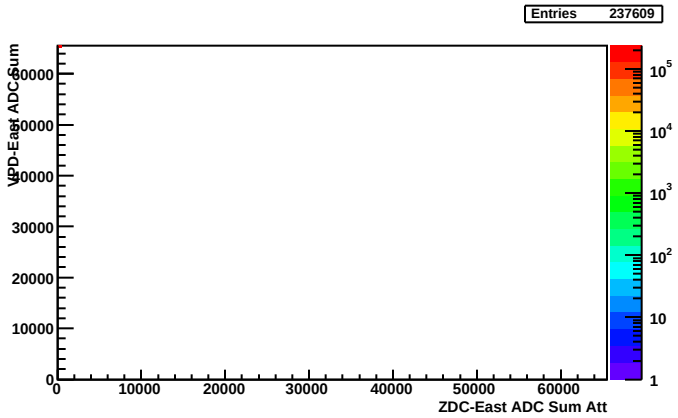
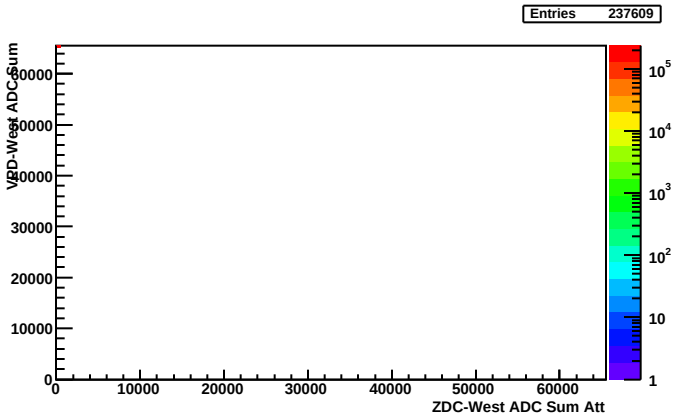
BBC-S-East ADC Sum
VPD-East ADC Sum

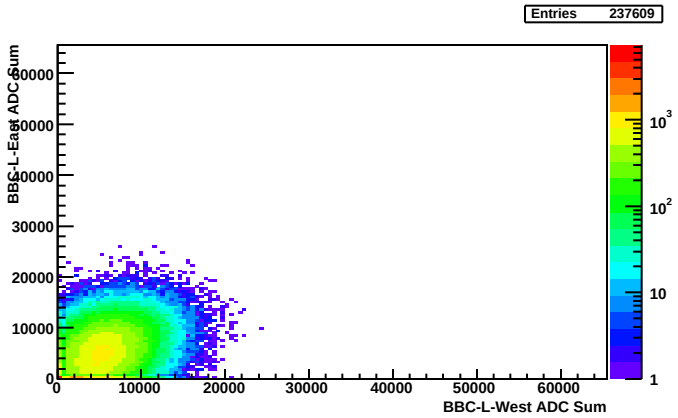

Entries 237609
BBC-S-West ADC Sum
VPD-West ADC Sum

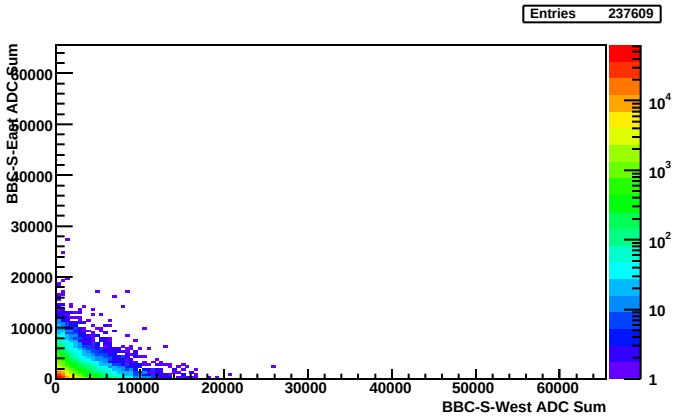

Entries 237609
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

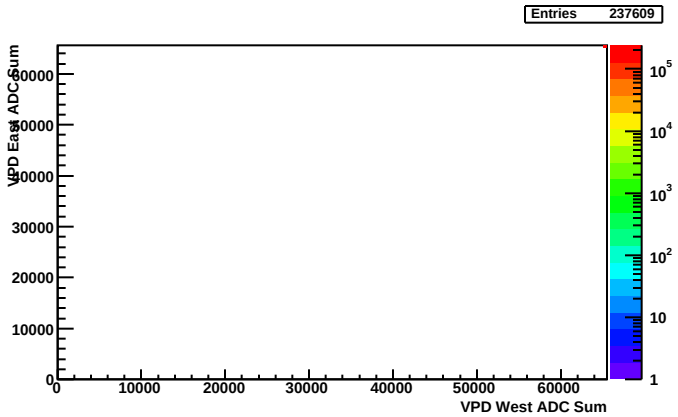

Entries 237609
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

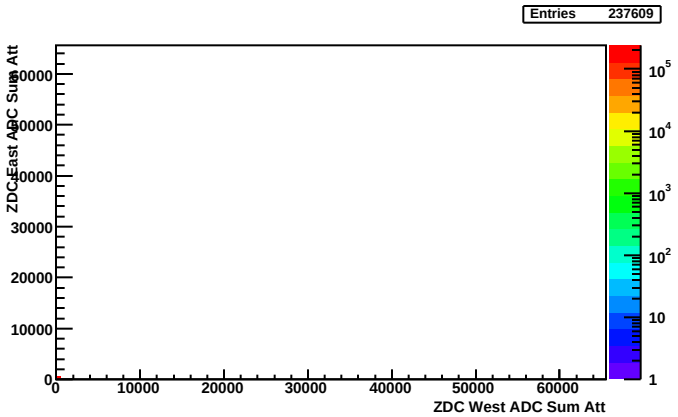

Entries 237609
VPD-East ADC Sum
ZDC-East ADC Sum Att

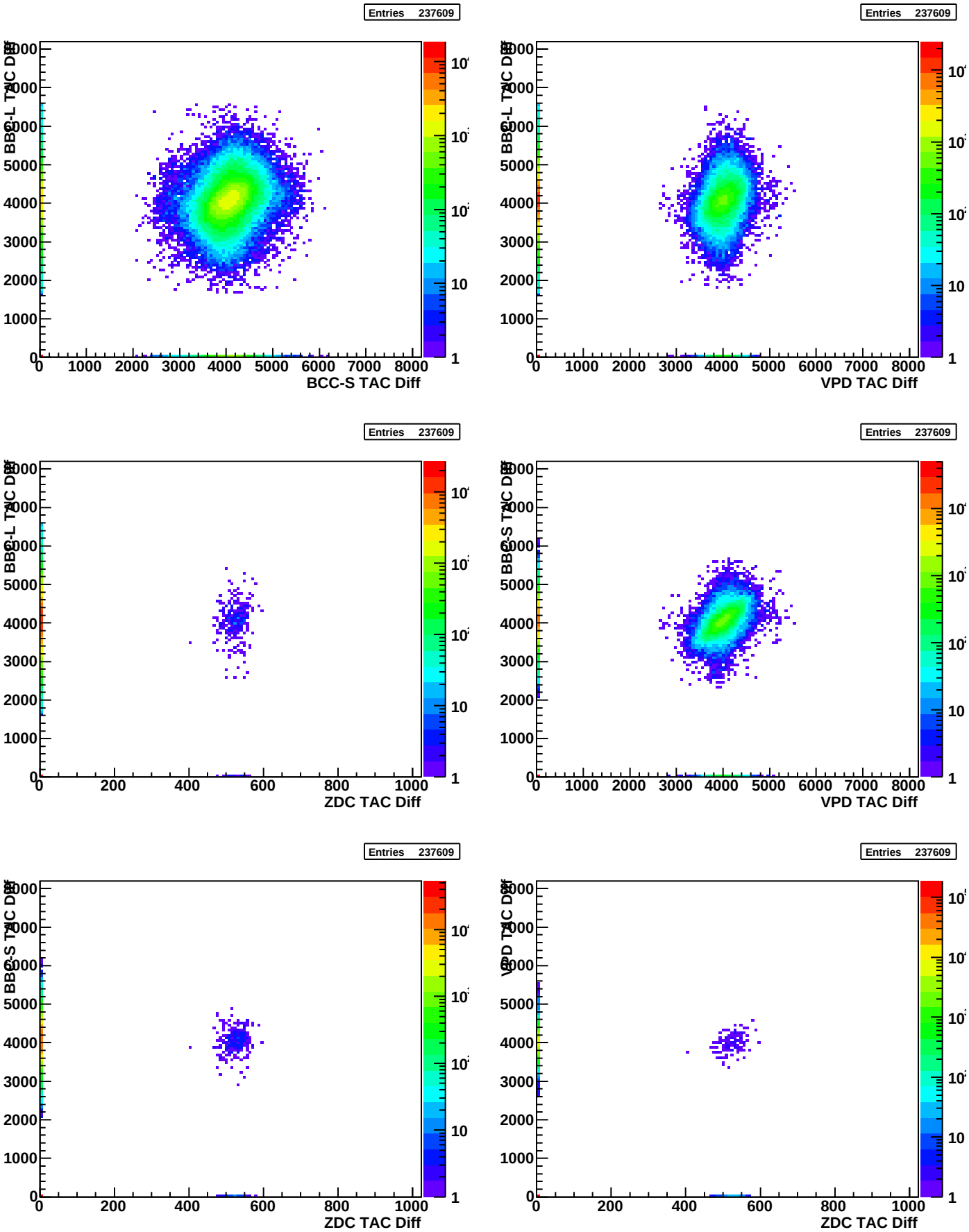

Entries 237609
VPD-West ADC Sum
ZDC-West ADC Sum Att

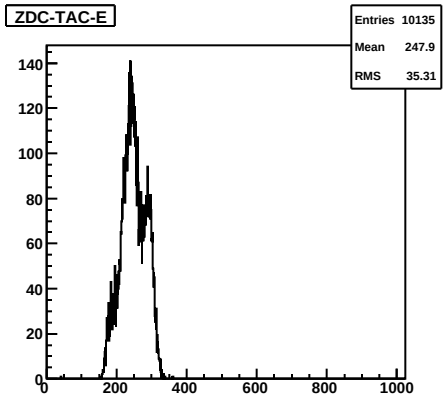

Entries 237609
BBC-L-East ADC Sum
BBC-L-West ADC Sum

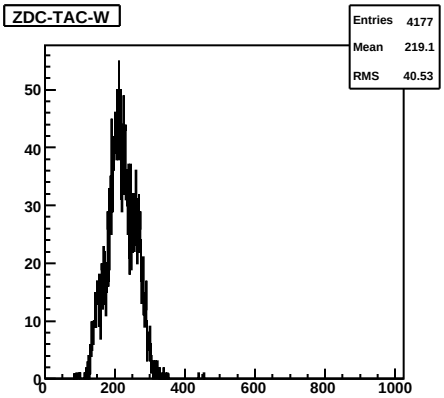

Entries 237609
BBC-S-East ADC Sum
BBC-S-West ADC Sum

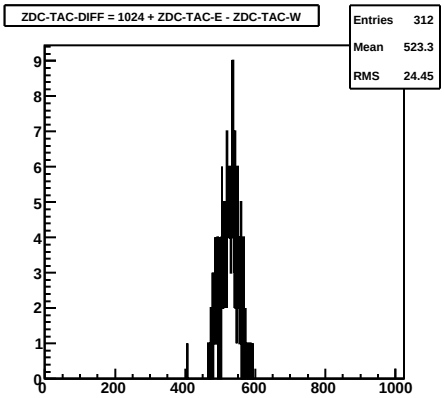

Entries 237609
VPD East ADC Sum
VPD West ADC Sum

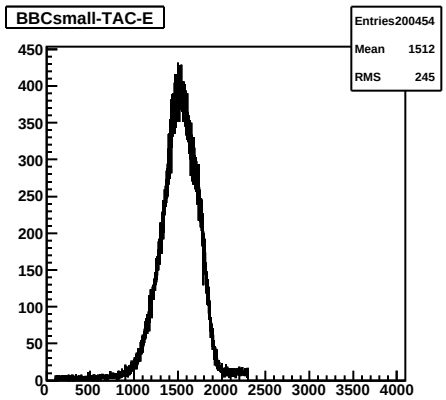

Entries 237609
ZDC East ADC Sum Att
ZDC West ADC Sum Att

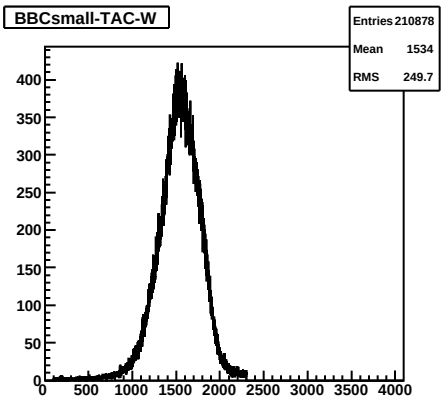

Entries 237609
BBC-L TAC Diff
BCC-S TAC Diff
Entries 237609
BBC-L TAC Diff
VPD TAC Diff
Entries 237609
BBC-L TAC Diff
ZDC TAC Diff
Entries 237609
BBC-S TAC Diff
VPD TAC Diff
Entries 237609
BBC-S TAC Diff
ZDC TAC Diff
Entries 237609
VPD TAC Diff
ZDC TAC Diff

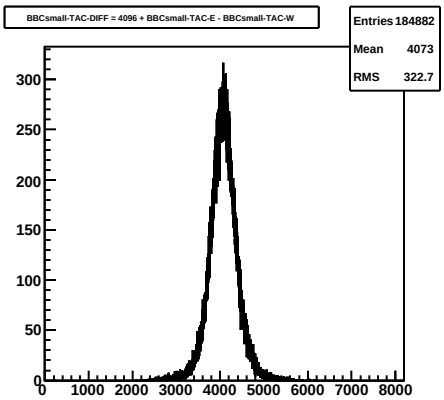

ZDC-TAC-E
Entries 10135
Mean 247.9
RMS 35.31

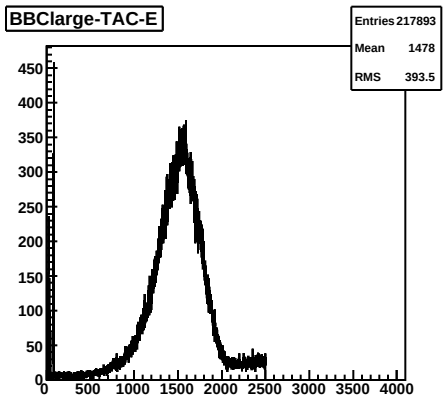

ZDC-TAC-W
Entries 4177
Mean 219.1
RMS 40.53

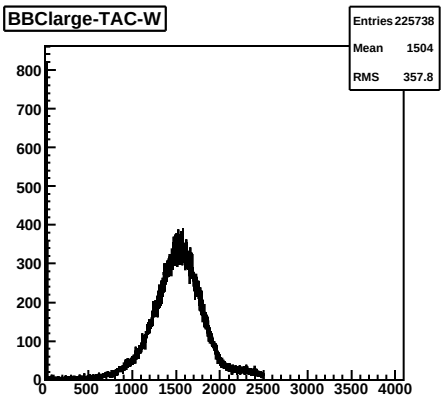

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 312
Mean 523.3
RMS 24.45

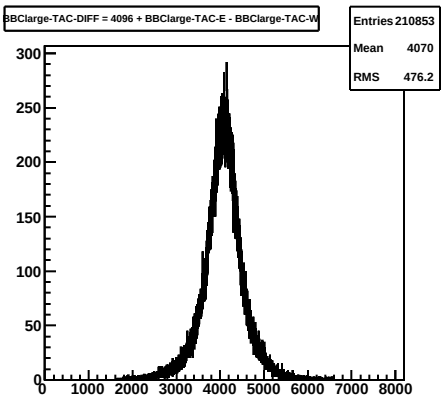

BBCsmall-TAC-E
Entries 200454
Mean 1512
RMS 245

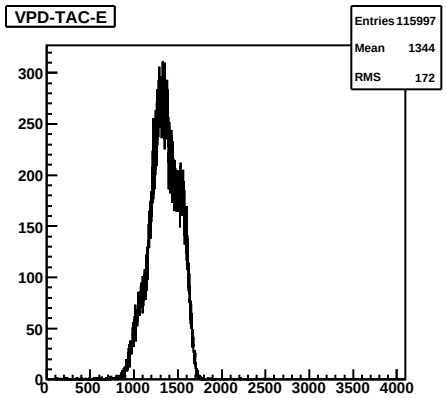

BBCsmall-TAC-W
Entries 210878
Mean 1534
RMS 249.7

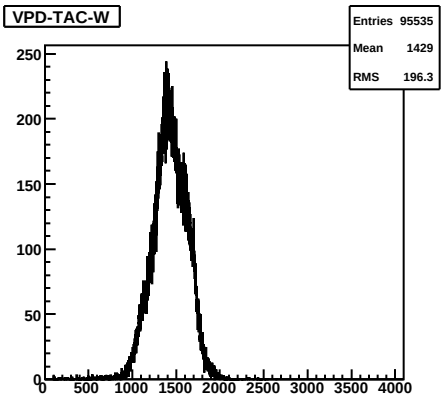

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 184882
Mean 4073
RMS 322.7

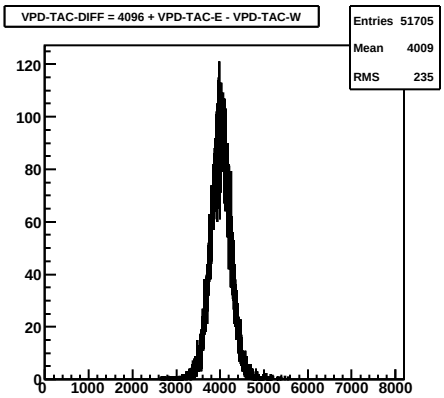

BBClarge-TAC-E
Entries 217893
Mean 1478
RMS 393.5

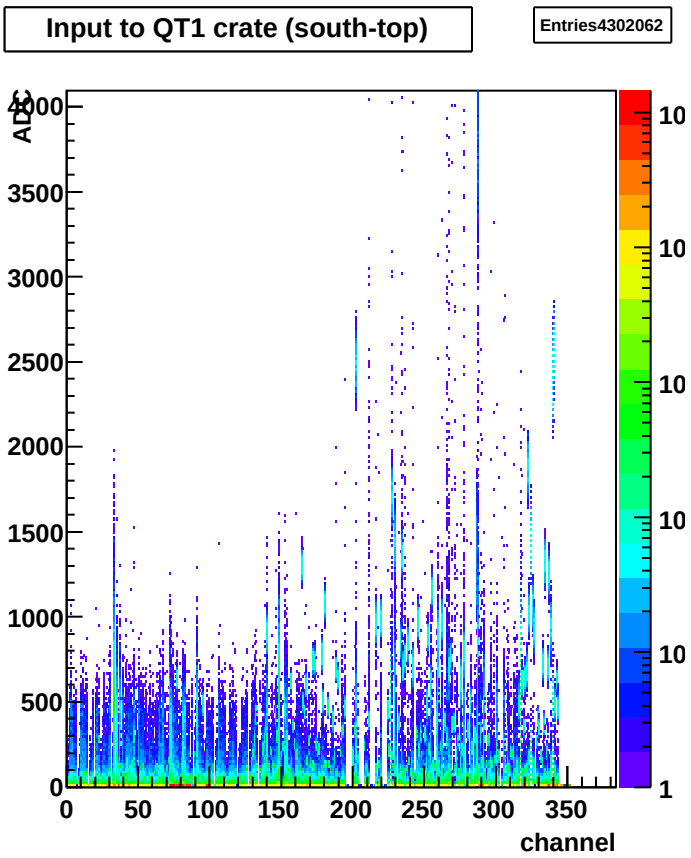

BBClarge-TAC-W
Entries 225738
Mean 1504
RMS 357.8

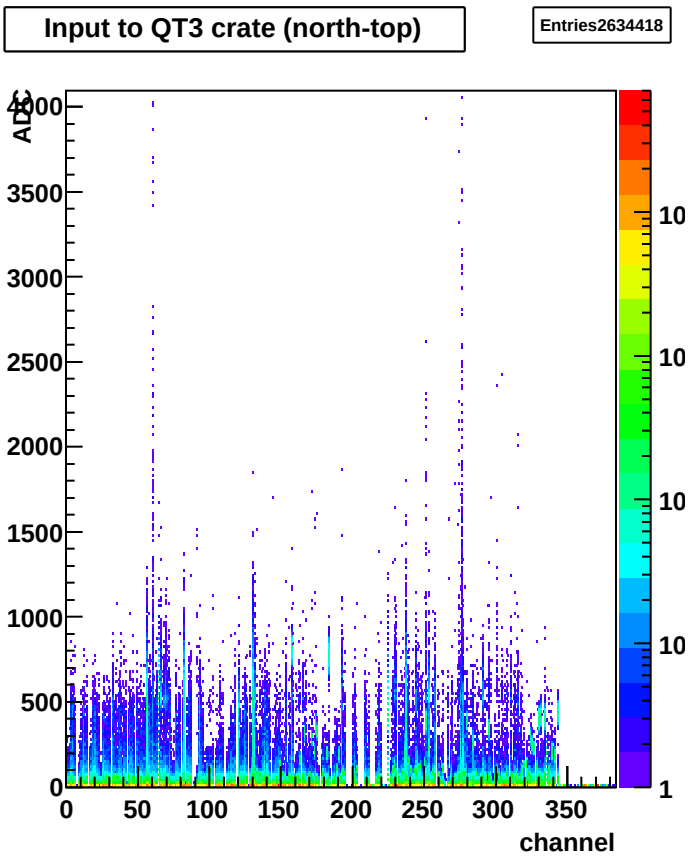

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 210853
Mean 4070
RMS 476.2

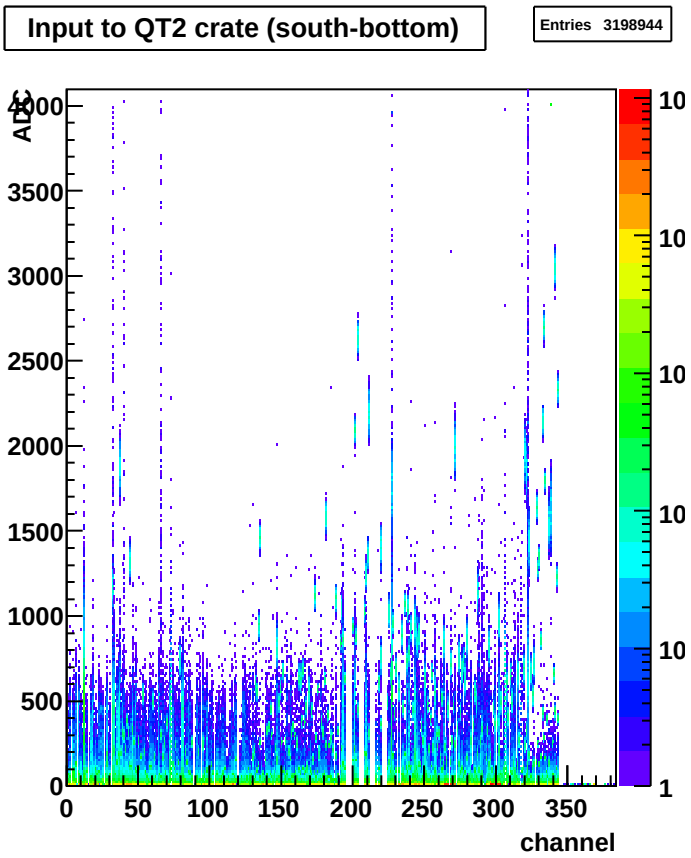

VPD-TAC-E
Entries 115997
Mean 1344
RMS 172

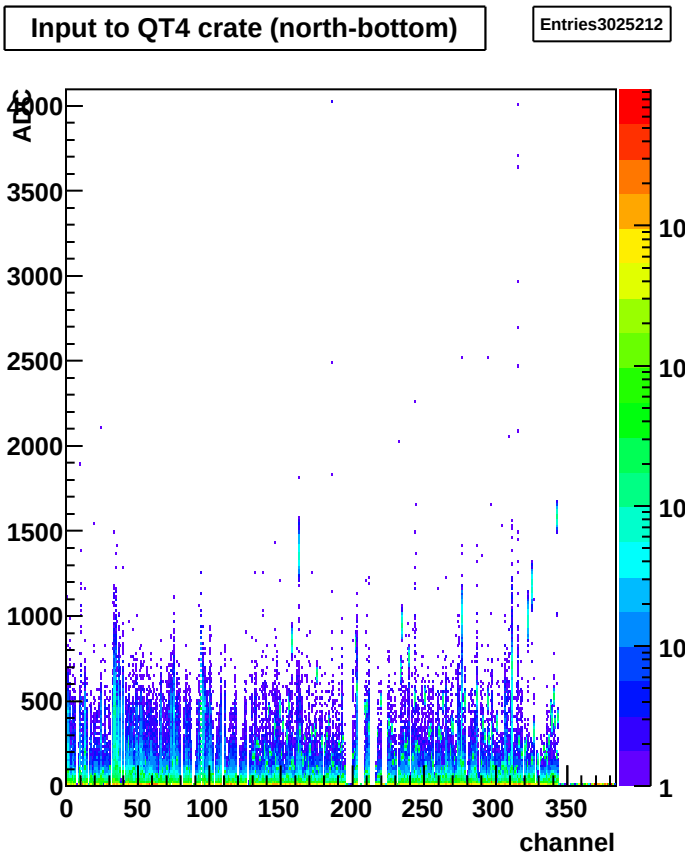

VPD-TAC-W
Entries 95535
Mean 1429
RMS 196.3

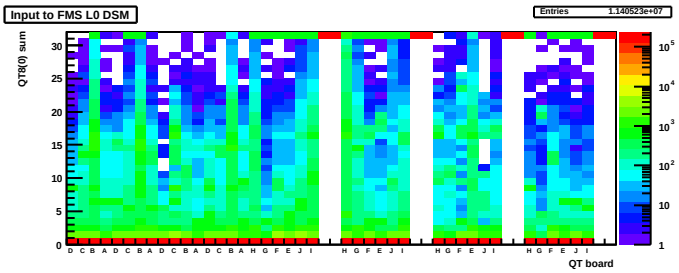

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 51705
Mean 4009
RMS 235

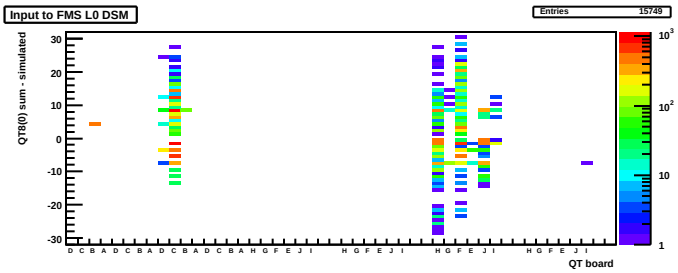

Input to QT1 crate (south-top)
Entries4302062
ADC
channel

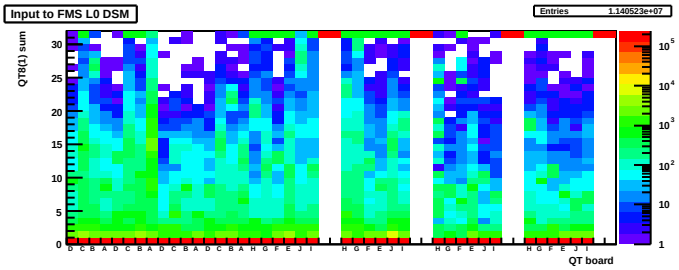

Input to QT3 crate (north-top)
Entries2634418
ADC
channel

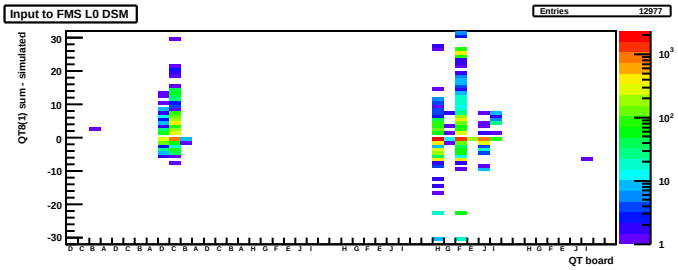

Input to QT2 crate (south-bottom)
Entries 3198944
ADC
channel

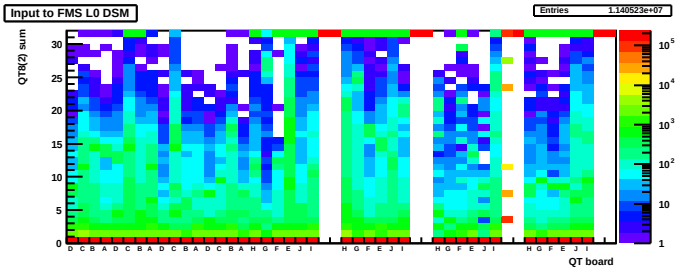

Input to QT4 crate (north-bottom)
Entries3025212
ADC
channel

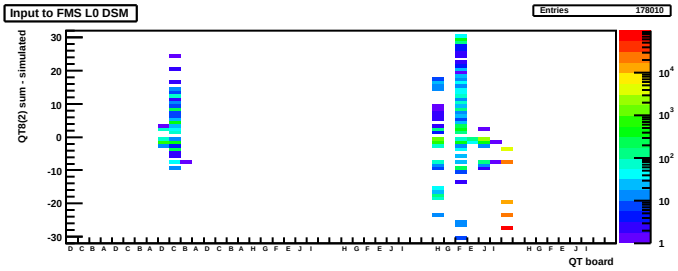

Input to FMS L0 DSM
Entries 1.140523e+07
QT8(0) sum
QT board

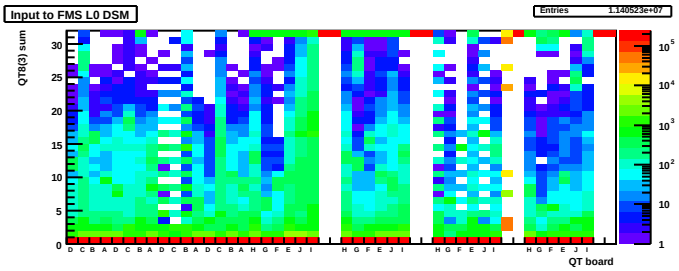

Input to FMS L0 DSM
Entries 15749
QT8(0) sum - simulated
QT board

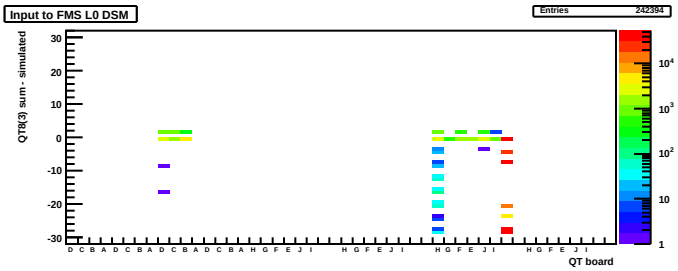

Input to FMS L0 DSM
Entries 1.140523e+07
QT8(1) sum
QT board

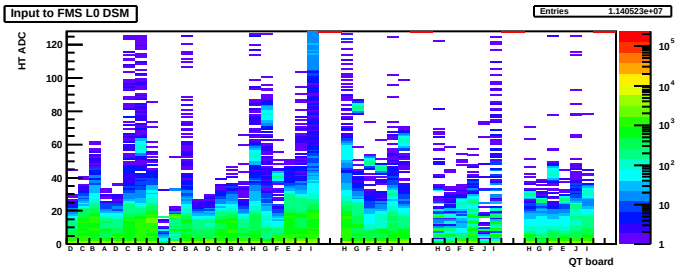

Input to FMS L0 DSM
Entries 12977
QT8(1) sum - simulated
QT board

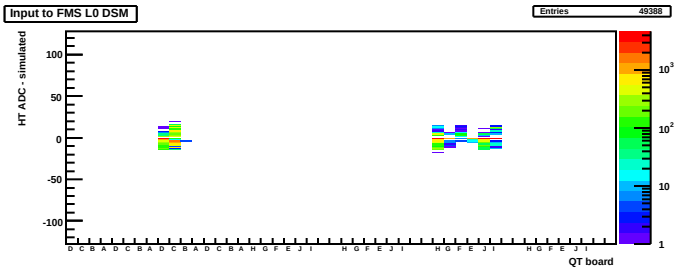

Input to FMS L0 DSM
Entries 1.140523e+07
QT8(2) sum
QT board

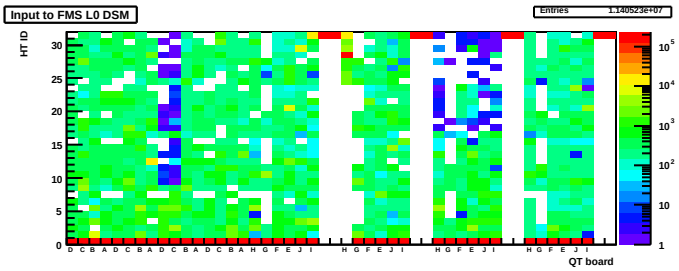

Input to FMS L0 DSM
Entries 178010
QT8(2) sum - simulated
QT board

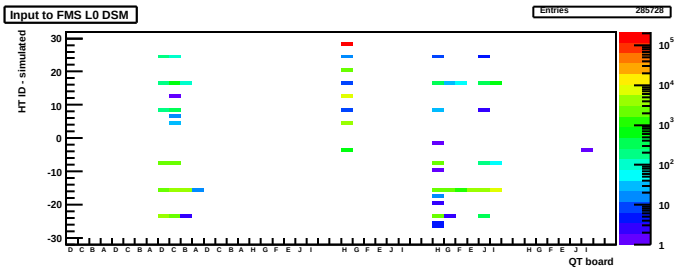

Input to FMS L0 DSM
Entries 1.140523e+07
QT8(3) sum
QT board

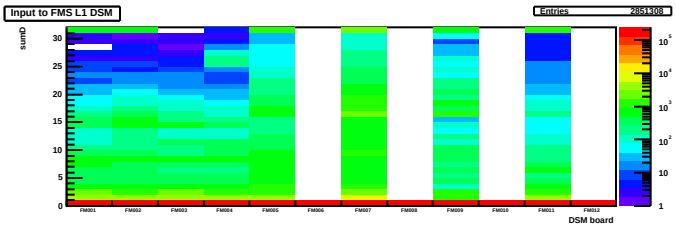

Input to FMS L0 DSM
Entries 242394
QT8(3) sum - simulated
QT board

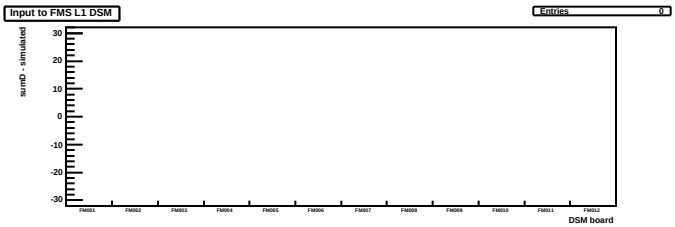

Input to FMS L0 DSM
Entries 1.140523e+07
HT ADC
QT board

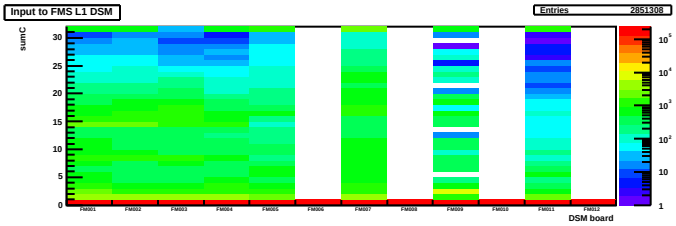

Input to FMS L0 DSM
Entries 49388
HT ADC - simulated
QT board

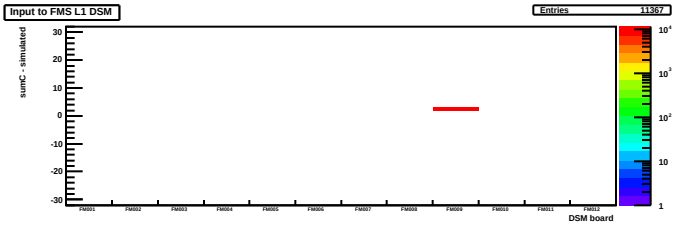

Input to FMS L0 DSM
Entries 1.140523e+07
HT ID
QT board

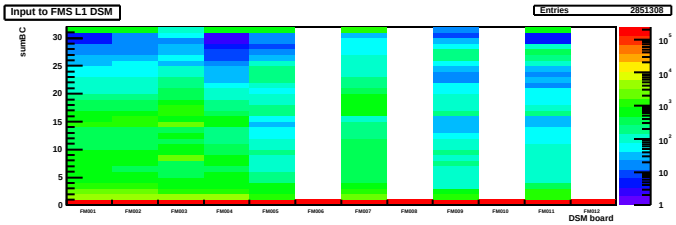

Input to FMS L0 DSM
Entries 285728
HT ID - simulated
QT board

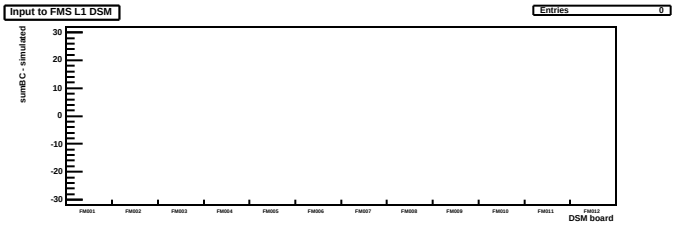

Input to FMS L1 DSM
Entries 2851308
sumD
DSM board

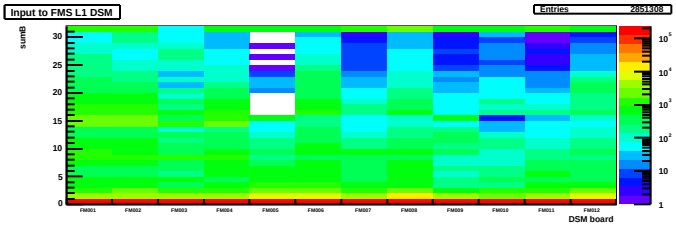

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

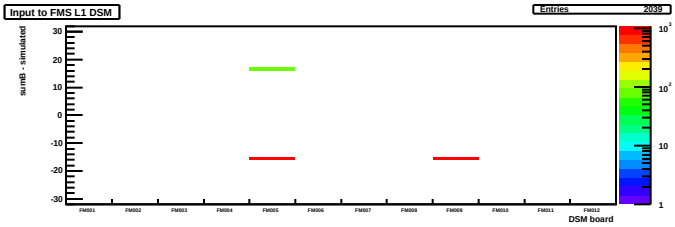

Input to FMS L1 DSM
Entries 2851308
sumC
DSM board

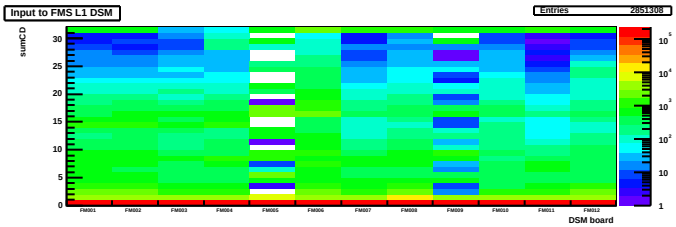

Input to FMS L1 DSM
Entries 11367
sumC - simulated
DSM board

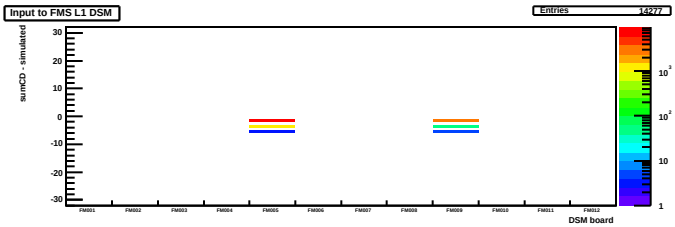

Input to FMS L1 DSM
Entries 2851308
sumBC
DSM board

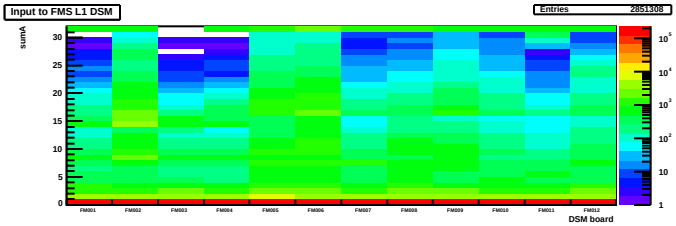

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

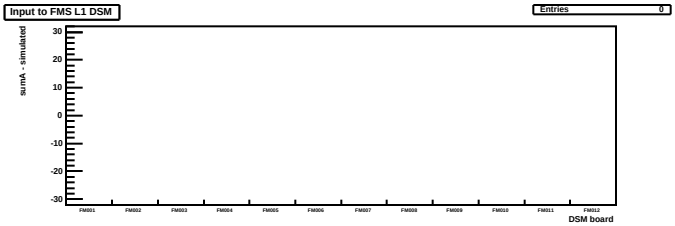

Input to FMS L1 DSM
Entries 2851308
sumB
DSM board

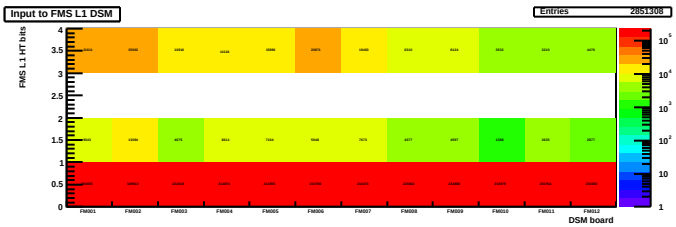

Input to FMS L1 DSM
Entries 2039
sumB - simulated
DSM board

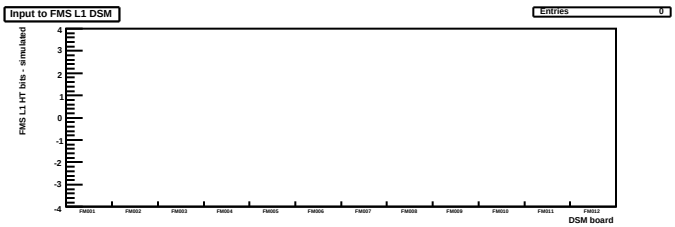

Input to FMS L1 DSM
Entries 2851308
sumCD
DSM board

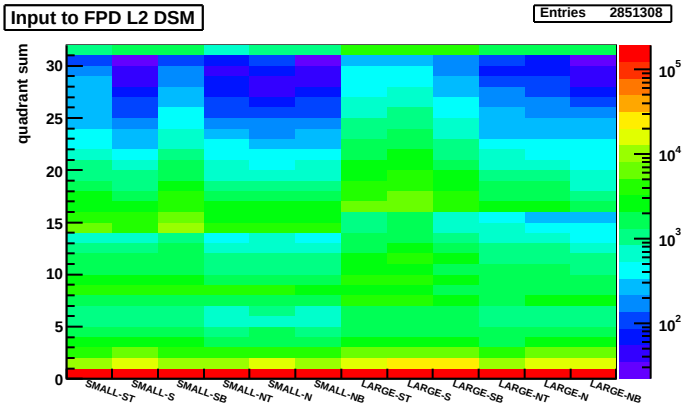

Input to FMS L1 DSM
Entries 14277
sumCD - simulated
DSM board

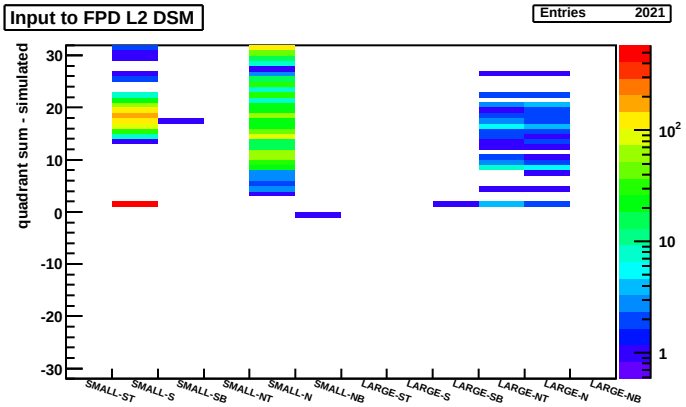

Input to FMS L1 DSM
Entries 2851308
sumA
DSM board

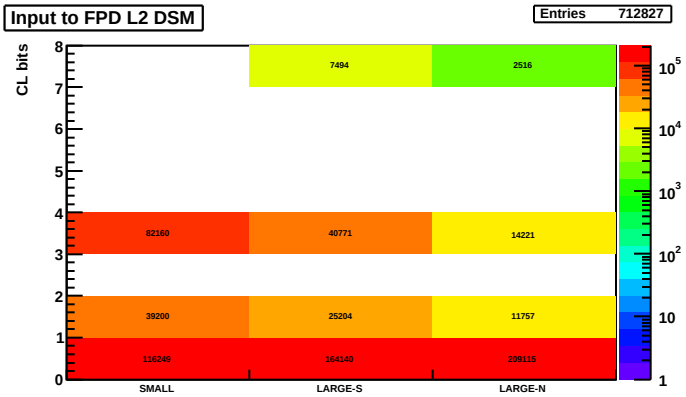

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

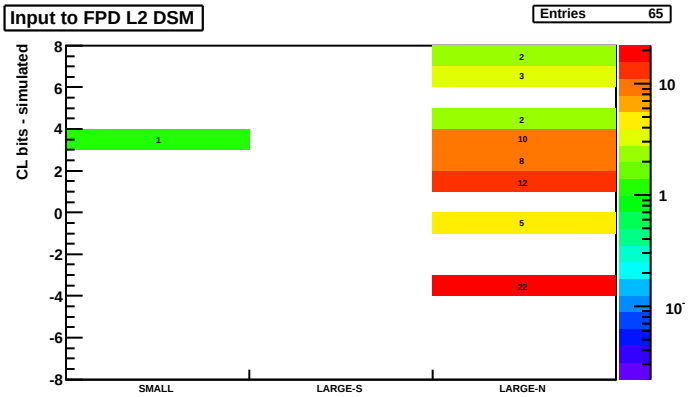

Input to FMS L1 DSM
Entries 2851308
FMS L1 HT bits
DSM board

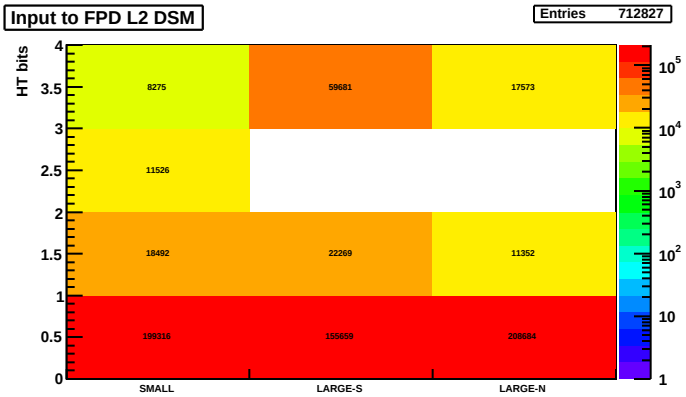

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

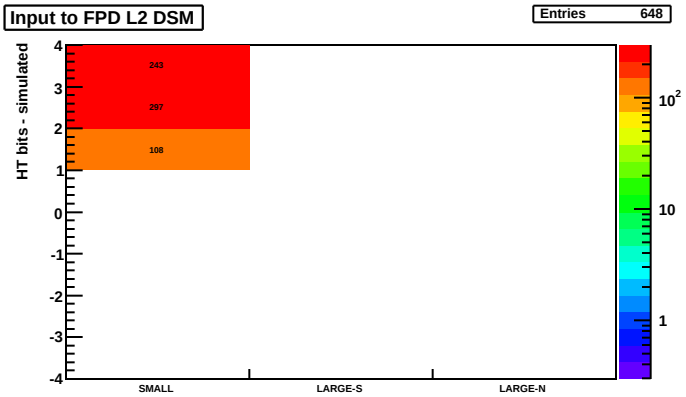

Input to FPD L2 DSM
Entries 2851308
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

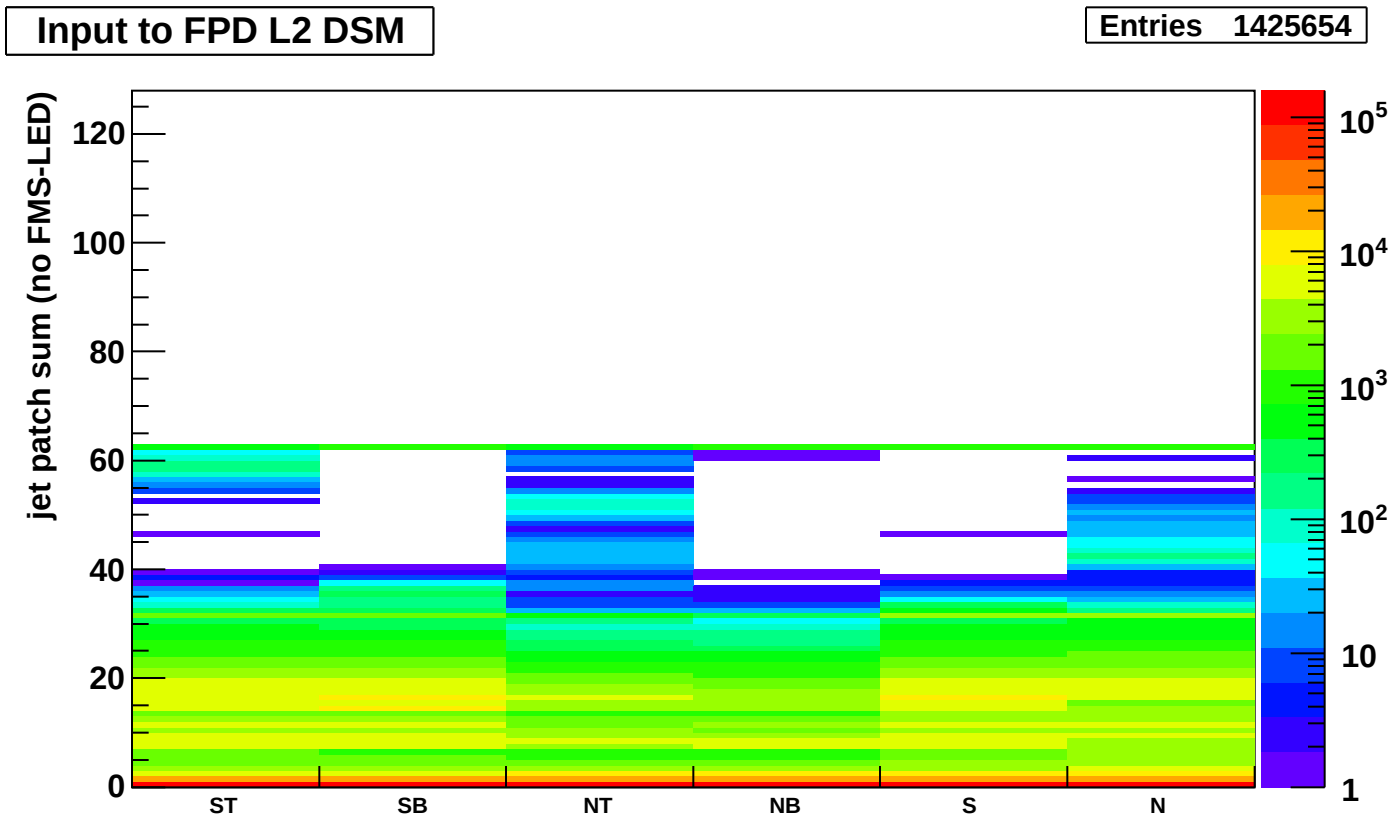

Input to FPD L2 DSM
Entries 2021
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

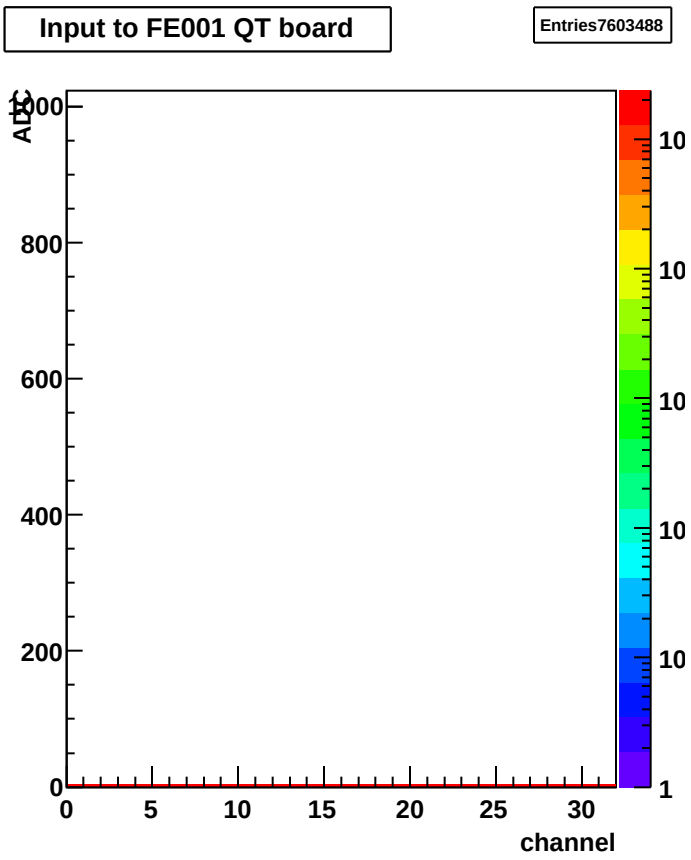

Input to FPD L2 DSM
Entries 712827
CL bits
7494
2516
82160
40771
14221
39200
25204
11757
116249
164140
209115
SMALL
LARGE-S
LARGE-N

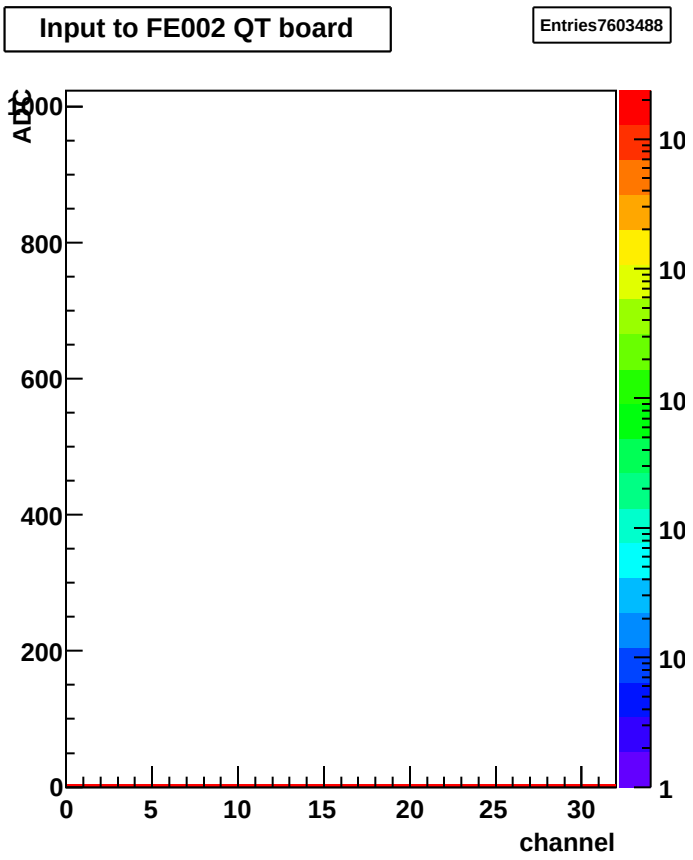

Input to FPD L2 DSM
Entries 65
CL bits - simulated
1
2
3
2
10
8
12
5
22
SMALL
LARGE-S
LARGE-N

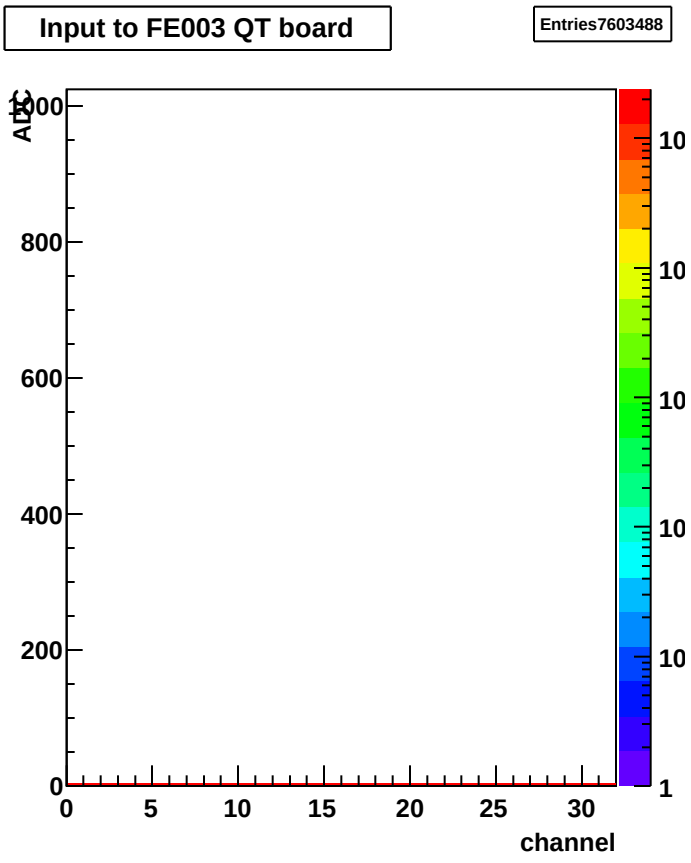

Input to FPD L2 DSM
Entries 712827
HT bits
8275
59681
17573
11526
18492
22269
11352
199316
155659
208684
SMALL
LARGE-S
LARGE-N

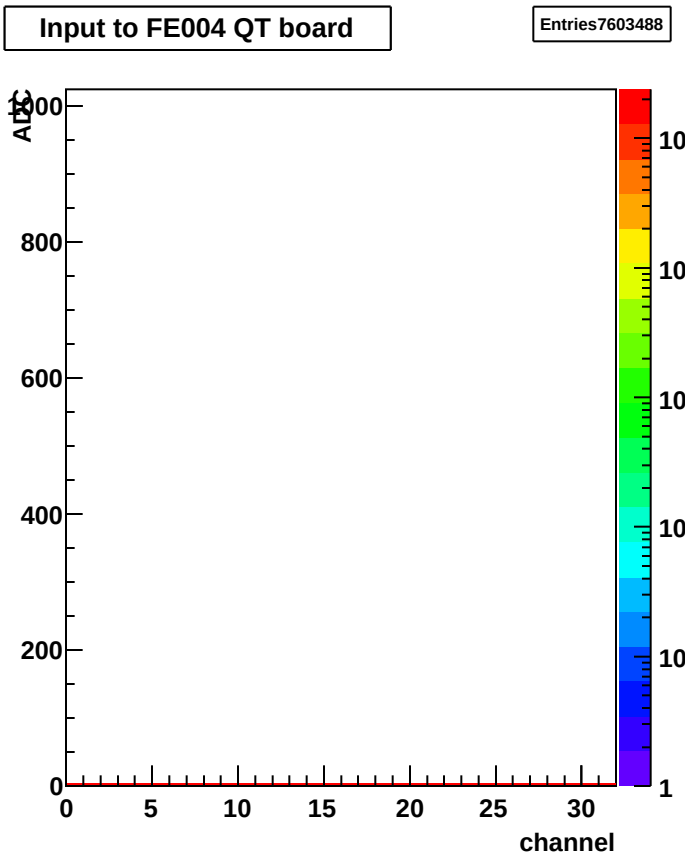

Input to FPD L2 DSM
Entries 648
HT bits - simulated
243
297
108
SMALL
LARGE-S
LARGE-N

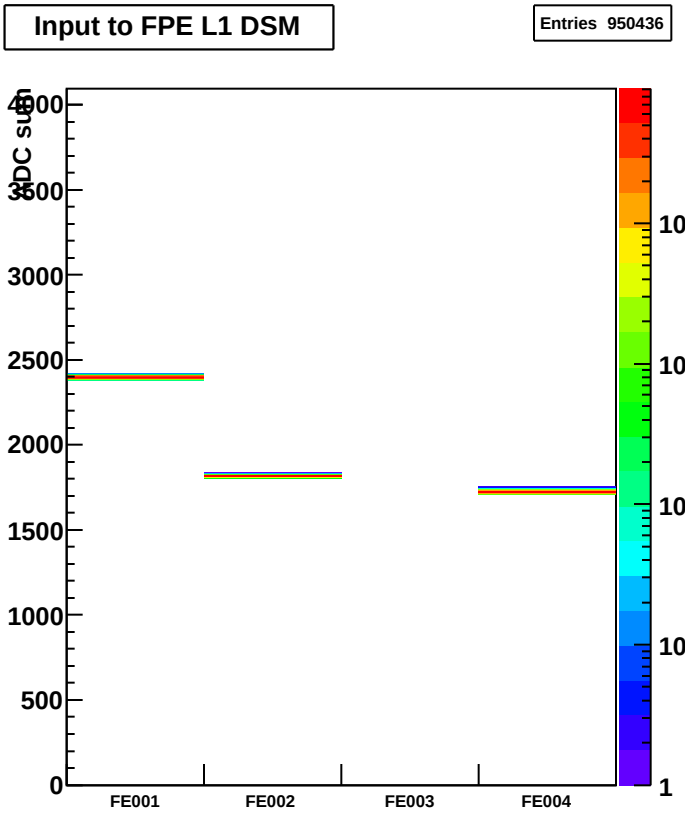

Input to FPD L2 DSM
Entries 1425654
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

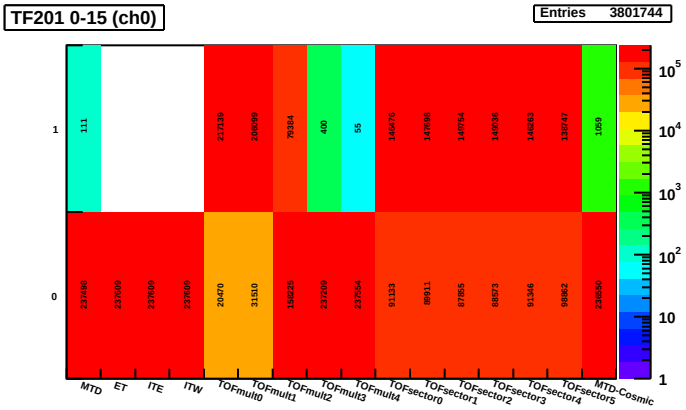

Input to FE001 QT board
Entries7603488
ADC
channel

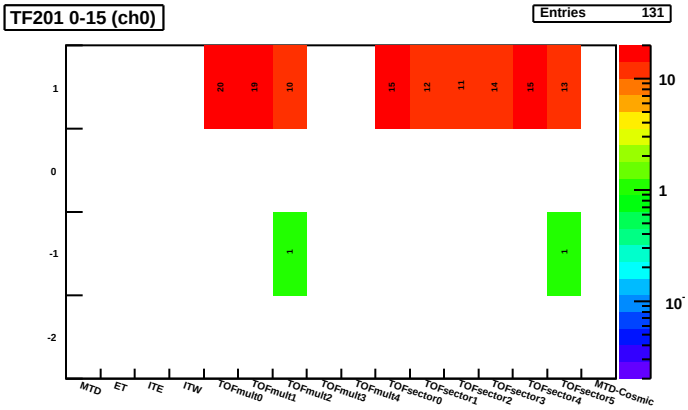

Input to FE002 QT board
Entries7603488
ADC
channel

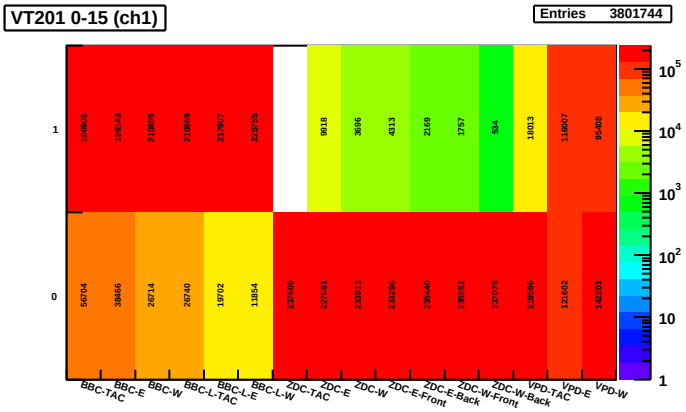

Input to FE003 QT board
Entries7603488
ADC
channel

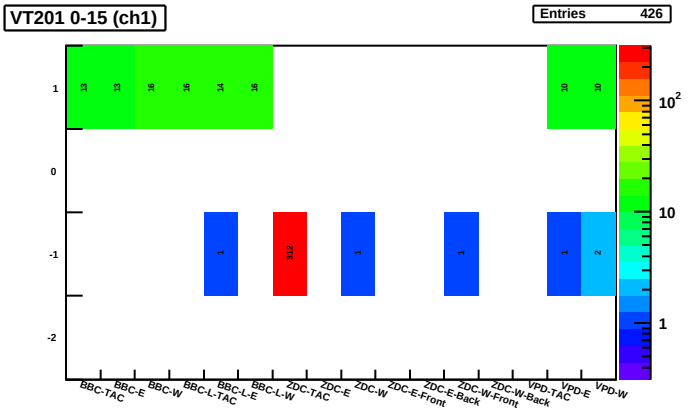

Input to FE004 QT board
Entries7603488
ADC
channel

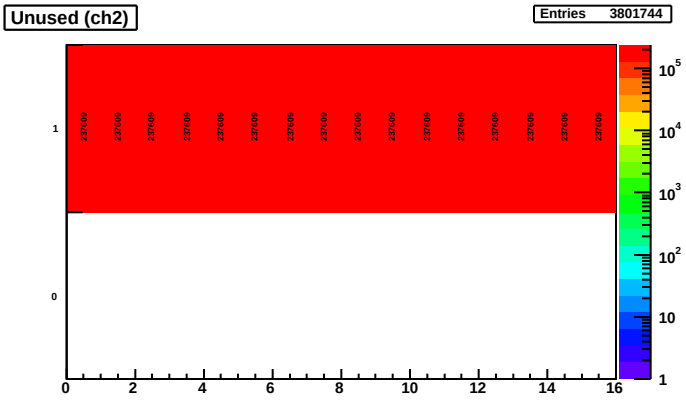

Input to FPE L1 DSM
Entries 950436
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

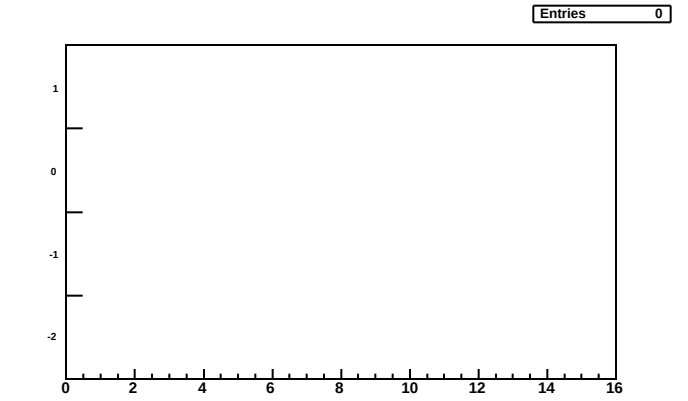

TF201 0-15 (ch0)
Entries 3801744
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

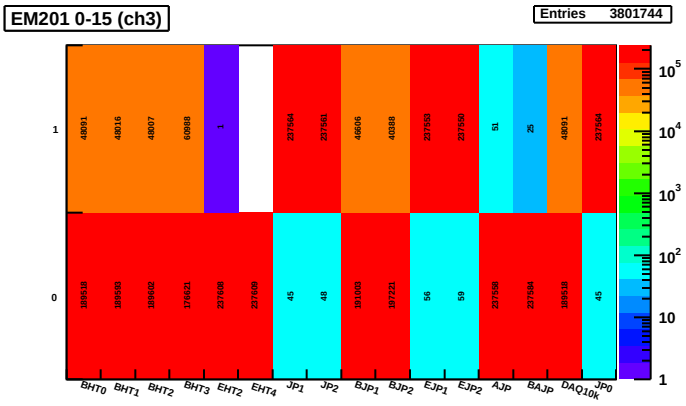

TF201 0-15 (ch0)
Entries 131
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3801744
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 426
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3801744


Entries 0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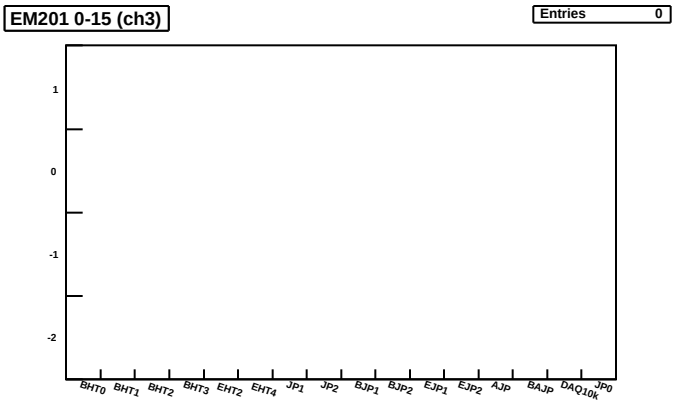
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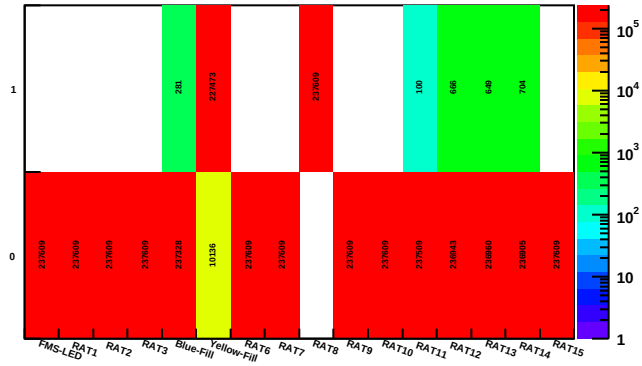
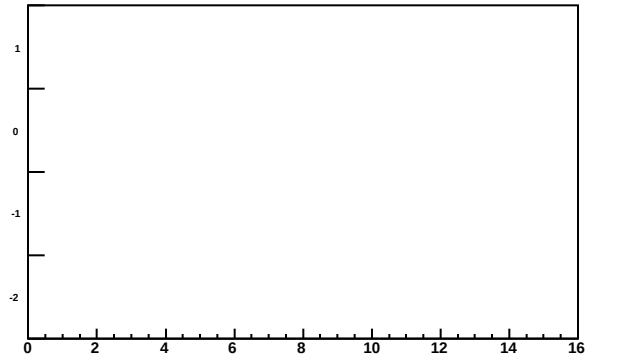
RAT board (ch4)





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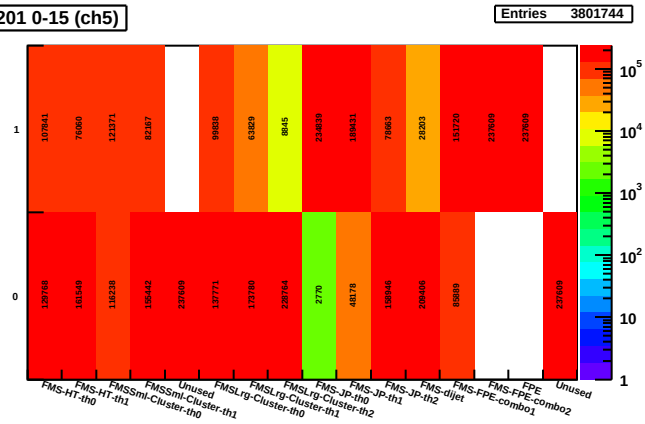

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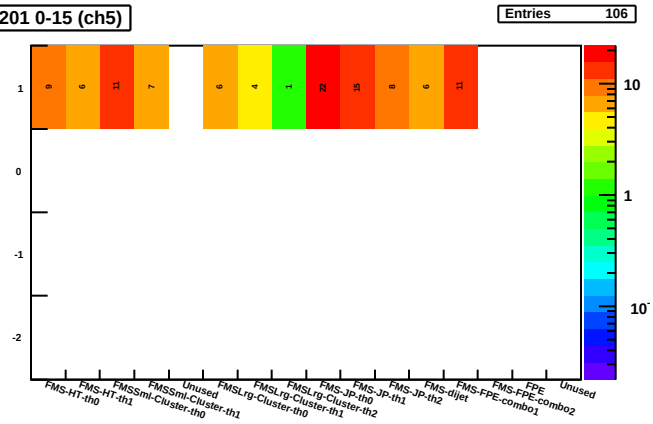

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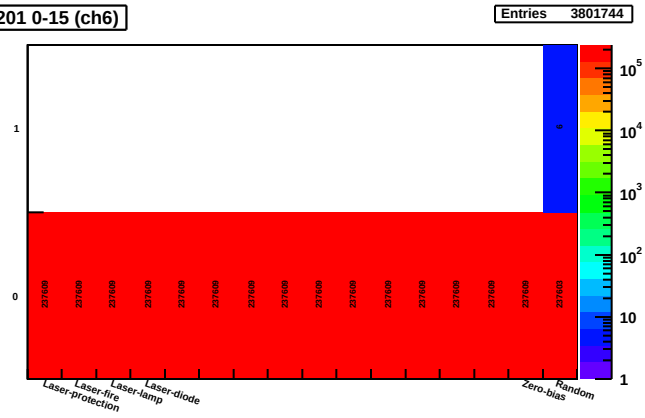

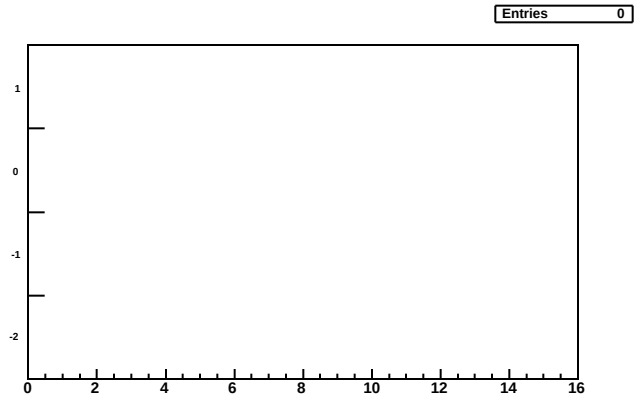
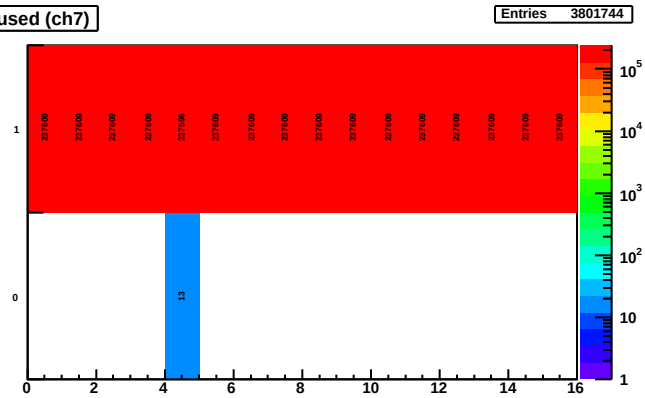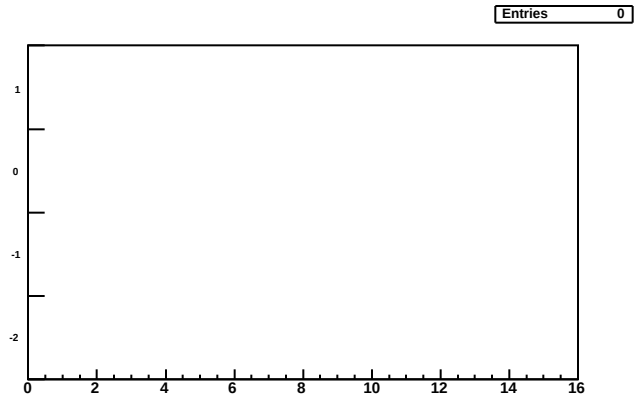
Unused (ch7)