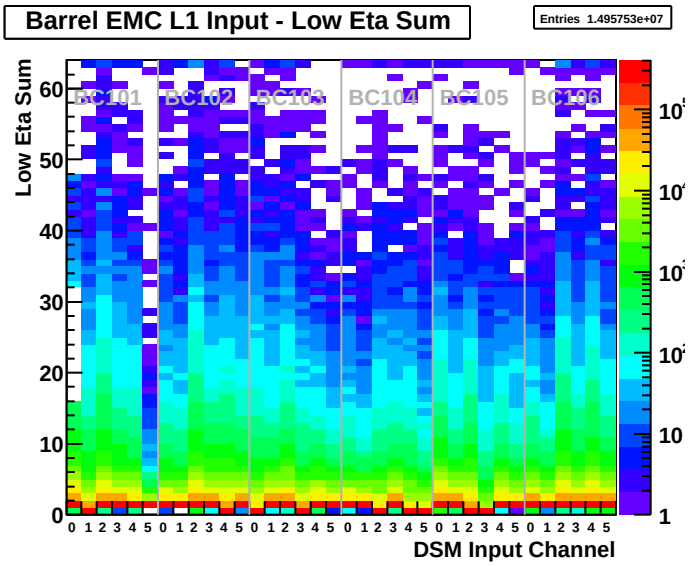
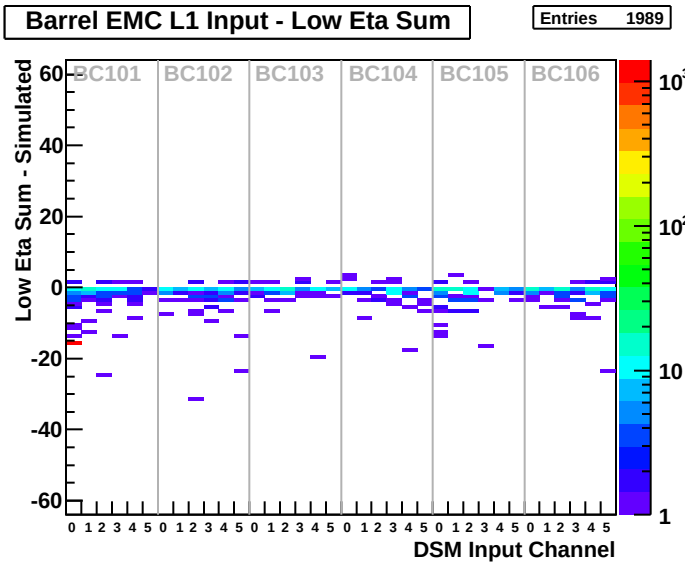
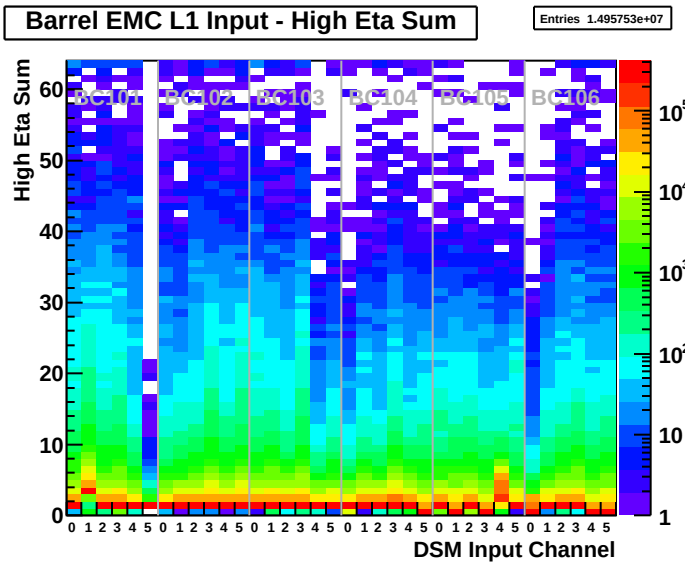
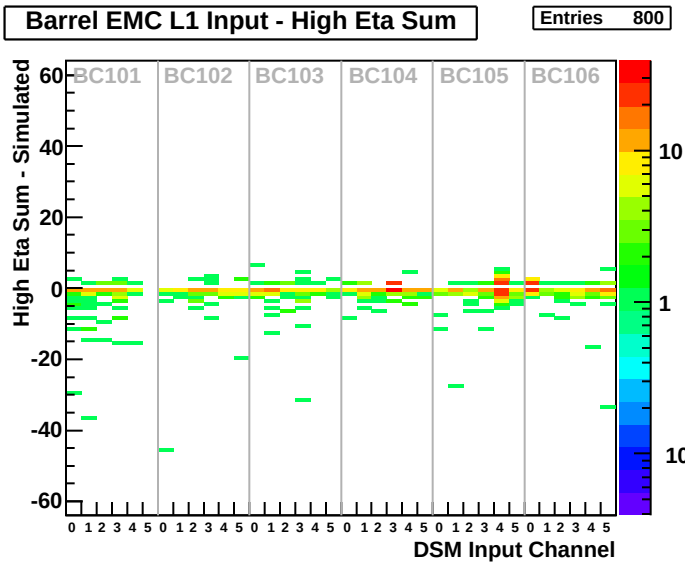
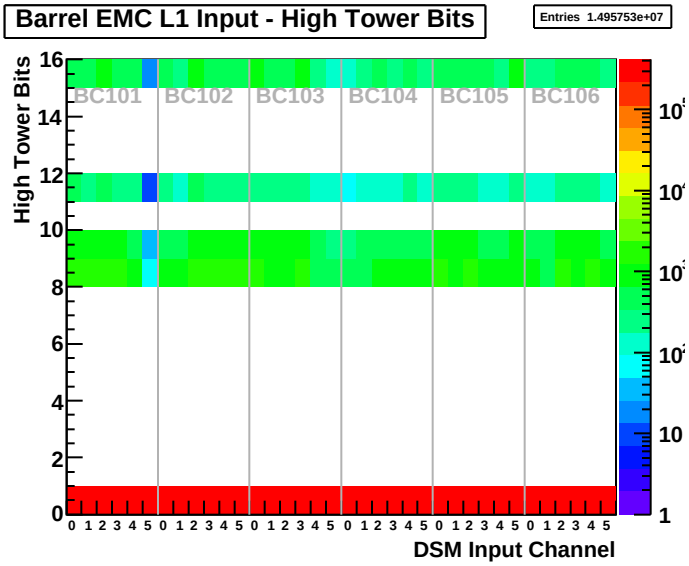
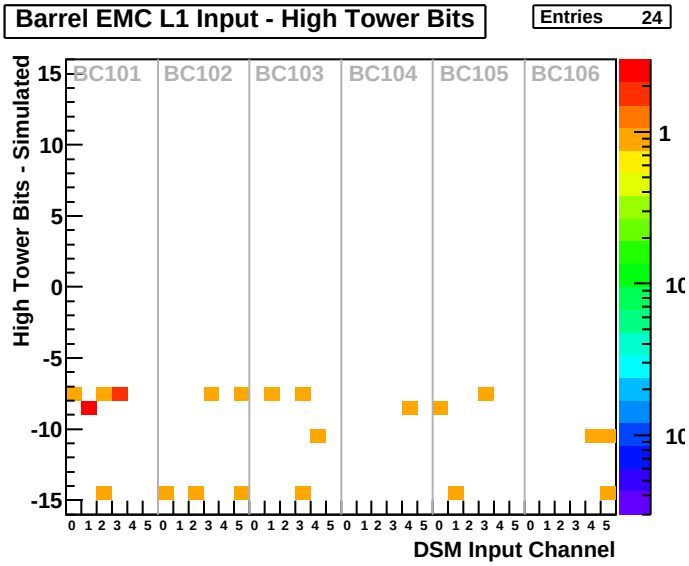

Barrel EMC L1 Input - Low Eta Sum
Entries 1.495753e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

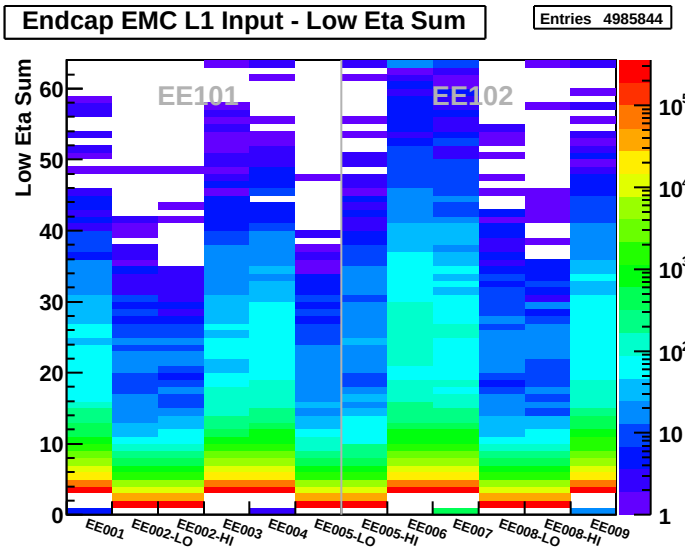

Barrel EMC L1 Input - Low Eta Sum
Entries 1989
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

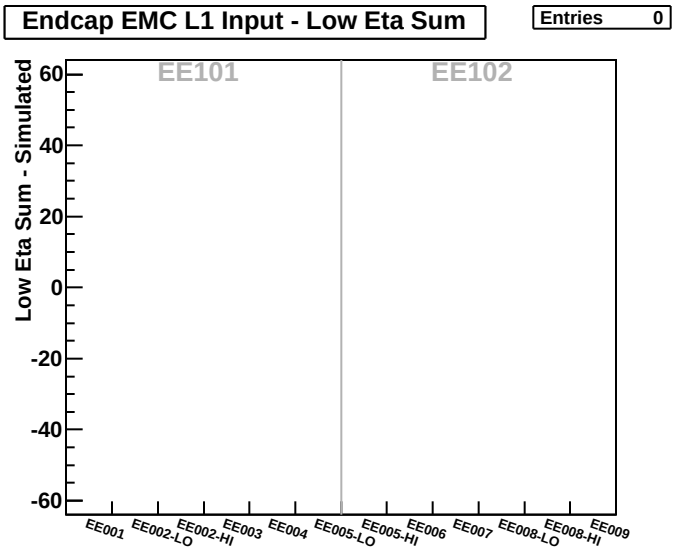

Barrel EMC L1 Input - High Eta Sum
Entries 1.495753e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

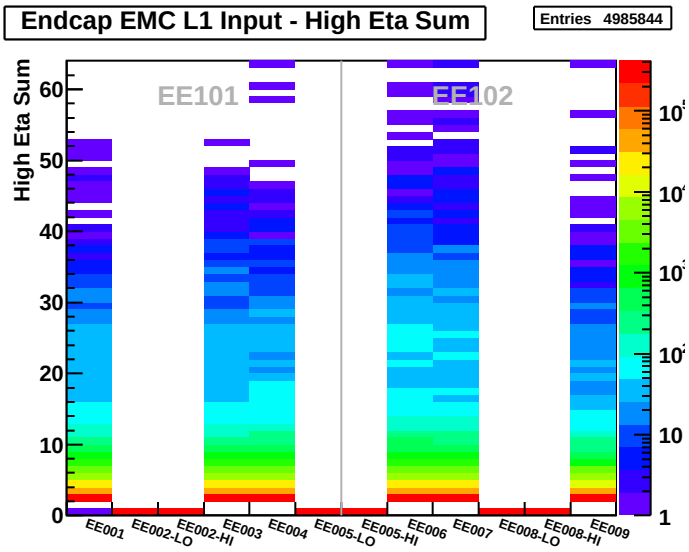

Barrel EMC L1 Input - High Eta Sum
Entries 800
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

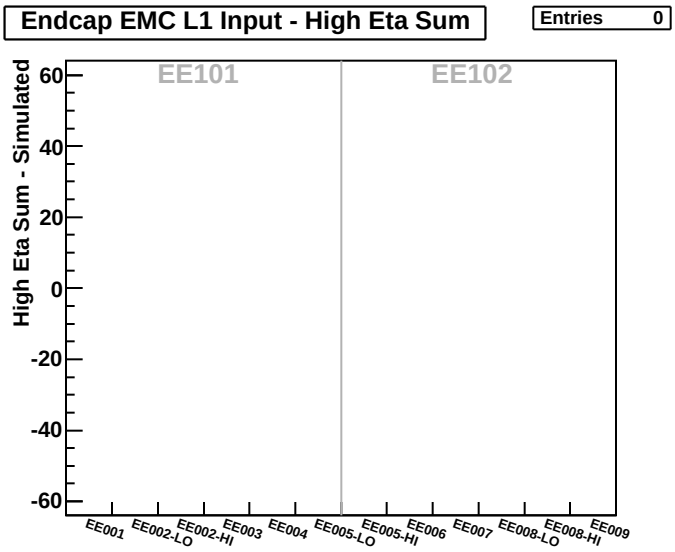

Barrel EMC L1 Input - High Tower Bits
Entries 1.495753e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

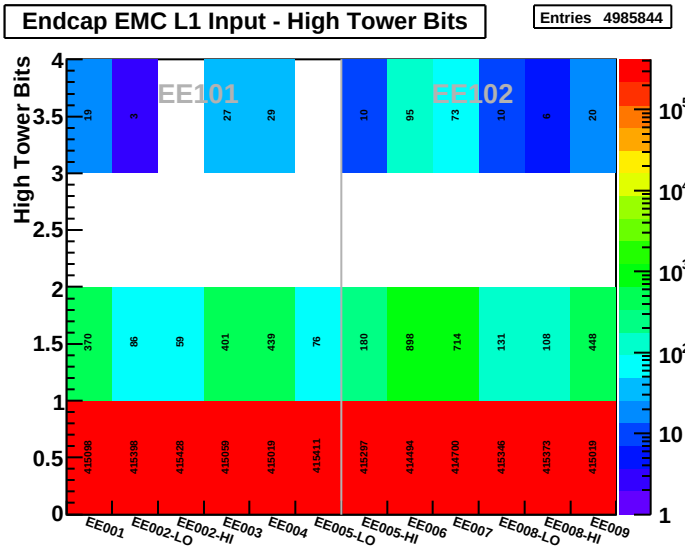

Barrel EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

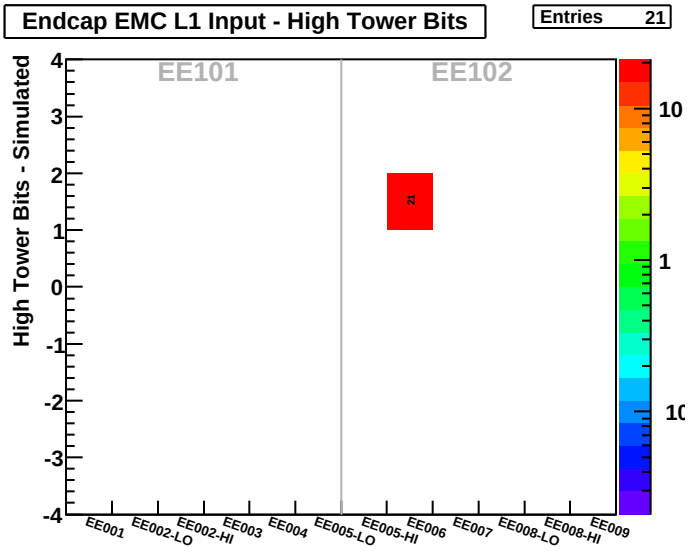

Endcap EMC L1 Input - Low Eta Sum
Entries 4985844
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

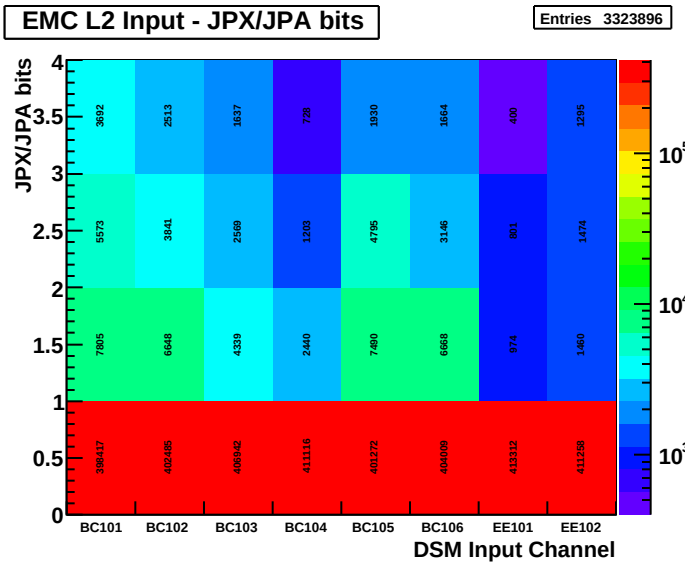

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

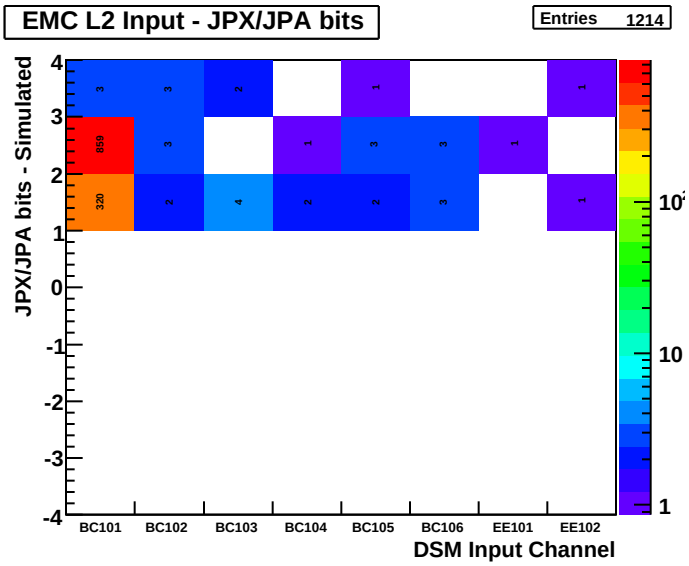

Endcap EMC L1 Input - High Eta Sum
Entries 4985844
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

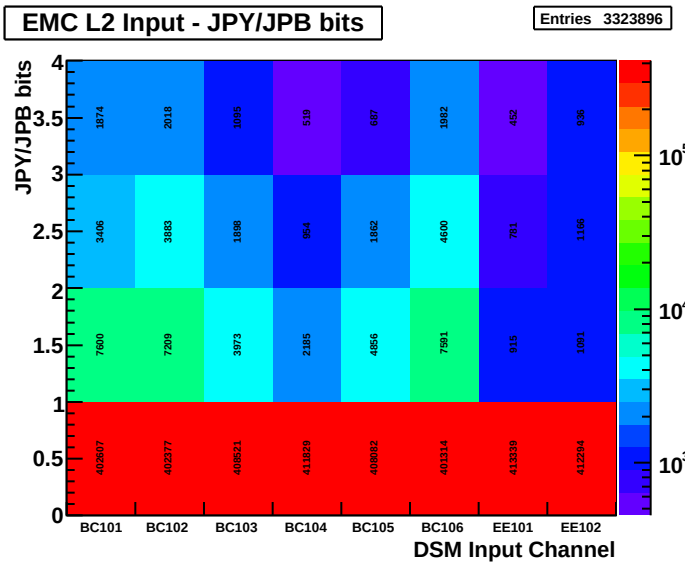

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

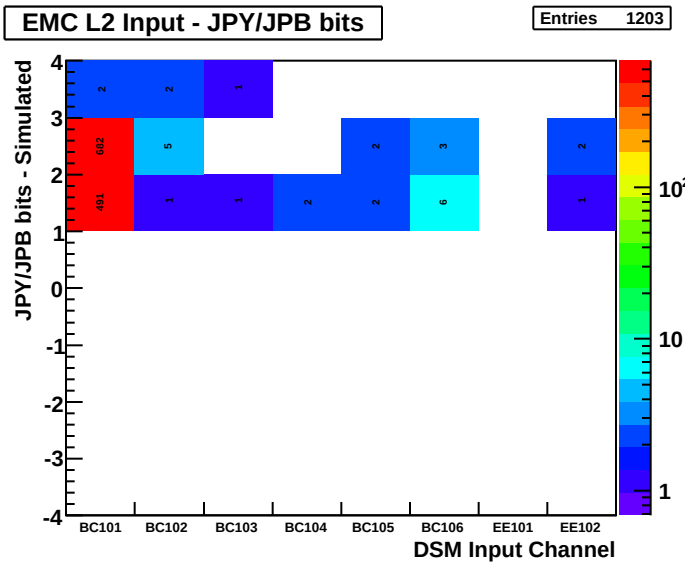

Endcap EMC L1 Input - High Tower Bits
Entries 4985844
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

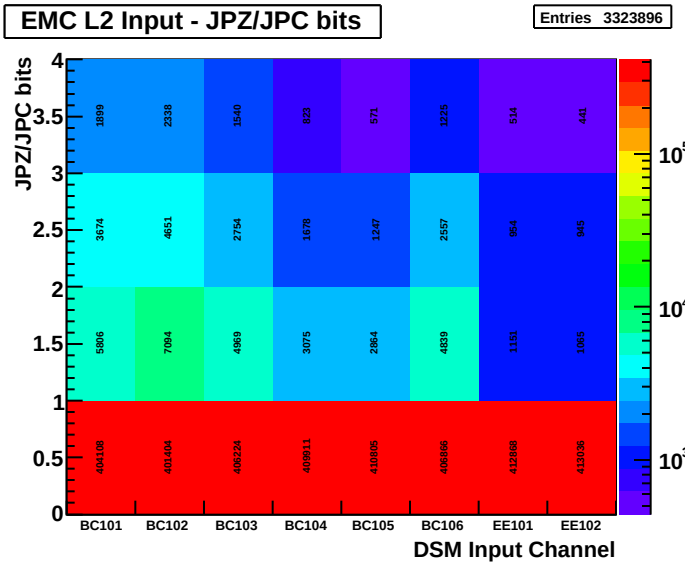

Endcap EMC L1 Input - High Tower Bits
Entries 21
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

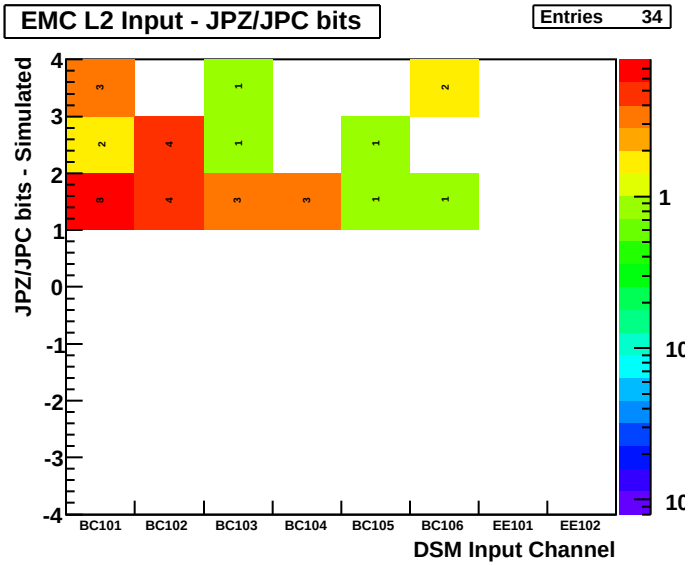

EMC L2 Input - JPX/JPA bits
Entries 3323896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3692 2513 1637 728 1930 1664 400 1295
5573 3841 2569 1203 4795 3146 801 1474
7865 6648 4339 2440 7490 6668 974 1480
398417 402485 406942 411116 401272 404009 413312 411258

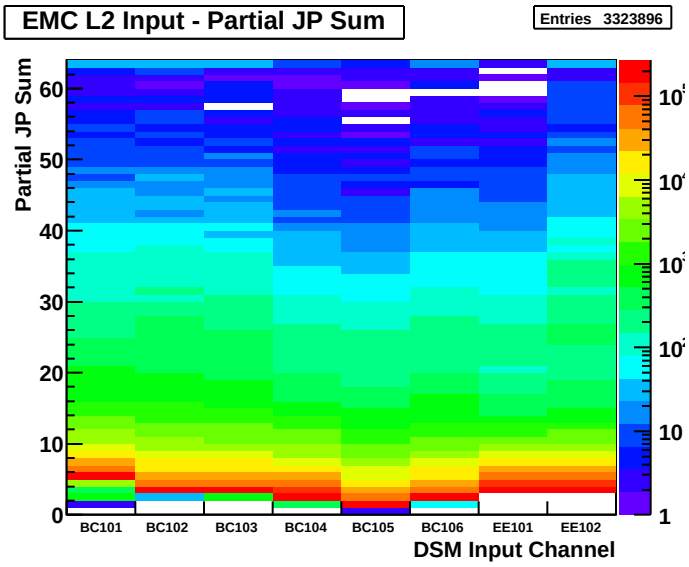

EMC L2 Input - JPX/JPA bits
Entries 1214
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 3 2 1 1
859 3 1 3 3 1
320 2 4 2 2 3 1

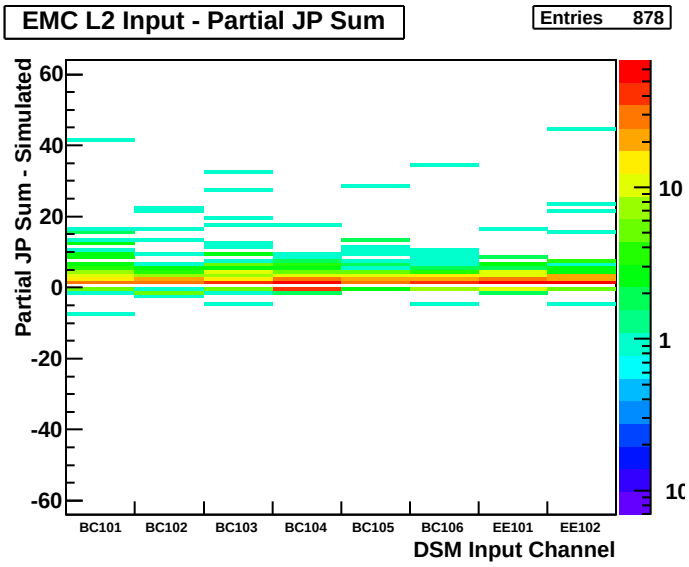

EMC L2 Input - JPY/JPB bits
Entries 3323896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1874 2018 1095 519 687 1982 452 936
3406 3883 1898 954 1862 4600 781 1166
7600 7209 3973 2185 4956 7591 915 1091
402607 402377 408521 411829 408982 401314 413339 412294

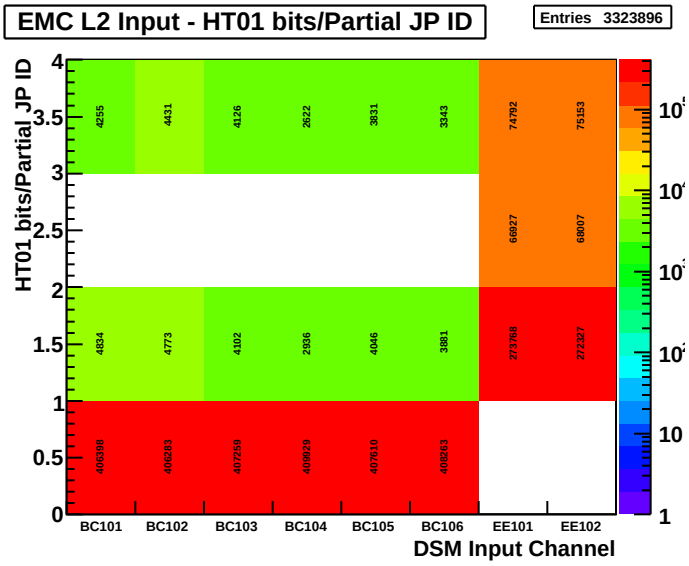

EMC L2 Input - JPY/JPB bits
Entries 1203
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 2 1
682 5 2 3 2
491 1 1 2 2 6 1

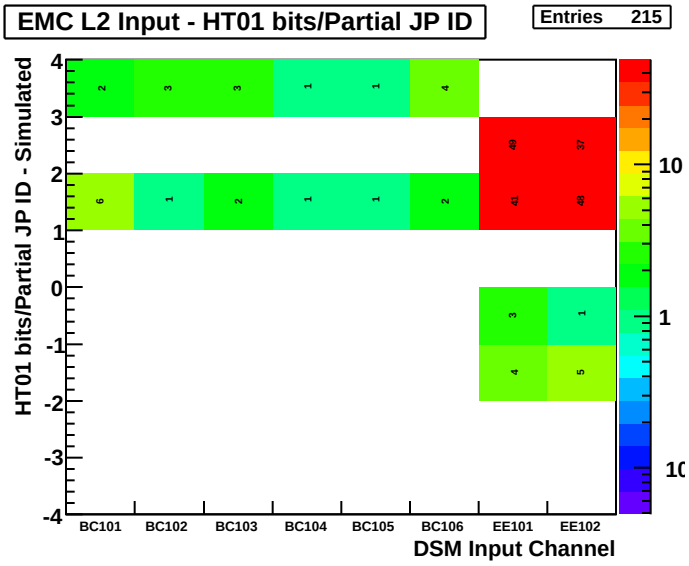

EMC L2 Input - JPZ/JPC bits
Entries 3323896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1899 2338 1540 823 571 1225 514 441
3674 4651 2754 1678 1247 2557 954 945
5806 7094 4969 3075 2864 4839 1151 1065
404108 401404 406224 409811 410805 406866 412868 413036

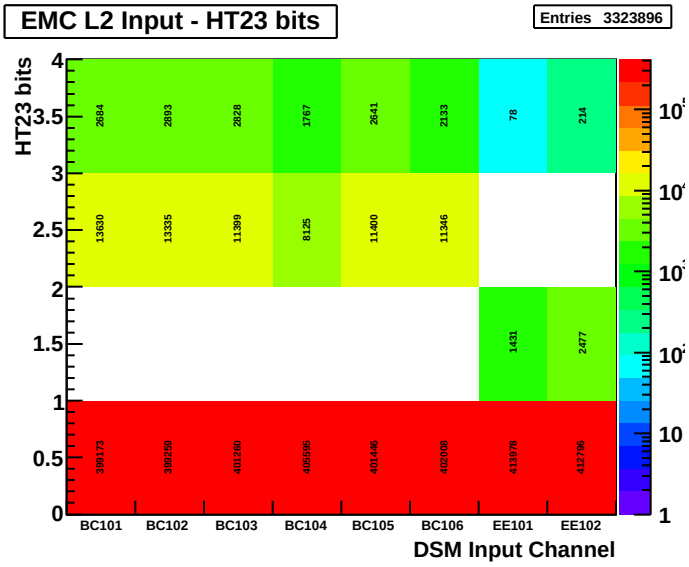

EMC L2 Input - JPZ/JPC bits
Entries 34
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 1 2
2 4 1 1
8 4 3 3 1 1

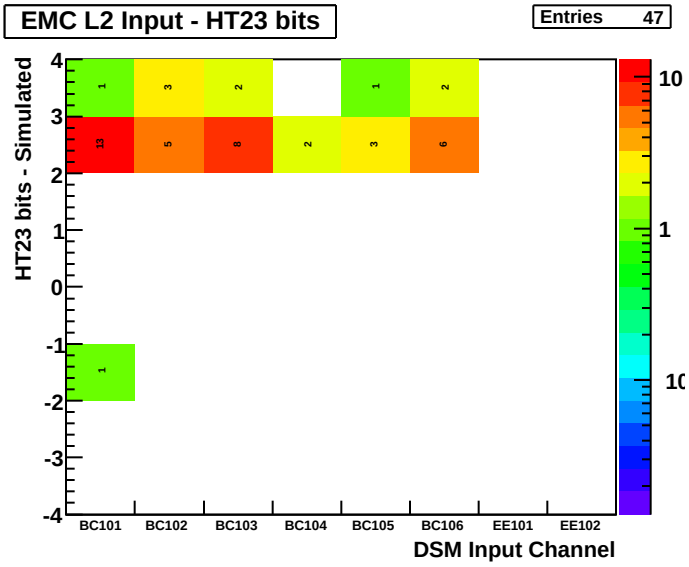

EMC L2 Input - Partial JP Sum
Entries 3323896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

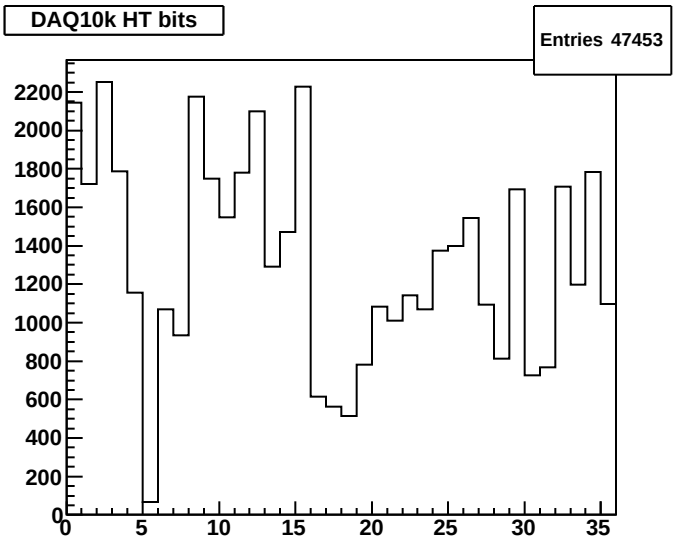

EMC L2 Input - Partial JP Sum
Entries 878
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

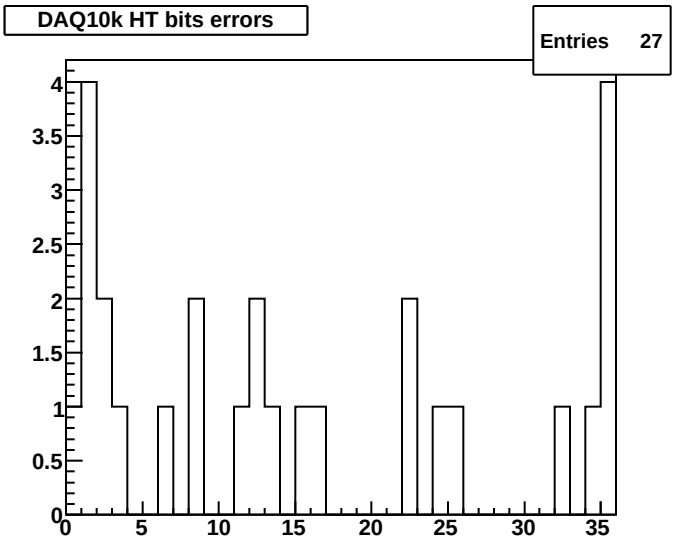

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3323896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

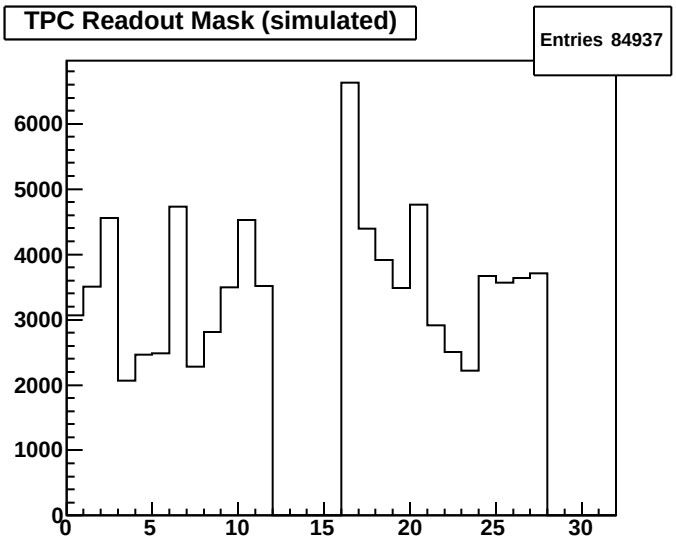

EMC L2 Input - HT01 bits/Partial JP ID
Entries 215
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

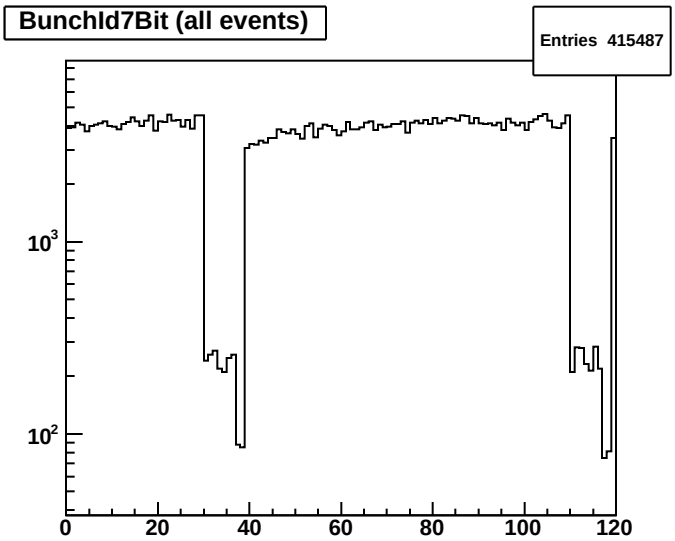

EMC L2 Input - HT23 bits
Entries 3323896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

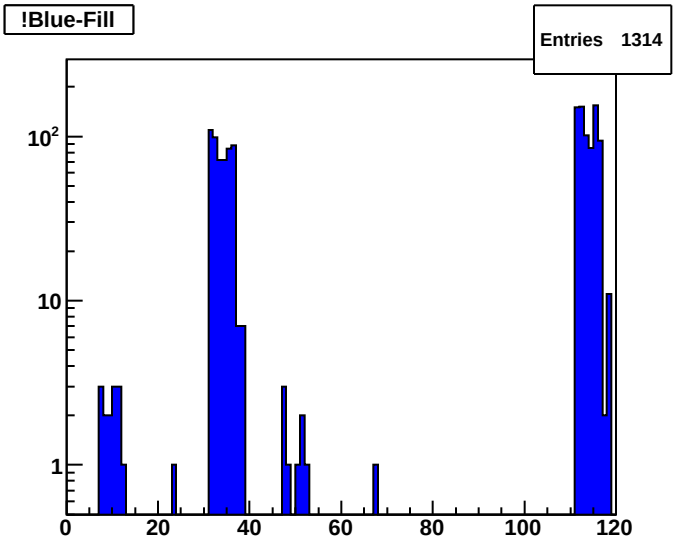

EMC L2 Input - HT23 bits
Entries 47
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

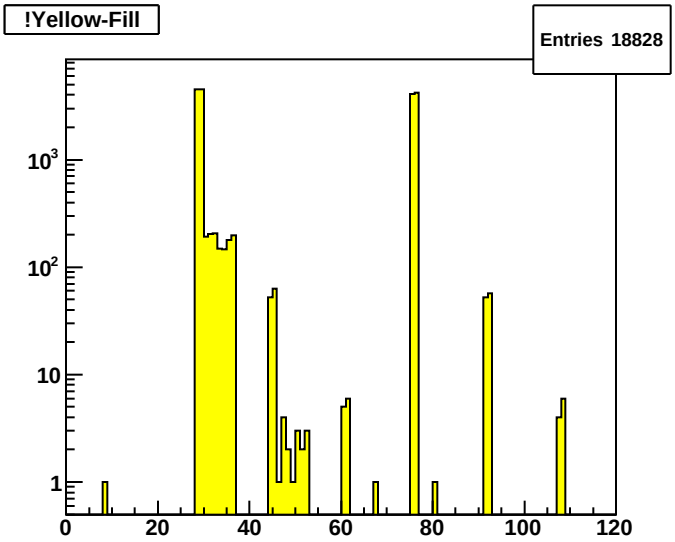

DAQ10k HT bits
Entries 47453

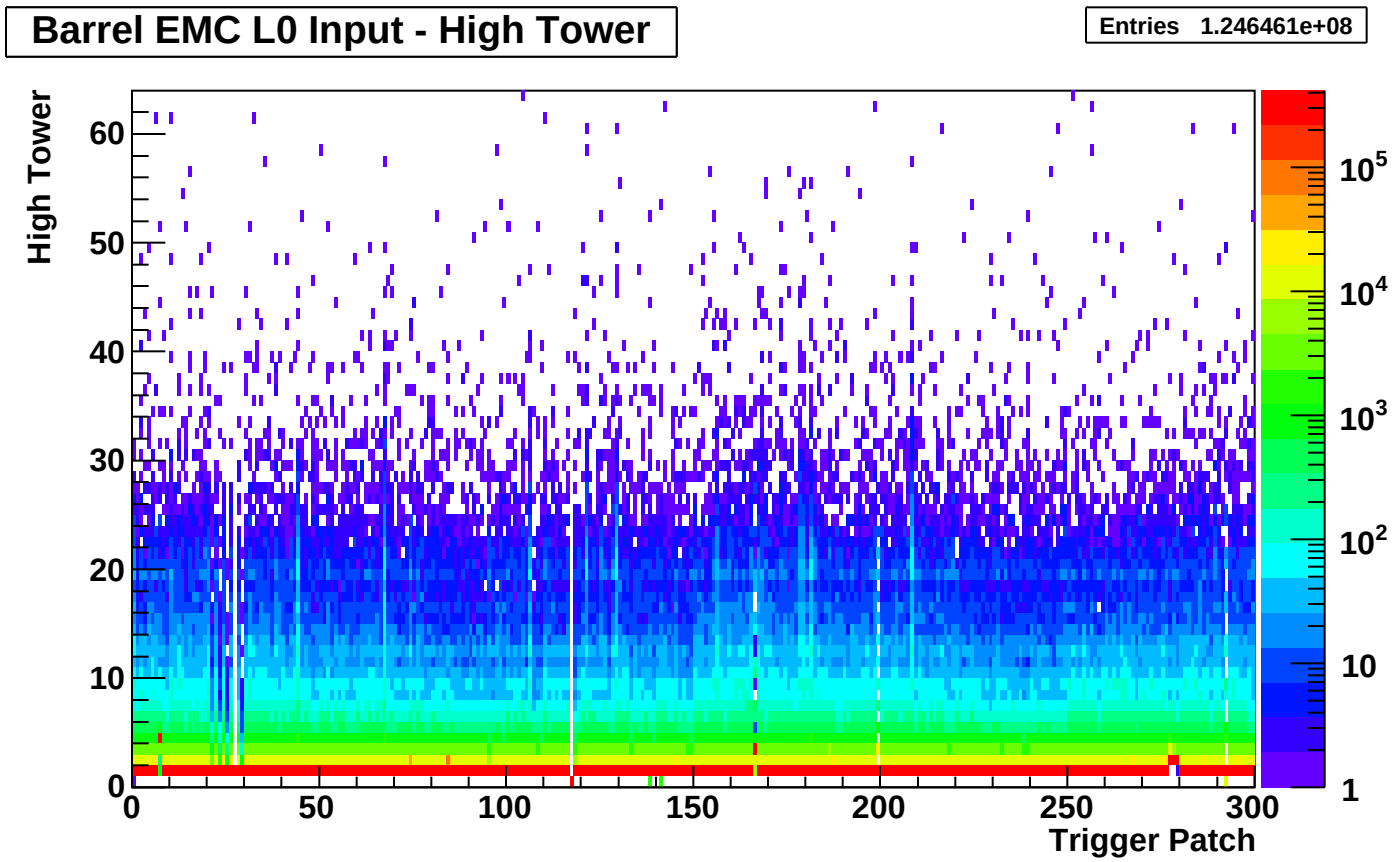

DAQ10k HT bits errors
Entries 27

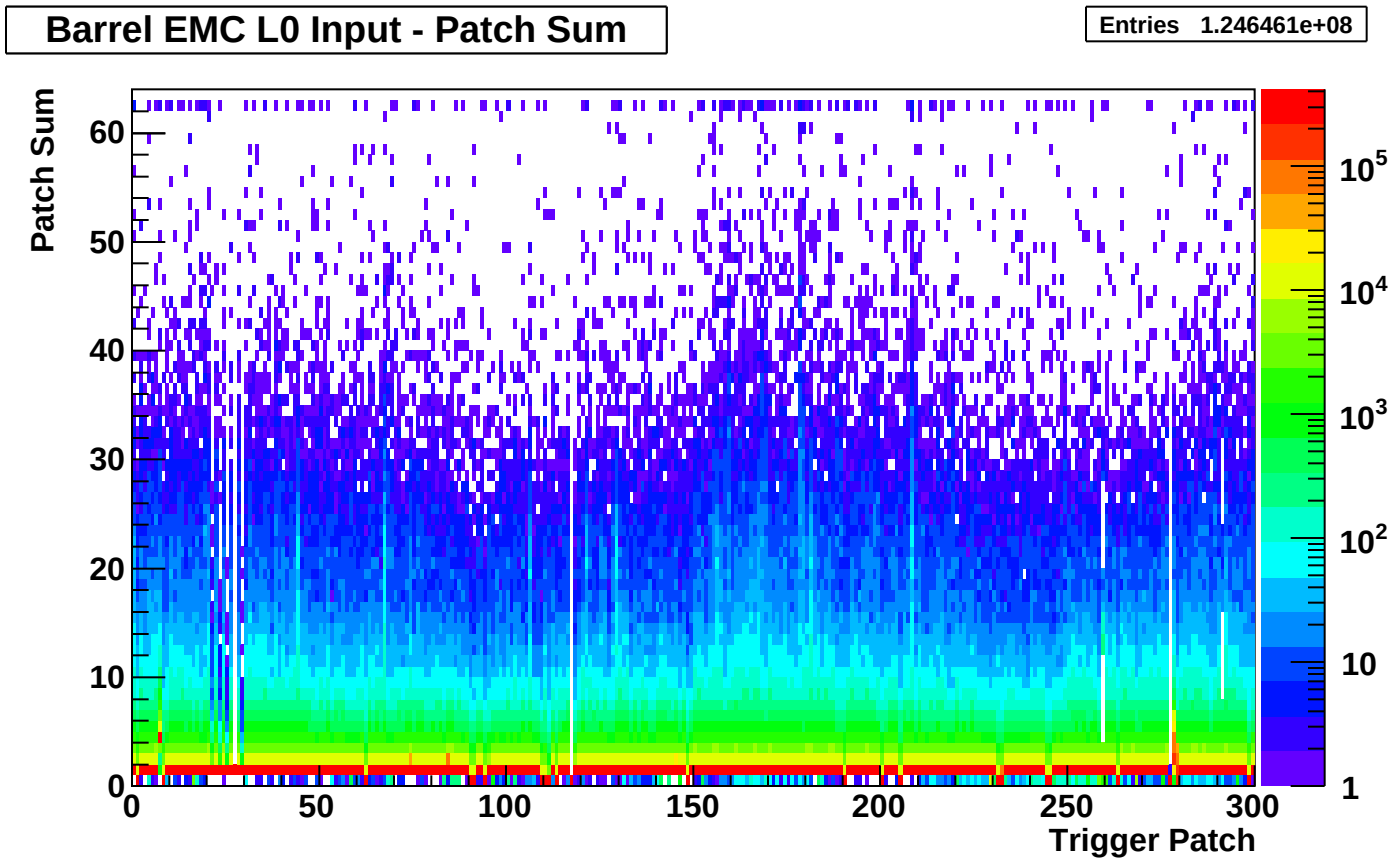

TPC Readout Mask (simulated)
Entries 84937

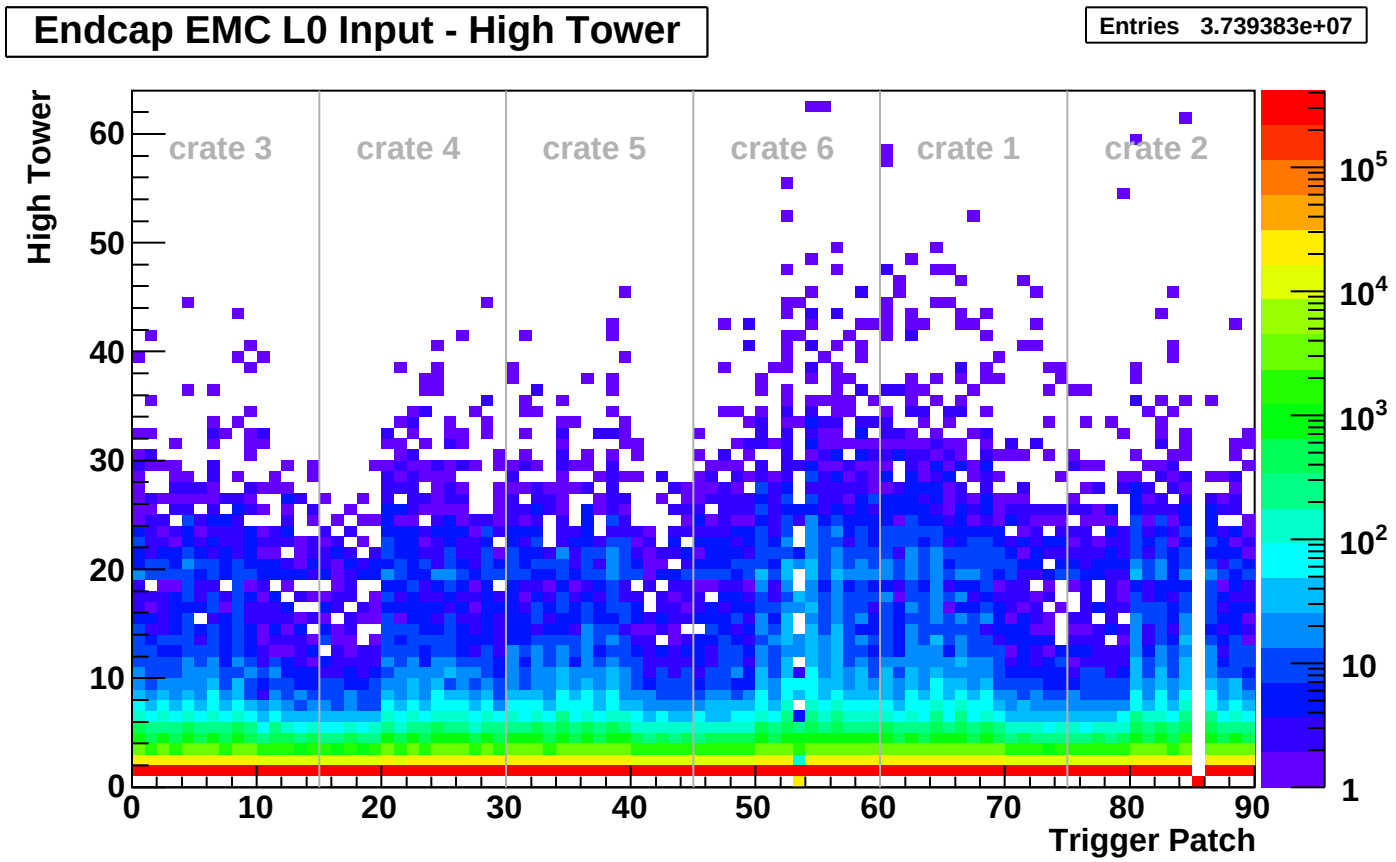

BunchId7Bit (all events)
Entries 415487

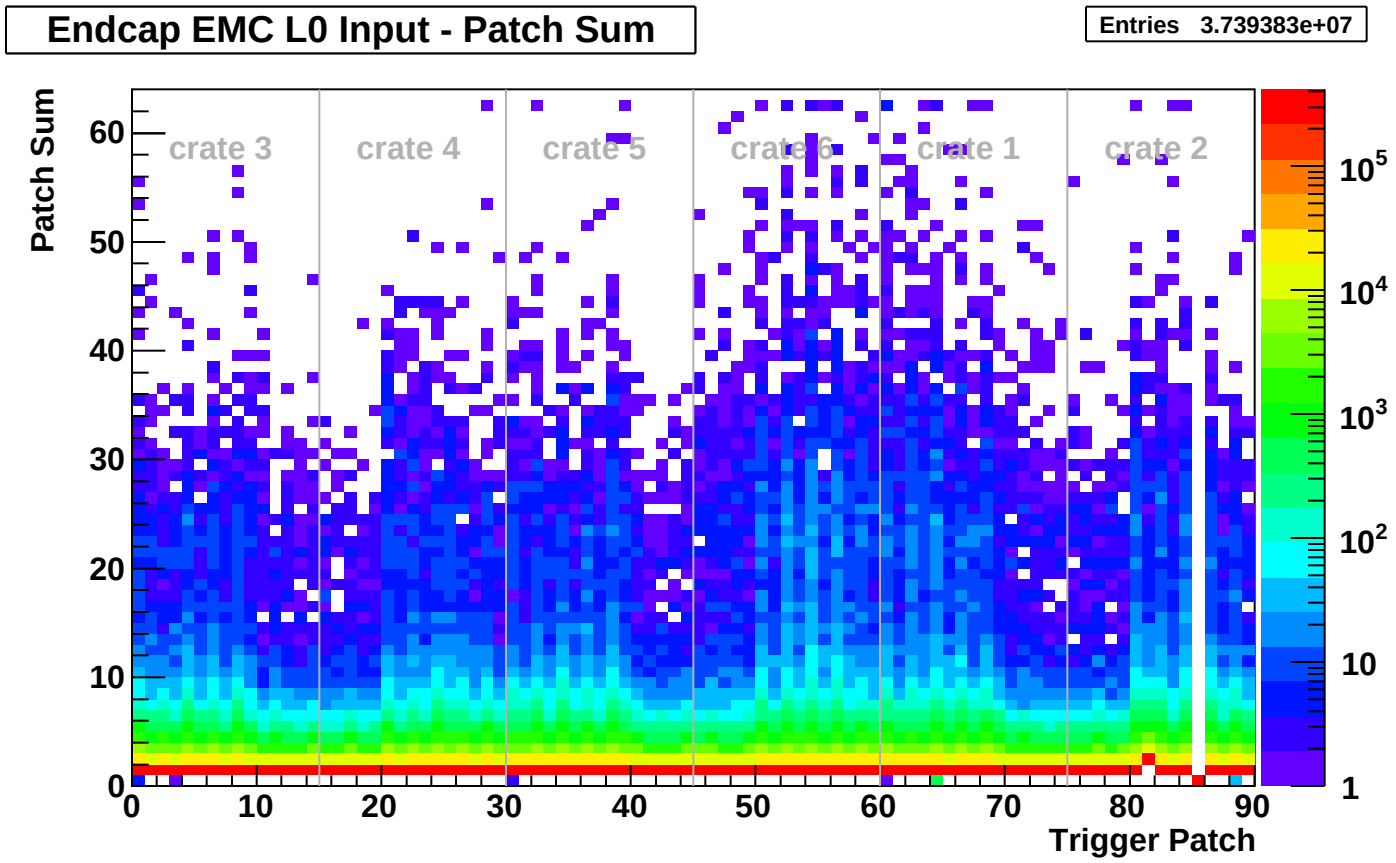

!Blue-Fill
Entries 1314

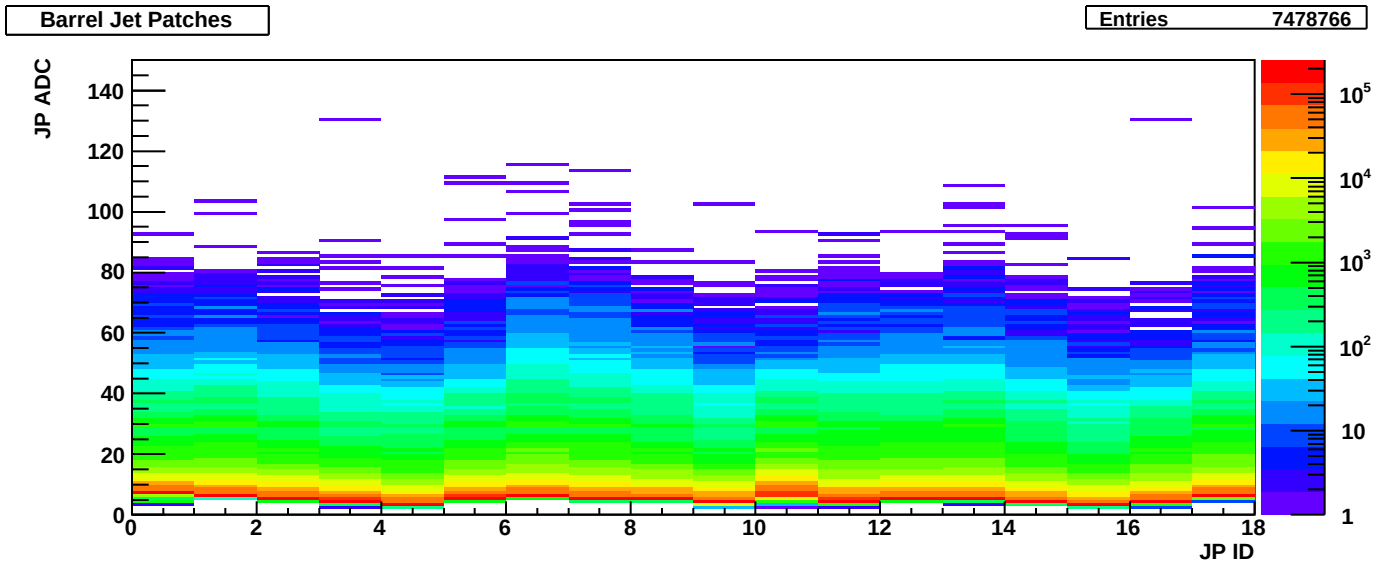

!Yellow-Fill
Entries 18828

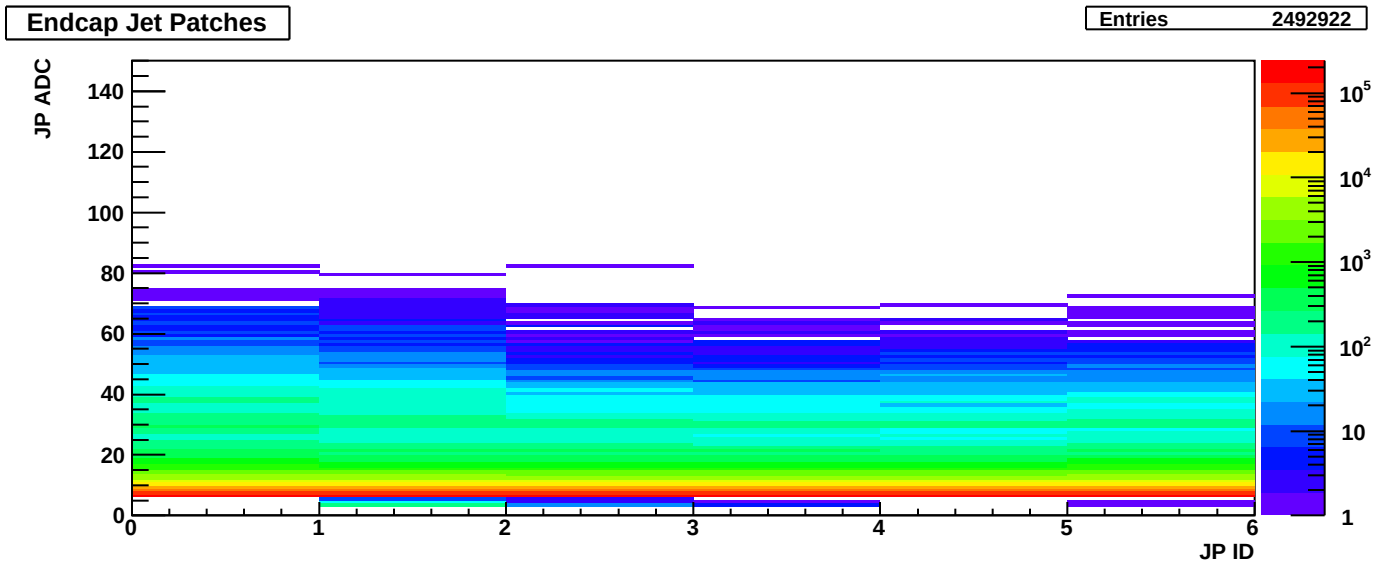

Barrel EMC L0 Input - High Tower
Entries 1.246461e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

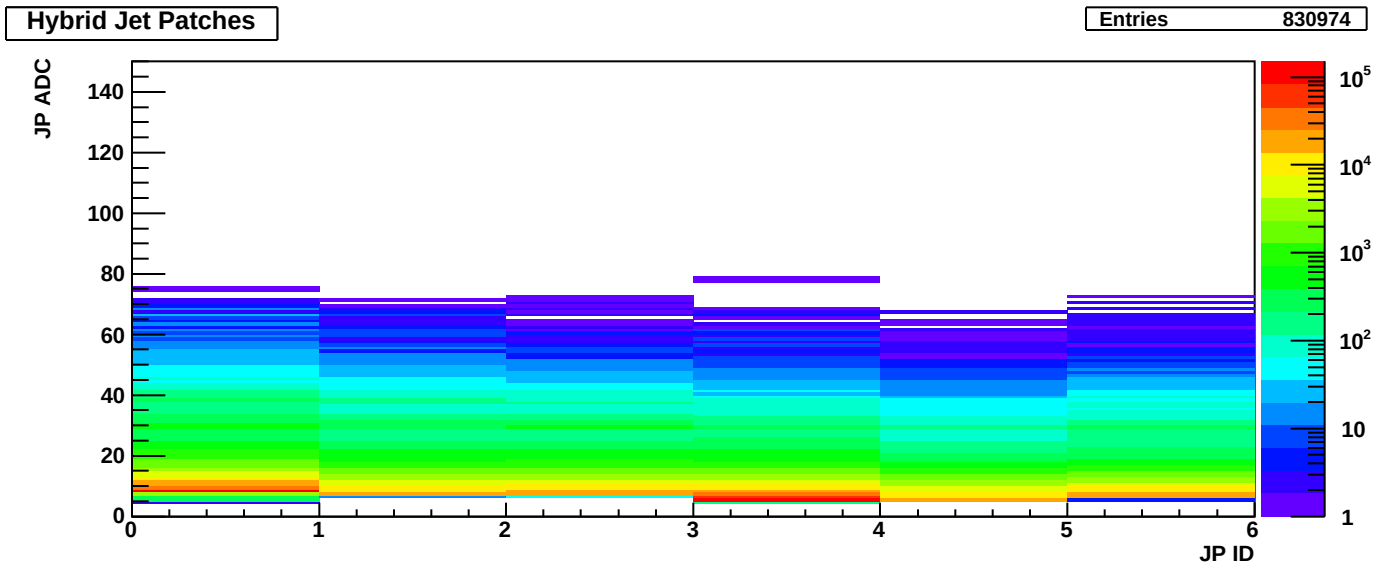

Barrel EMC L0 Input - Patch Sum
Entries 1.246461e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

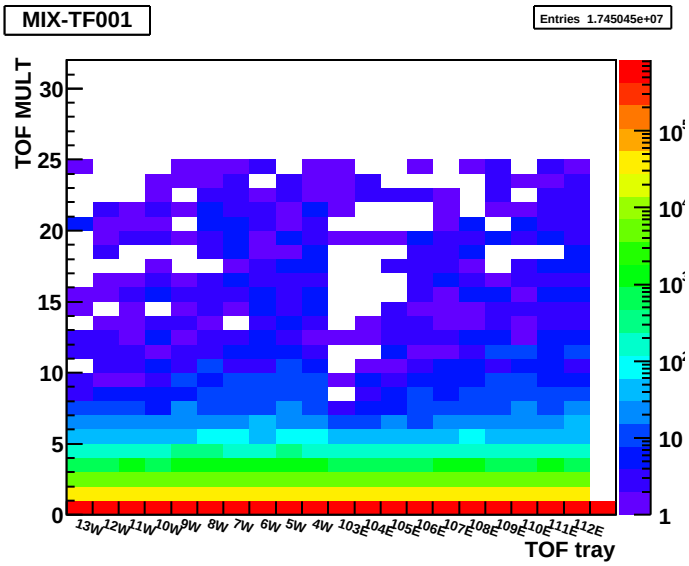

Endcap EMC L0 Input - High Tower
Entries 3.739383e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

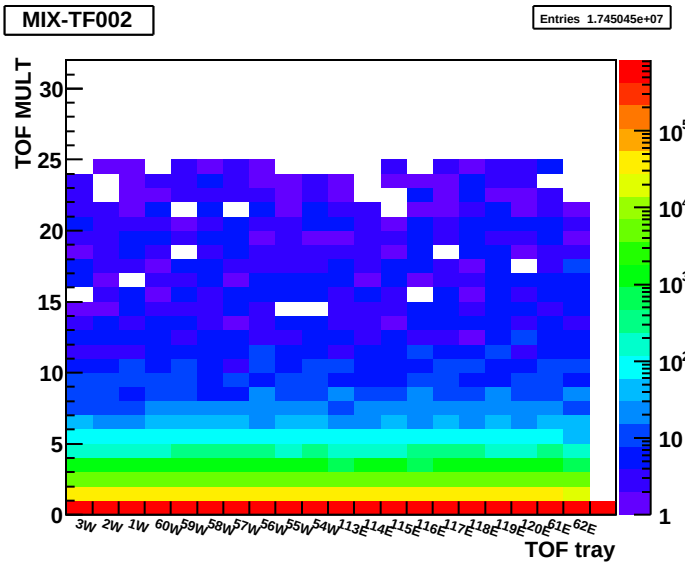

Endcap EMC L0 Input - Patch Sum
Entries 3.739383e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

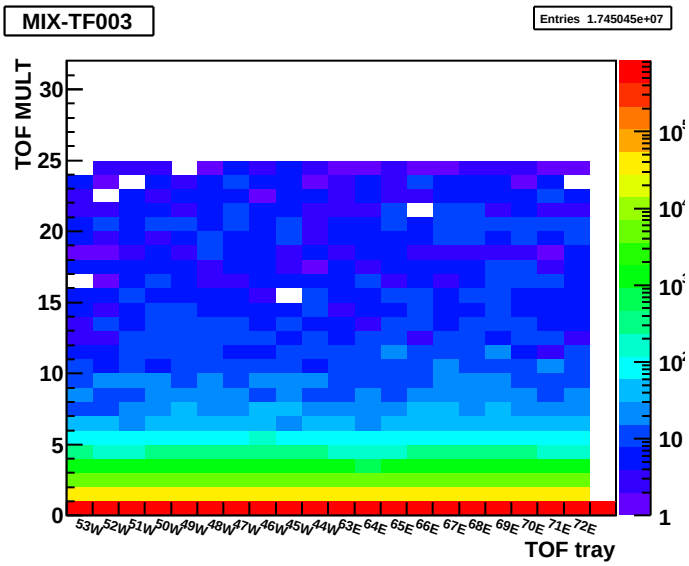

Barrel Jet Patches
Entries 7478766
JP ADC
JP ID

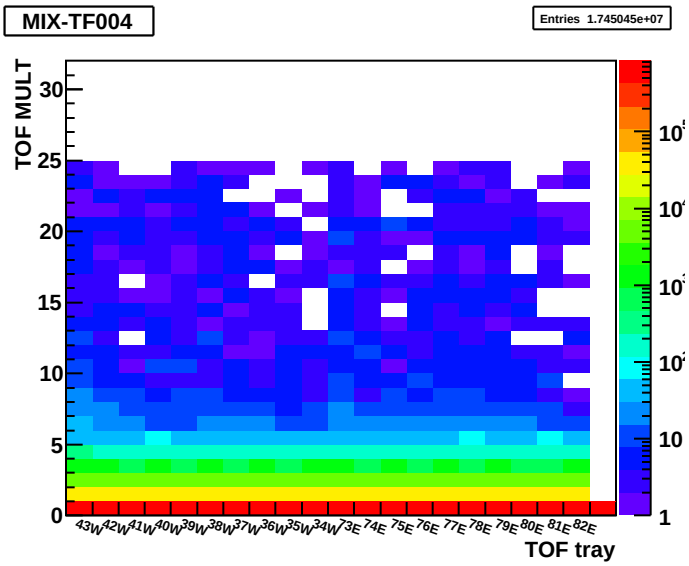

Endcap Jet Patches
Entries 2492922
JP ADC
JP ID

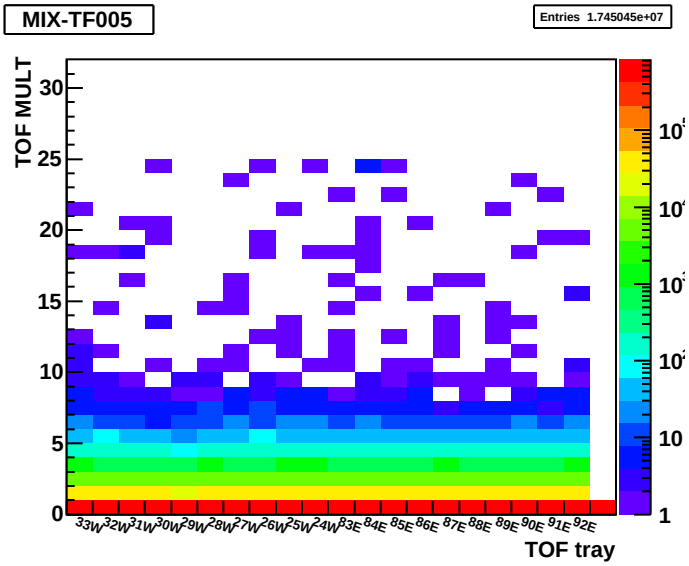

Hybrid Jet Patches
Entries 830974
JP ADC
JP ID

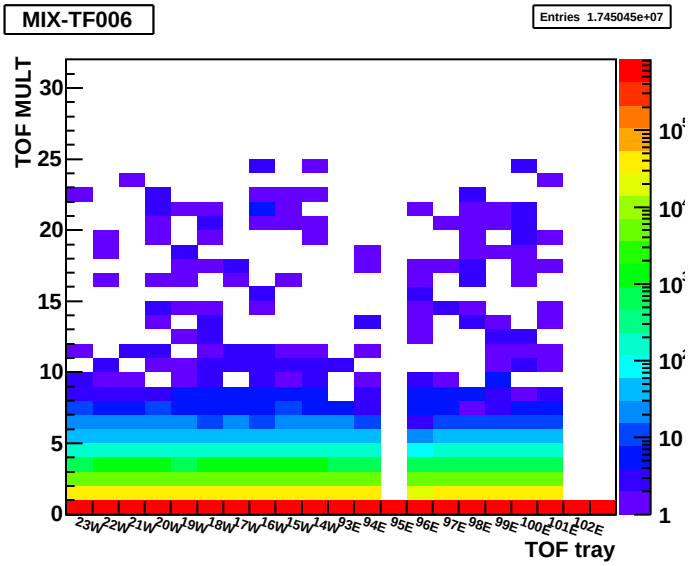

MIX-TF001
Entries 1.745045e+07
TOF MULT
TOF tray

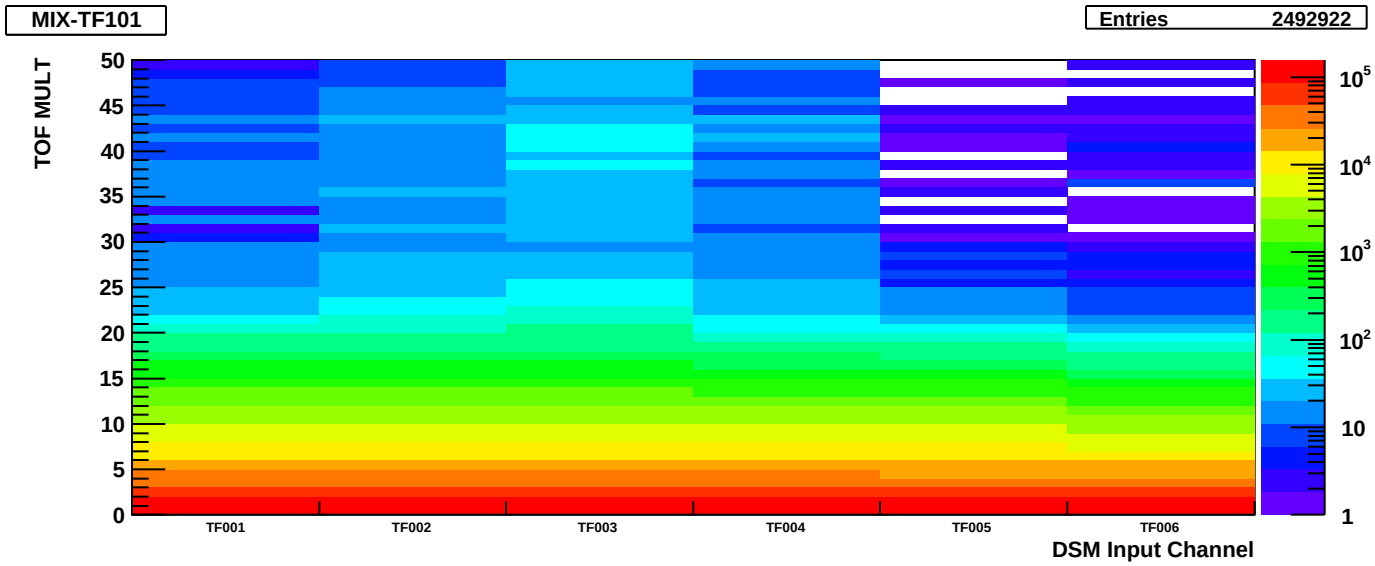

MIX-TF002
Entries 1.745045e+07
TOF MULT
TOF tray

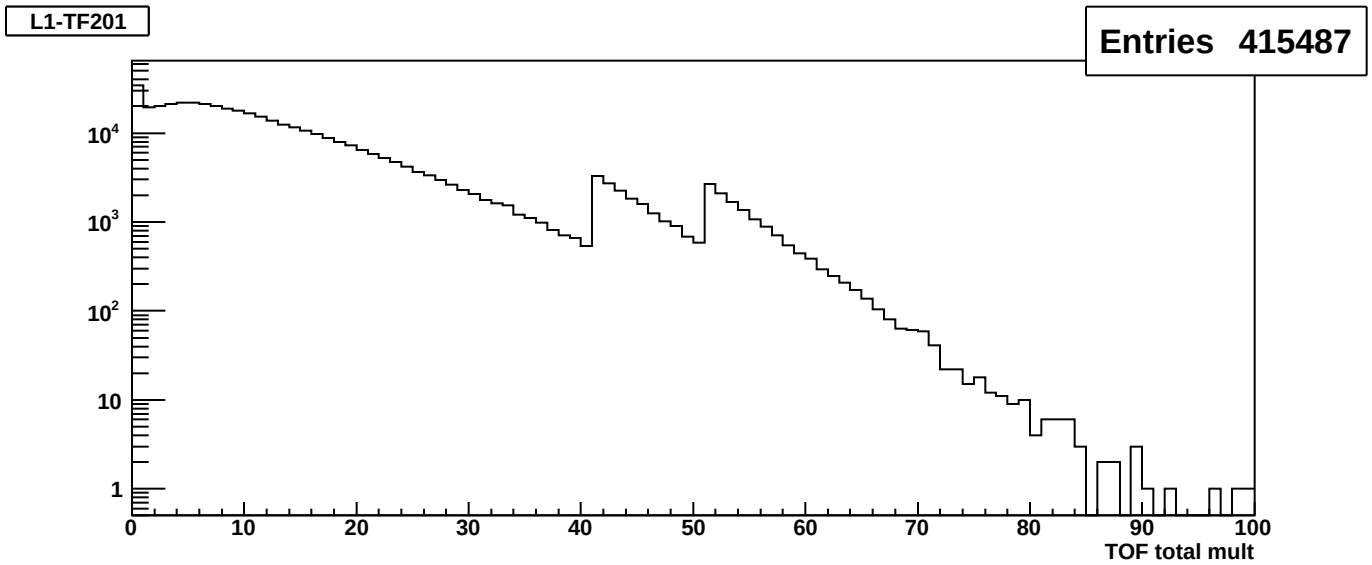

MIX-TF003
Entries 1.745045e+07
TOF MULT
TOF tray

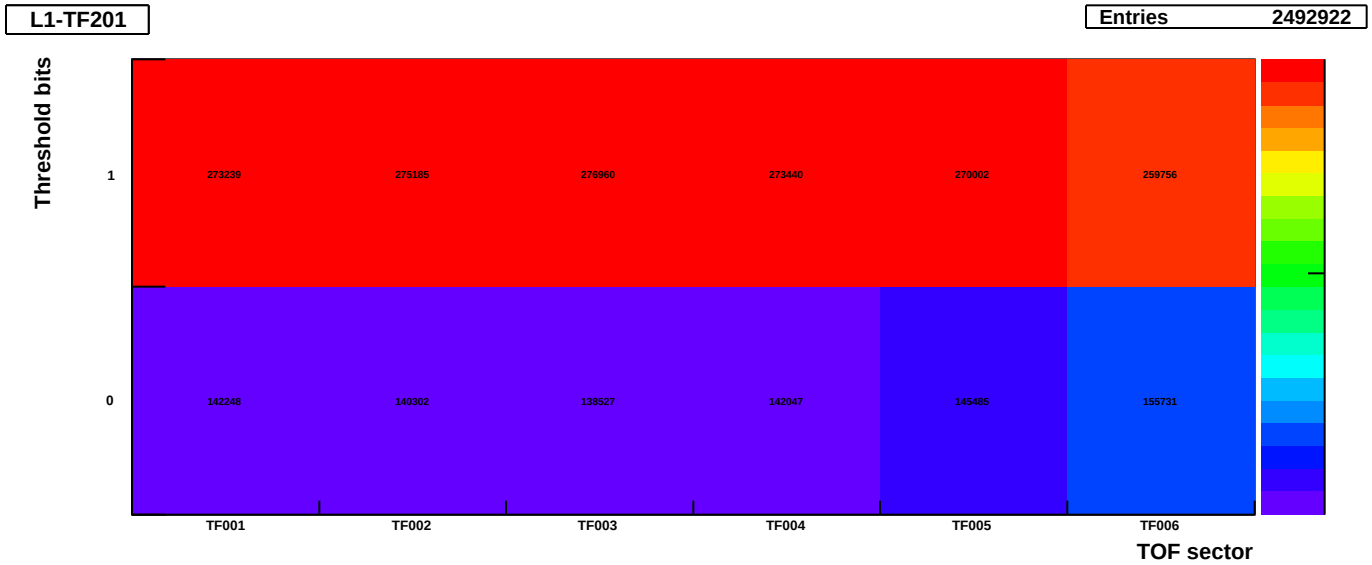

MIX-TF004
Entries 1.745045e+07
TOF MULT
TOF tray

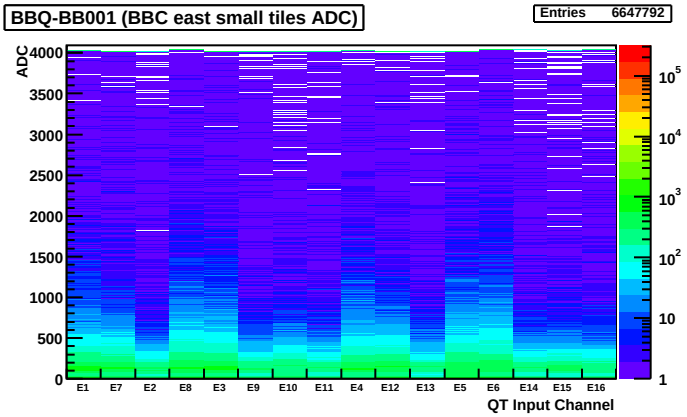

MIX-TF005
Entries 1.745045e+07
TOF MULT
TOF tray

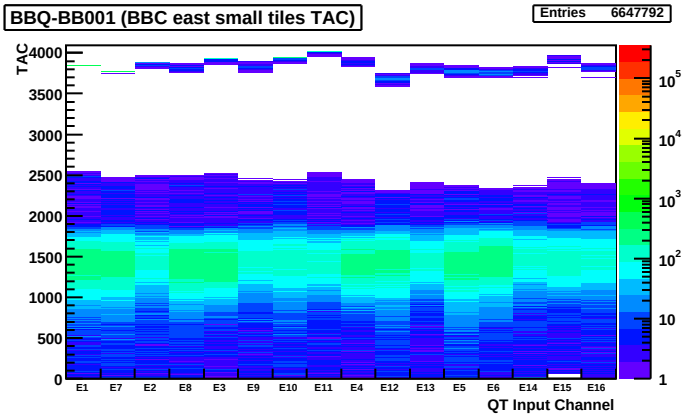

MIX-TF006
Entries 1.745045e+07
TOF MULT
TOF tray

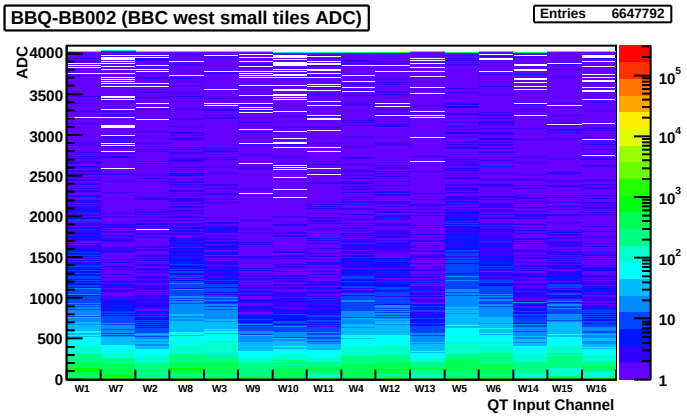

MIX-TF101
Entries 2492922
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

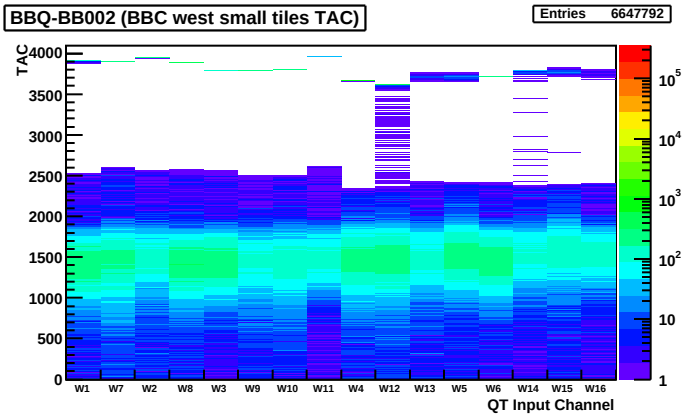

L1-TF201
Entries 415487
TOF total mult

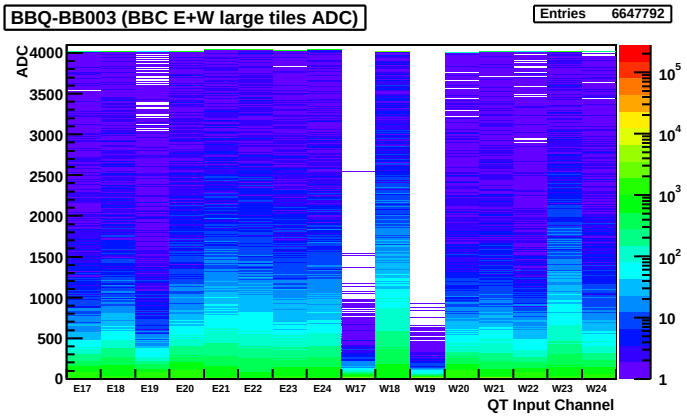

L1-TF201
Entries 2492922
Threshold bits
273239
275185
276960
273440
270002
259756
142248
140302
138527
142047
145485
155731
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

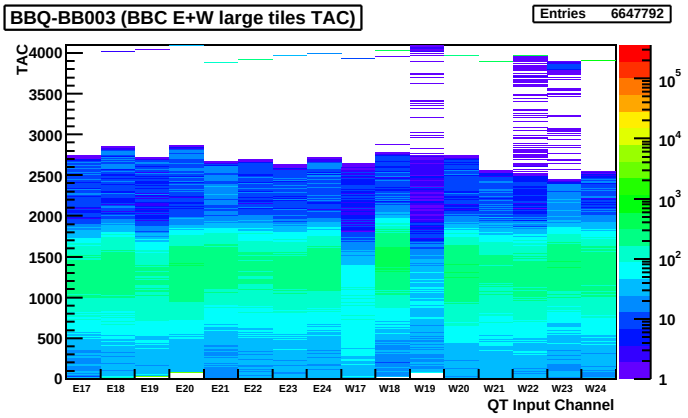

BBQ-BB001 (BBC east small tiles ADC)
Entries 6647792
ADC
QT Input Channel

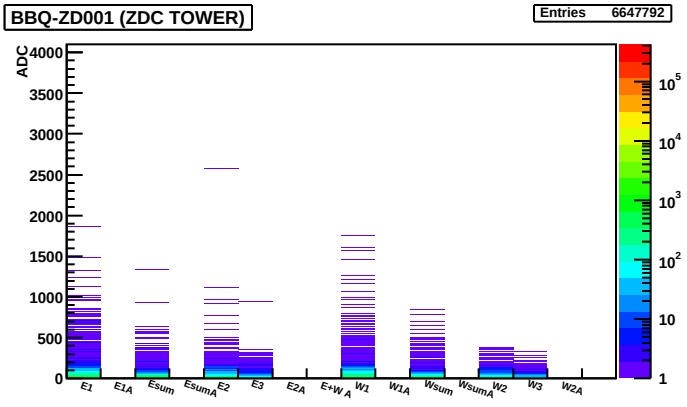

BBQ-BB001 (BBC east small tiles TAC)
Entries 6647792
TAC
QT Input Channel

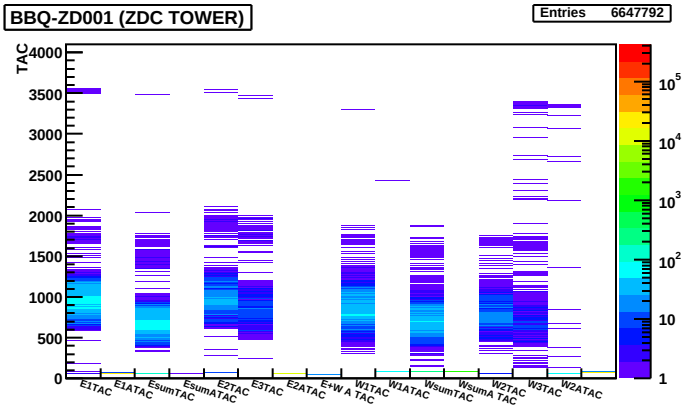

BBQ-BB002 (BBC west small tiles ADC)
Entries 6647792
ADC
QT Input Channel

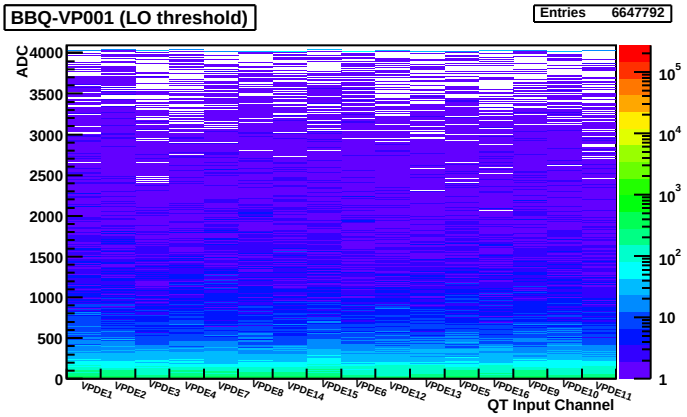

BBQ-BB002 (BBC west small tiles TAC)
Entries 6647792
TAC
QT Input Channel

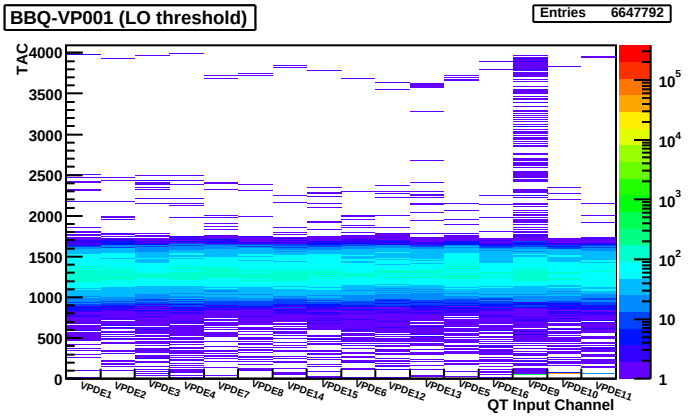

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6647792
ADC
QT Input Channel

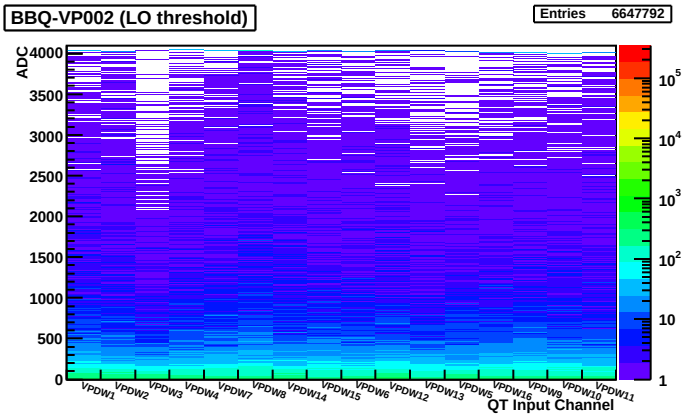

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6647792
TAC
QT Input Channel

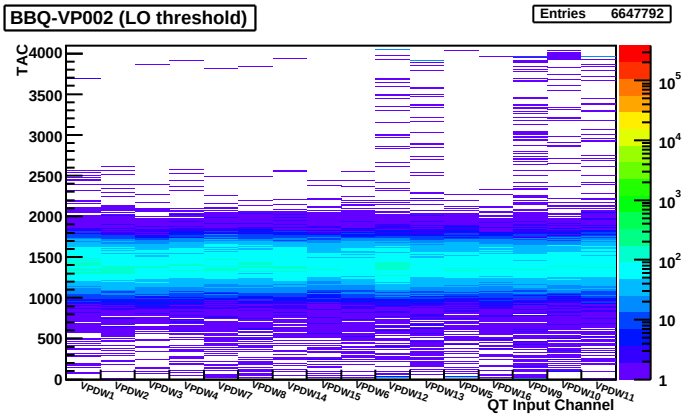

BBQ-ZD001 (ZDC TOWER)
Entries 6647792
ADC

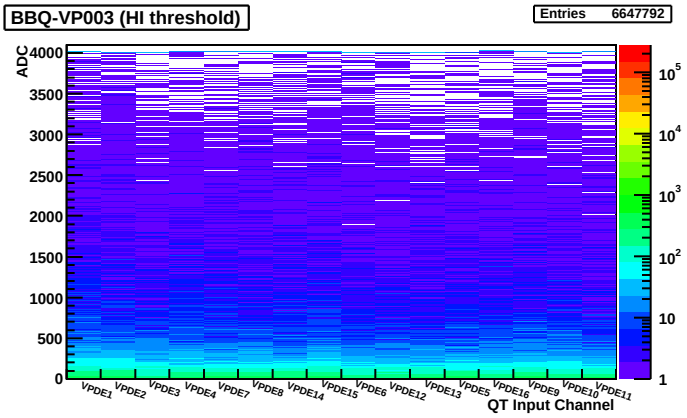

BBQ-ZD001 (ZDC TOWER)
Entries 6647792
TAC

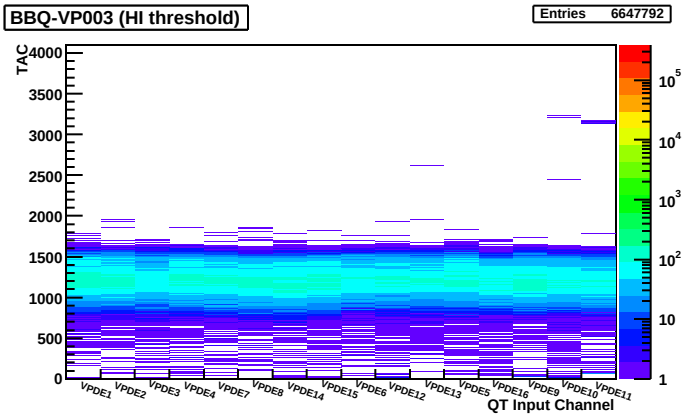

BBQ-VP001 (LO threshold)
Entries 6647792
ADC
QT Input Channel

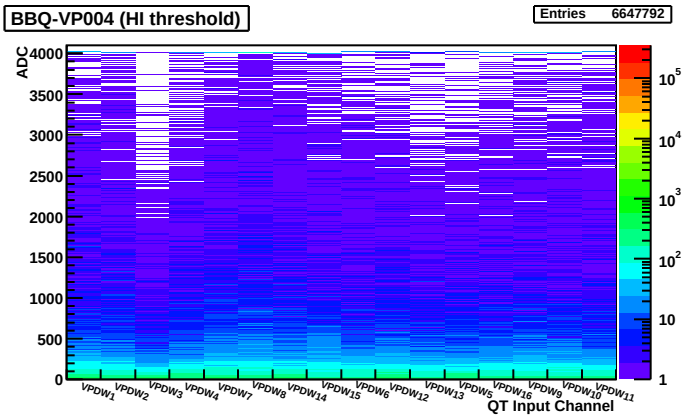

BBQ-VP001 (LO threshold)
Entries 6647792
TAC
QT Input Channel

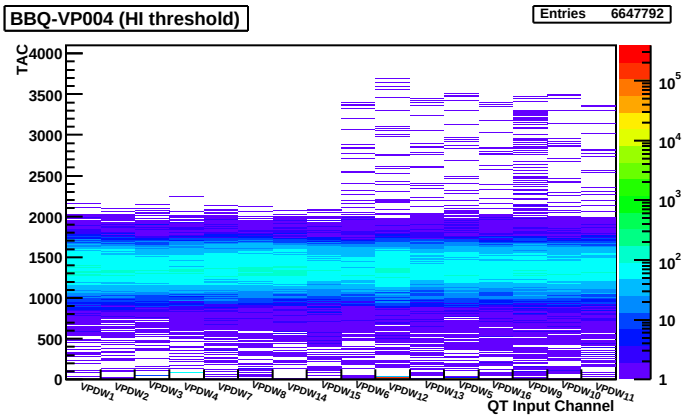

BBQ-VP002 (LO threshold)
Entries 6647792
ADC
QT Input Channel

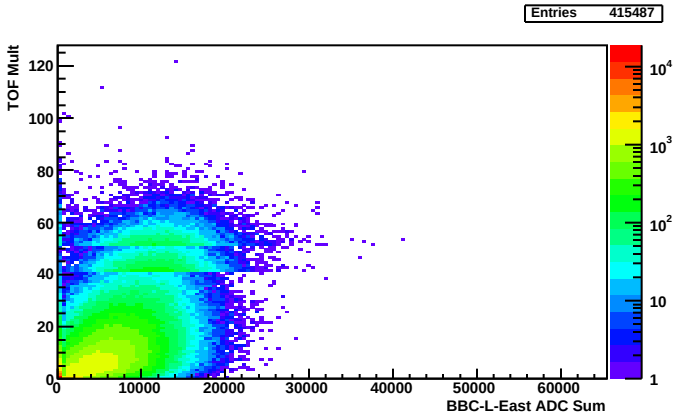

BBQ-VP002 (LO threshold)
Entries 6647792
TAC
QT Input Channel

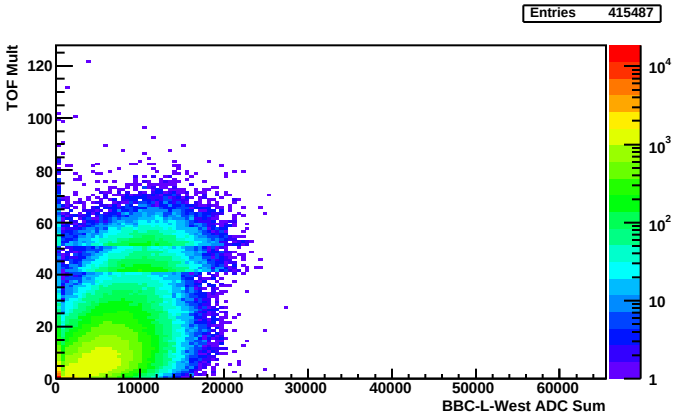

BBQ-VP003 (HI threshold)
Entries 6647792
ADC
QT Input Channel

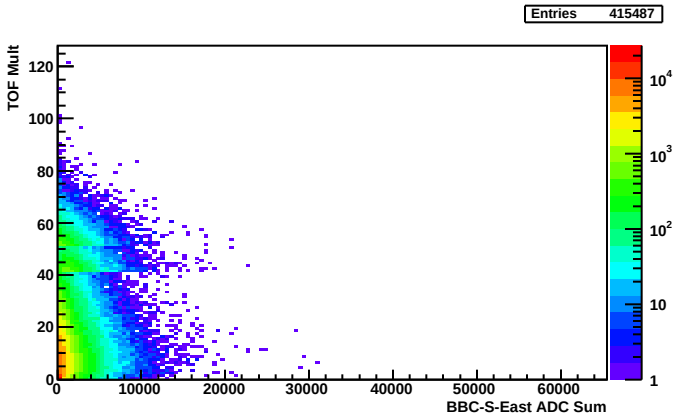

BBQ-VP003 (HI threshold)
Entries 6647792
TAC
QT Input Channel

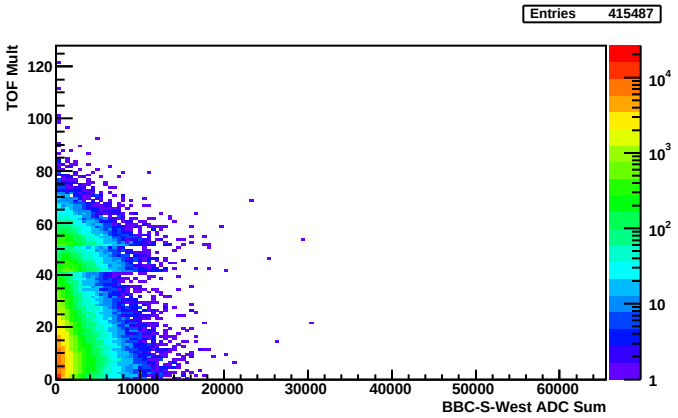

BBQ-VP004 (HI threshold)
Entries 6647792
ADC
QT Input Channel

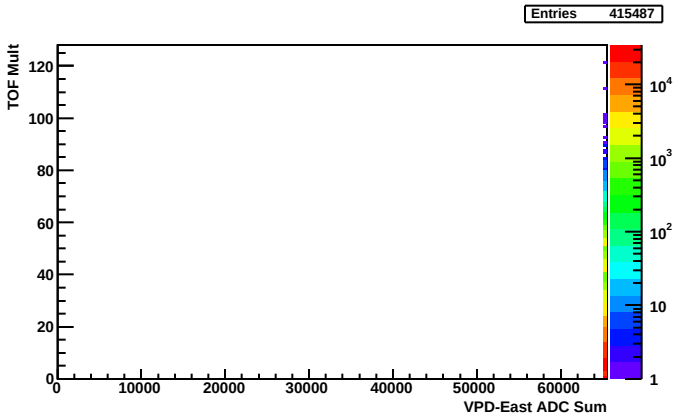

BBQ-VP004 (HI threshold)
Entries 6647792
TAC
QT Input Channel

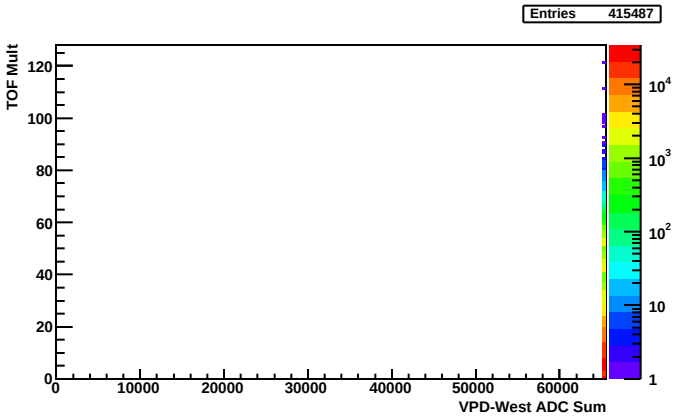

Entries 415487
TOF Mult
BBC-L-East ADC Sum

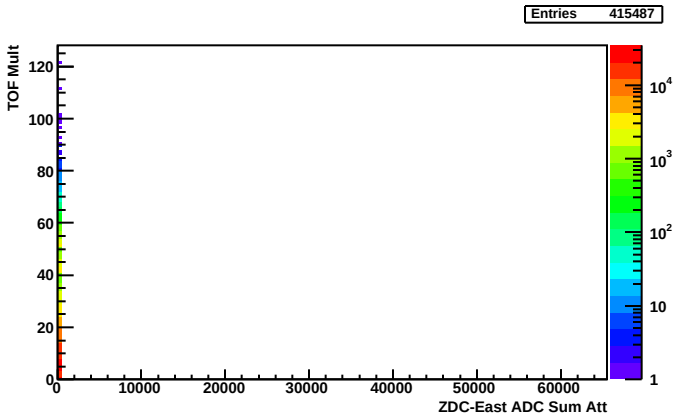

Entries 415487
TOF Mult
BBC-L-West ADC Sum

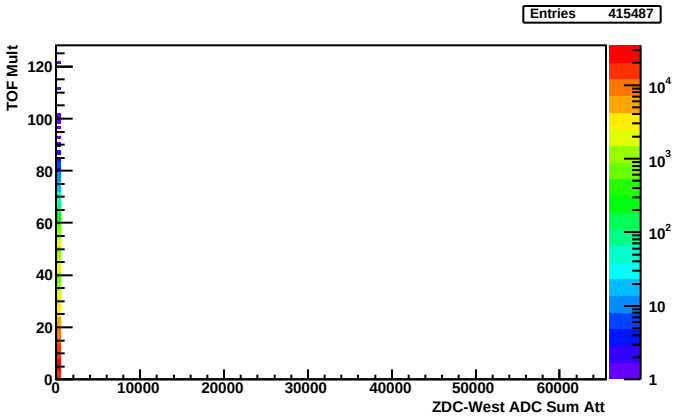

Entries 415487
TOF Mult
BBC-S-East ADC Sum

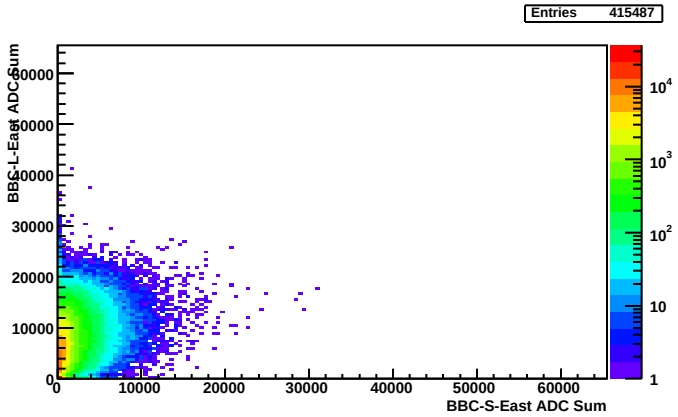

Entries 415487
TOF Mult
BBC-S-West ADC Sum

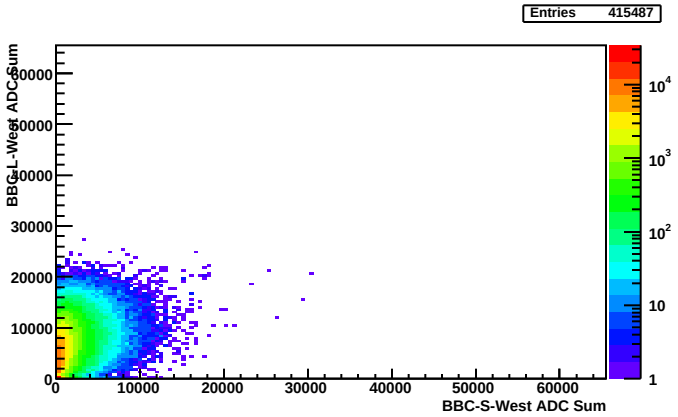

Entries 415487
TOF Mult
VPD-East ADC Sum

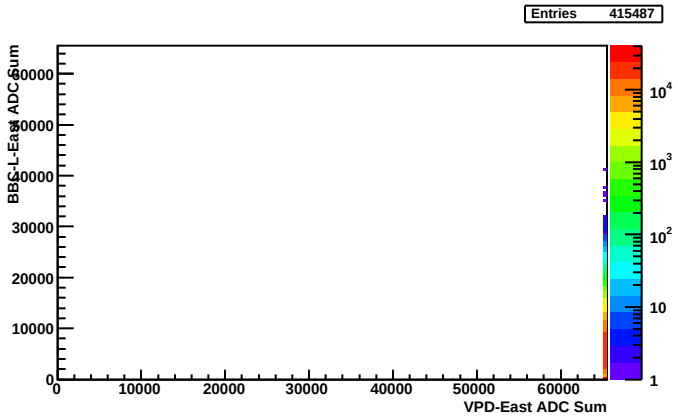

Entries 415487
TOF Mult
VPD-West ADC Sum

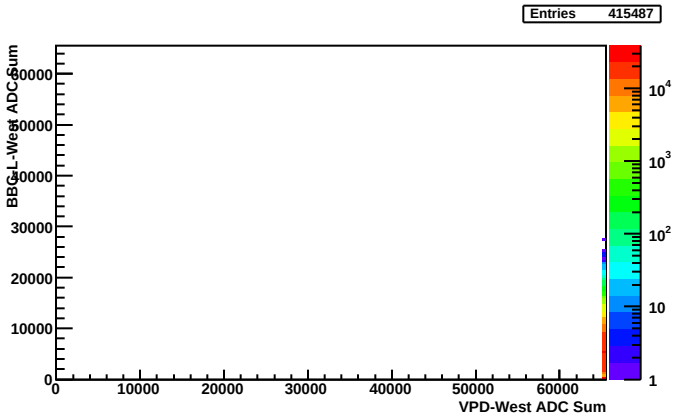

Entries 415487
TOF Mult
ZDC-East ADC Sum Att

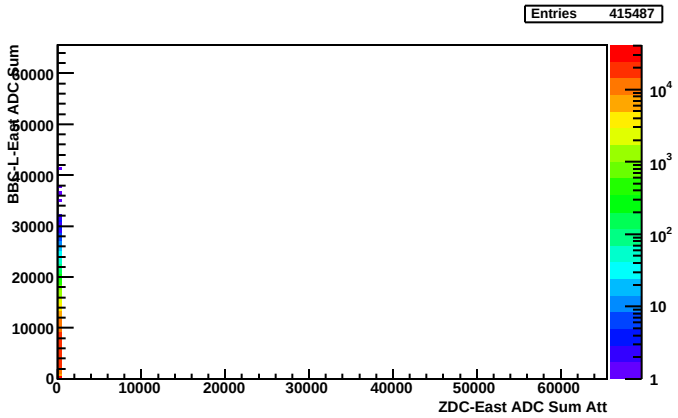

Entries 415487
TOF Mult
ZDC-West ADC Sum Att

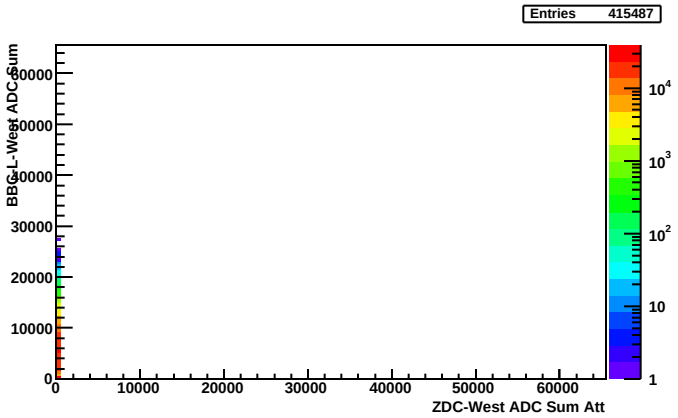

Entries 415487
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 415487
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 415487
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 415487
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 415487
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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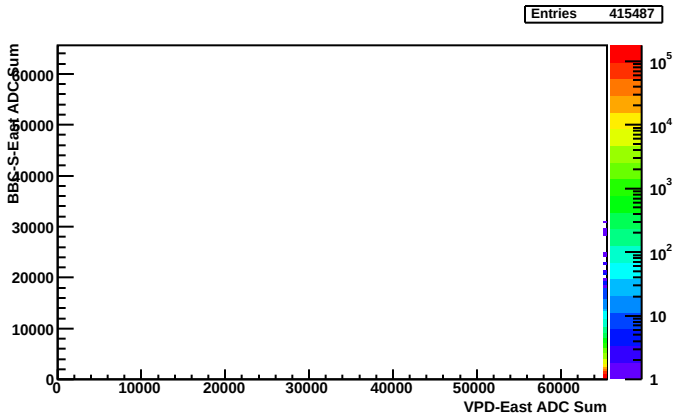
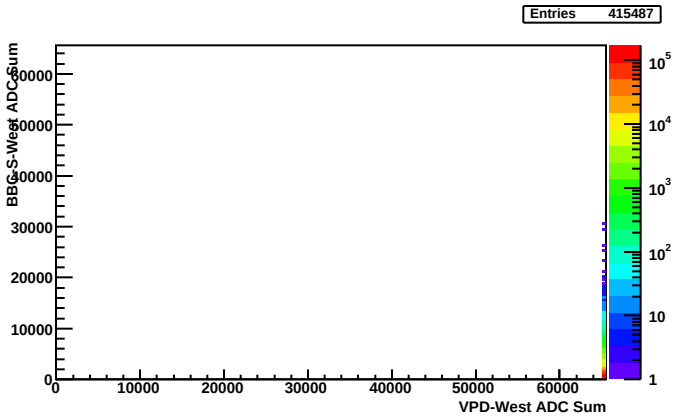
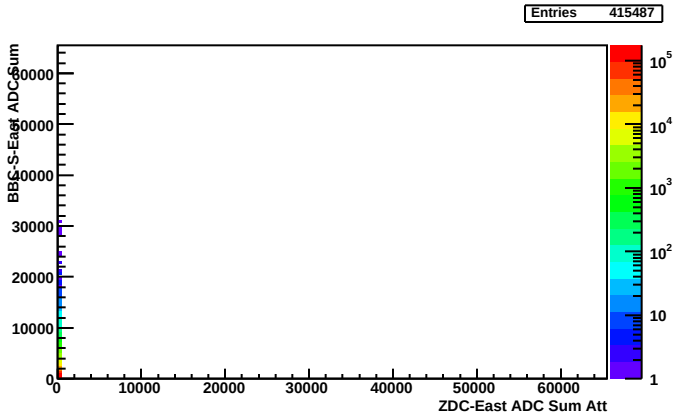
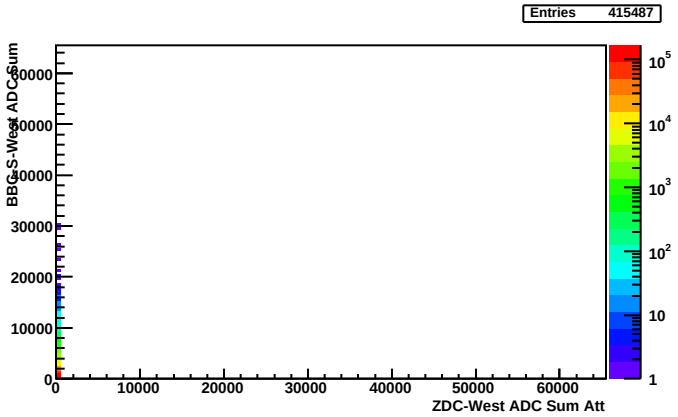
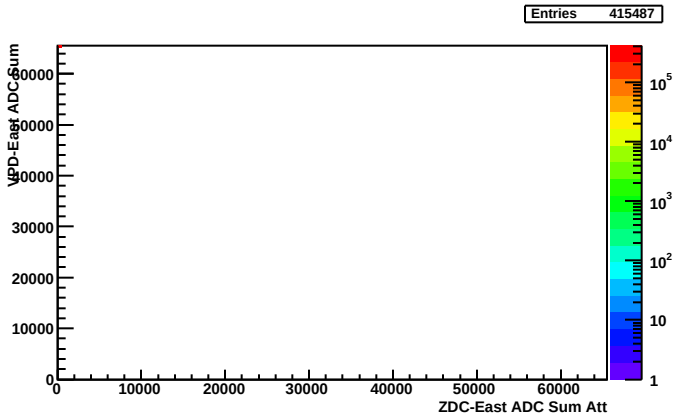
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

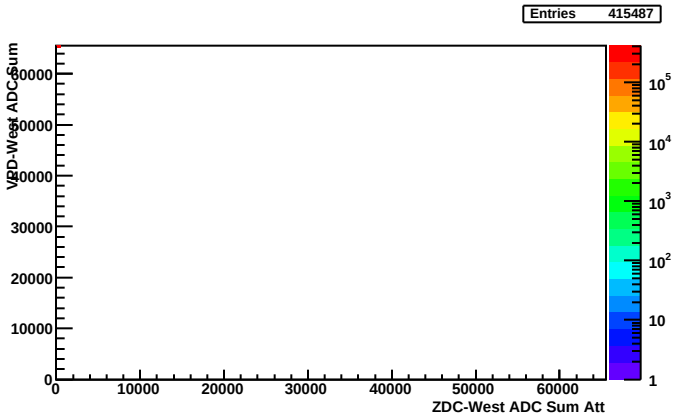

Entries 415487
BBC-S-East ADC Sum
VPD-East ADC Sum

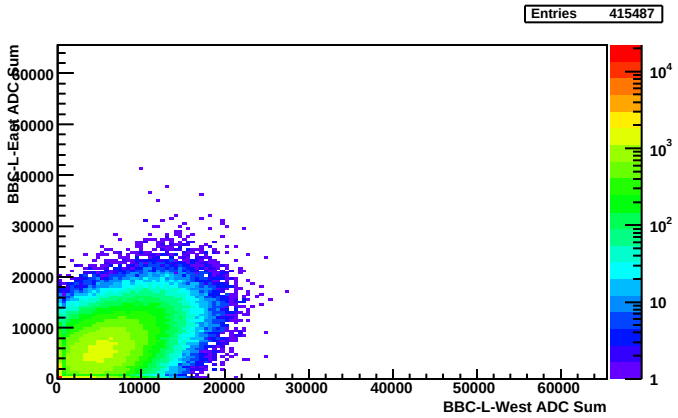

Entries 415487
BBC-S-West ADC Sum
VPD-West ADC Sum

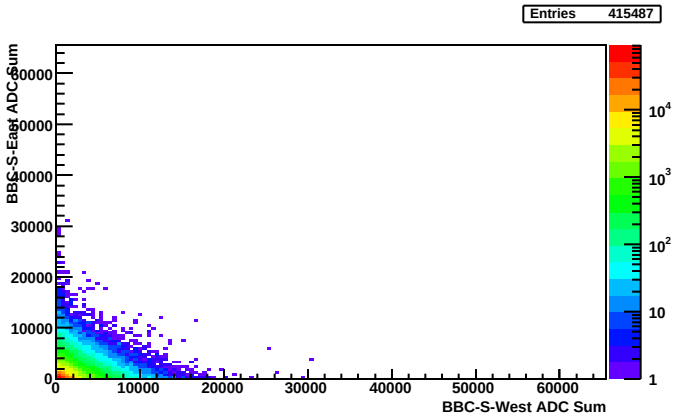

Entries 415487
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

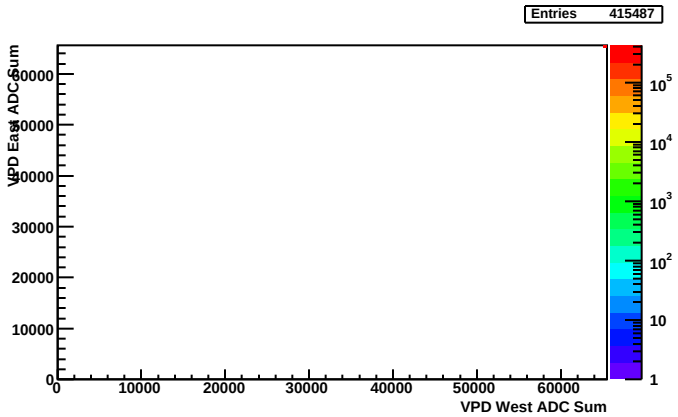

Entries 415487
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

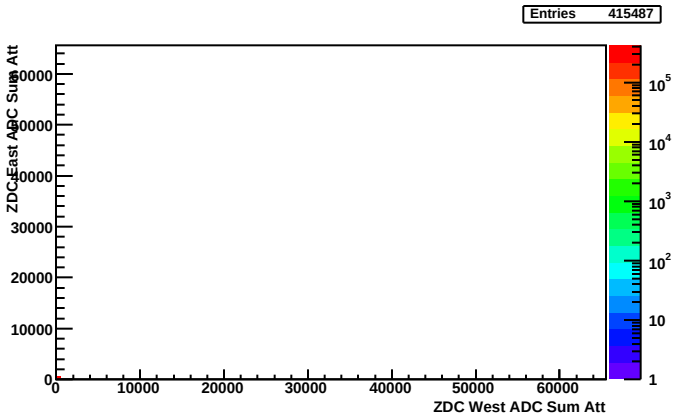

Entries 415487
VPD-East ADC Sum
ZDC-East ADC Sum Att

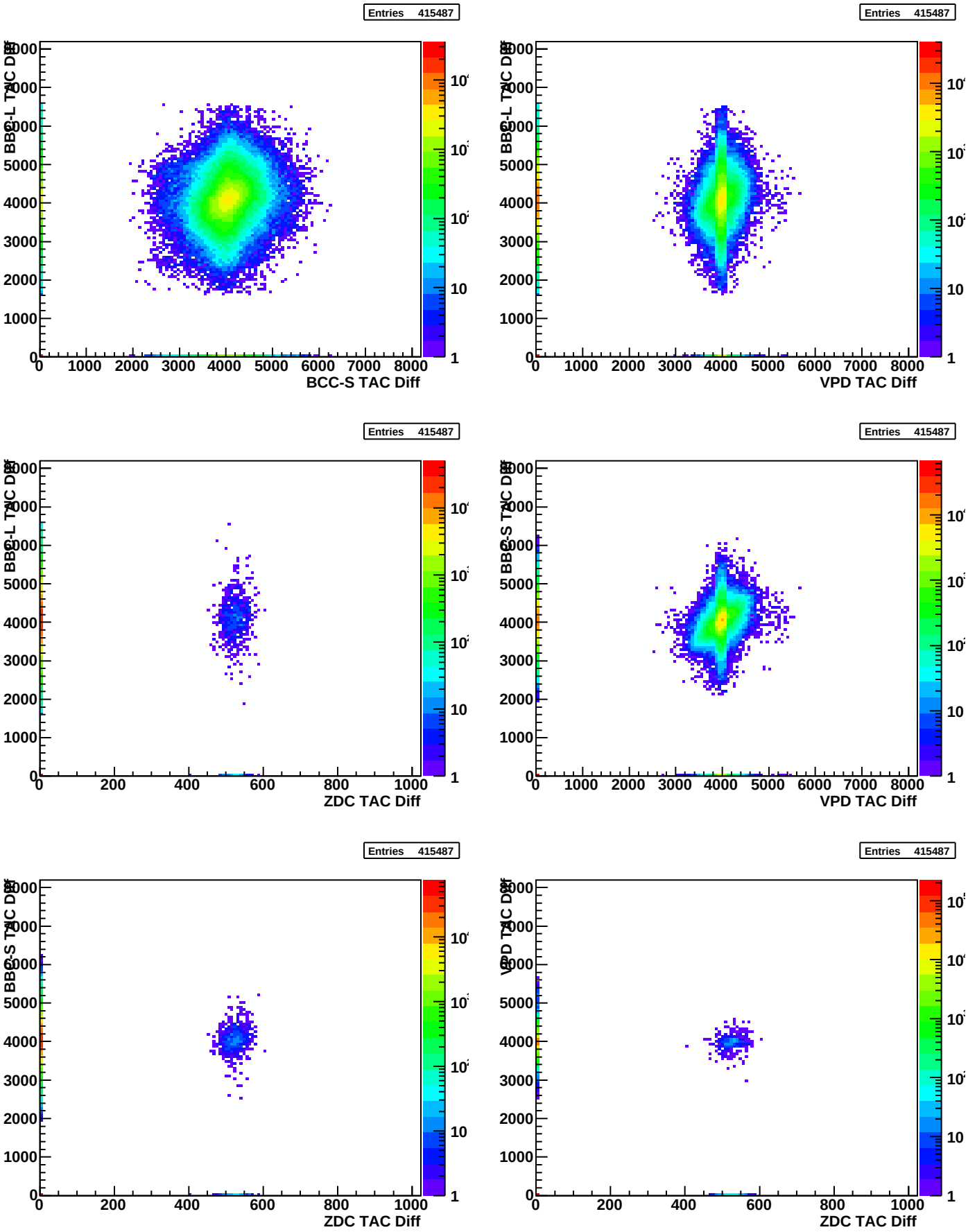

Entries 415487
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 415487
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 415487
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 415487
VPD East ADC Sum
VPD West ADC Sum


Entries 415487
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 415487
BBC-L TAC Diff
BCC-S TAC Diff
Entries 415487
BBC-L TAC Diff
VPD TAC Diff
Entries 415487
BBC-L TAC Diff
ZDC TAC Diff
Entries 415487
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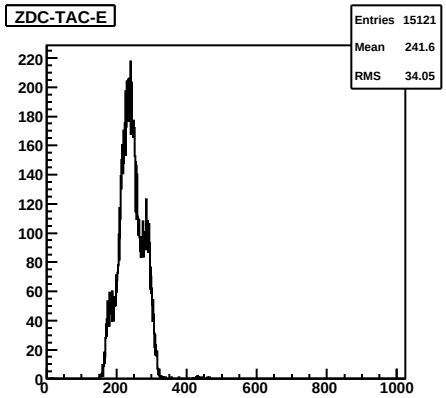
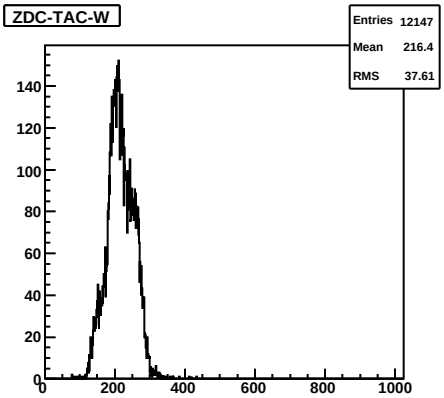
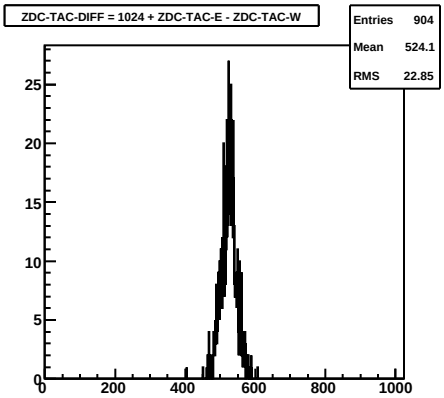
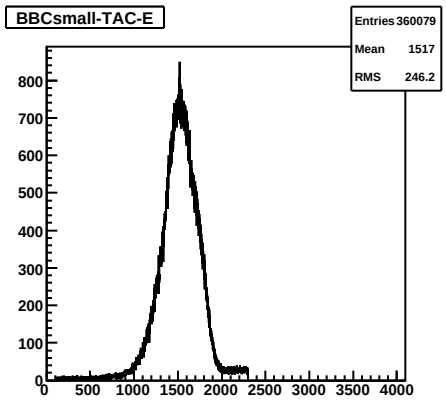
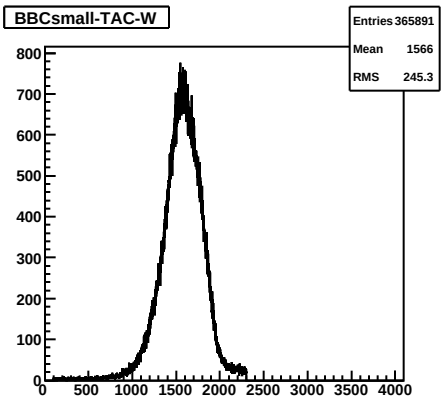
VPD TAC Diff
Entries 415487
BBC-S TAC Diff
ZDC TAC Diff
Entries 415487
VPD TAC Diff
ZDC TAC Diff

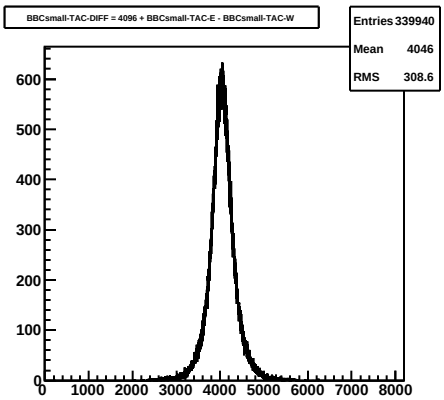

ZDC-TAC-E
Entries 15121
Mean 241.6
RMS 34.05

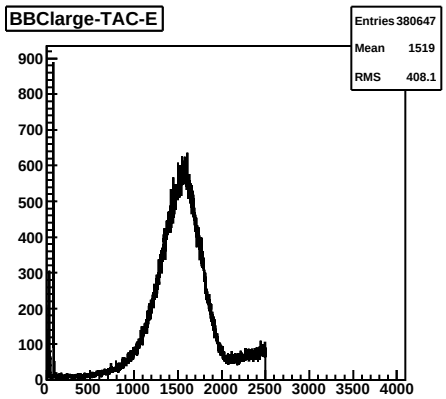

ZDC-TAC-W
Entries 12147
Mean 216.4
RMS 37.61

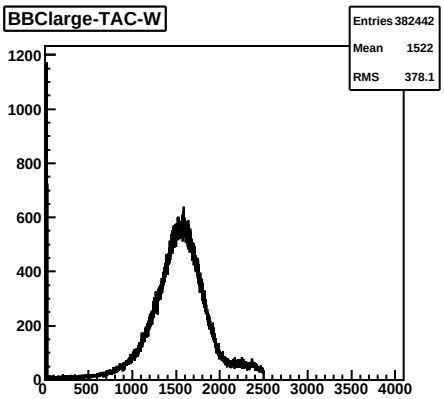

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 904
Mean 524.1
RMS 22.85

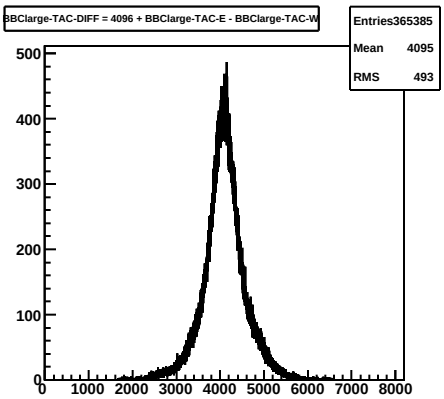

BBCsmall-TAC-E
Entries 360079
Mean 1517
RMS 246.2

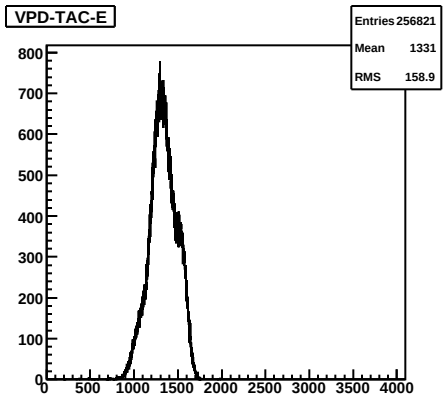

BBCsmall-TAC-W
Entries 365891
Mean 1566
RMS 245.3

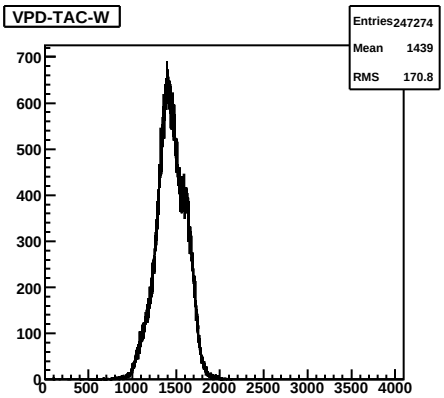

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 339940
Mean 4046
RMS 308.6

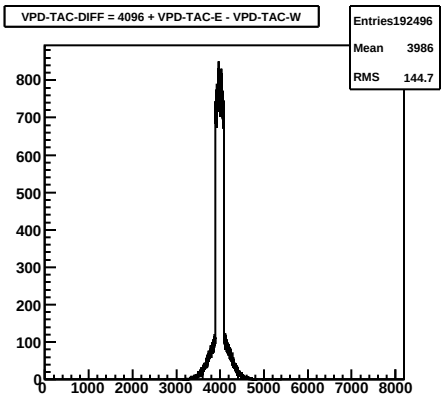

BBClarge-TAC-E
Entries 380647
Mean 1519
RMS 408.1

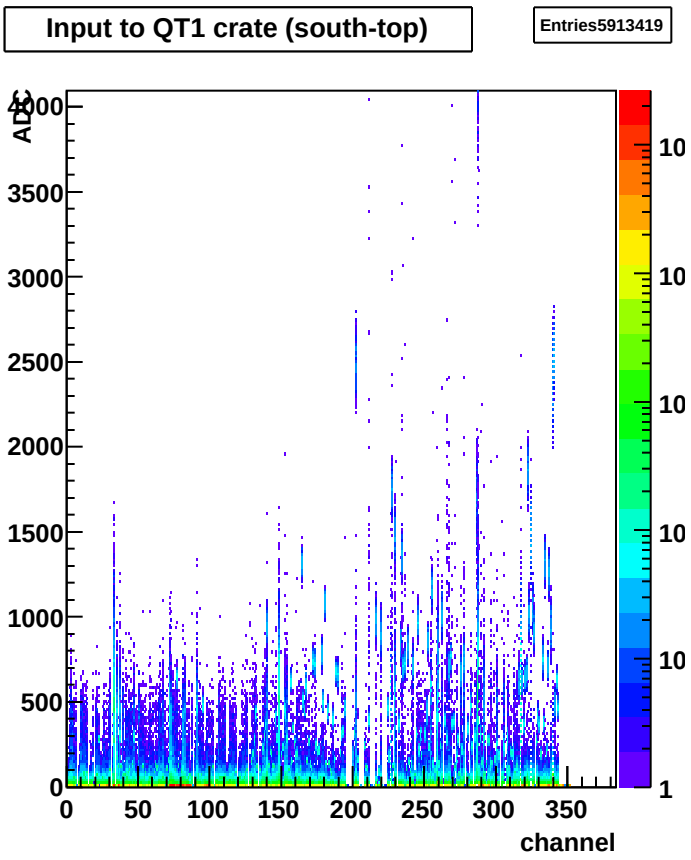

BBClarge-TAC-W
Entries 382442
Mean 1522
RMS 378.1

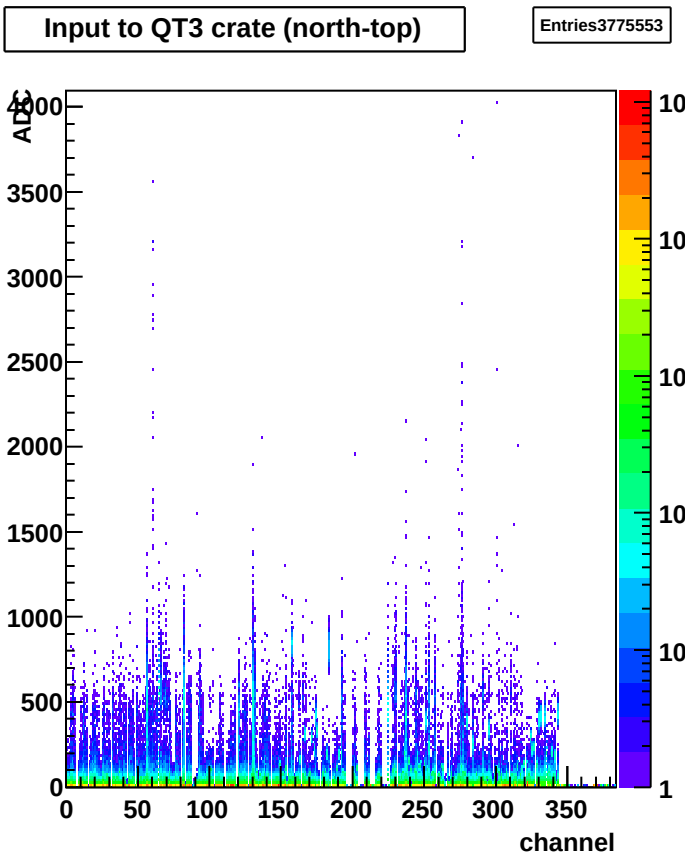

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 365385
Mean 4095
RMS 493

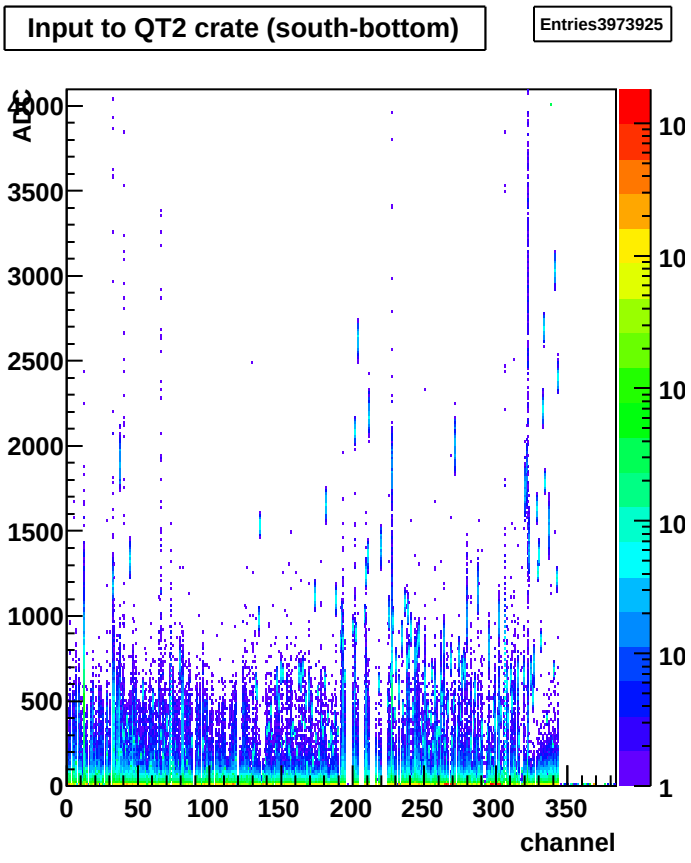

VPD-TAC-E
Entries 256821
Mean 1331
RMS 158.9

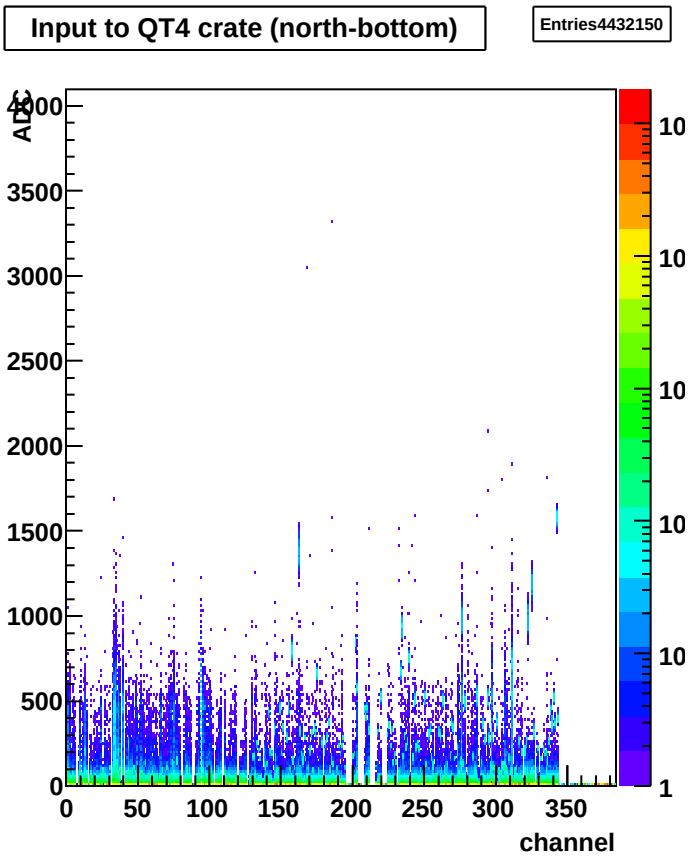

VPD-TAC-W
Entries 247274
Mean 1439
RMS 170.8

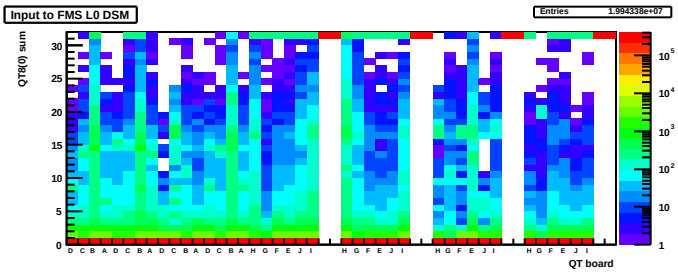

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 192496
Mean 3986
RMS 144.7

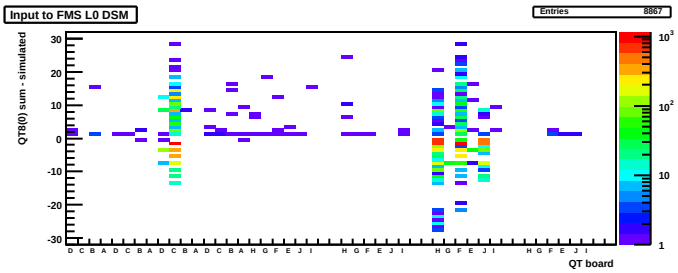

Input to QT1 crate (south-top)
Entries5913419
ADC
channel

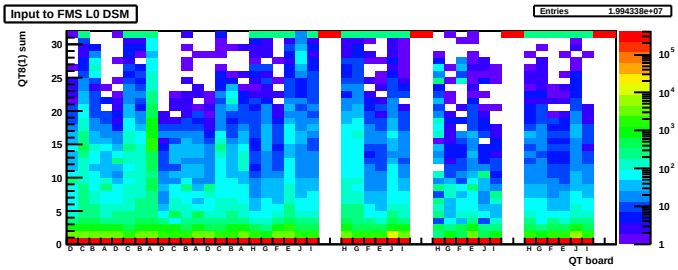

Input to QT3 crate (north-top)
Entries3775553
ADC
channel

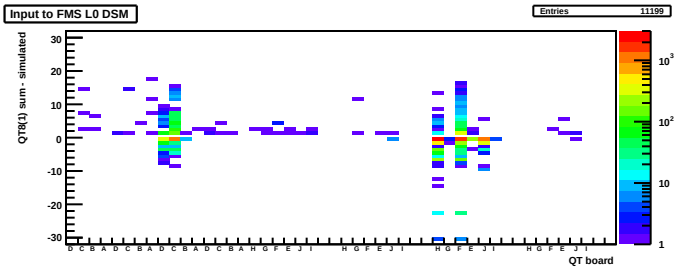

Input to QT2 crate (south-bottom)
Entries3973925
ADC
channel

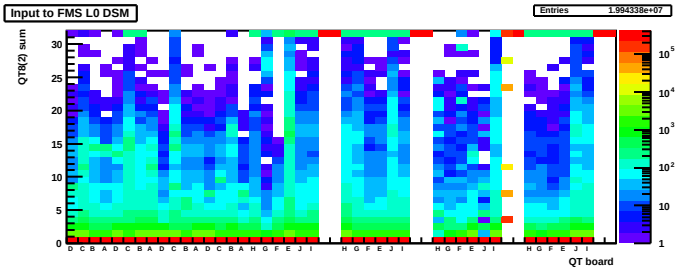

Input to QT4 crate (north-bottom)
Entries4432150
ADC
channel

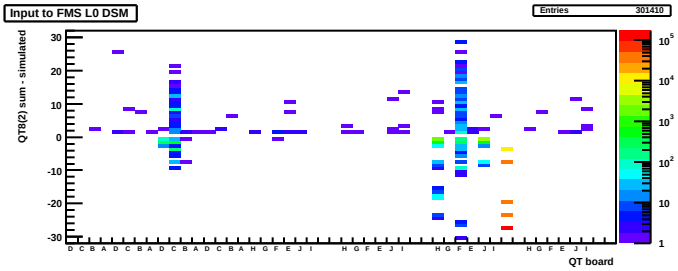

Input to FMS L0 DSM
Entries 1.994338e+07
QT8(0) sum
QT board

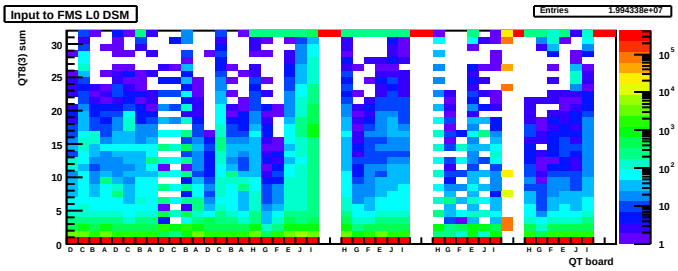

Input to FMS L0 DSM
Entries 8867
QT8(0) sum - simulated
QT board

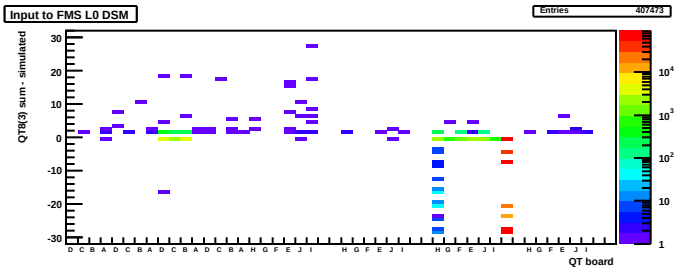

Input to FMS L0 DSM
Entries 1.994338e+07
QT8(1) sum
QT board

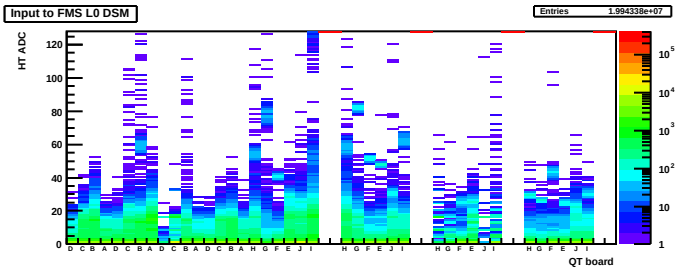

Input to FMS L0 DSM
Entries 11199
QT8(1) sum - simulated
QT board

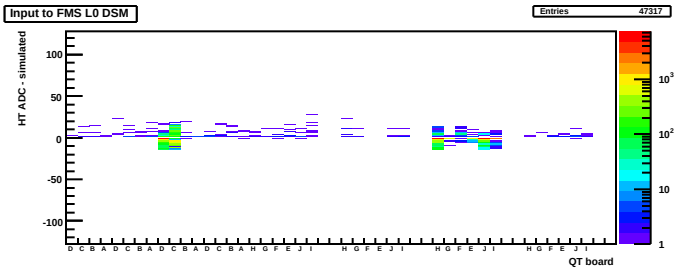

Input to FMS L0 DSM
Entries 1.994338e+07
QT8(2) sum
QT board

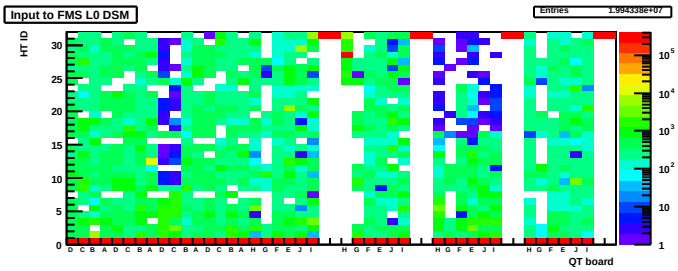

Input to FMS L0 DSM
Entries 301410
QT8(2) sum - simulated
QT board

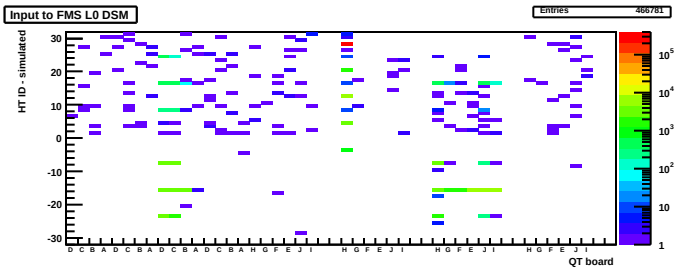

Input to FMS L0 DSM
Entries 1.994338e+07
QT8(3) sum
QT board

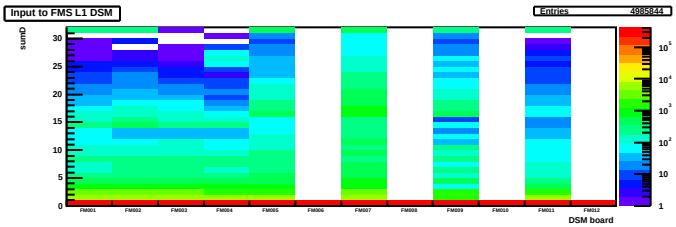

Input to FMS L0 DSM
Entries 407473
QT8(3) sum - simulated
QT board

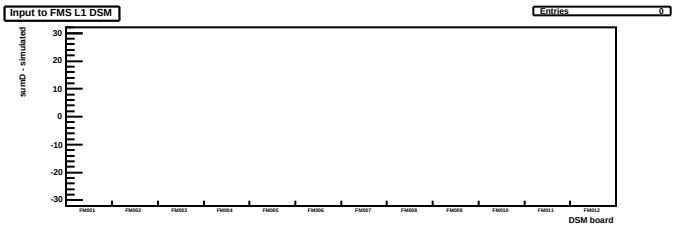

Input to FMS L0 DSM
Entries 1.994338e+07
HT ADC
QT board

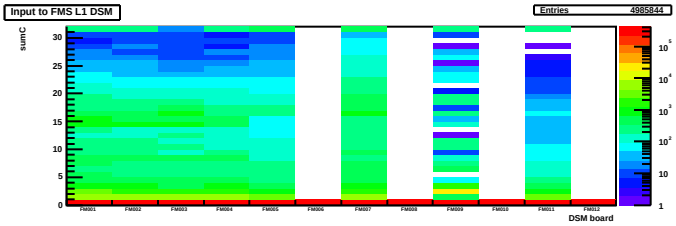

Input to FMS L0 DSM
Entries 47317
HT ADC - simulated
QT board

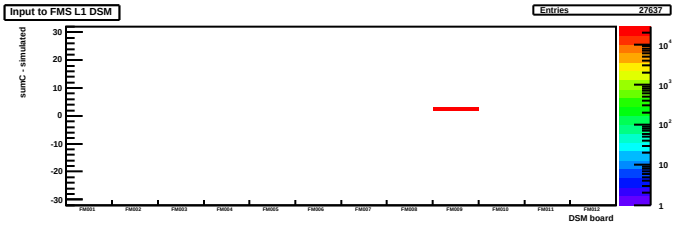

Input to FMS L0 DSM
Entries 1.994338e+07
HT ID
QT board

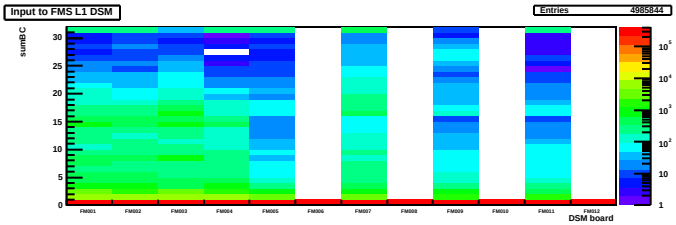

Input to FMS L0 DSM
Entries 466781
HT ID - simulated
QT board

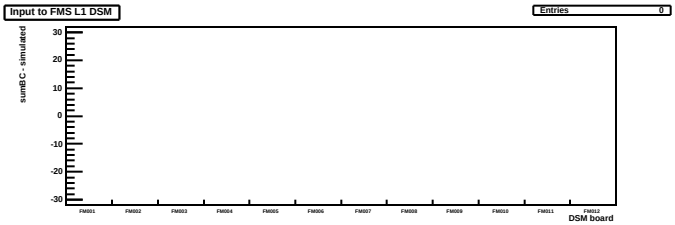

Input to FMS L1 DSM
Entries 4985844
sumD
DSM board

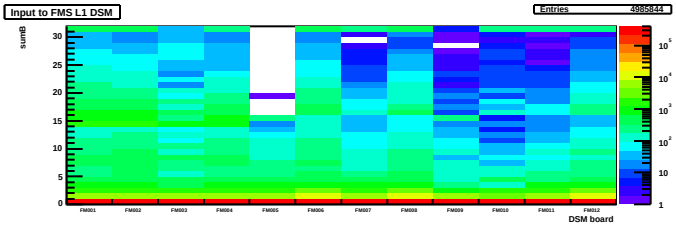

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

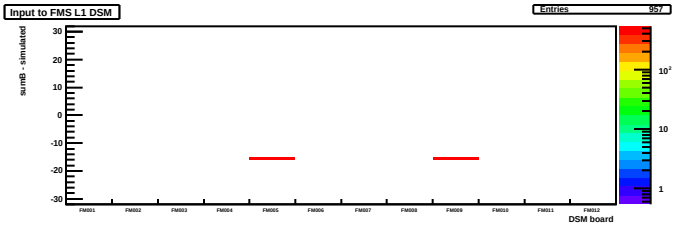

Input to FMS L1 DSM
Entries 4985844
sumC
DSM board

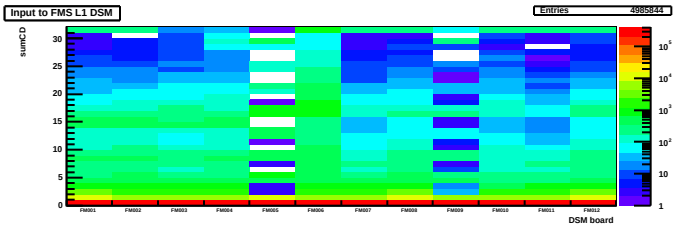

Input to FMS L1 DSM
Entries 27637
sumC - simulated
DSM board

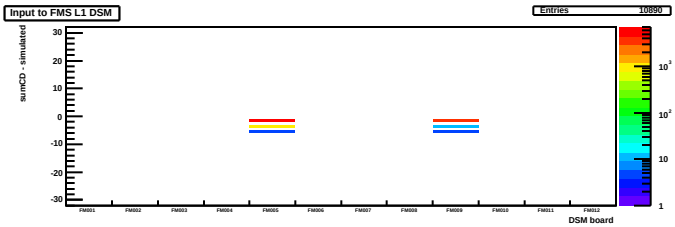

Input to FMS L1 DSM
Entries 4985844
sumBC
DSM board

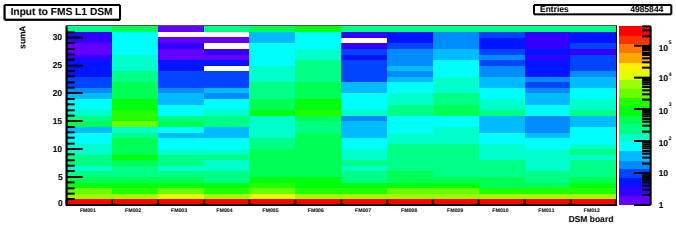

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

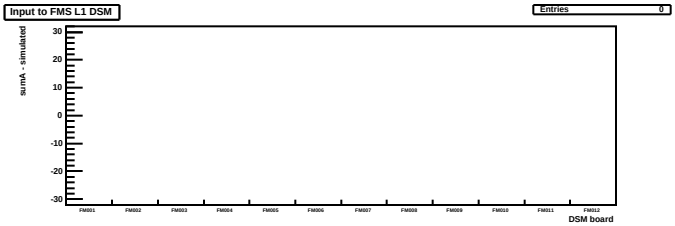

Input to FMS L1 DSM
Entries 4985844
sumB
DSM board

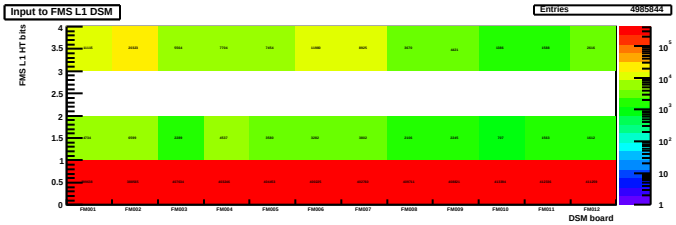

Input to FMS L1 DSM
Entries 957
sumB - simulated
DSM board

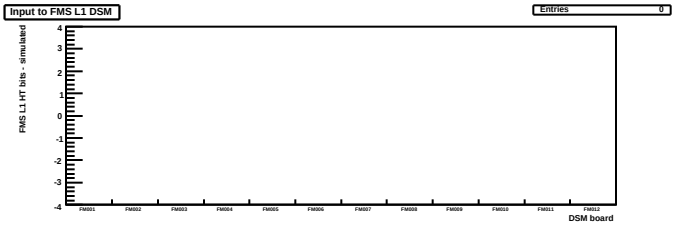

Input to FMS L1 DSM
Entries 4985844
sumCD
DSM board

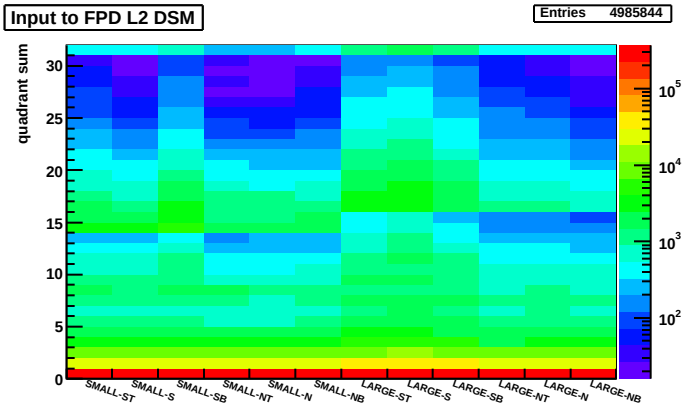

Input to FMS L1 DSM
Entries 10890
sumCD - simulated
DSM board

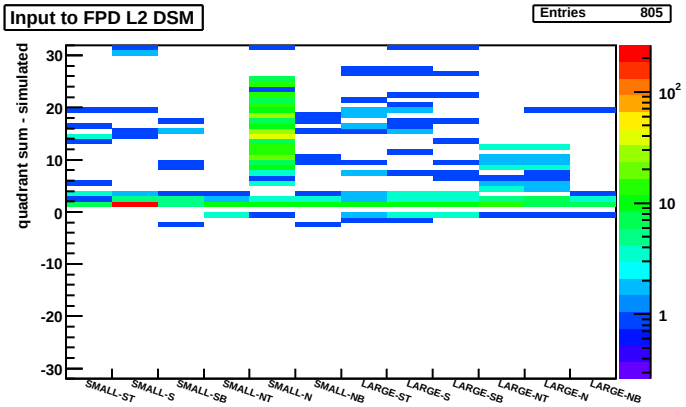

Input to FMS L1 DSM
Entries 4985844
sumA
DSM board

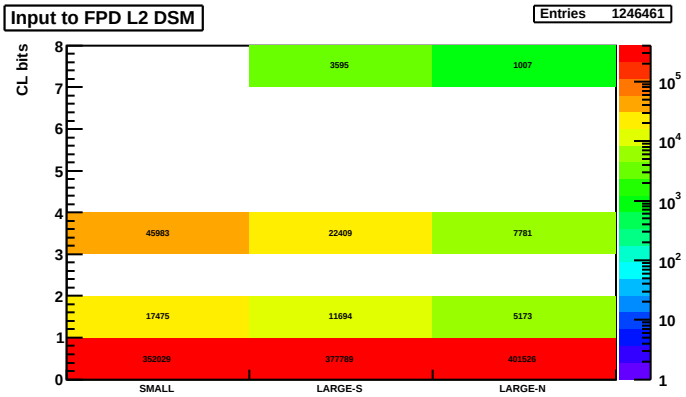

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

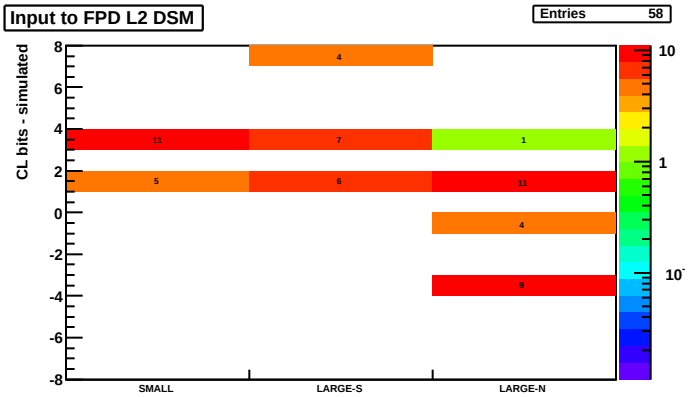

Input to FMS L1 DSM
Entries 4985844
FMS L1 HT bits
DSM board

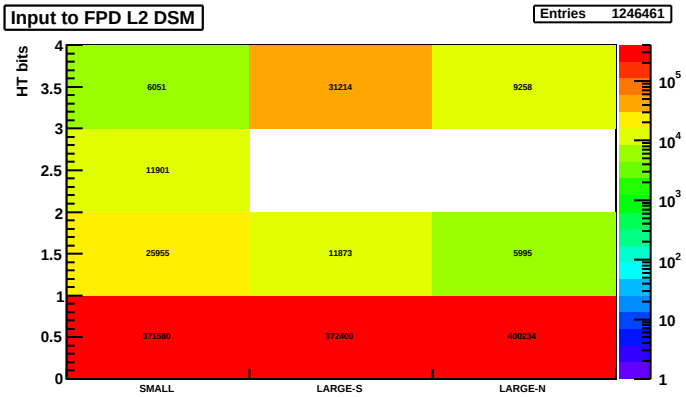

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

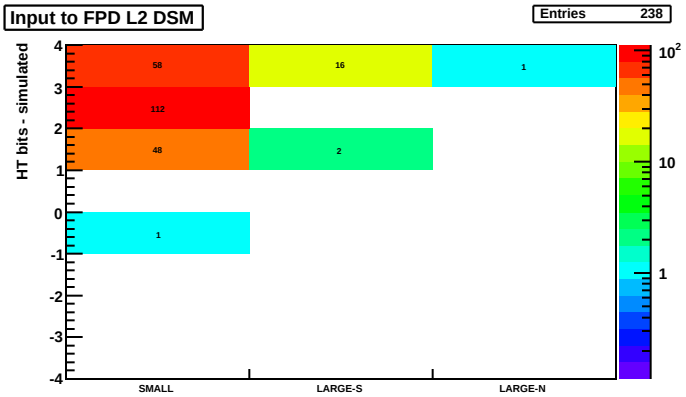

Input to FPD L2 DSM
Entries 4985844
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

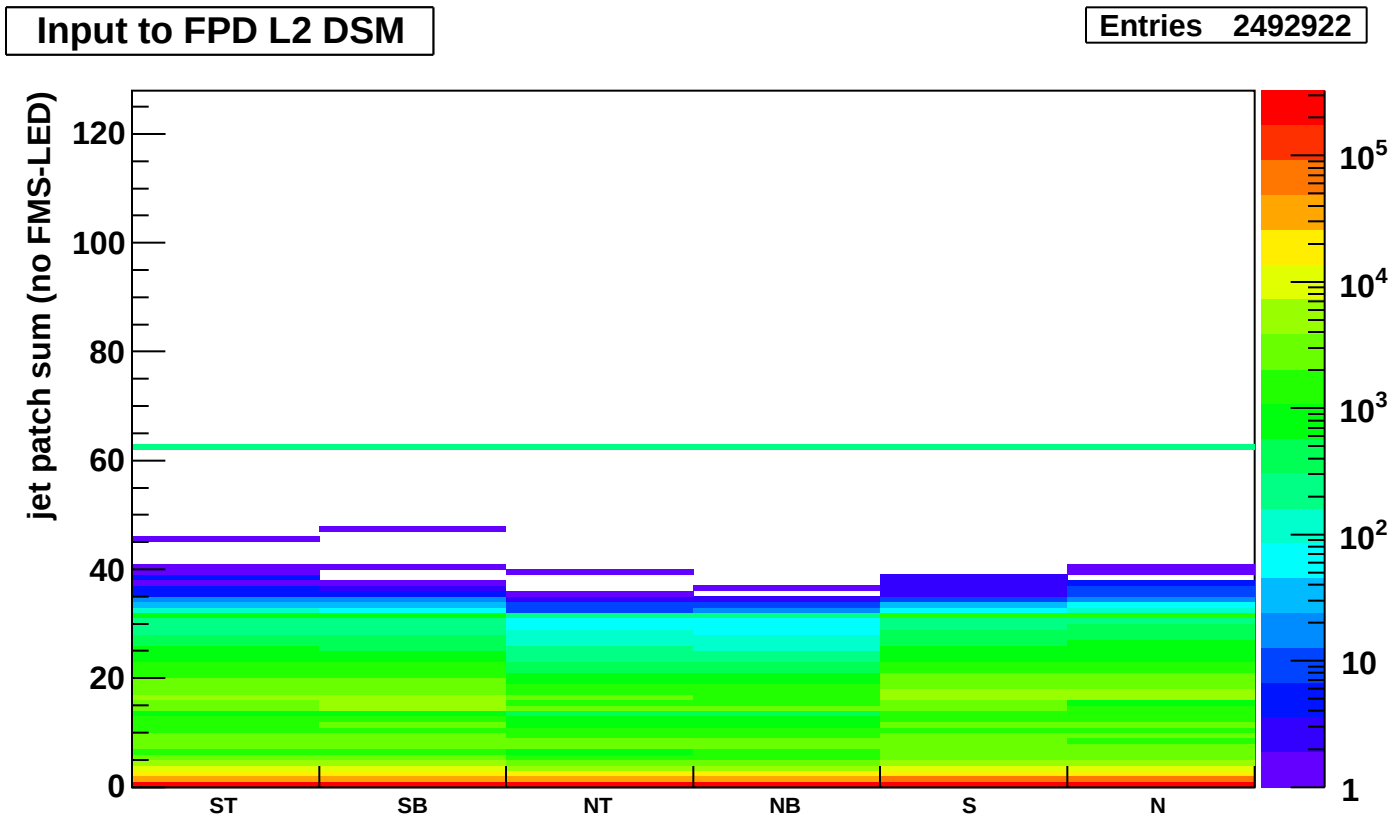

Input to FPD L2 DSM
Entries 805
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

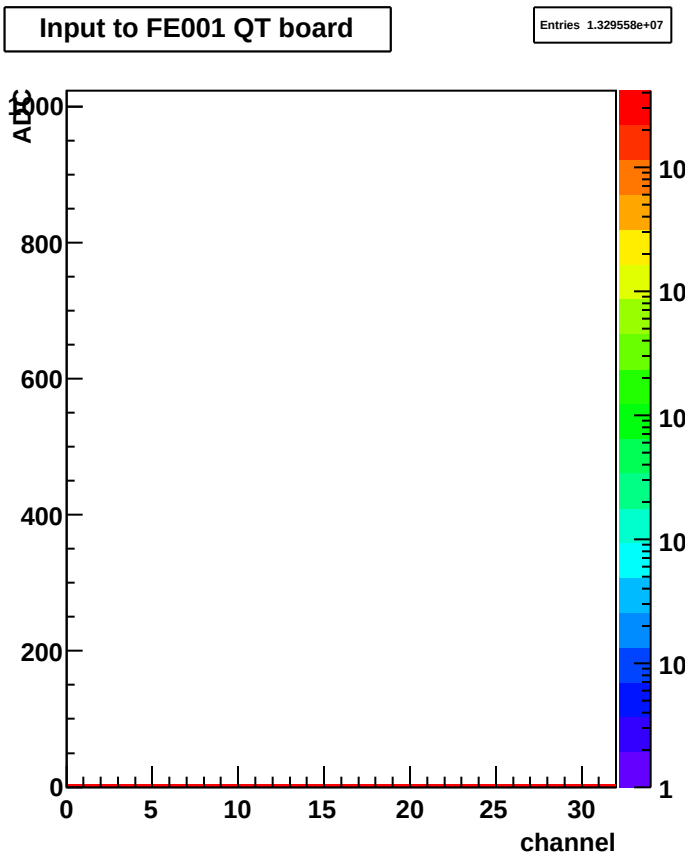

Input to FPD L2 DSM
Entries 1246461
CL bits
3595
1007
45983
22409
7781
17475
11694
5173
352029
377789
401526
SMALL
LARGE-S
LARGE-N

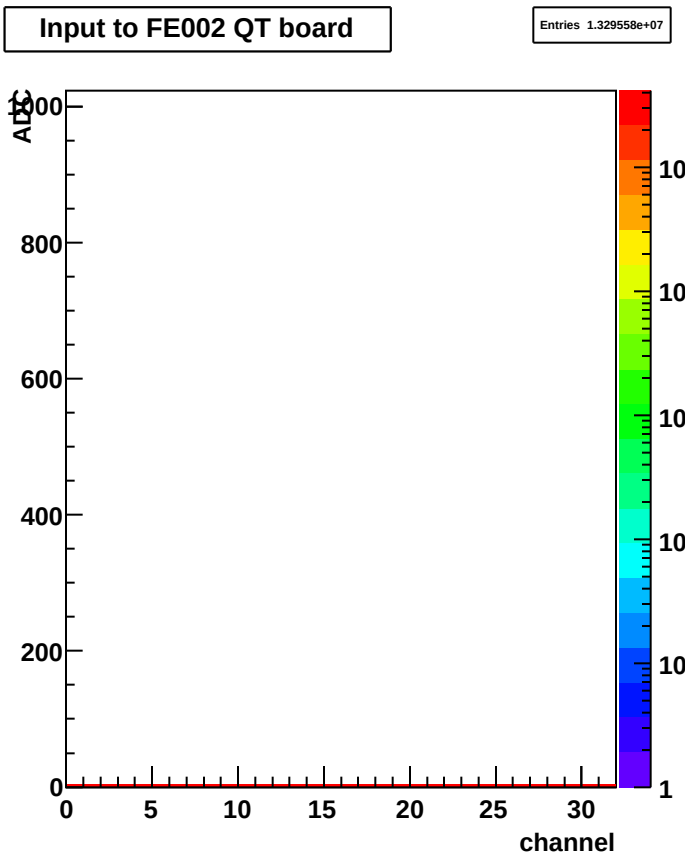

Input to FPD L2 DSM
Entries 58
CL bits - simulated
4
11
7
1
5
6
11
4
9
SMALL
LARGE-S
LARGE-N

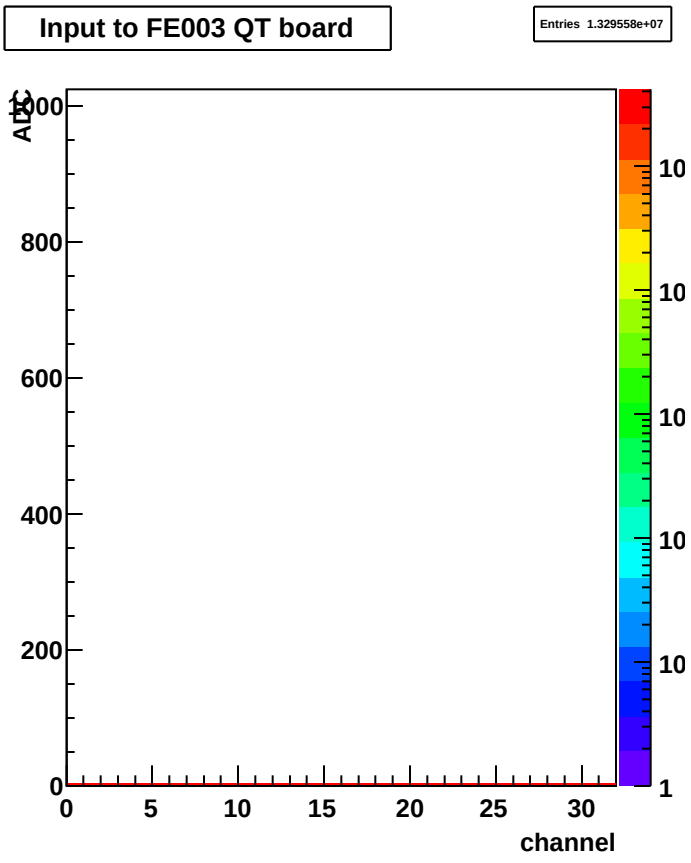

Input to FPD L2 DSM
Entries 1246461
HT bits
6051
31214
9258
11901
25955
11873
5995
371580
372400
400234
SMALL
LARGE-S
LARGE-N

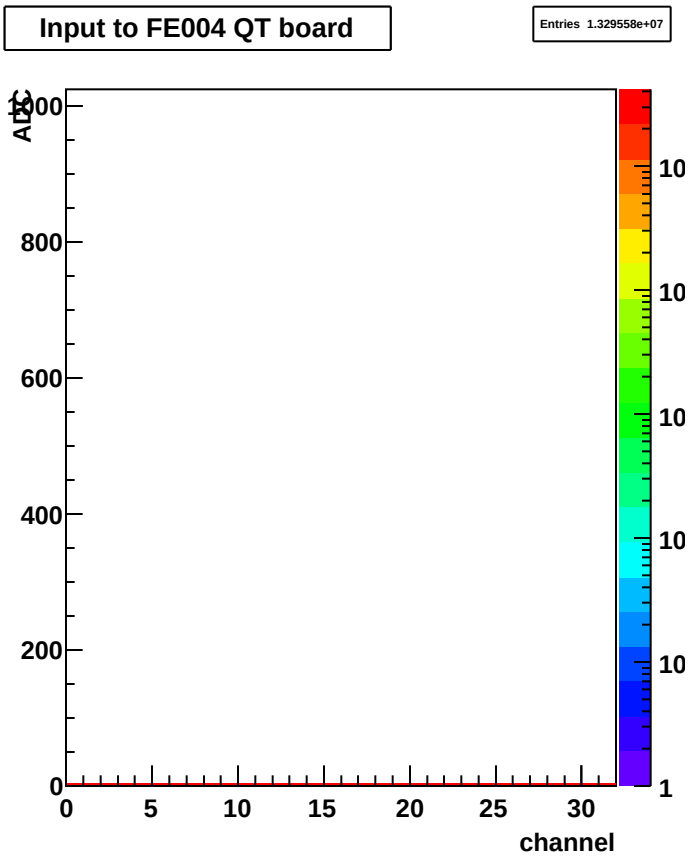

Input to FPD L2 DSM
Entries 238
HT bits - simulated
58
16
1
112
48
2
1
SMALL
LARGE-S
LARGE-N

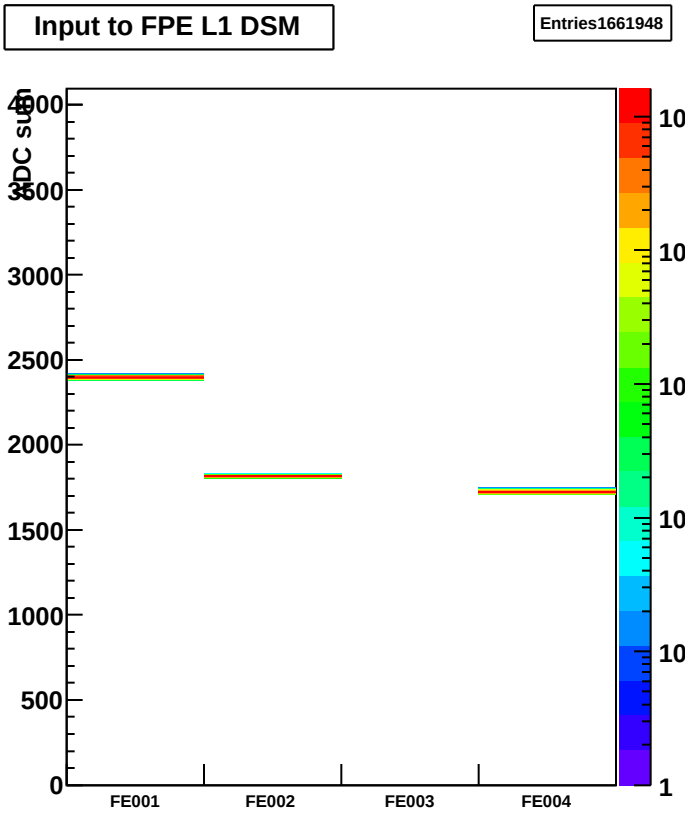

Input to FPD L2 DSM
Entries 2492922
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

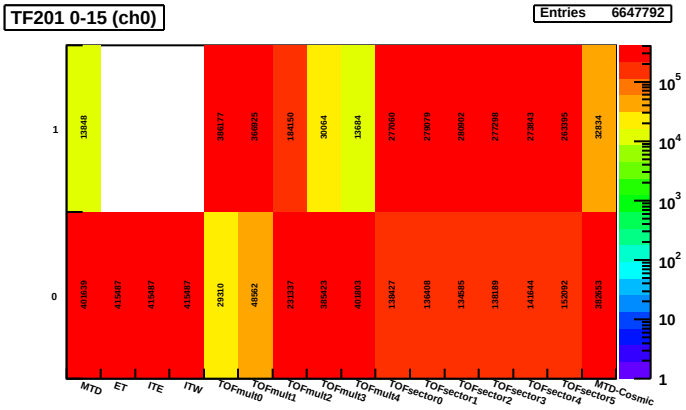

Input to FE001 QT board
Entries 1.329558e+07
ADC
channel

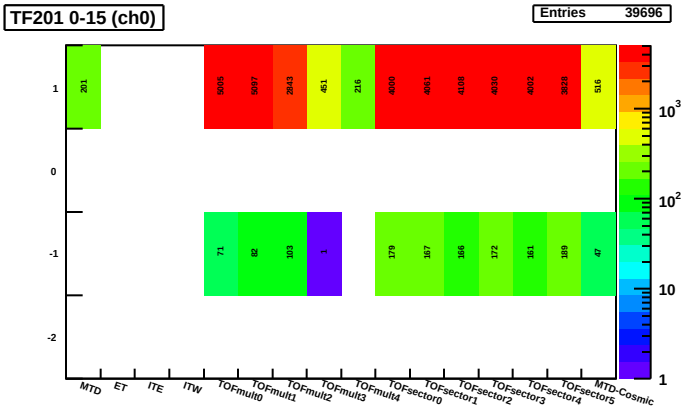

Input to FE002 QT board
Entries 1.329558e+07
ADC
channel

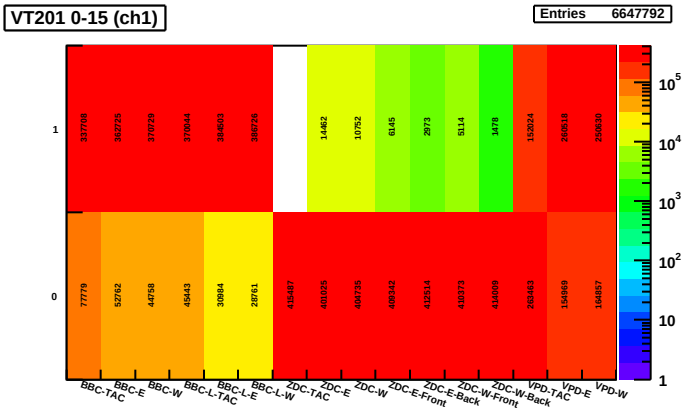

Input to FE003 QT board
Entries 1.329558e+07
ADC
channel

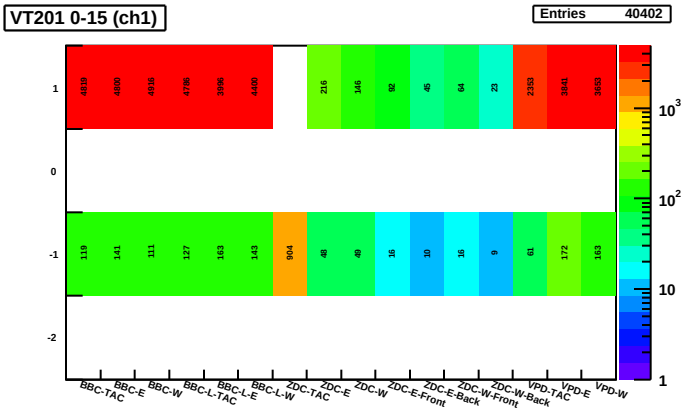

Input to FE004 QT board
Entries 1.329558e+07
ADC
channel


Input to FPE L1 DSM
Entries1661948
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 6647792
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 39696
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6647792
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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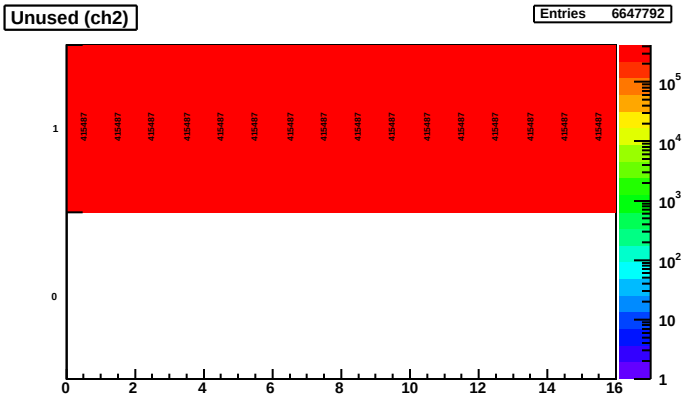
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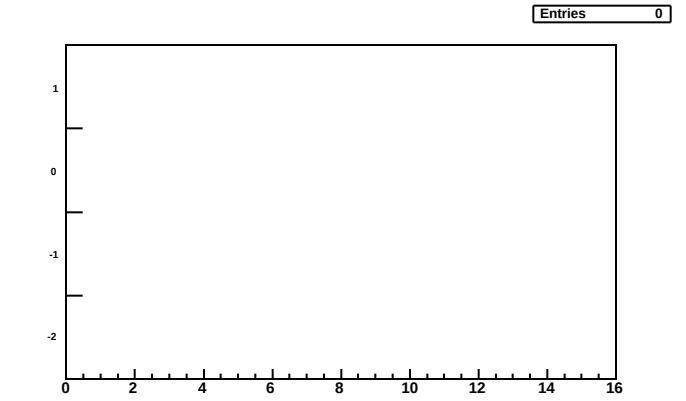
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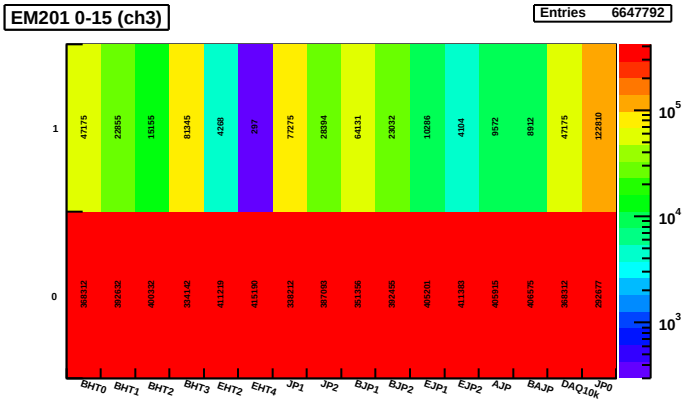
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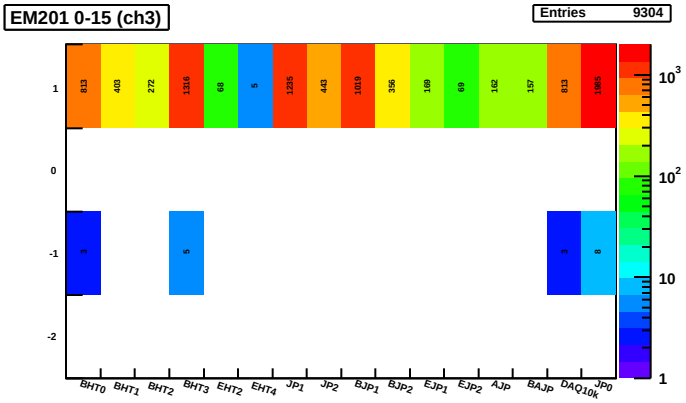
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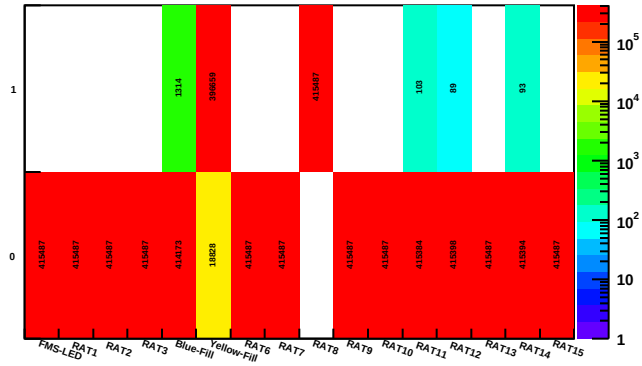
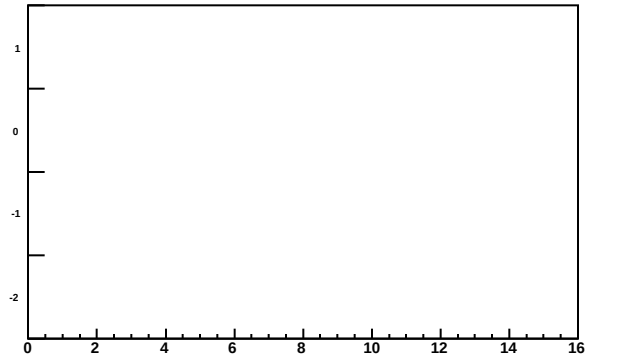
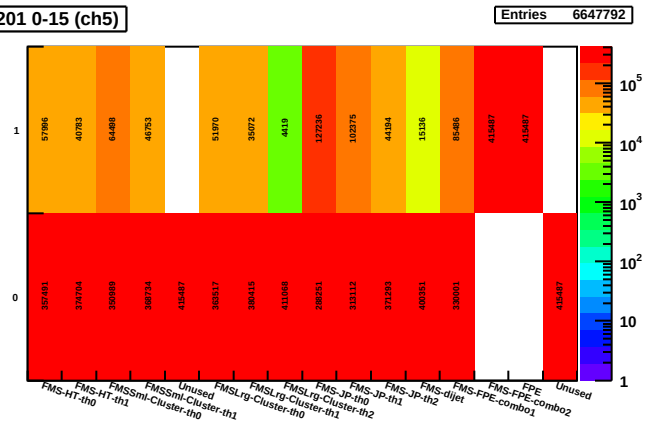
RAT board (ch4)

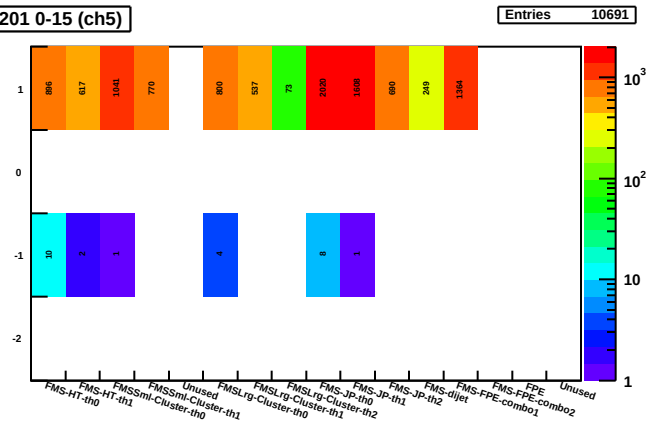




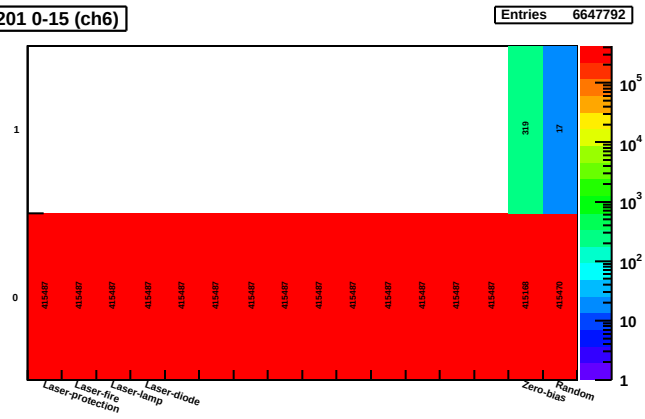
FP201 0-15 (ch5)

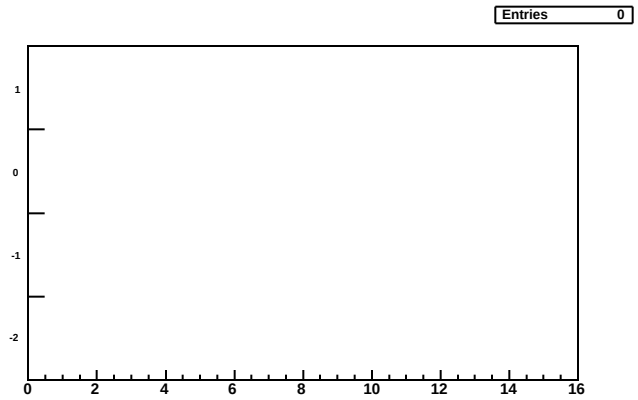
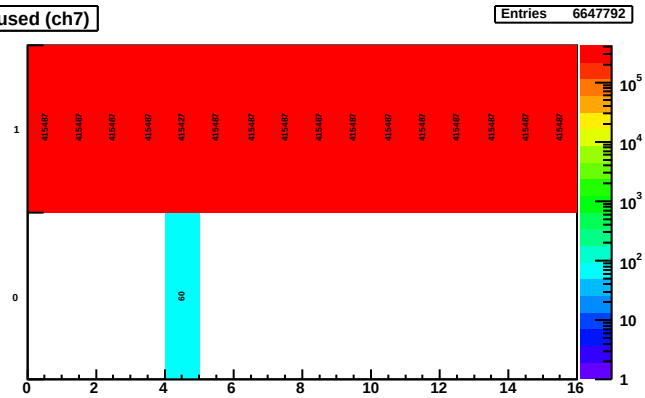
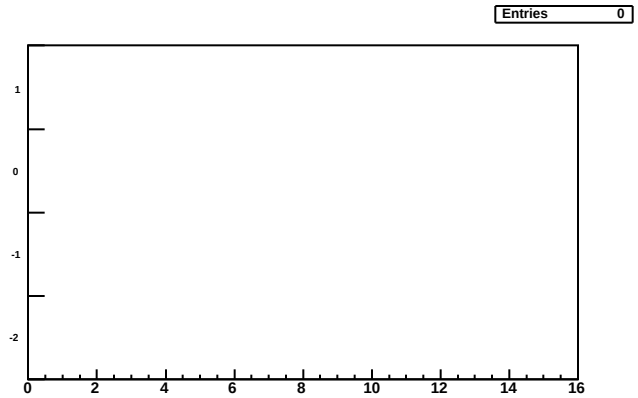


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)