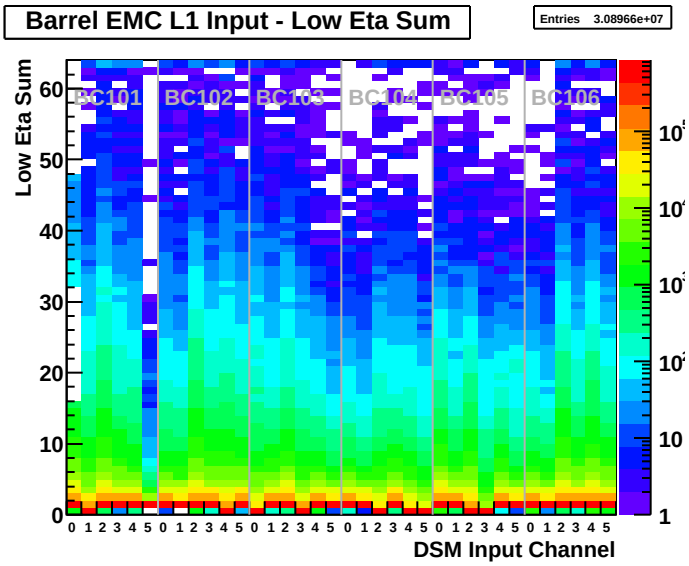
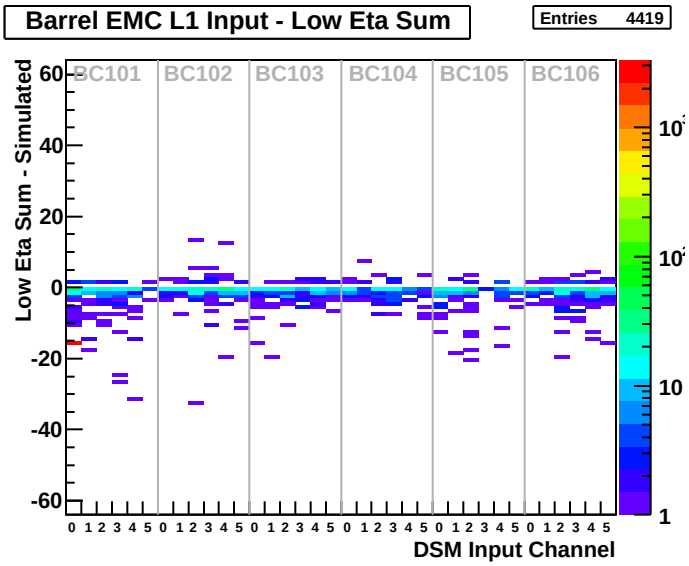
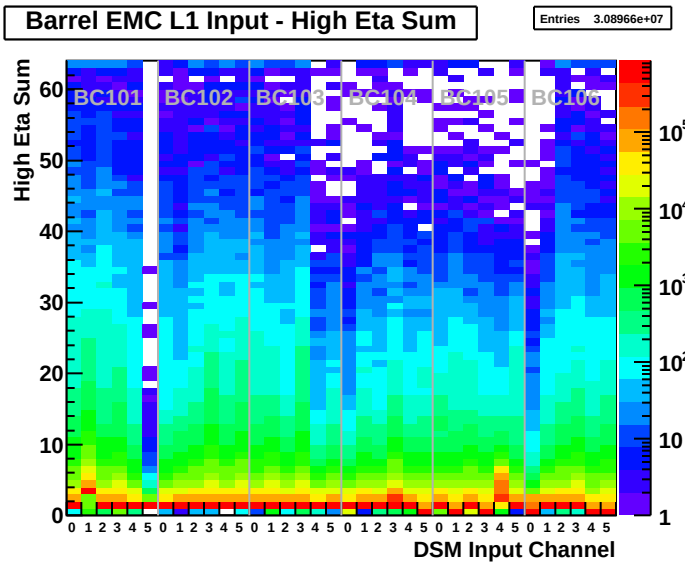
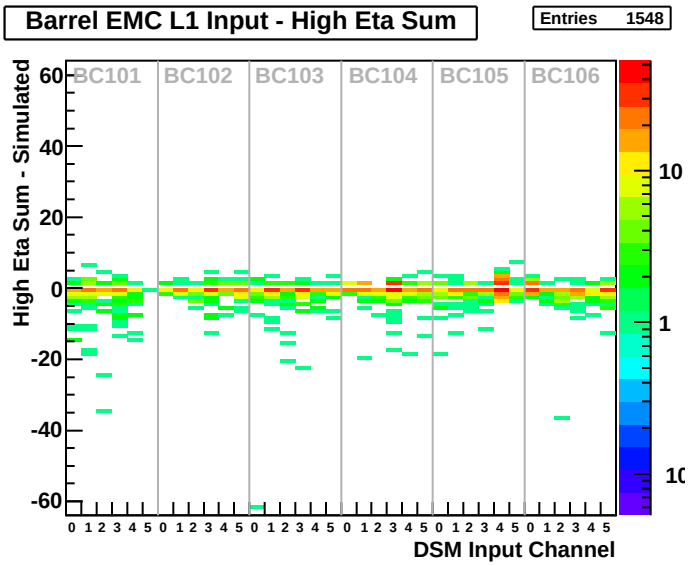
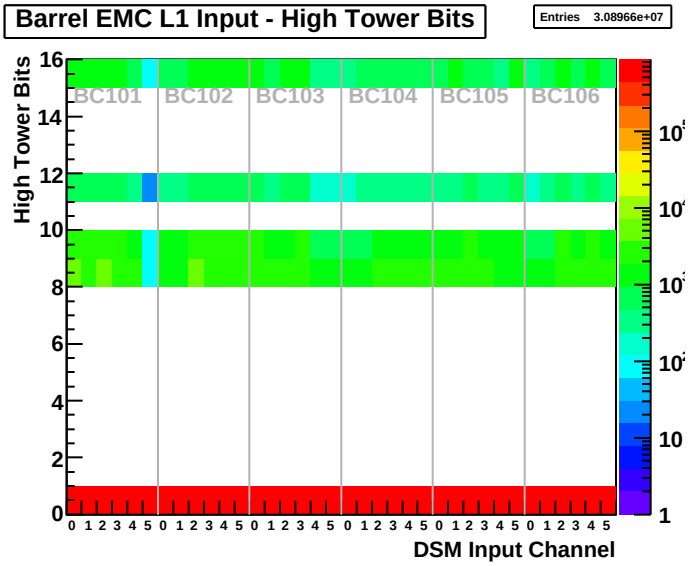
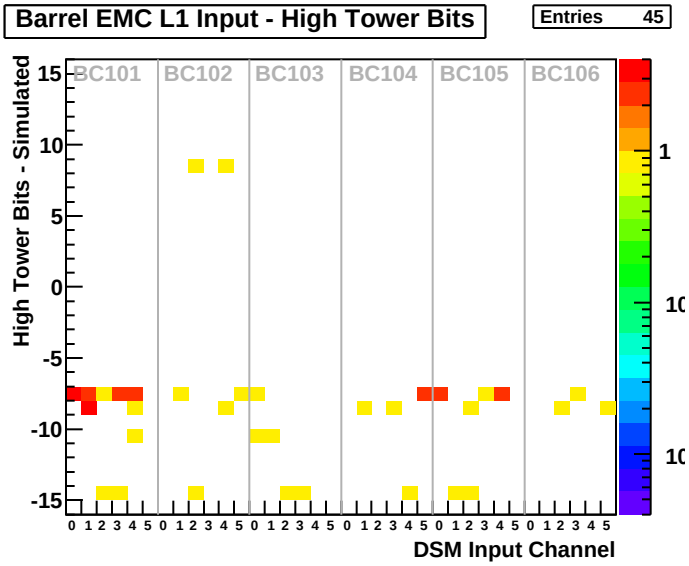

Barrel EMC L1 Input - Low Eta Sum
Entries 3.08966e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

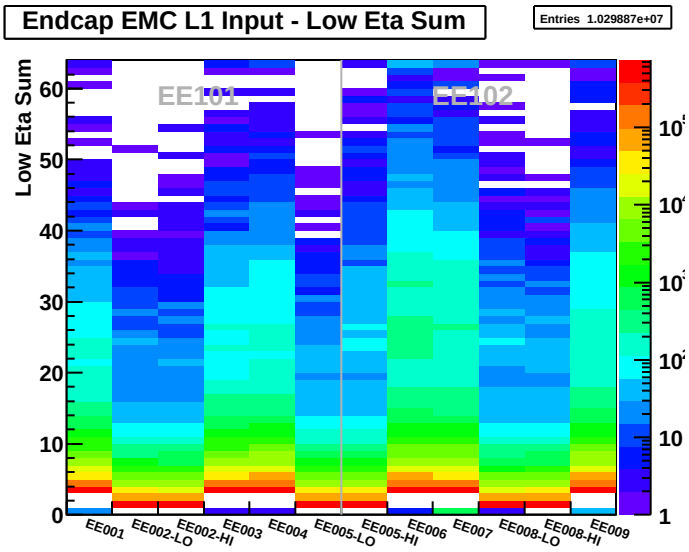

Barrel EMC L1 Input - Low Eta Sum
Entries 4419
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

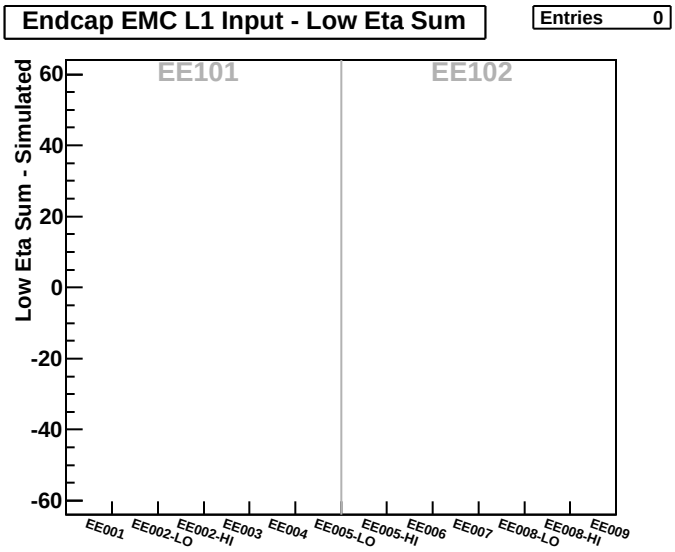

Barrel EMC L1 Input - High Eta Sum
Entries 3.08966e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

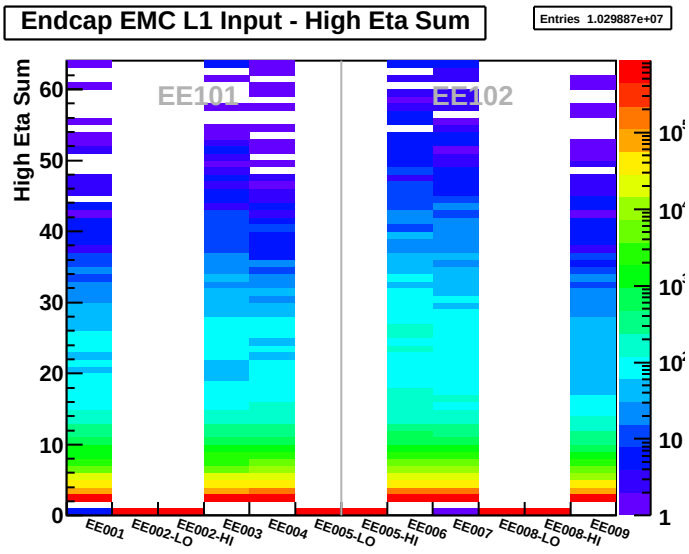

Barrel EMC L1 Input - High Eta Sum
Entries 1548
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

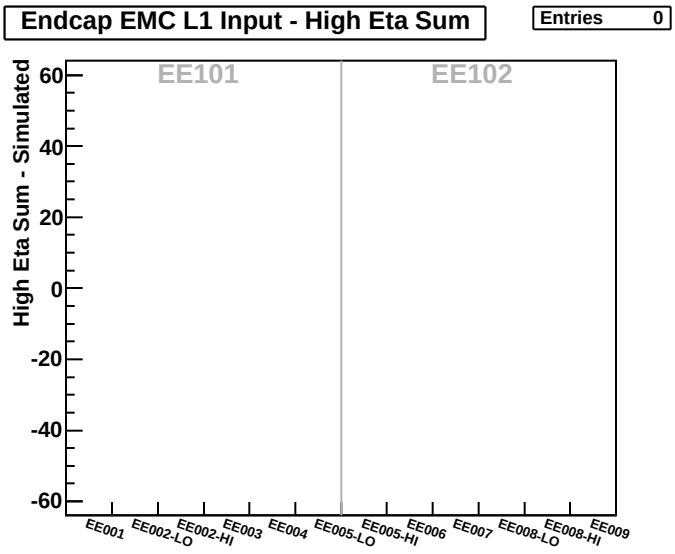

Barrel EMC L1 Input - High Tower Bits
Entries 3.08966e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

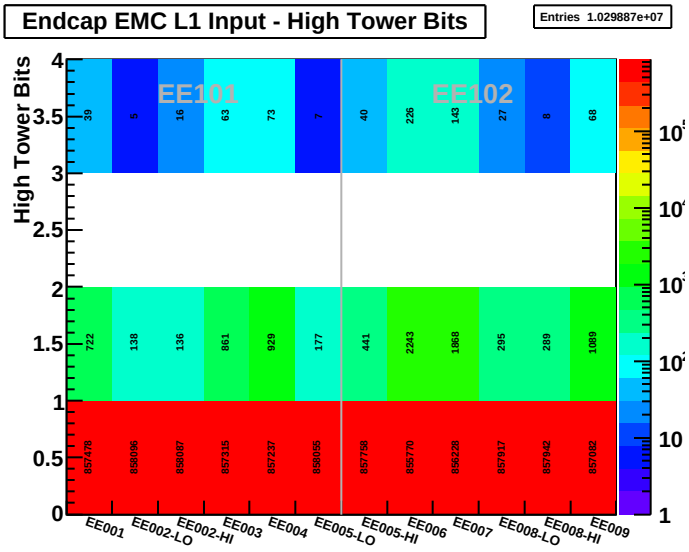

Barrel EMC L1 Input - High Tower Bits
Entries 45
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

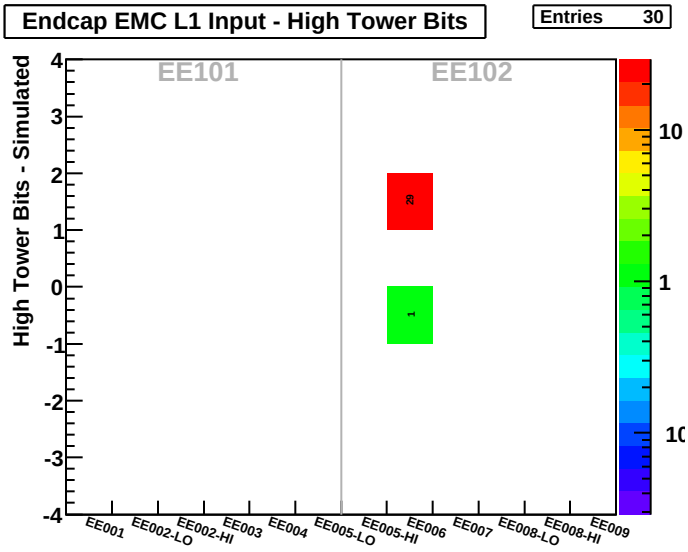

Endcap EMC L1 Input - Low Eta Sum
Entries 1.029887e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

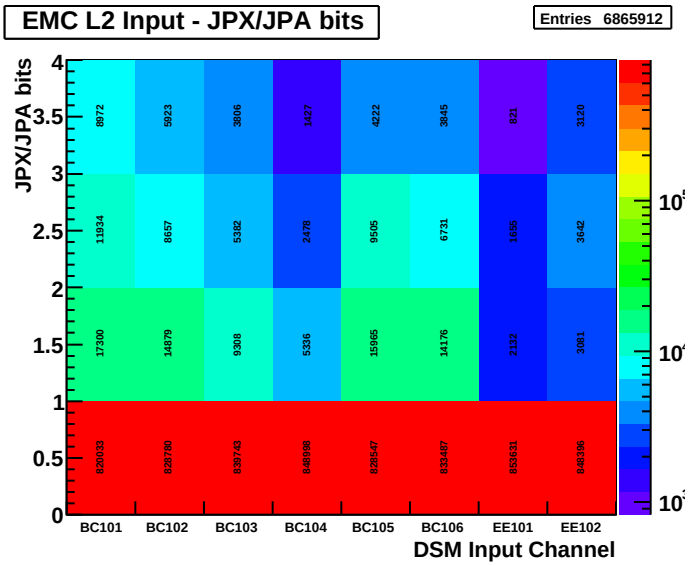

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

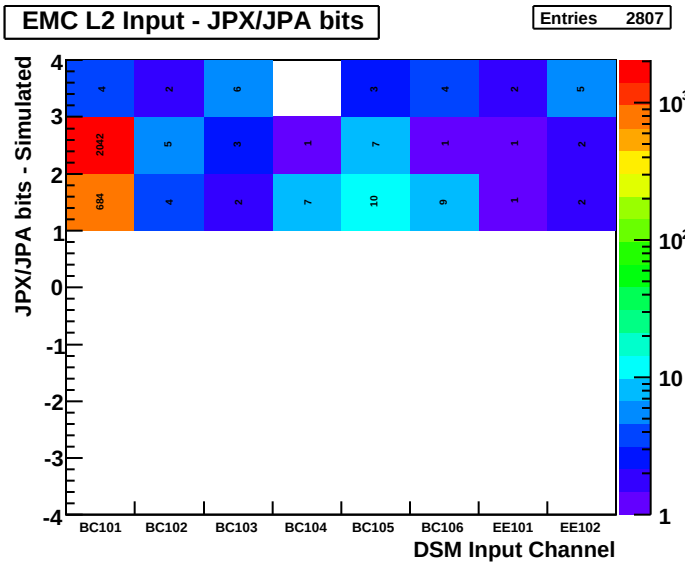

Endcap EMC L1 Input - High Eta Sum
Entries 1.029887e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

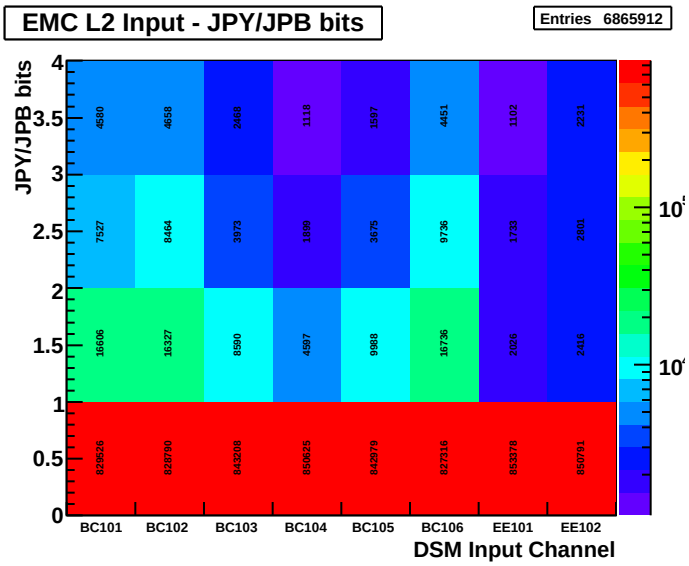

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

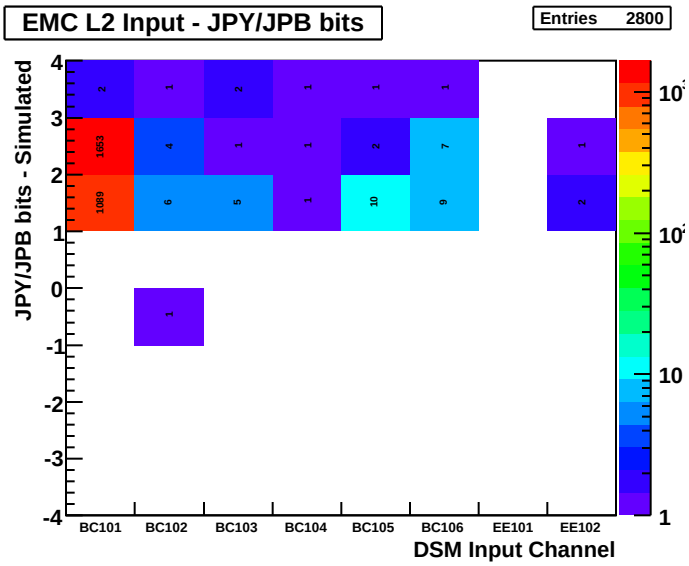

Endcap EMC L1 Input - High Tower Bits
Entries 1.029887e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

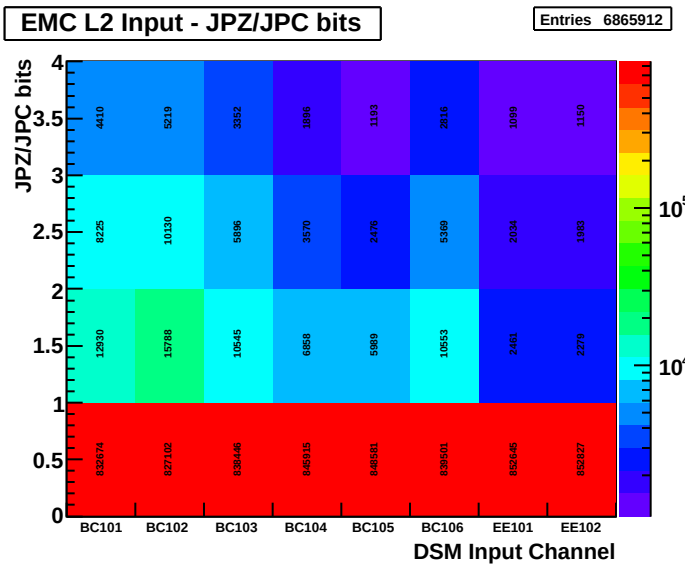

Endcap EMC L1 Input - High Tower Bits
Entries 30
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

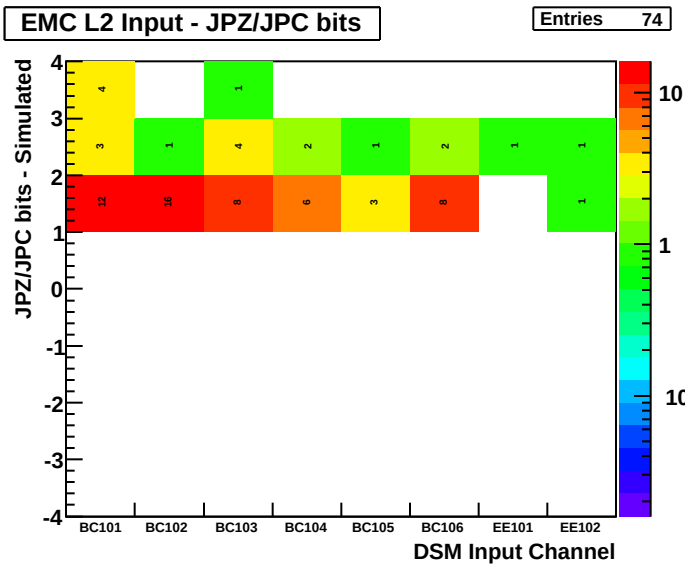

EMC L2 Input - JPX/JPA bits
Entries 6865912
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

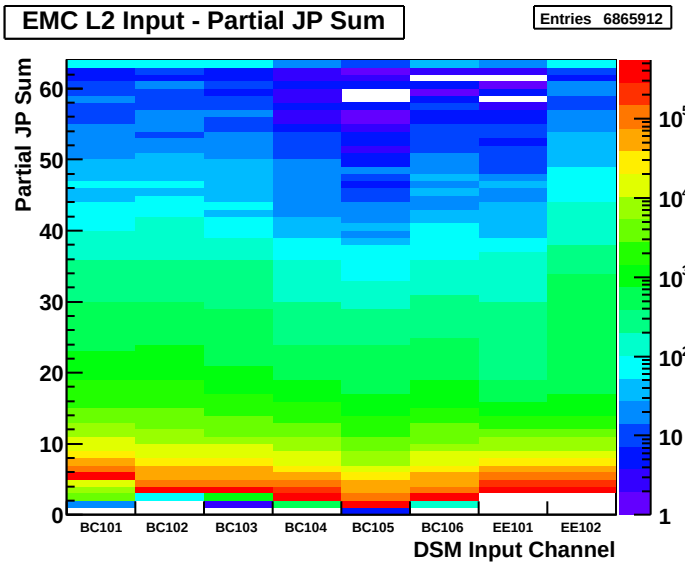

EMC L2 Input - JPX/JPA bits
Entries 2807
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

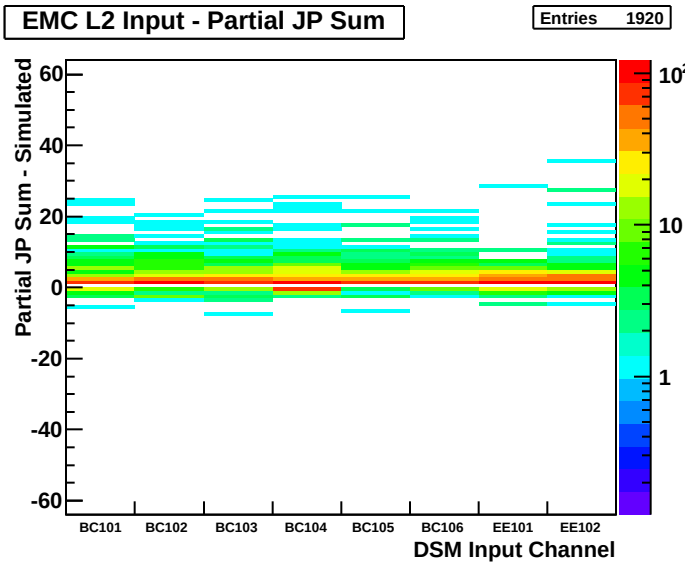

EMC L2 Input - JPY/JPB bits
Entries 6865912
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

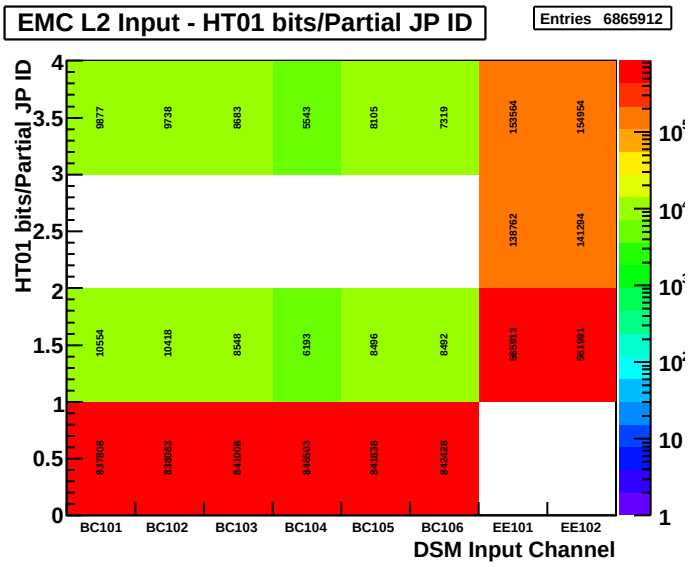

EMC L2 Input - JPY/JPB bits
Entries 2800
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

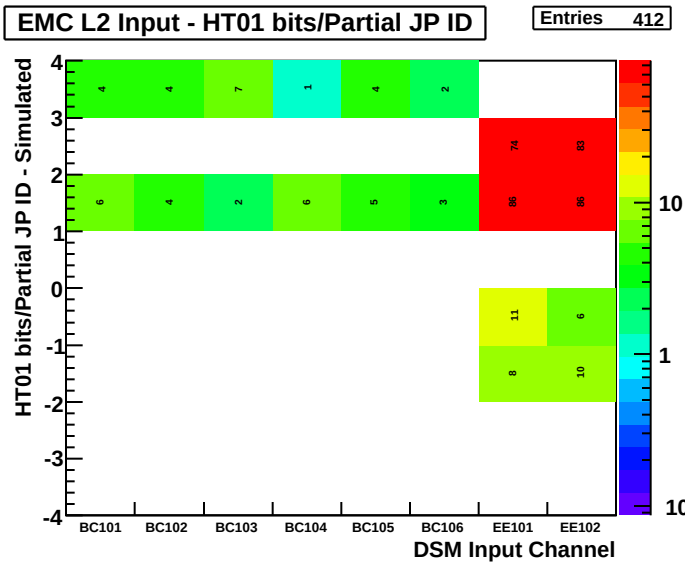

EMC L2 Input - JPZ/JPC bits
Entries 6865912
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

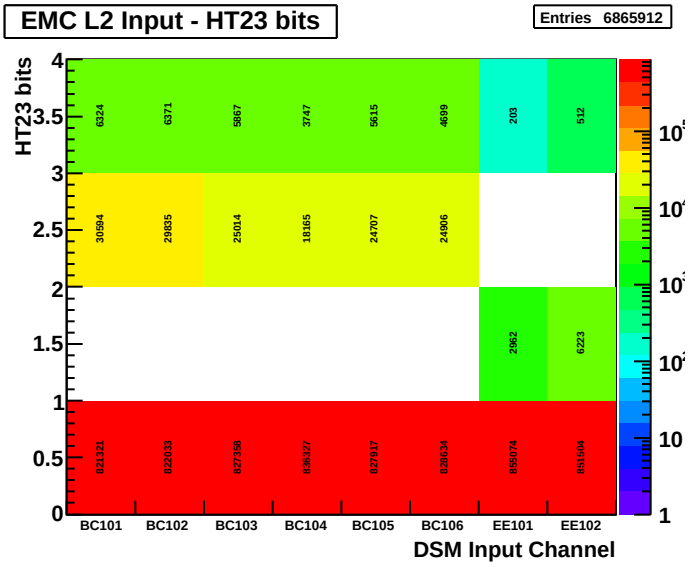

EMC L2 Input - JPZ/JPC bits
Entries 74
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

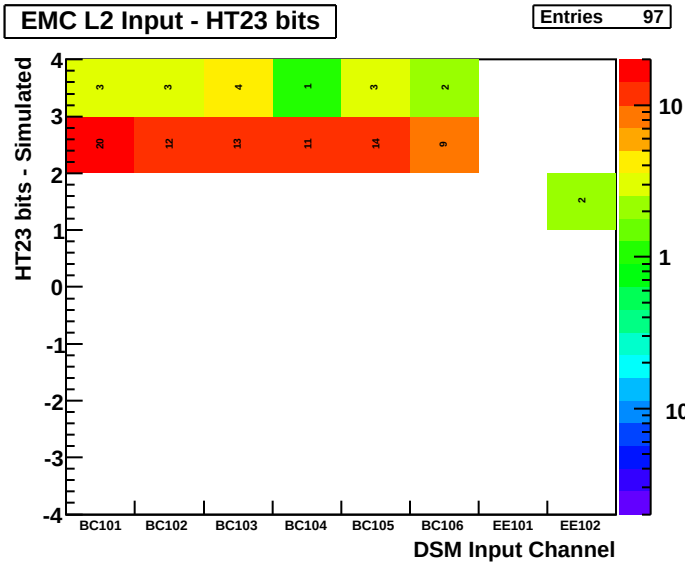

EMC L2 Input - Partial JP Sum
Entries 6865912
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

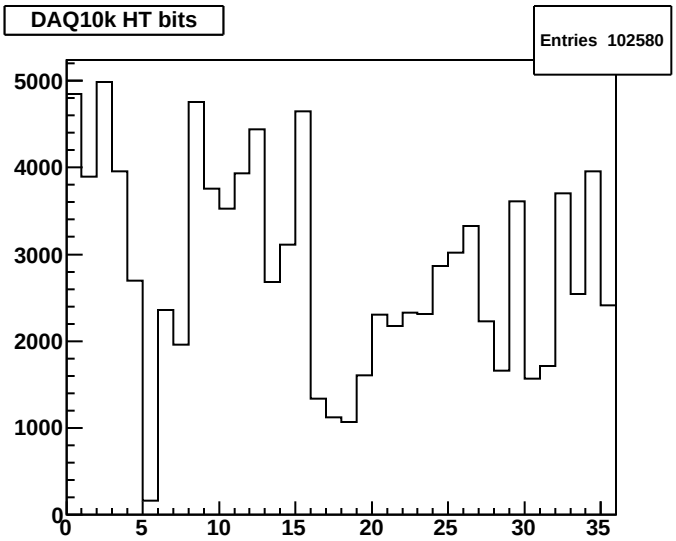

EMC L2 Input - Partial JP Sum
Entries 1920
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

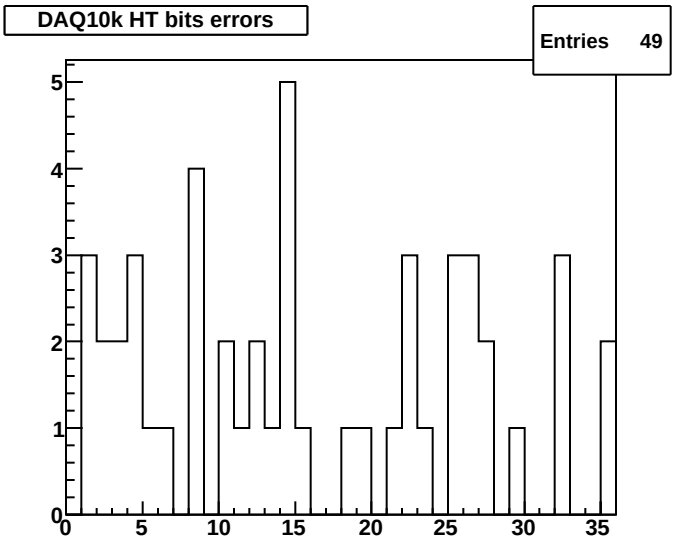

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6865912
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

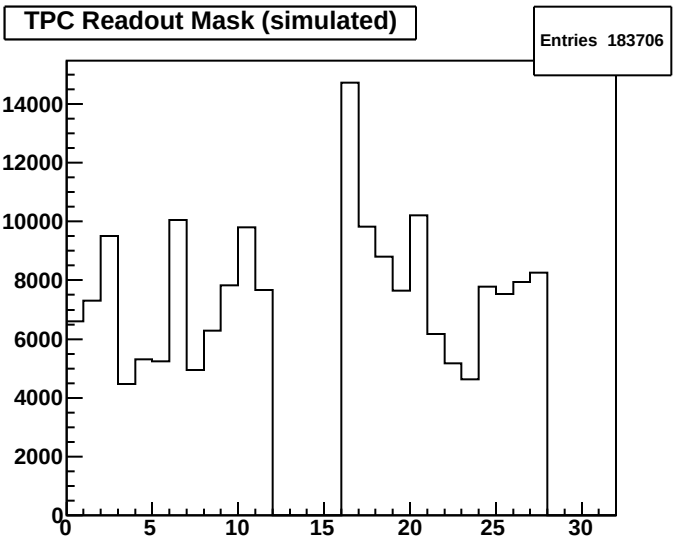

EMC L2 Input - HT01 bits/Partial JP ID
Entries 412
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

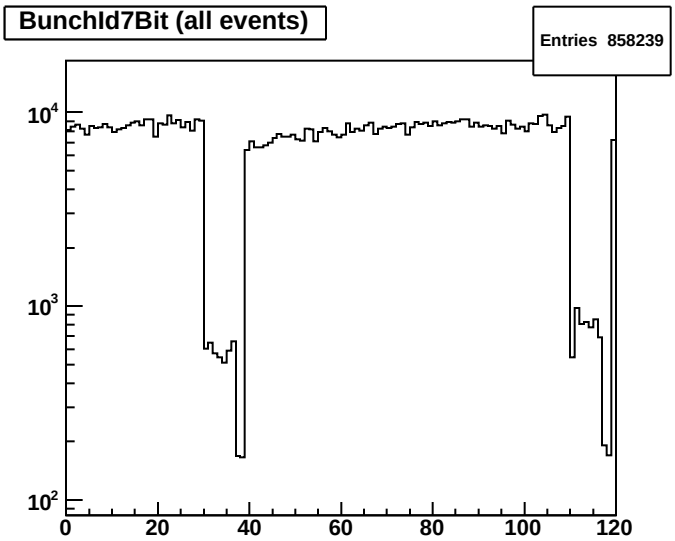

EMC L2 Input - HT23 bits
Entries 6865912
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

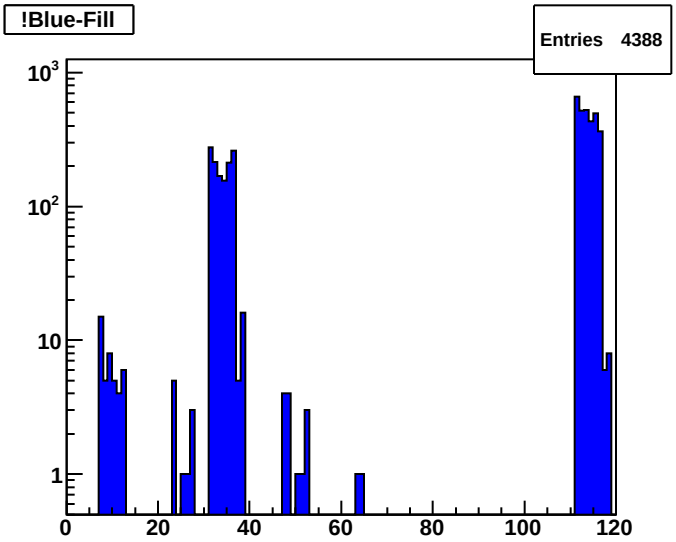

EMC L2 Input - HT23 bits
Entries 97
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

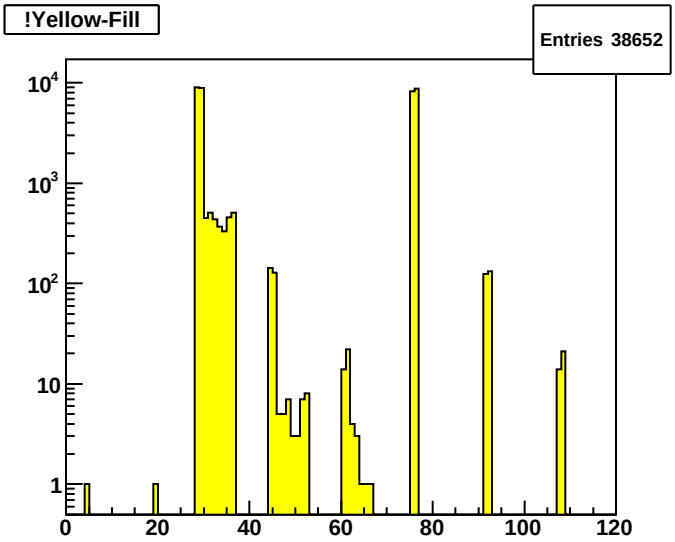

DAQ10k HT bits
Entries 102580

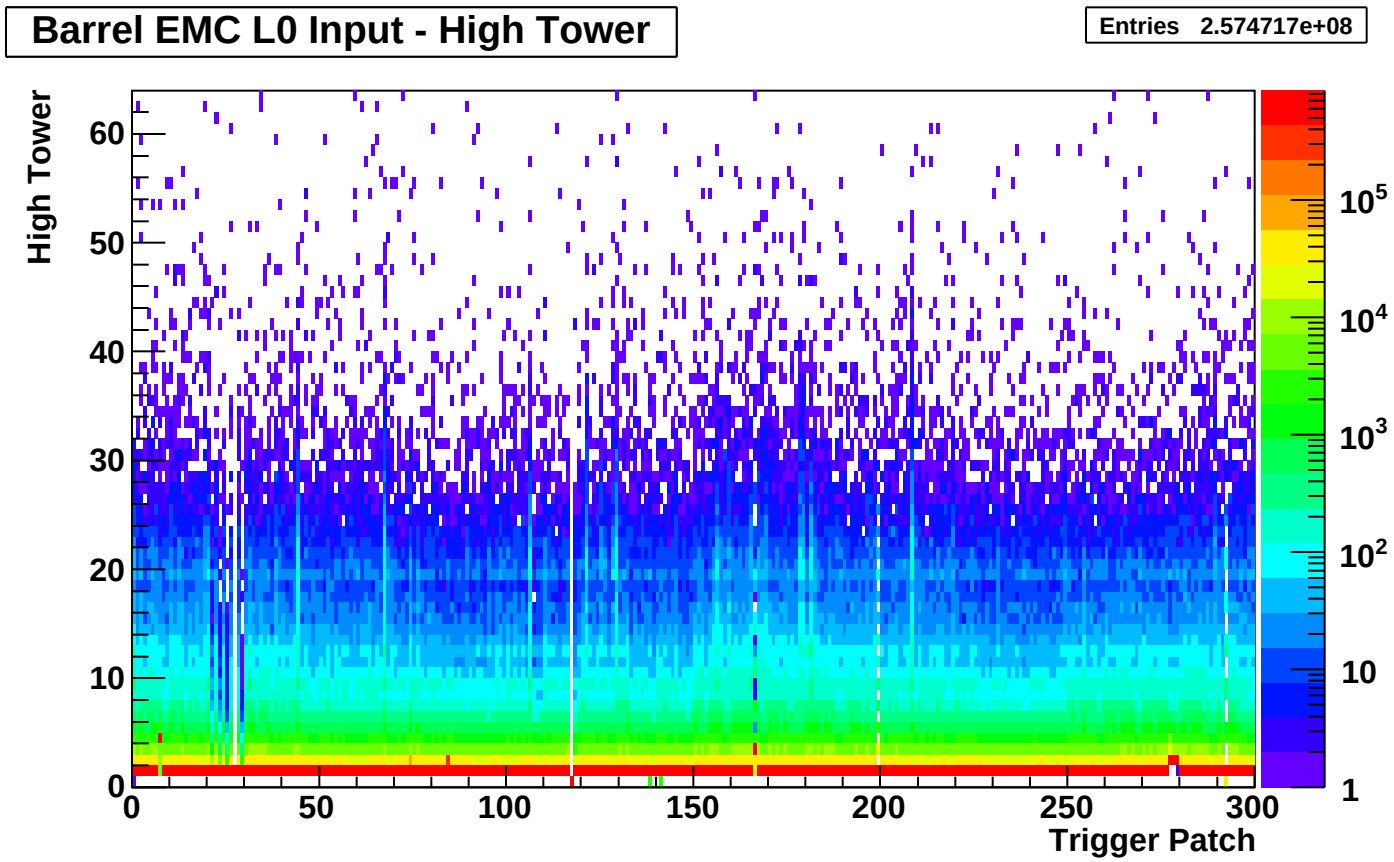

DAQ10k HT bits errors
Entries 49

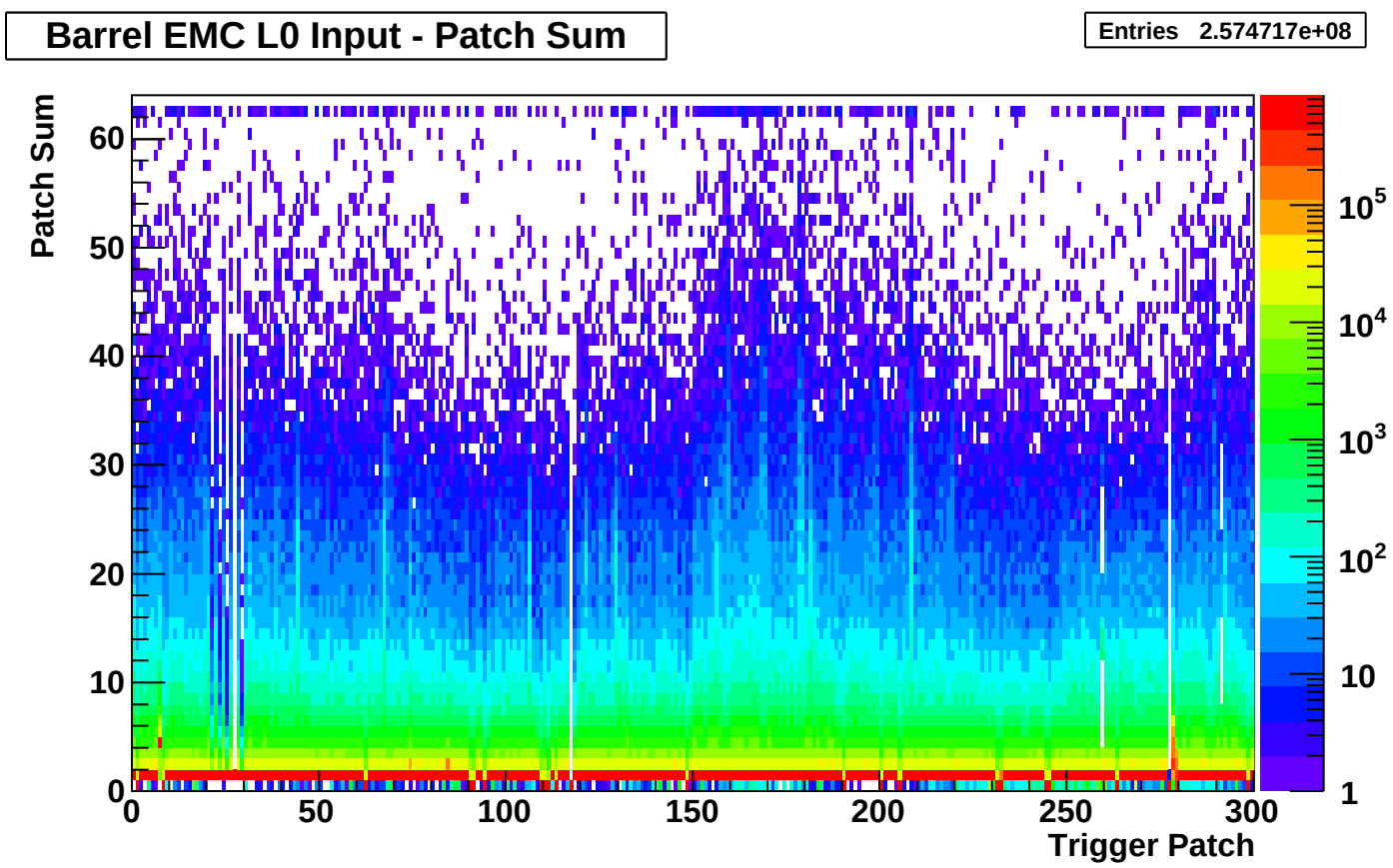

TPC Readout Mask (simulated)
Entries 183706

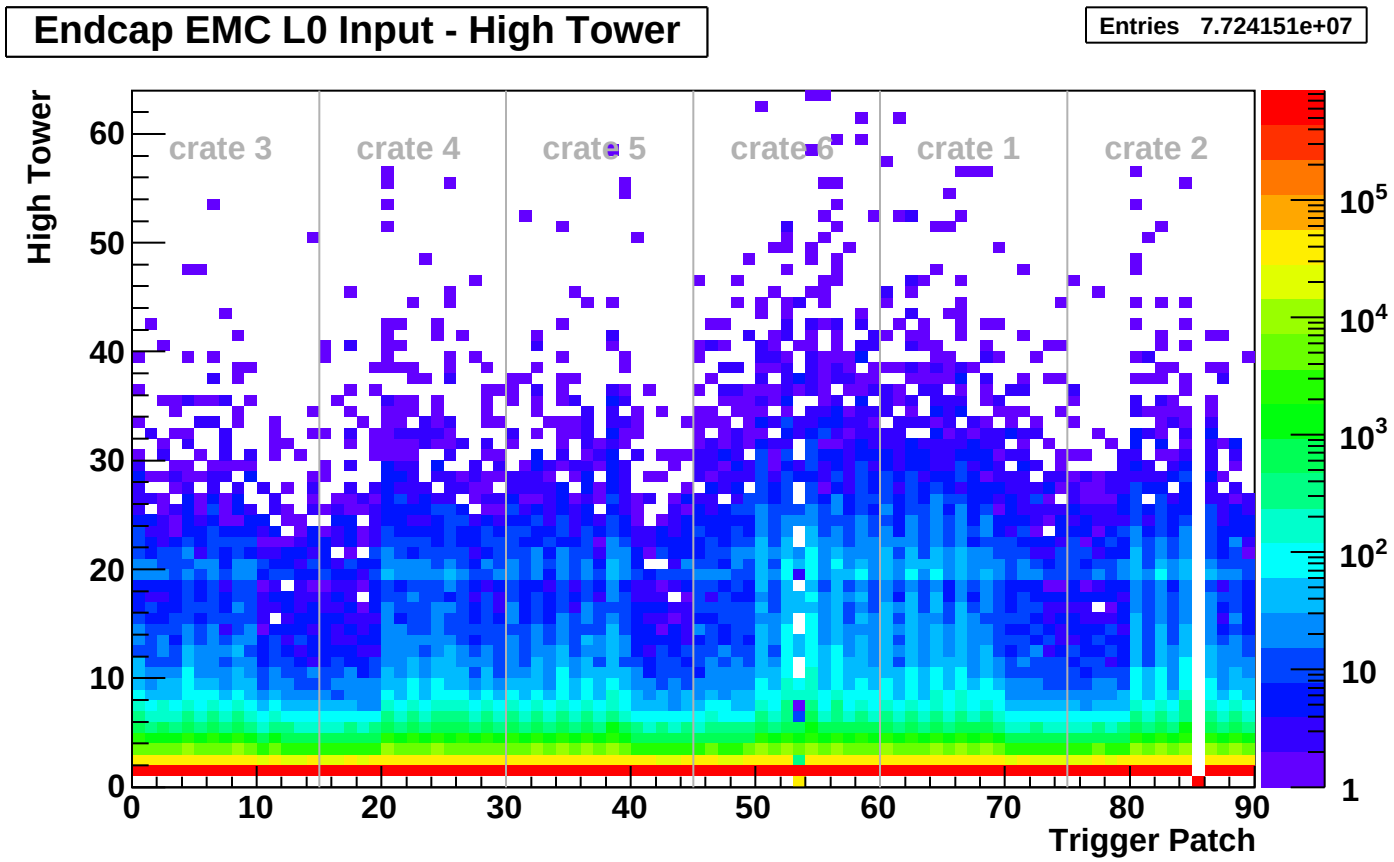

BunchId7Bit (all events)
Entries 858239

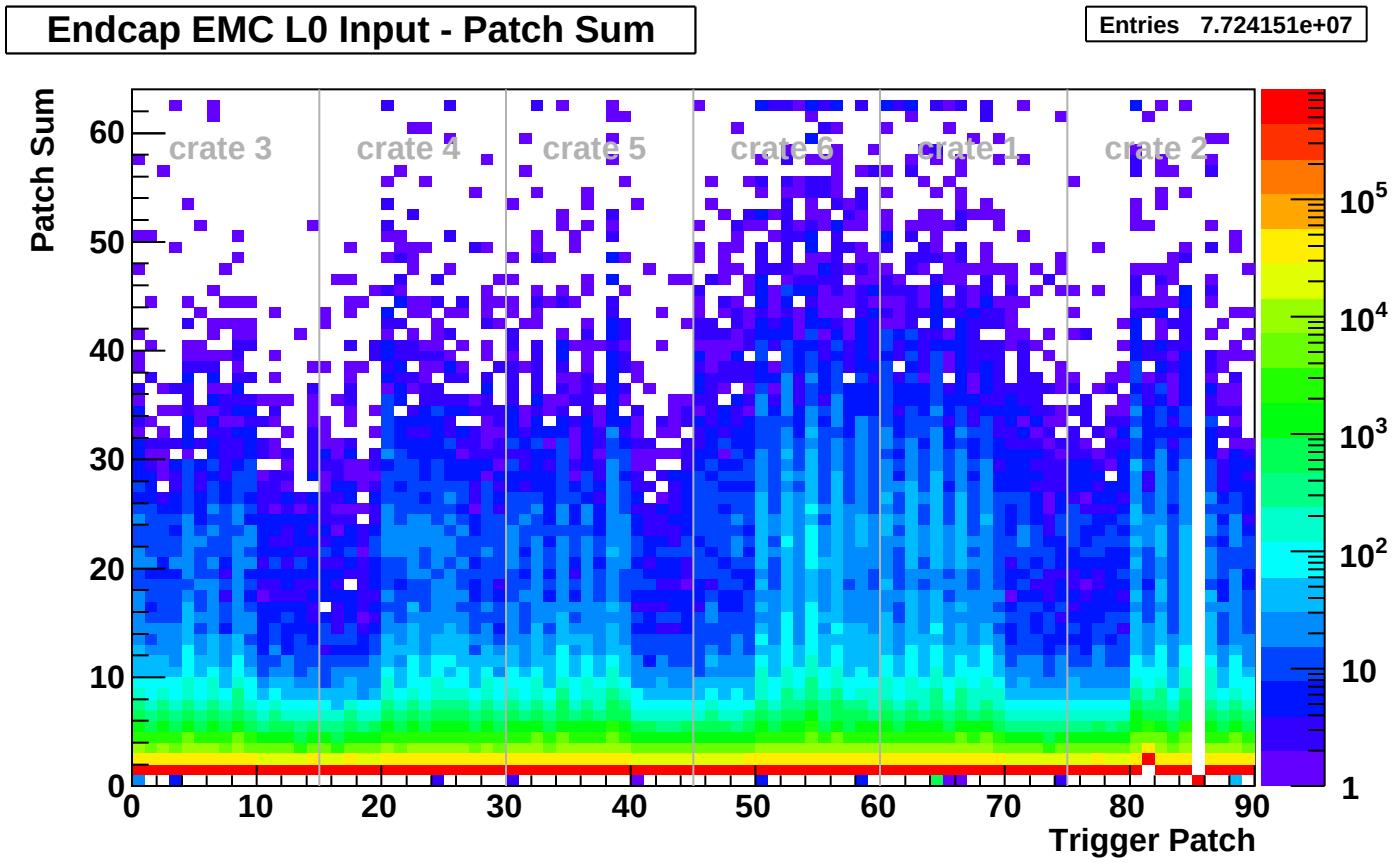

!Blue-Fill
Entries 4388

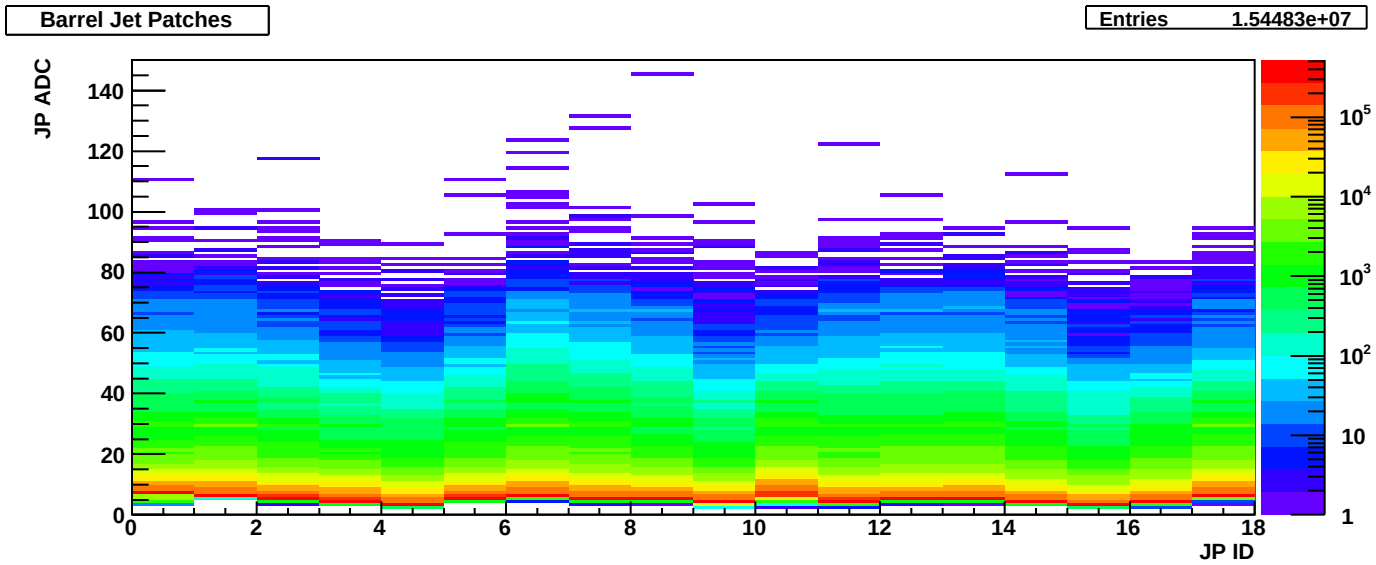

!Yellow-Fill
Entries 38652

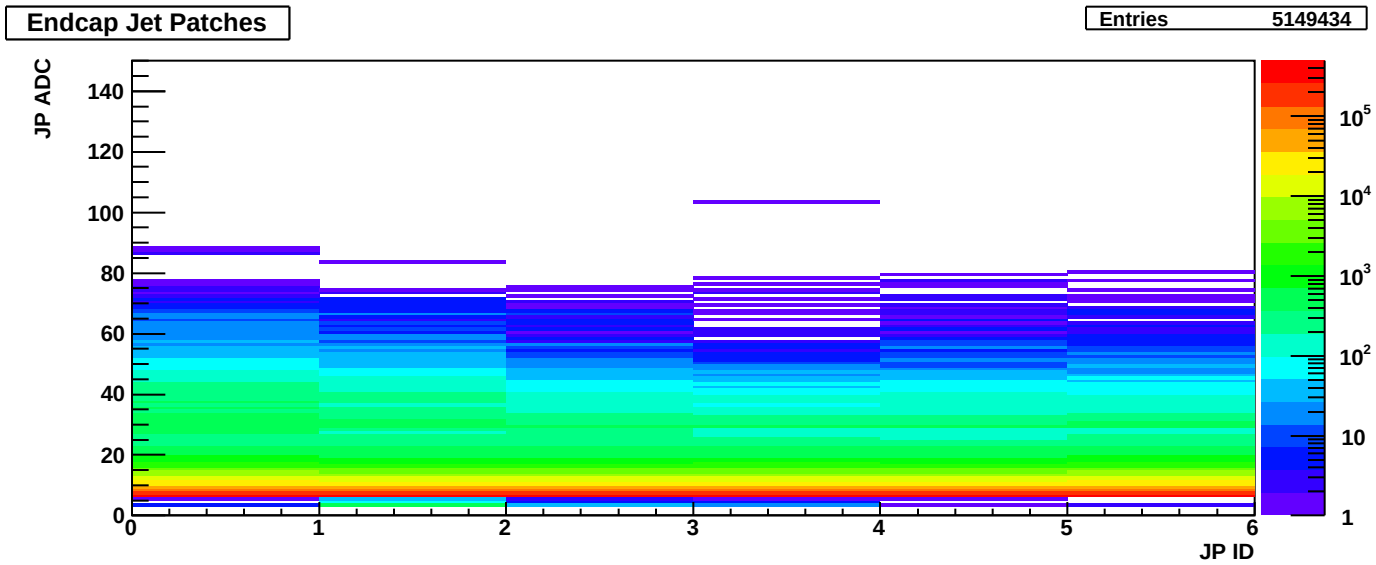

Barrel EMC L0 Input - High Tower
Entries 2.574717e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

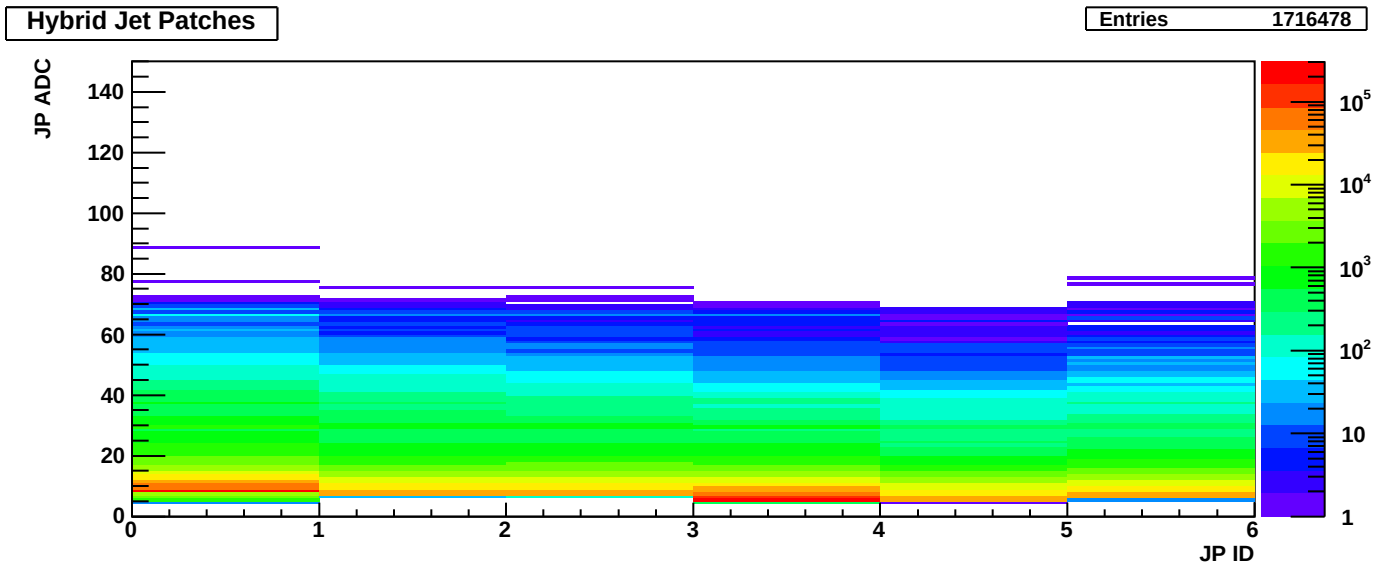

Barrel EMC L0 Input - Patch Sum
Entries 2.574717e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

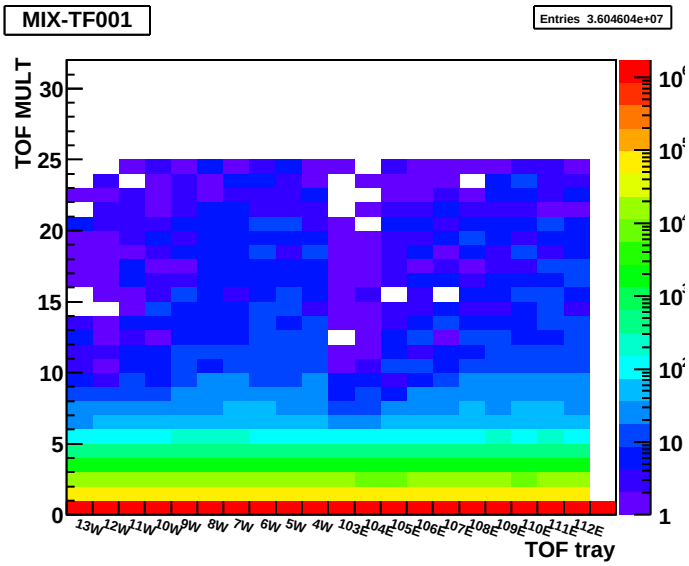

Endcap EMC L0 Input - High Tower
Entries 7.724151e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

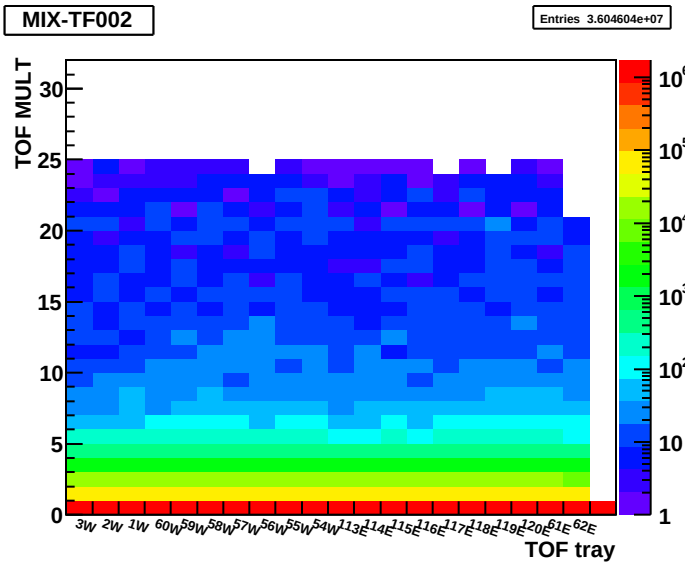

Endcap EMC L0 Input - Patch Sum
Entries 7.724151e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

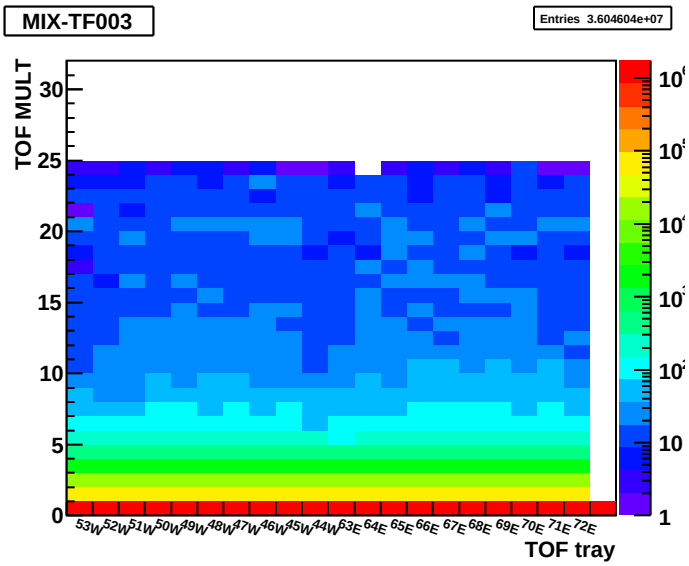

Barrel Jet Patches
Entries 1.54483e+07
JP ADC
JP ID

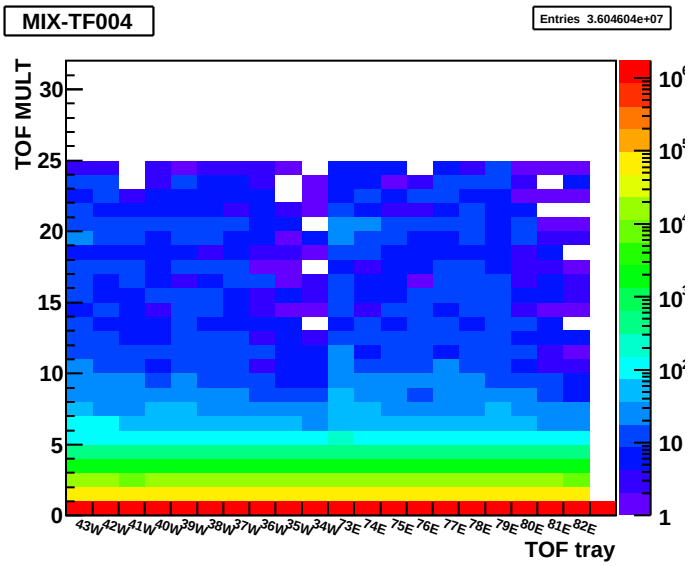

Endcap Jet Patches
Entries 5149434
JP ADC
JP ID

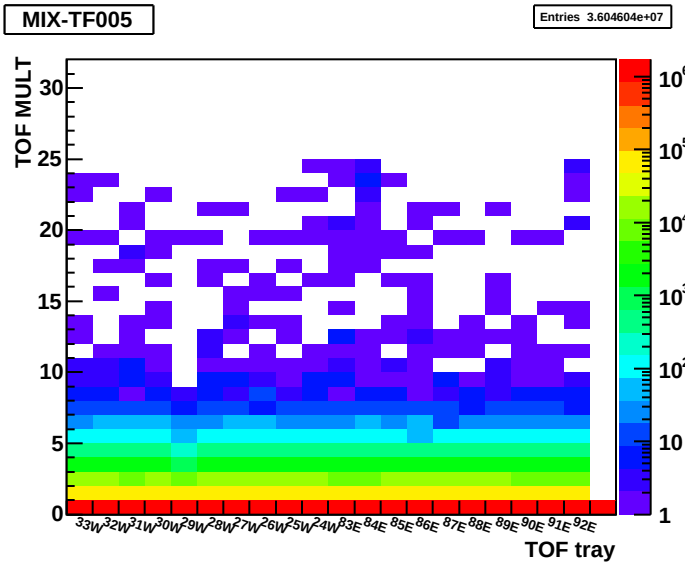

Hybrid Jet Patches
Entries 1716478
JP ADC
JP ID

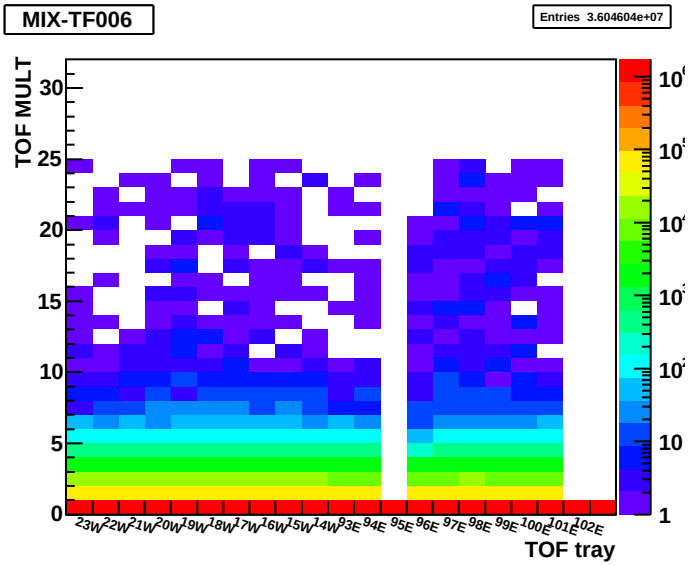

MIX-TF001
Entries 3.604604e+07
TOF MULT
TOF tray

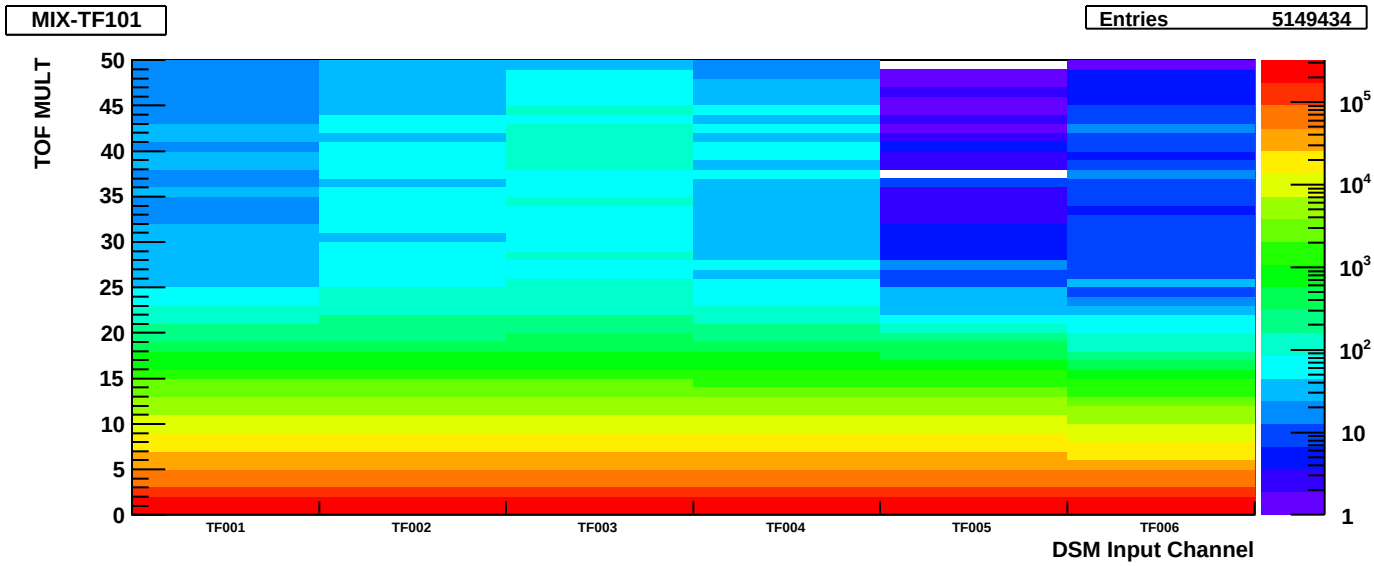

MIX-TF002
Entries 3.604604e+07
TOF MULT
TOF tray

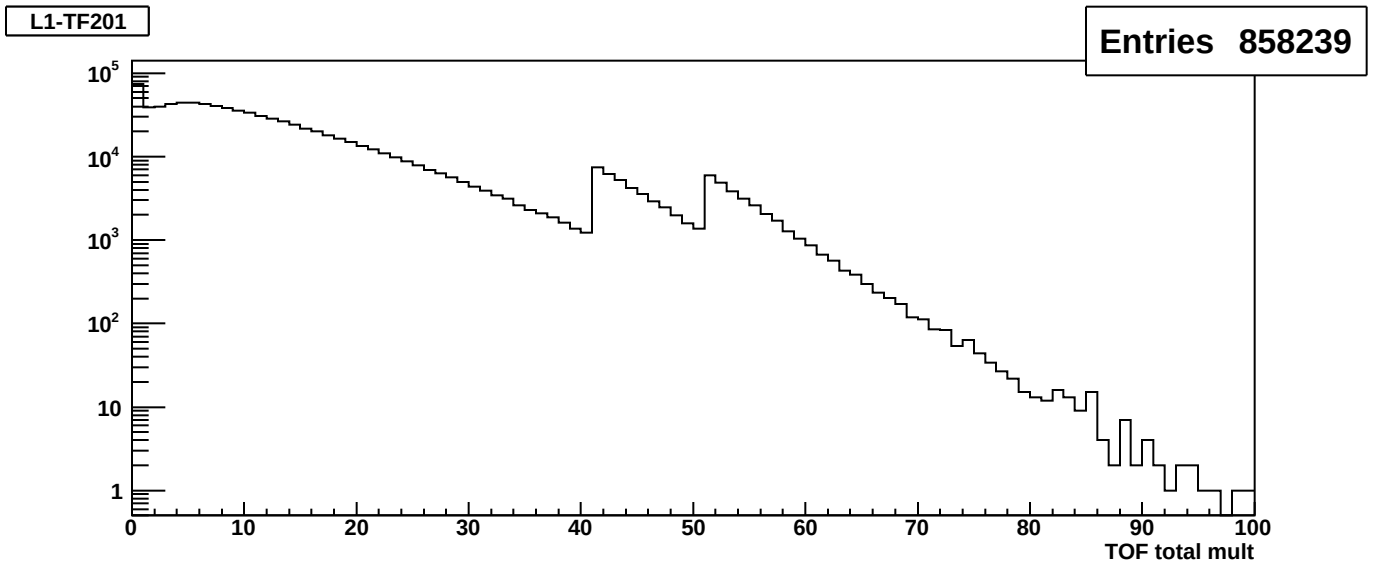

MIX-TF003
Entries 3.604604e+07
TOF MULT
TOF tray

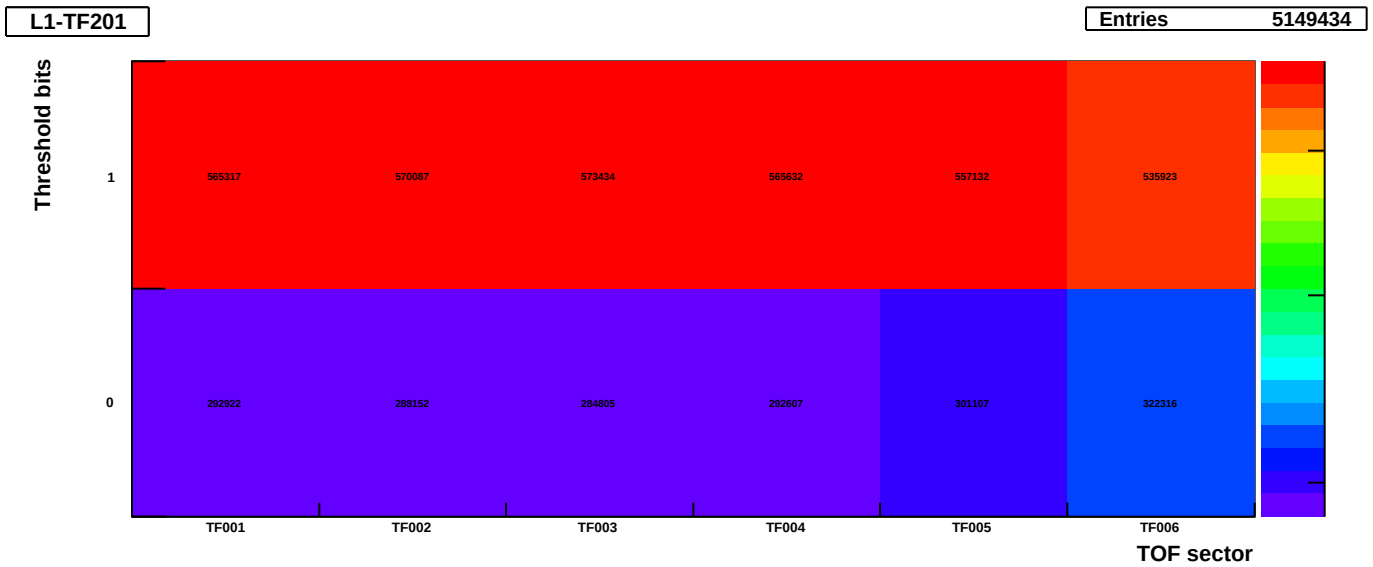

MIX-TF004
Entries 3.604604e+07
TOF MULT
TOF tray

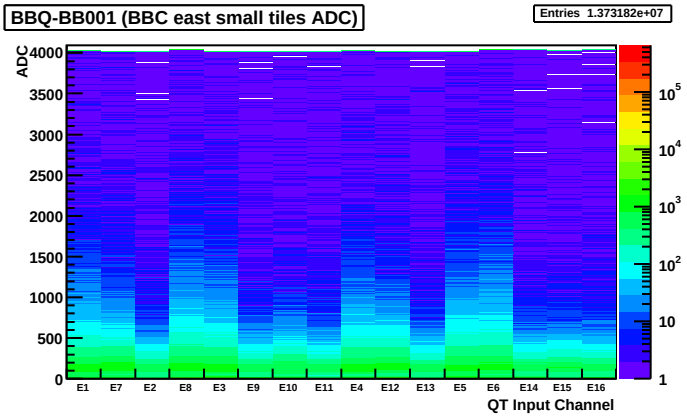

MIX-TF005
Entries 3.604604e+07
TOF MULT
TOF tray

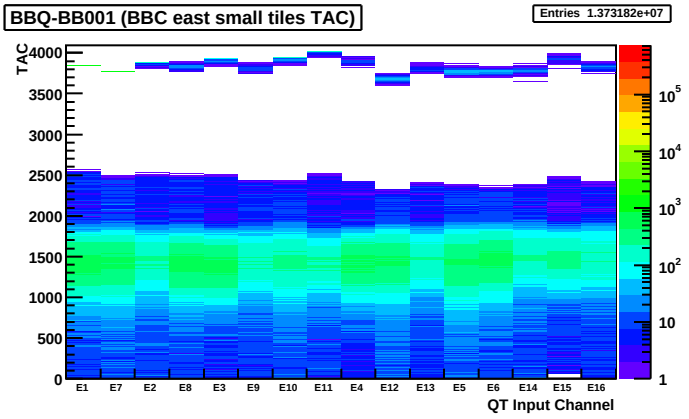

MIX-TF006
Entries 3.604604e+07
TOF MULT
TOF tray

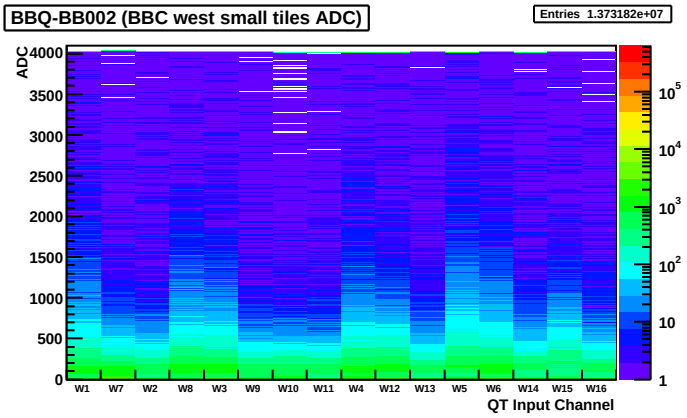

MIX-TF101
Entries 5149434
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

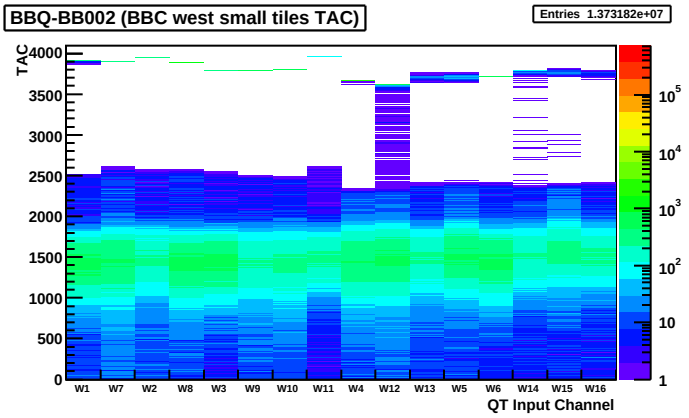

L1-TF201
Entries 858239
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

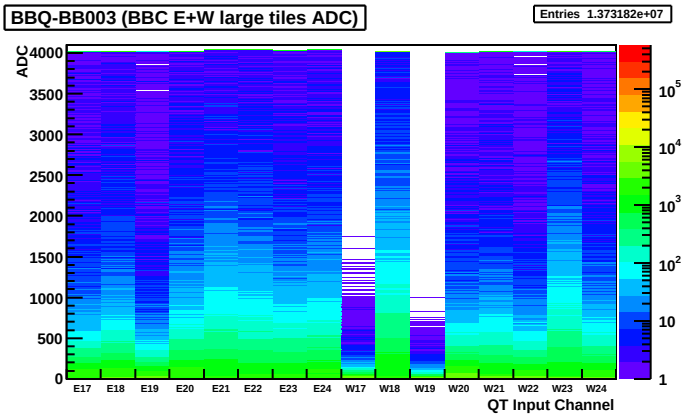

L1-TF201
Entries 5149434
Threshold bits
1
0
565317
570087
573434
565632
557132
535923
292922
288152
284805
292607
301107
322316
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

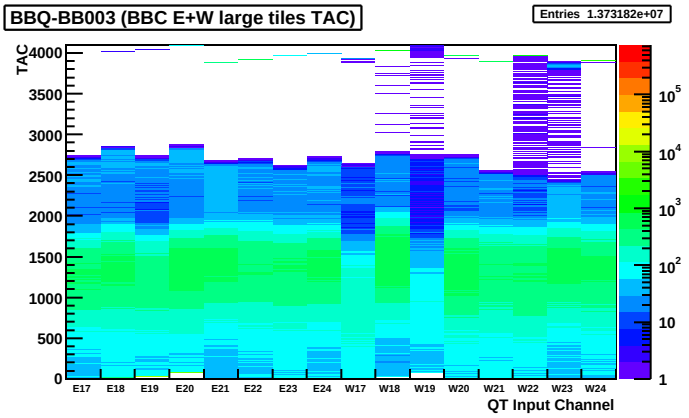

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.373182e+07
ADC
QT Input Channel

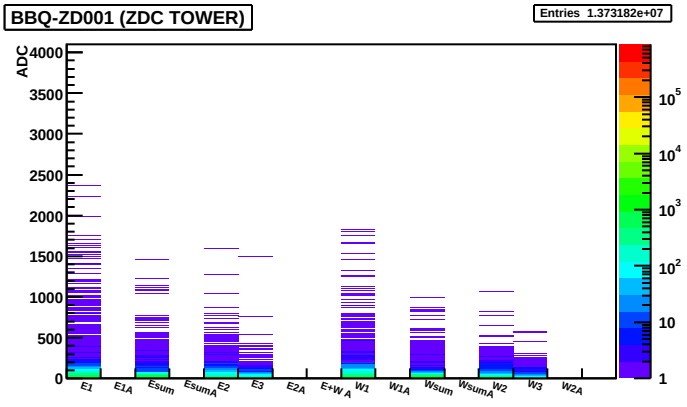

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.373182e+07
TAC
QT Input Channel

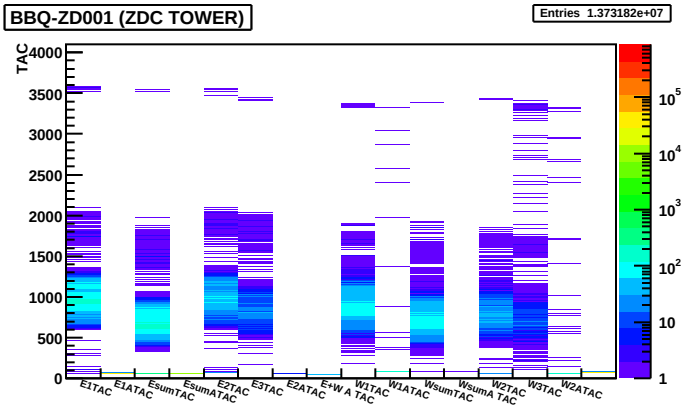

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.373182e+07
ADC
QT Input Channel

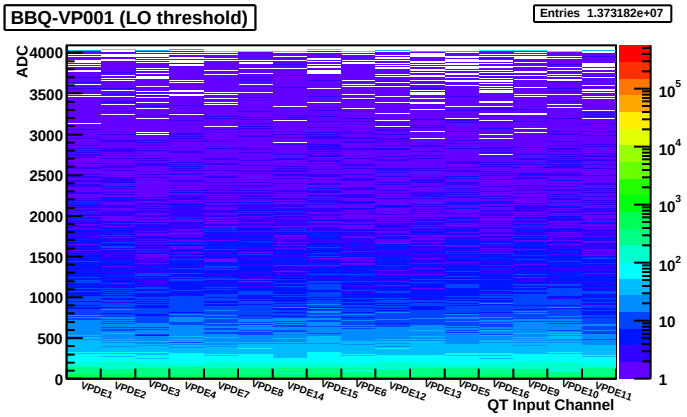

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.373182e+07
TAC
QT Input Channel

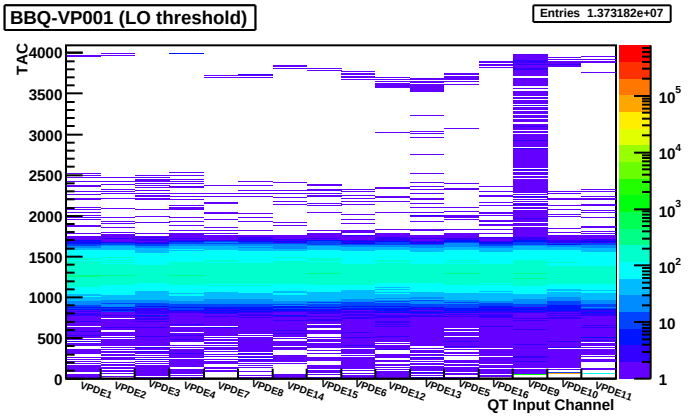

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.373182e+07
ADC
QT Input Channel

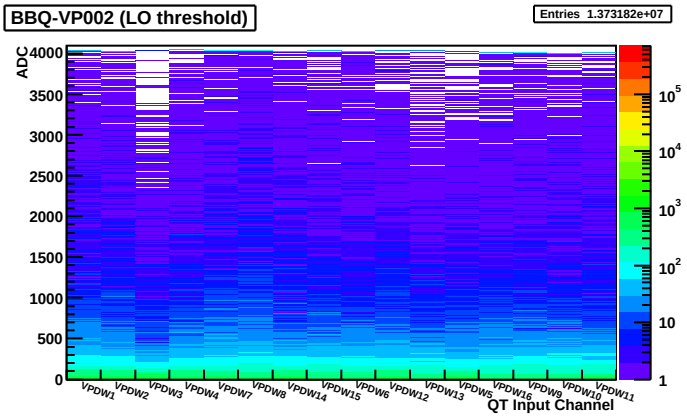

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.373182e+07
TAC
QT Input Channel

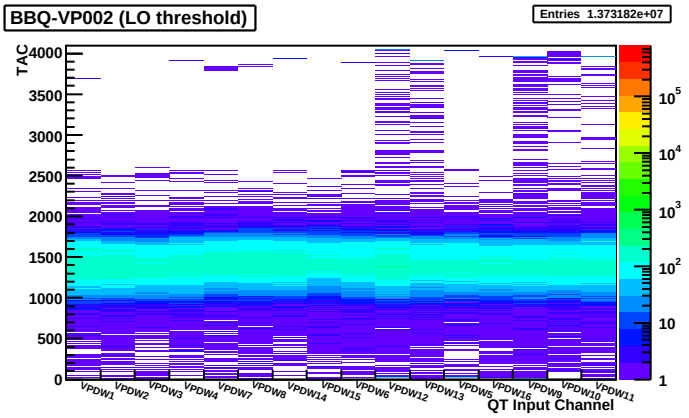

BBQ-ZD001 (ZDC TOWER)
Entries 1.373182e+07
ADC

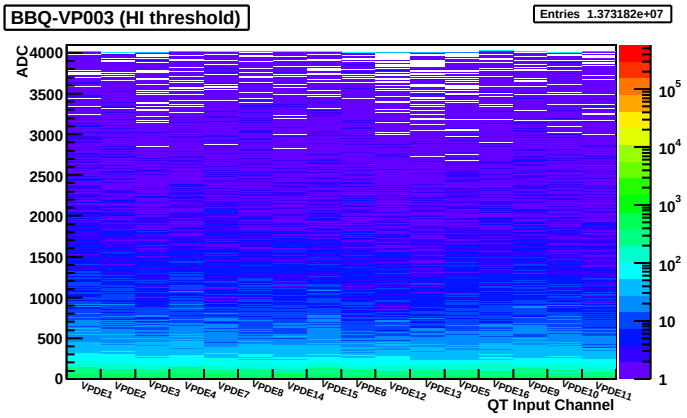

BBQ-ZD001 (ZDC TOWER)
Entries 1.373182e+07
TAC

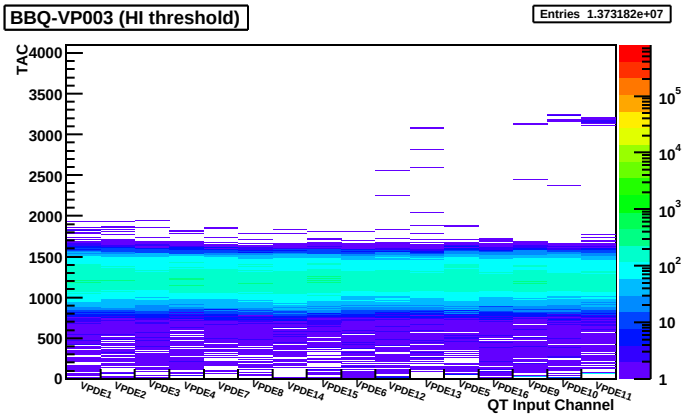

BBQ-VP001 (LO threshold)
Entries 1.373182e+07
ADC
QT Input Channel

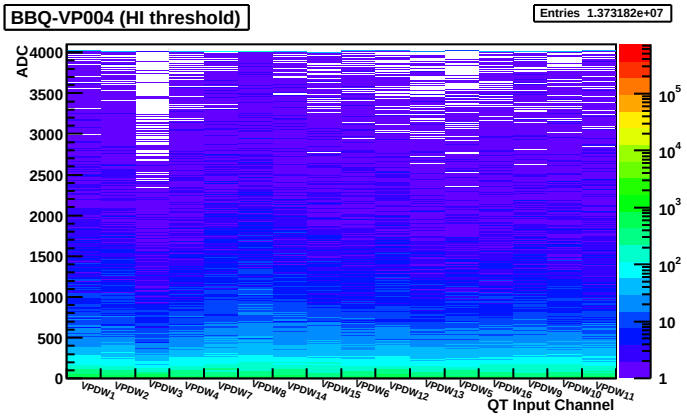

BBQ-VP001 (LO threshold)
Entries 1.373182e+07
TAC
QT Input Channel

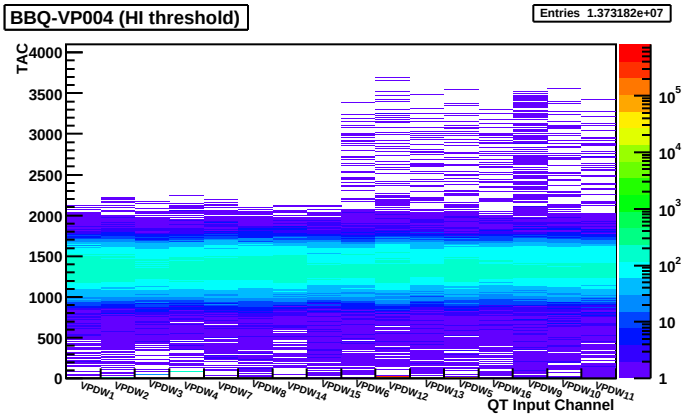

BBQ-VP002 (LO threshold)
Entries 1.373182e+07
ADC
QT Input Channel

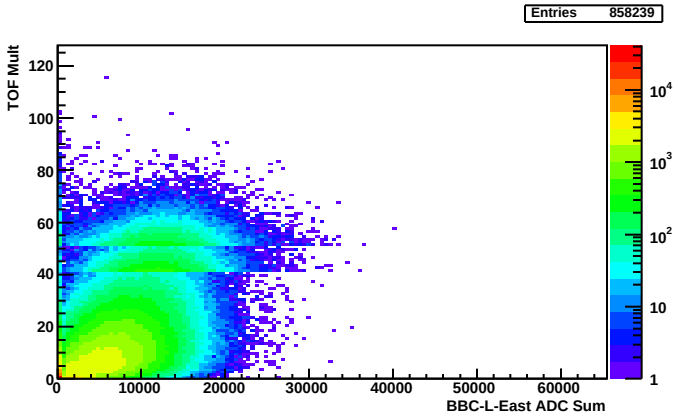

BBQ-VP002 (LO threshold)
Entries 1.373182e+07
TAC
QT Input Channel

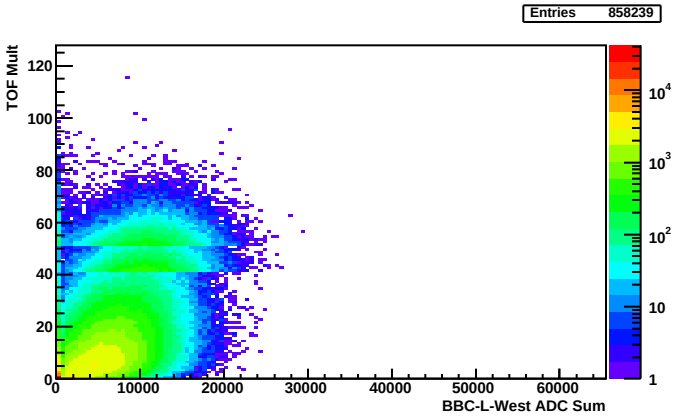

BBQ-VP003 (HI threshold)
Entries 1.373182e+07
ADC
QT Input Channel

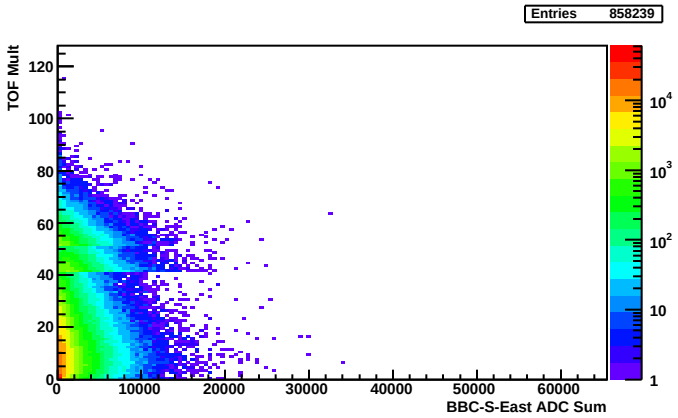

BBQ-VP003 (HI threshold)
Entries 1.373182e+07
TAC
QT Input Channel

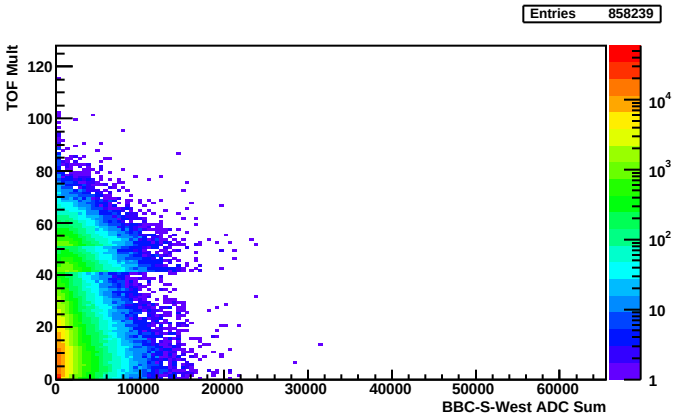

BBQ-VP004 (HI threshold)
Entries 1.373182e+07
ADC
QT Input Channel

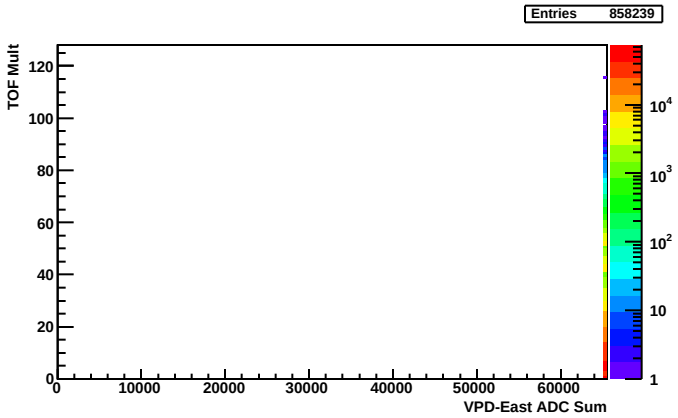

BBQ-VP004 (HI threshold)
Entries 1.373182e+07
TAC
QT Input Channel

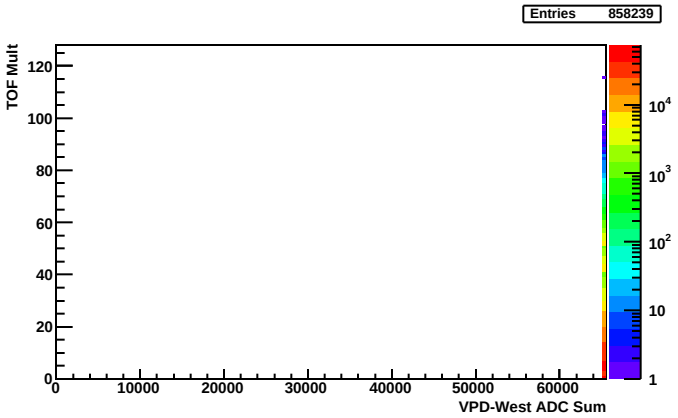

Entries 858239
TOF Mult
BBC-L-East ADC Sum

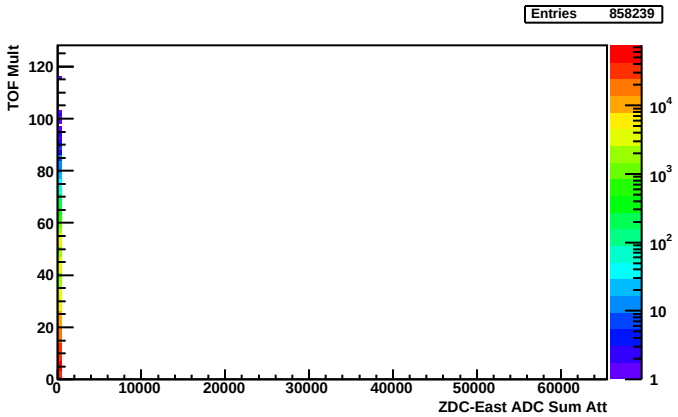

Entries 858239
TOF Mult
BBC-L-West ADC Sum

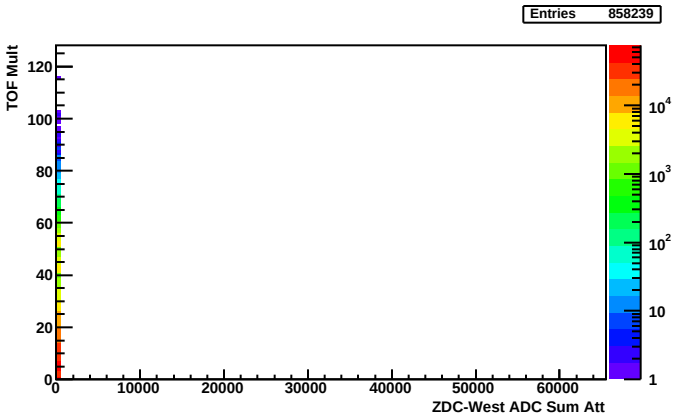

Entries 858239
TOF Mult
BBC-S-East ADC Sum

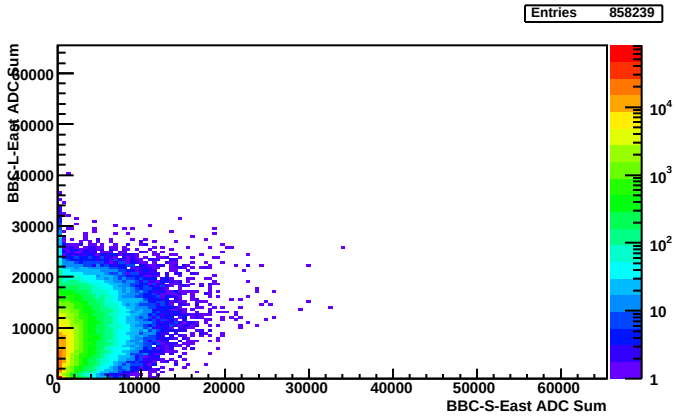

Entries 858239
TOF Mult
BBC-S-West ADC Sum

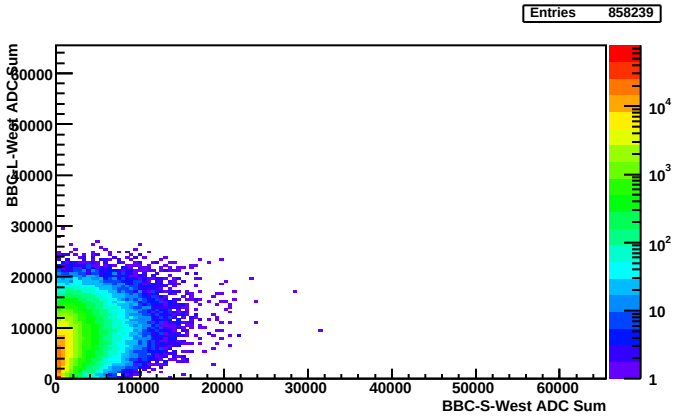

Entries 858239
TOF Mult
VPD-East ADC Sum

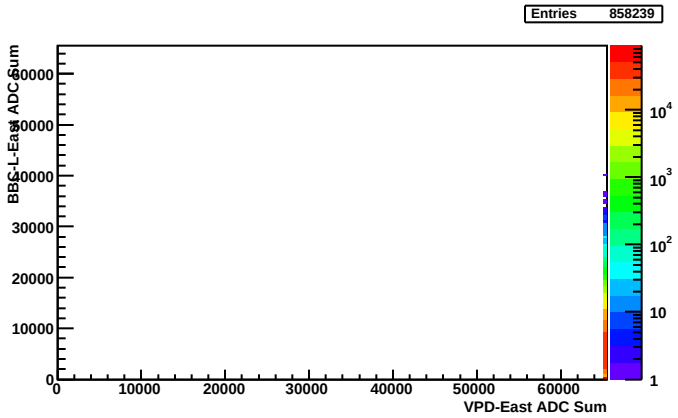

Entries 858239
TOF Mult
VPD-West ADC Sum


Entries 858239
TOF Mult
ZDC-East ADC Sum Att


Entries 858239
TOF Mult
ZDC-West ADC Sum Att


Entries 858239
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 858239
BBC-L-West ADC Sum
BBC-S-West ADC Sum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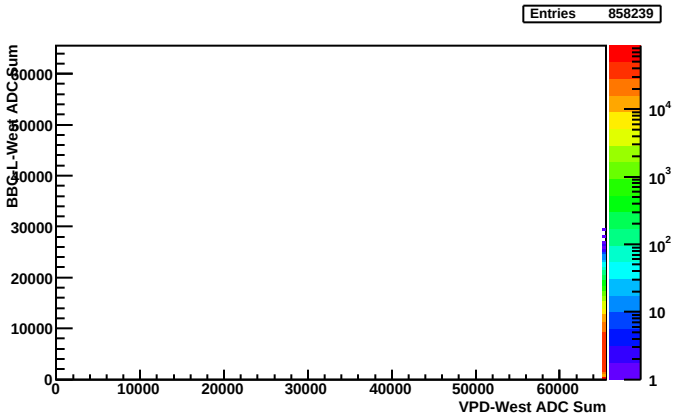
BBC-L-East ADC Sum
VPD-East ADC Sum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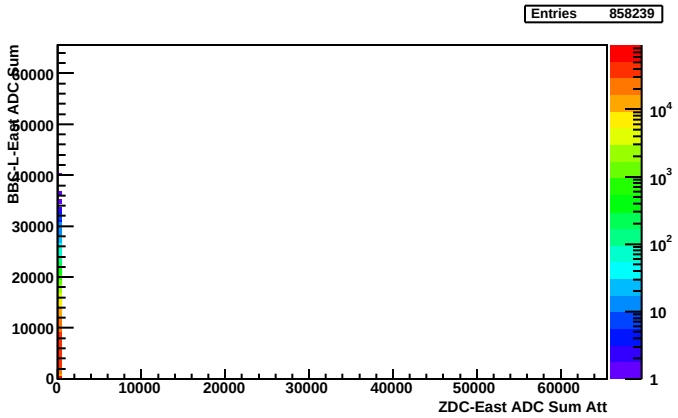
BBC-L-West ADC Sum
VPD-West ADC Sum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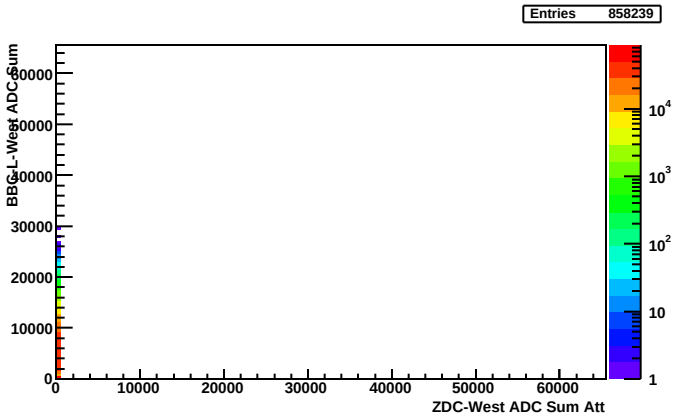
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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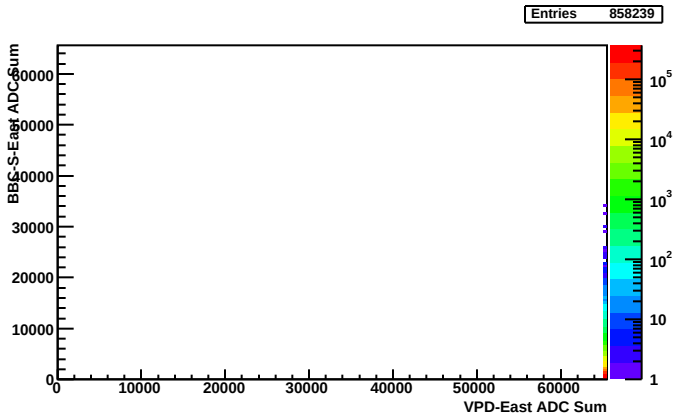
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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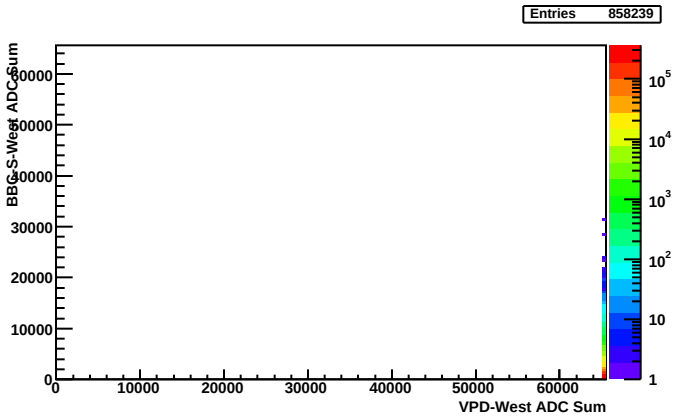
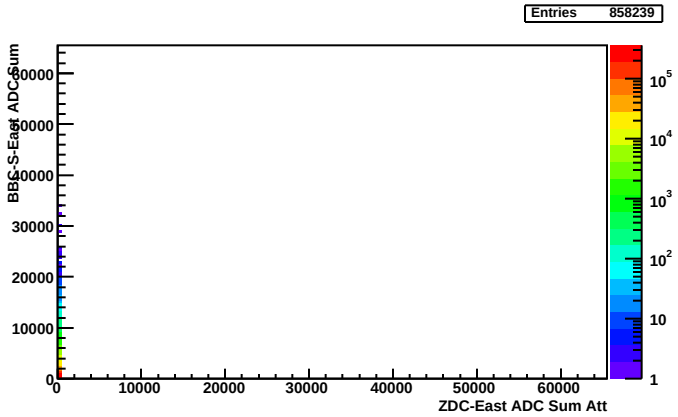
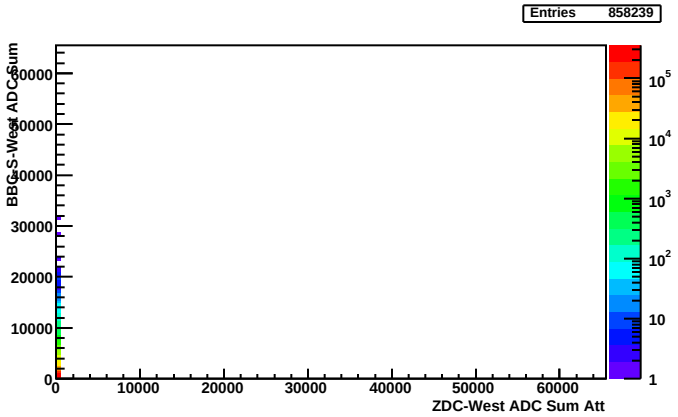
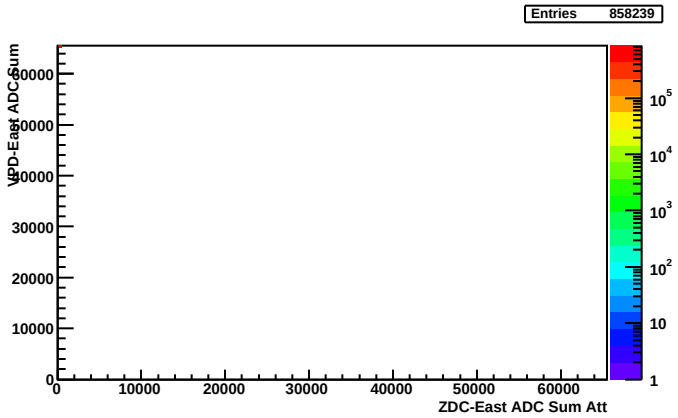
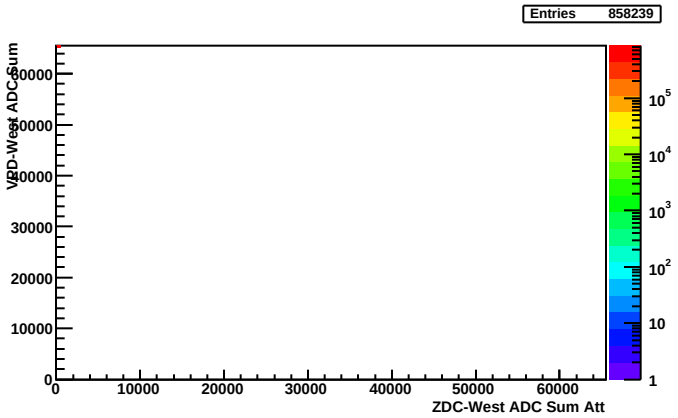
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 858239
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 858239
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 858239
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 858239
VPD-East ADC Sum
ZDC-East ADC Sum Att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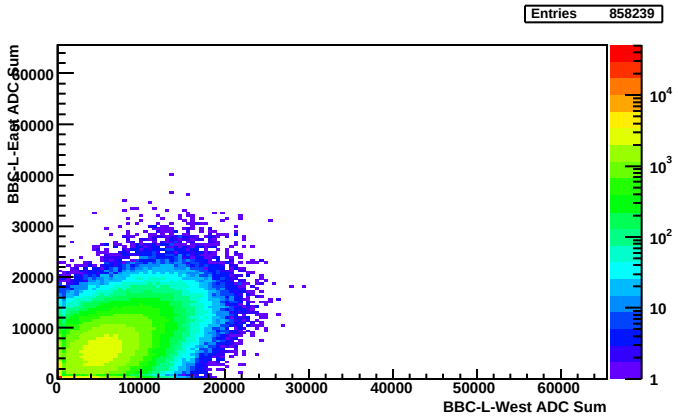
VPD-West ADC Sum
ZDC-West ADC Sum Att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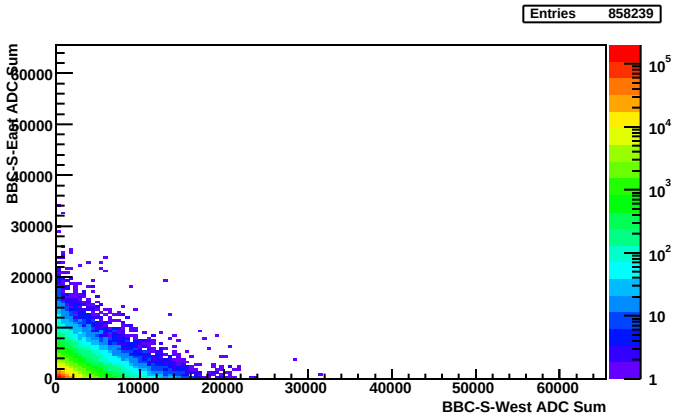
BBC-L-East ADC Sum
BBC-L-West ADC Sum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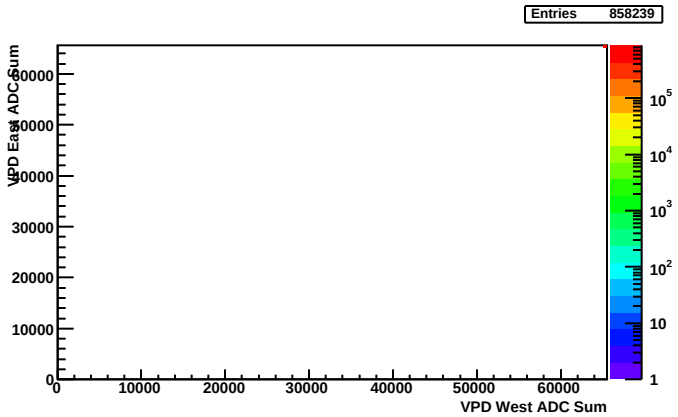
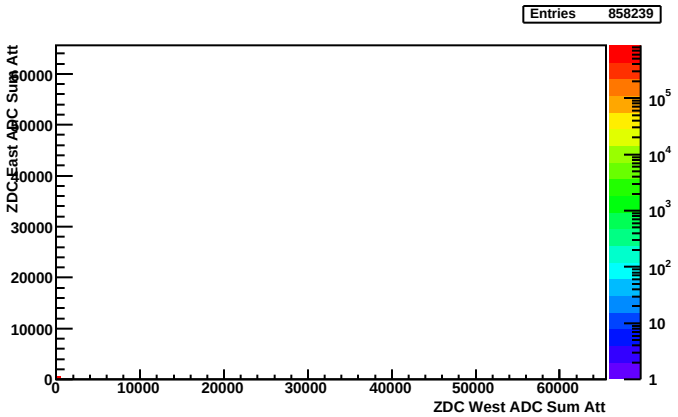
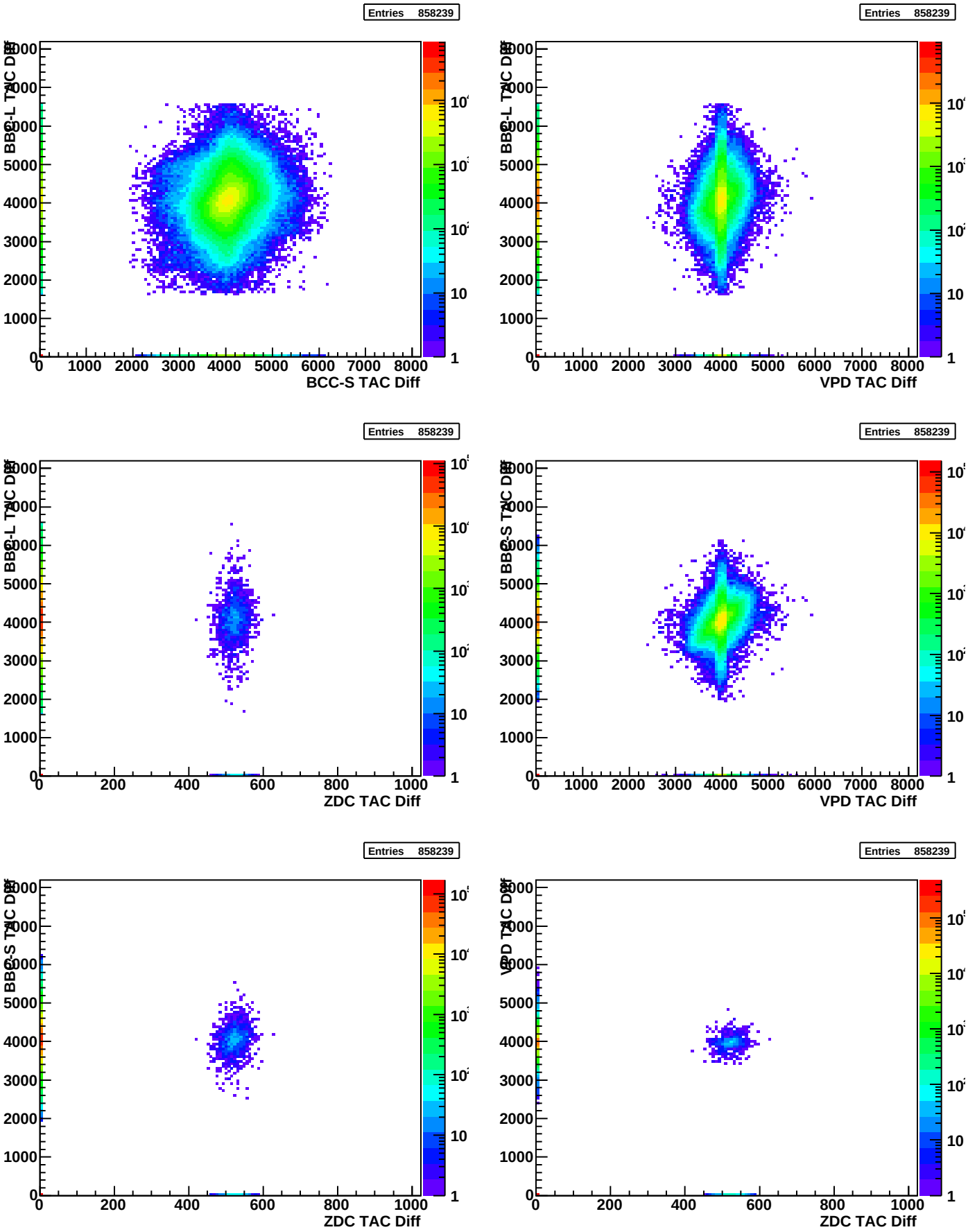
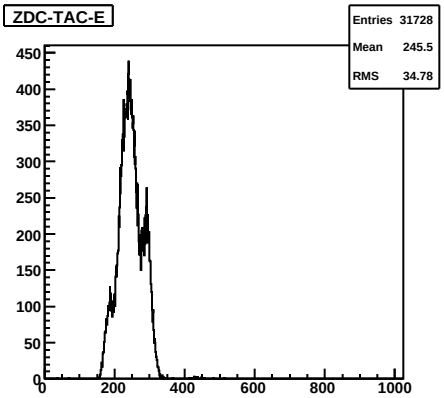
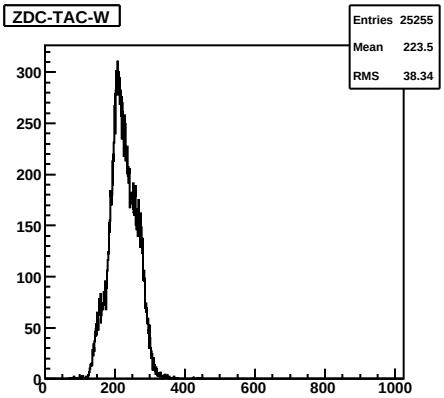
BBC-S-East ADC Sum
BBC-S-West ADC Sum

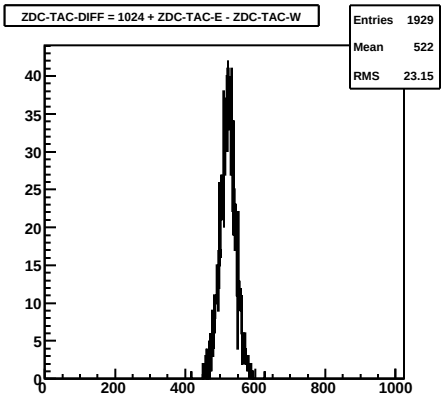

Entries 858239
VPD East ADC Sum
VPD West ADC Sum

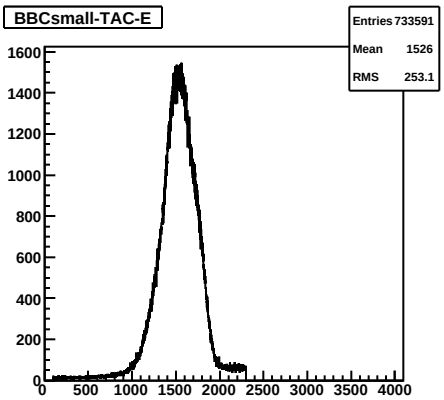

Entries 858239
ZDC East ADC Sum Att
ZDC West ADC Sum Att

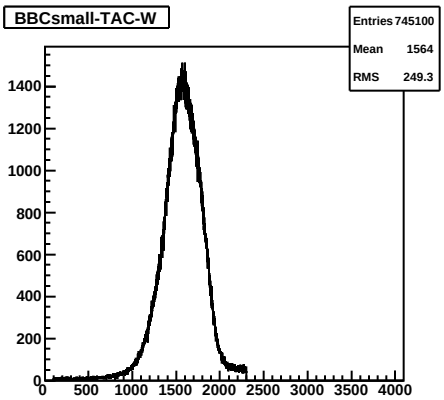

Entries 858239
BBC-L TAC Diff
BCC-S TAC Diff
Entries 858239
BBC-L TAC Diff
VPD TAC Diff
Entries 858239
BBC-L TAC Diff
ZDC TAC Diff
Entries 858239
BBC-S TAC Diff
VPD TAC Diff
Entries 858239
BBC-S TAC Diff
ZDC TAC Diff
Entries 858239
VPD TAC Diff
ZDC TAC Diff

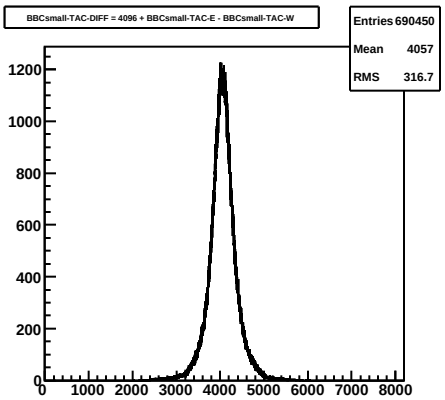

ZDC-TAC-E
Entries 31728
Mean 245.5
RMS 34.78

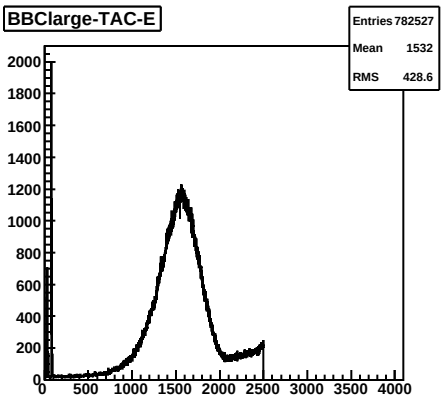

ZDC-TAC-W
Entries 25255
Mean 223.5
RMS 38.34

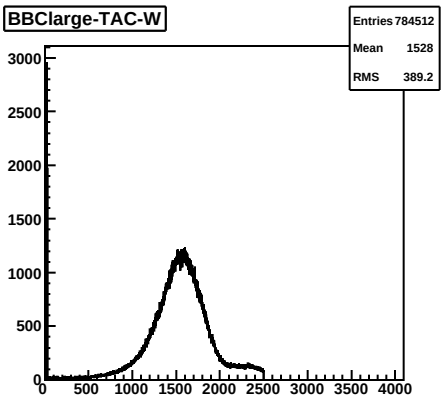

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1929
Mean 522
RMS 23.15

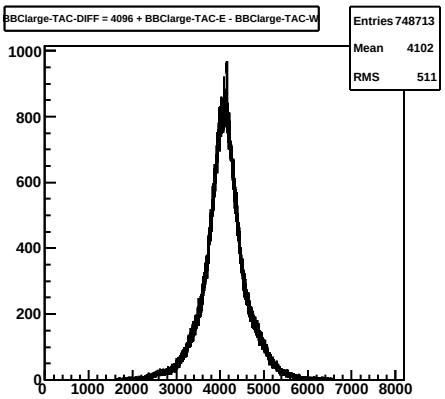

BBCsmall-TAC-E
Entries 733591
Mean 1526
RMS 253.1

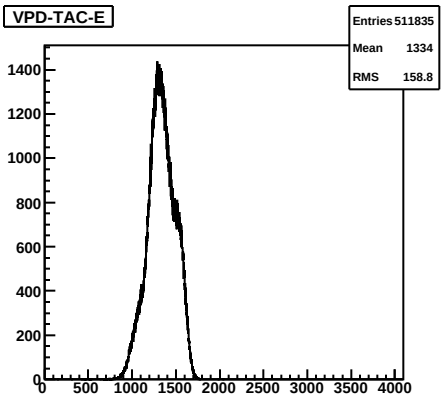

BBCsmall-TAC-W
Entries 745100
Mean 1564
RMS 249.3

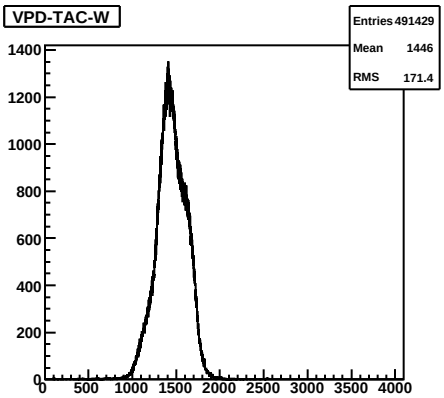

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 690450
Mean 4057
RMS 316.7

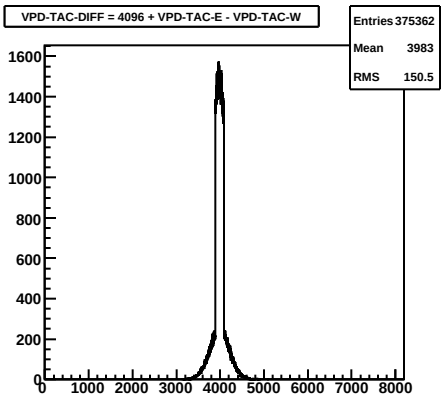

BBClarge-TAC-E
Entries 782527
Mean 1532
RMS 428.6

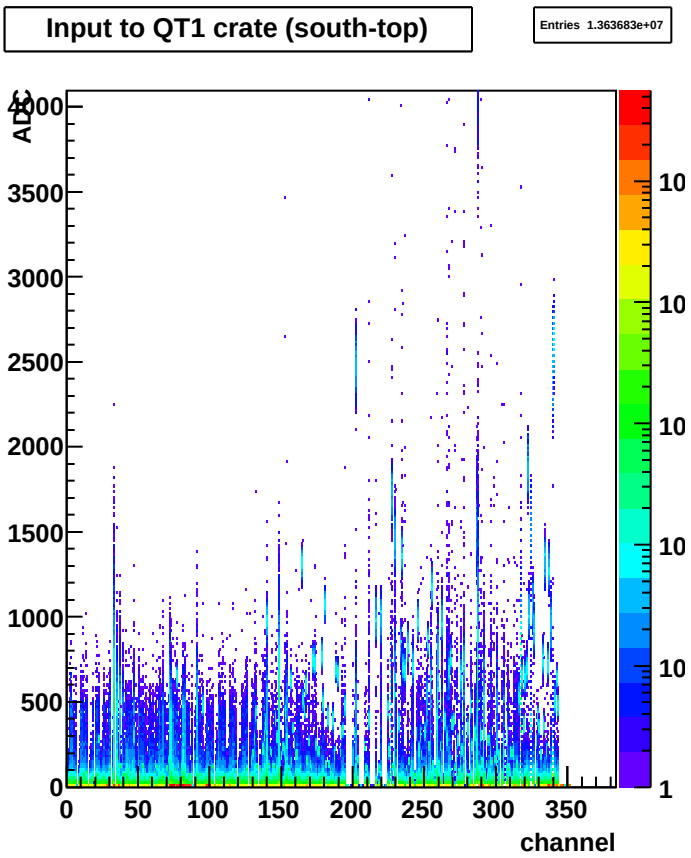

BBClarge-TAC-W
Entries 784512
Mean 1528
RMS 389.2

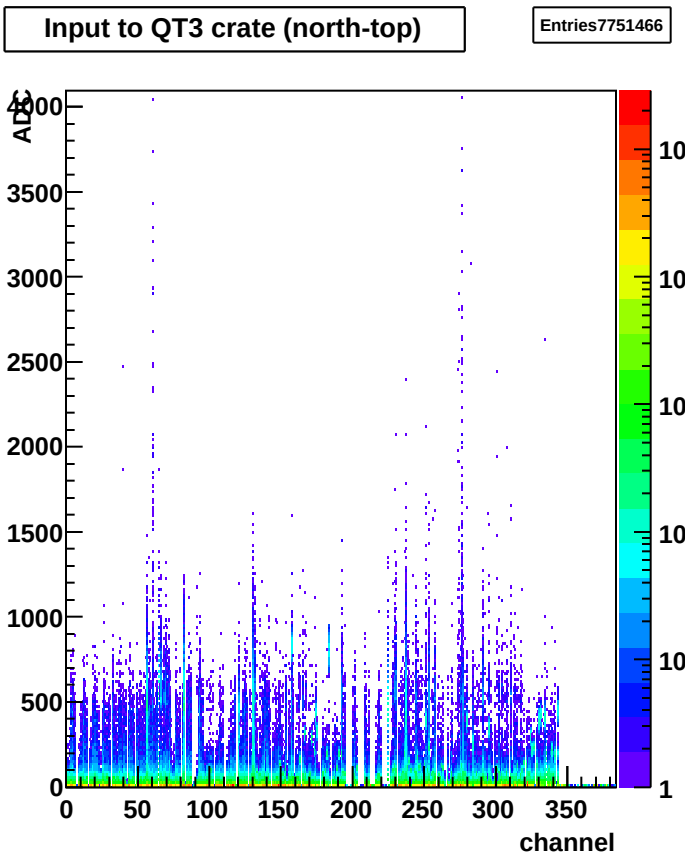

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 748713
Mean 4102
RMS 511

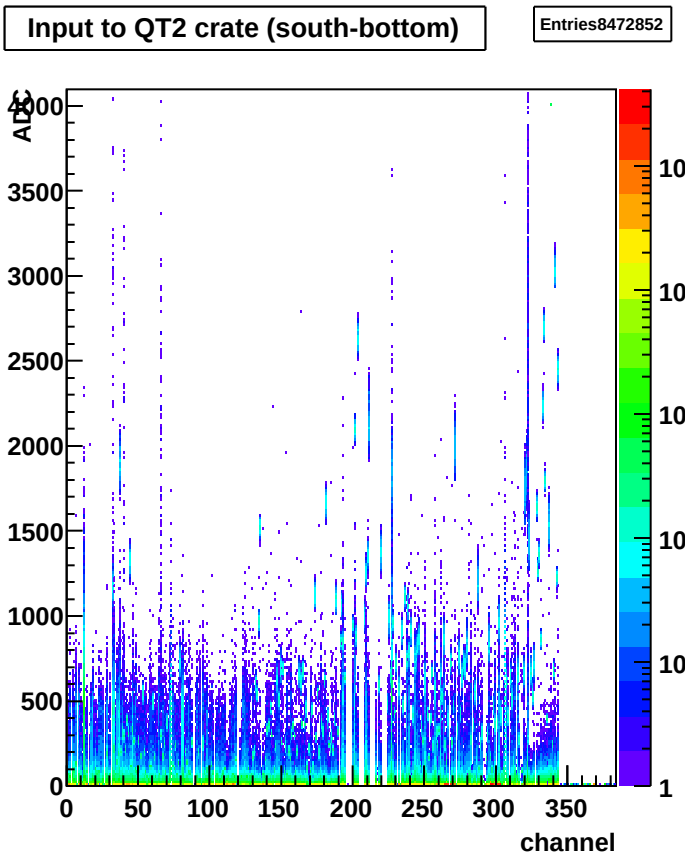

VPD-TAC-E
Entries 511835
Mean 1334
RMS 158.8

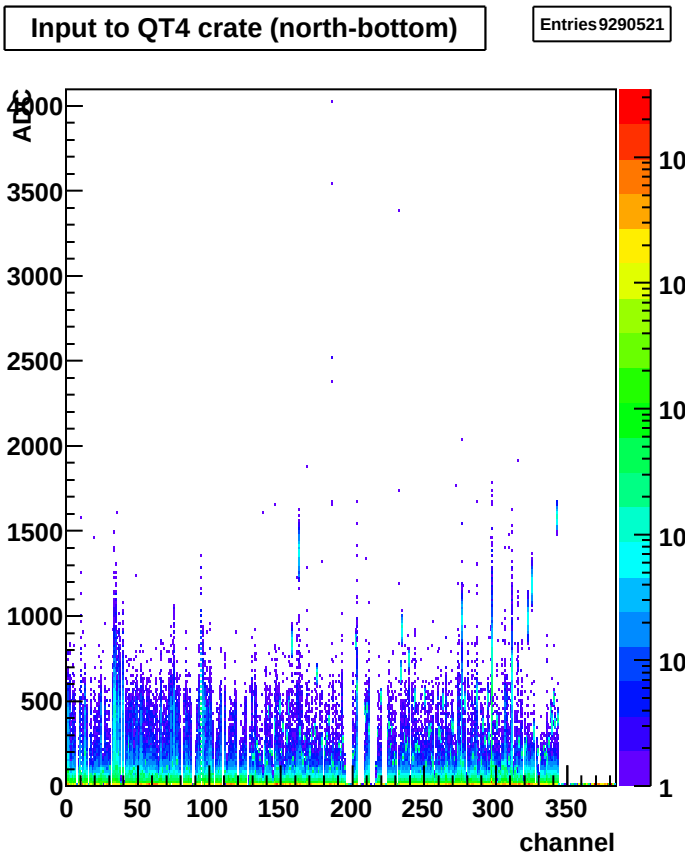

VPD-TAC-W
Entries 491429
Mean 1446
RMS 171.4

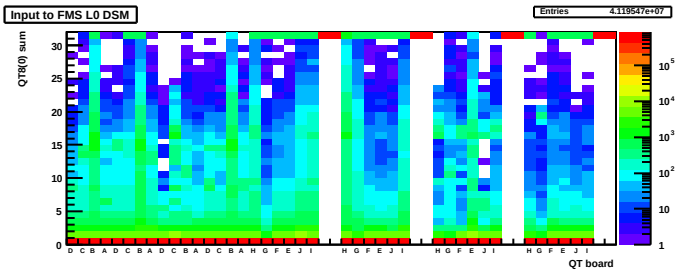

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 375362
Mean 3983
RMS 150.5

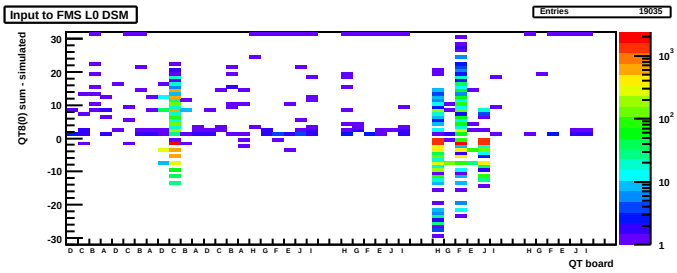

Input to QT1 crate (south-top)
Entries 1.363683e+07
ADC
channel

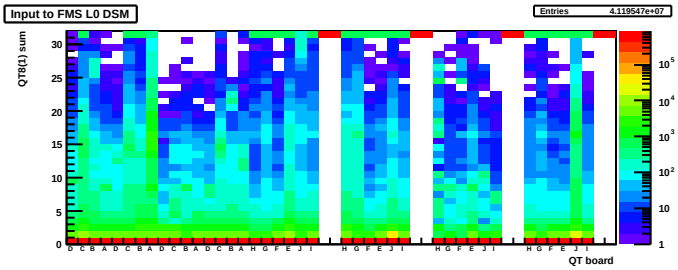

Input to QT3 crate (north-top)
Entries7751466
ADC
channel

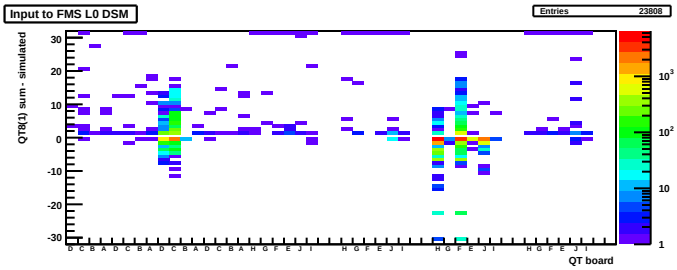

Input to QT2 crate (south-bottom)
Entries8472852
ADC
channel

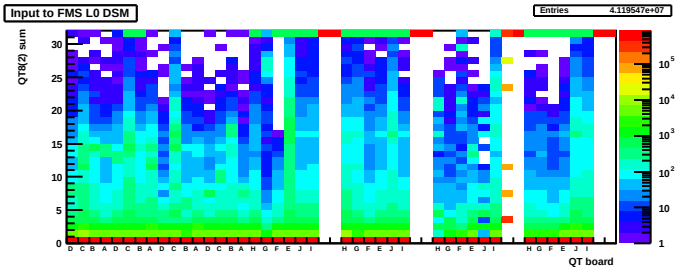

Input to QT4 crate (north-bottom)
Entries9290521
ADC
channel

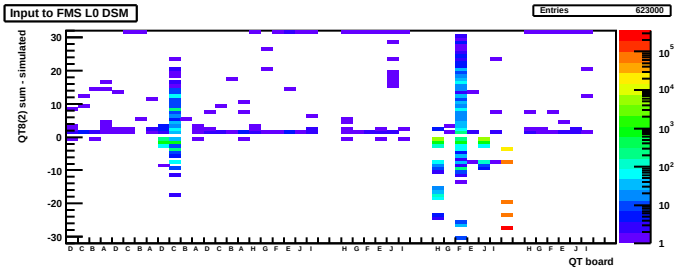

Input to FMS L0 DSM
Entries 4.119547e+07
QT8(0) sum
QT board

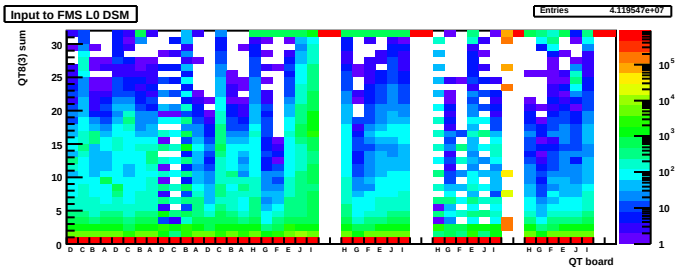

Input to FMS L0 DSM
Entries 19035
QT8(0) sum - simulated
QT board

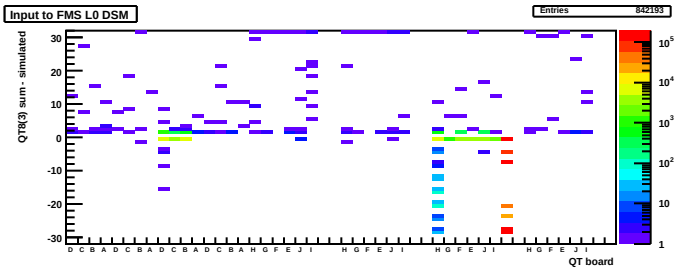

Input to FMS L0 DSM
Entries 4.119547e+07
QT8(1) sum
QT board

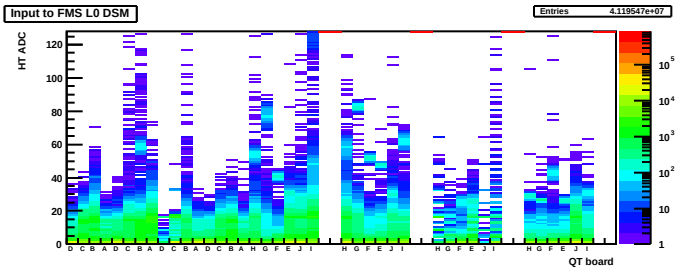

Input to FMS L0 DSM
Entries 23808
QT8(1) sum - simulated
QT board

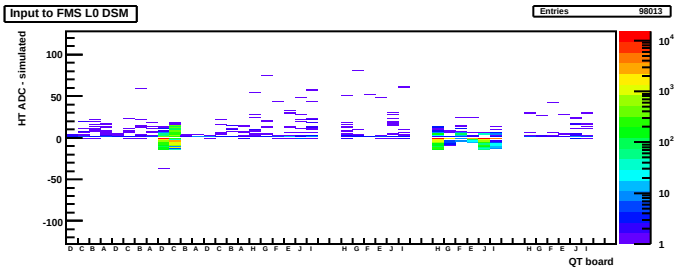

Input to FMS L0 DSM
Entries 4.119547e+07
QT8(2) sum
QT board

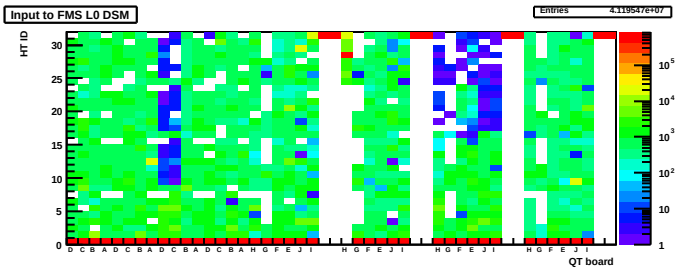

Input to FMS L0 DSM
Entries 623000
QT8(2) sum - simulated
QT board

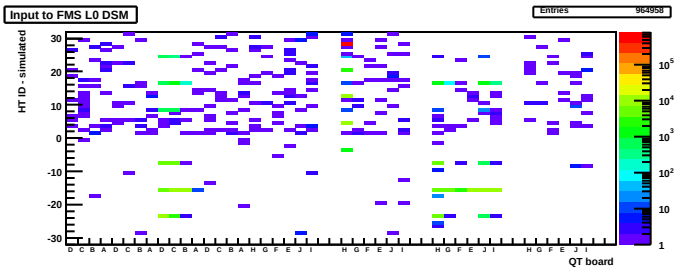

Input to FMS L0 DSM
Entries 4.119547e+07
QT8(3) sum
QT board

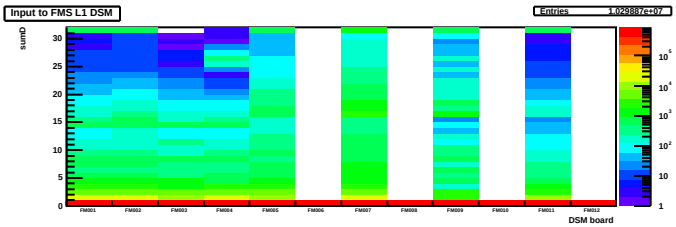

Input to FMS L0 DSM
Entries 842193
QT8(3) sum - simulated
QT board

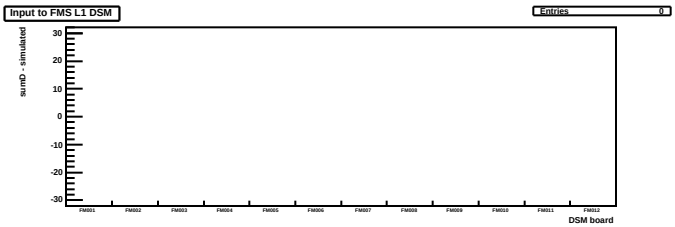

Input to FMS L0 DSM
Entries 4.119547e+07
HT ADC
QT board

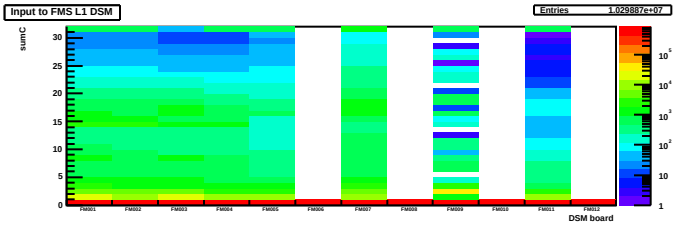

Input to FMS L0 DSM
Entries 98013
HT ADC - simulated
QT board

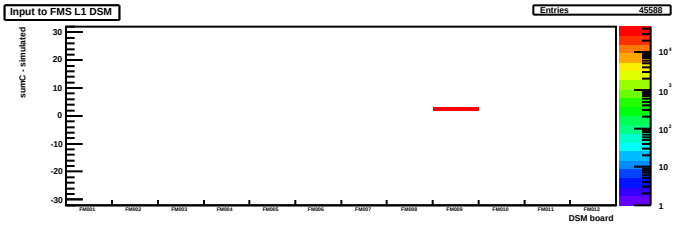

Input to FMS L0 DSM
Entries 4.119547e+07
HT ID
QT board

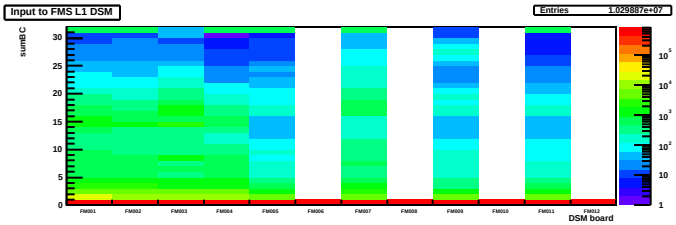

Input to FMS L0 DSM
Entries 964958
HT ID - simulated
QT board

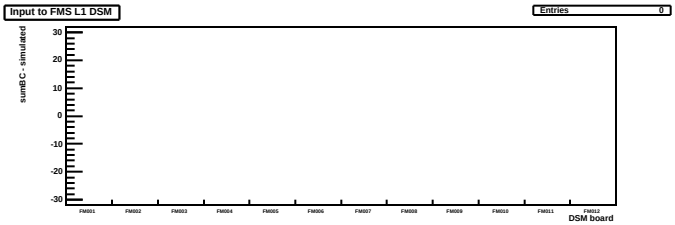

Input to FMS L1 DSM
Entries 1.029887e+07
sumD
DSM board

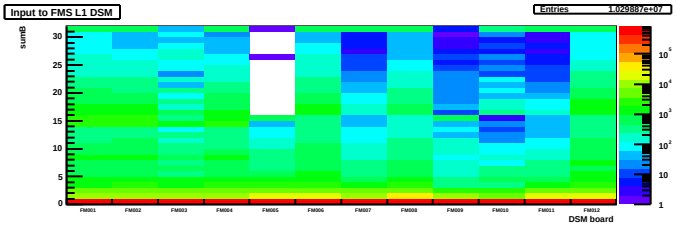

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

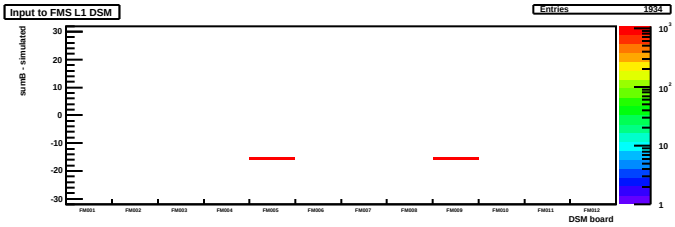

Input to FMS L1 DSM
Entries 1.029887e+07
sumC
DSM board

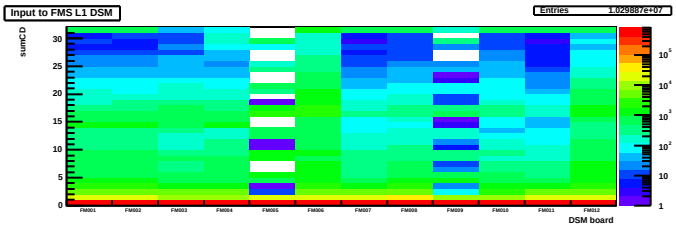

Input to FMS L1 DSM
Entries 45588
sumC - simulated
DSM board

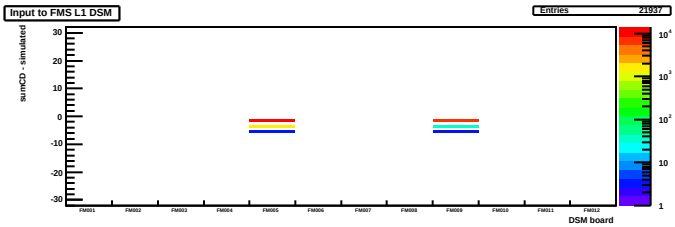

Input to FMS L1 DSM
Entries 1.029887e+07
sumBC
DSM board

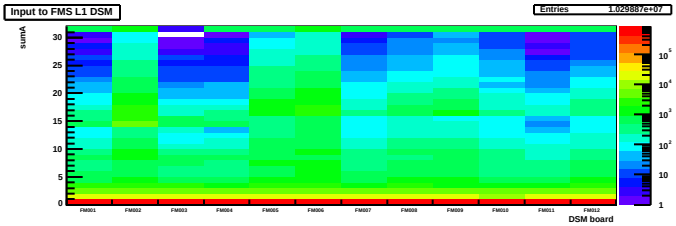

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

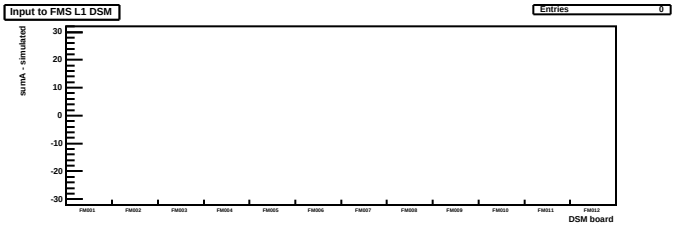

Input to FMS L1 DSM
Entries 1.029887e+07
sumB
DSM board

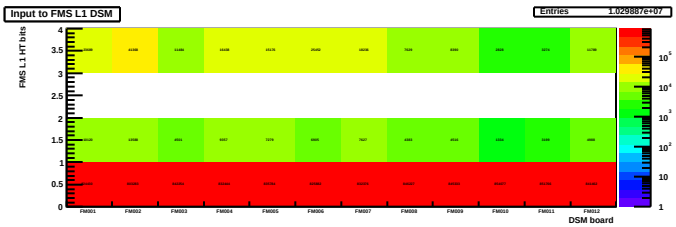

Input to FMS L1 DSM
Entries 1934
sumB - simulated
DSM board

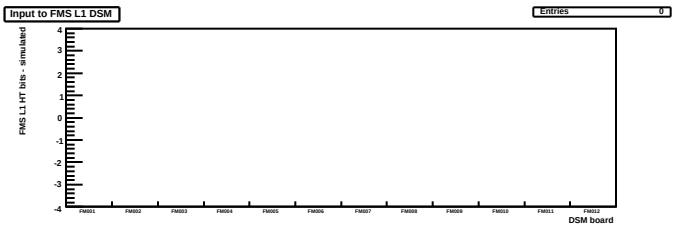

Input to FMS L1 DSM
Entries 1.029887e+07
sumCD
DSM board

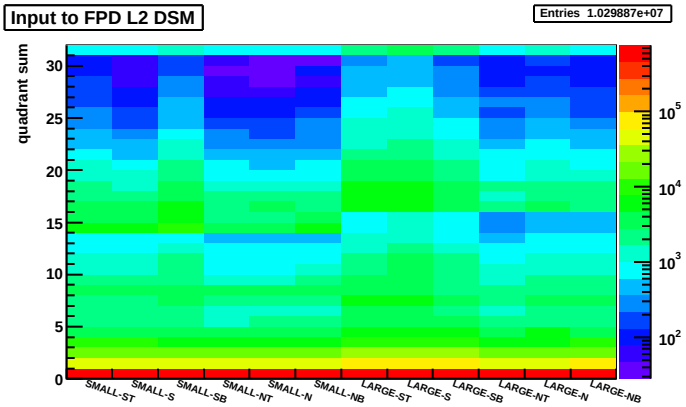

Input to FMS L1 DSM
Entries 21937
sumCD - simulated
DSM board

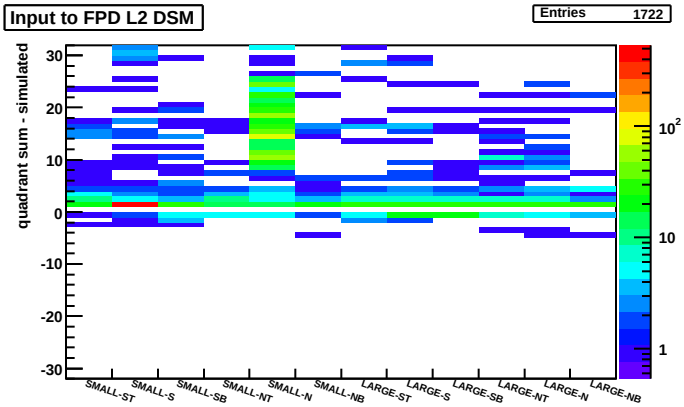

Input to FMS L1 DSM
Entries 1.029887e+07
sumA
DSM board

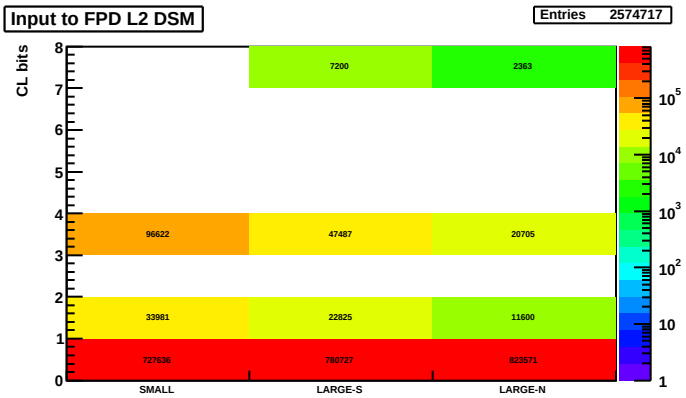

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

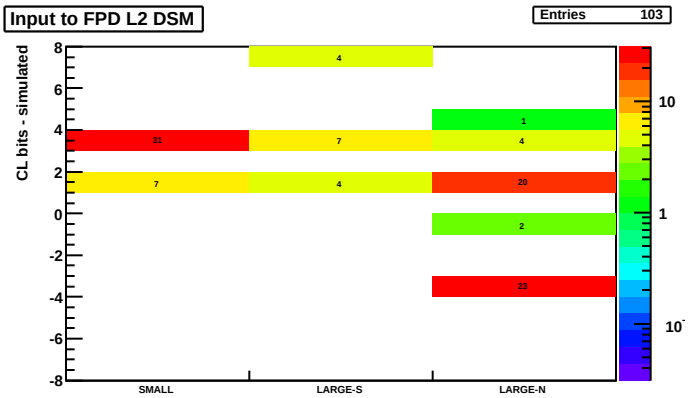

Input to FMS L1 DSM
Entries 1.029887e+07
FMS L1 HT bits
DSM board

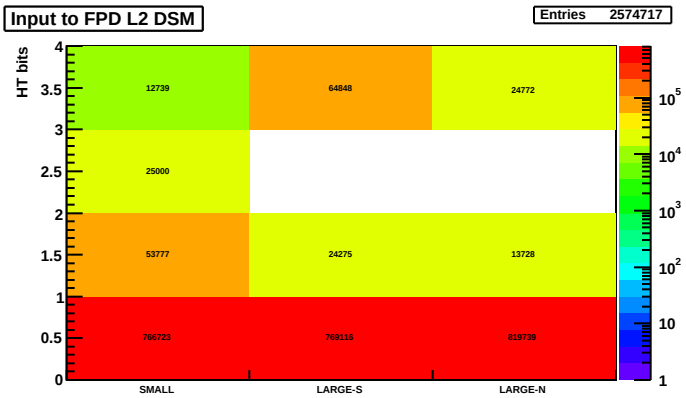

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

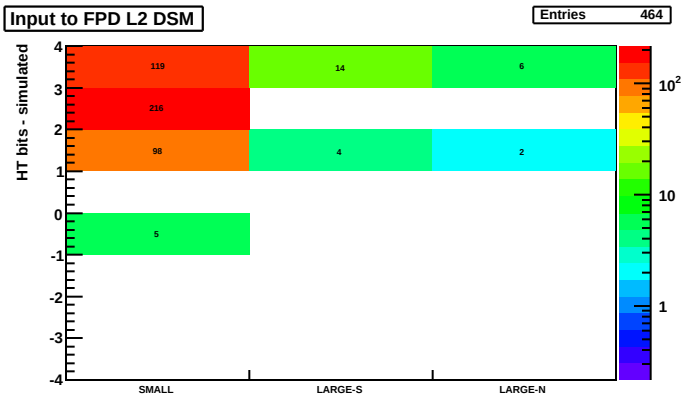

Input to FPD L2 DSM
Entries 1.029887e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

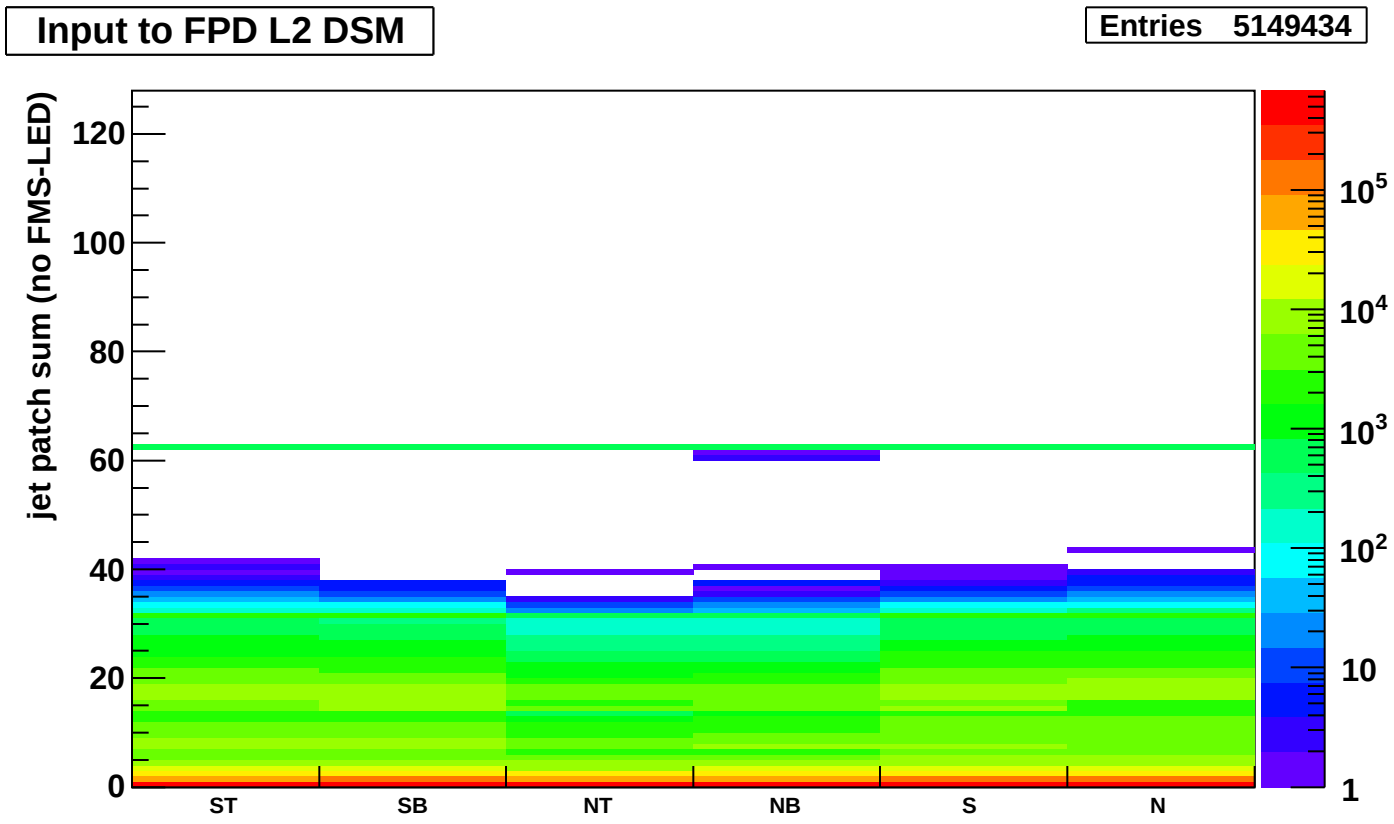

Input to FPD L2 DSM
Entries 1722
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

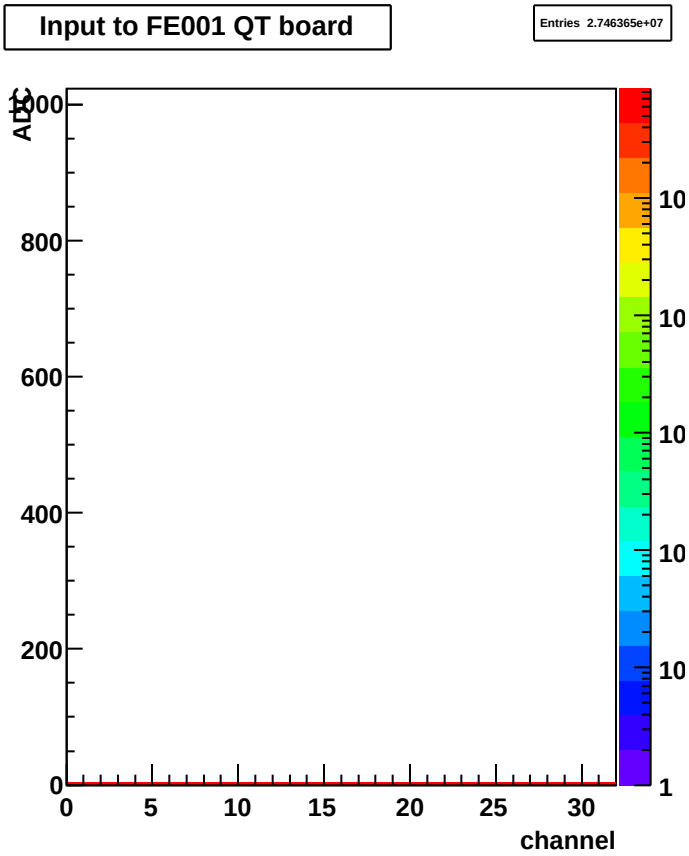

Input to FPD L2 DSM
Entries 2574717
CL bits
7200
2363
96622
47487
20705
33981
22825
11600
727636
780727
823571
SMALL
LARGE-S
LARGE-N

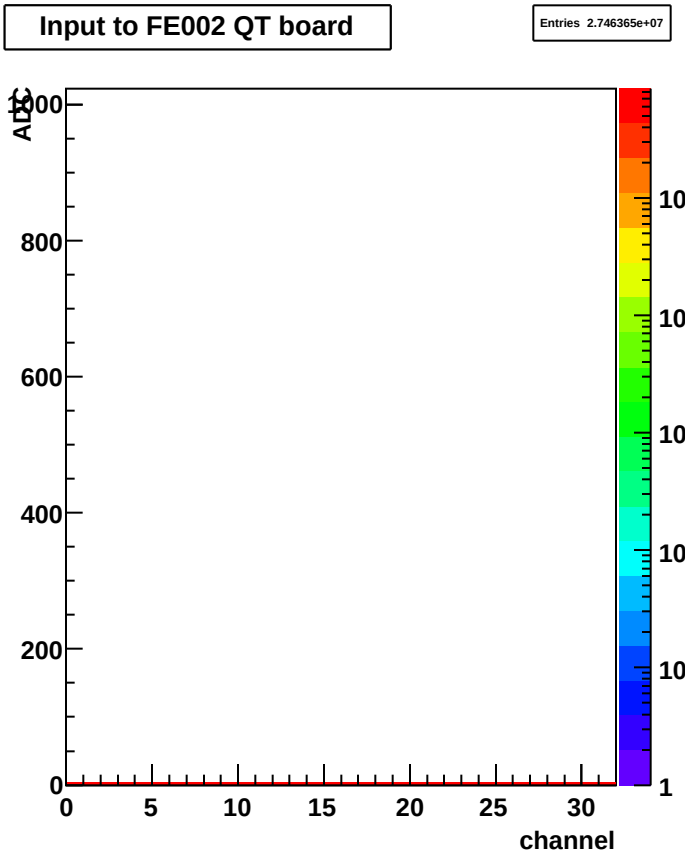

Input to FPD L2 DSM
Entries 103
CL bits - simulated
4
1
31
7
4
7
4
20
2
23
SMALL
LARGE-S
LARGE-N

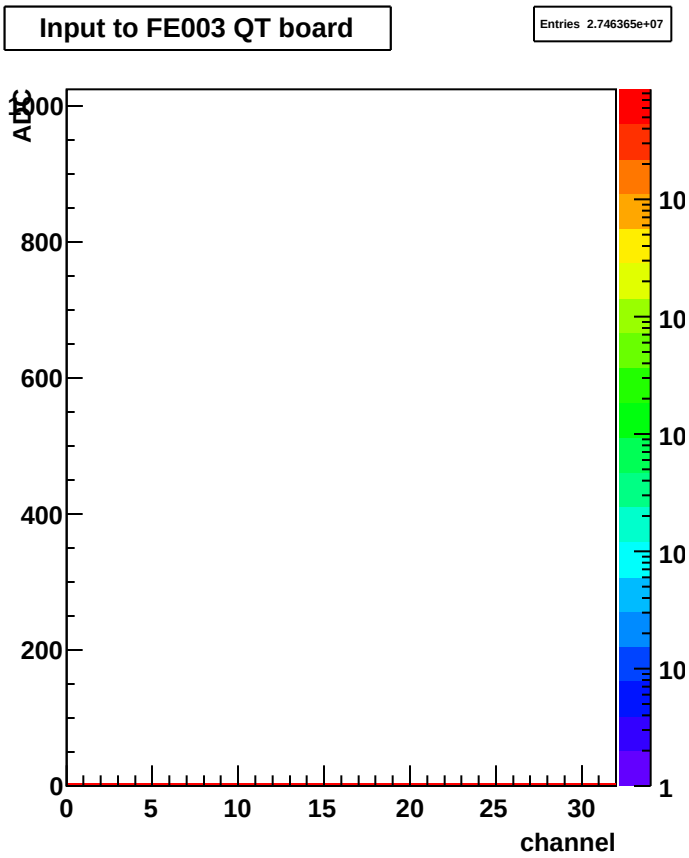

Input to FPD L2 DSM
Entries 2574717
HT bits
12739
64848
24772
25000
53777
24275
13728
766723
769116
819739
SMALL
LARGE-S
LARGE-N

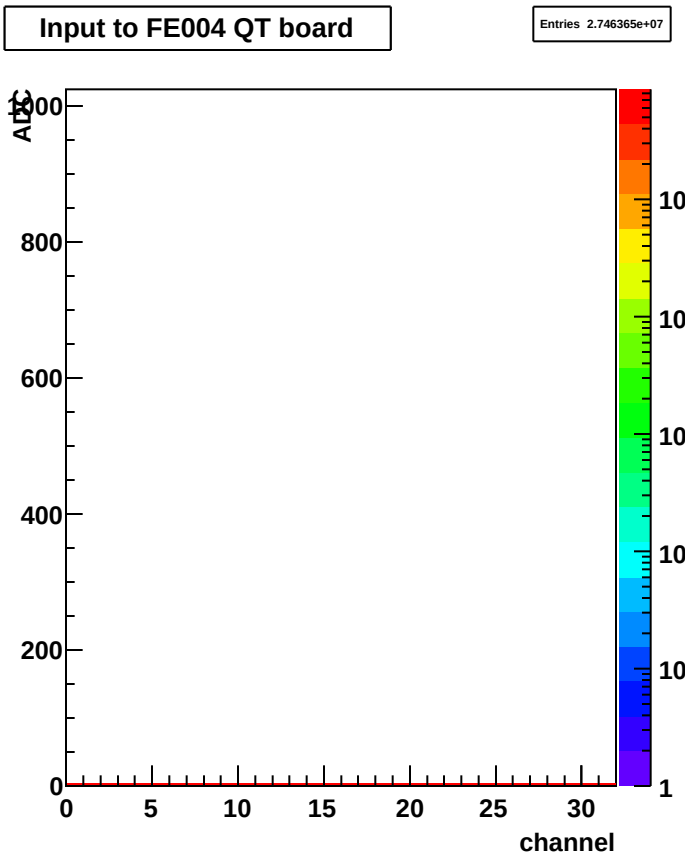

Input to FPD L2 DSM
Entries 464
HT bits - simulated
119
14
6
216
98
4
2
5
SMALL
LARGE-S
LARGE-N

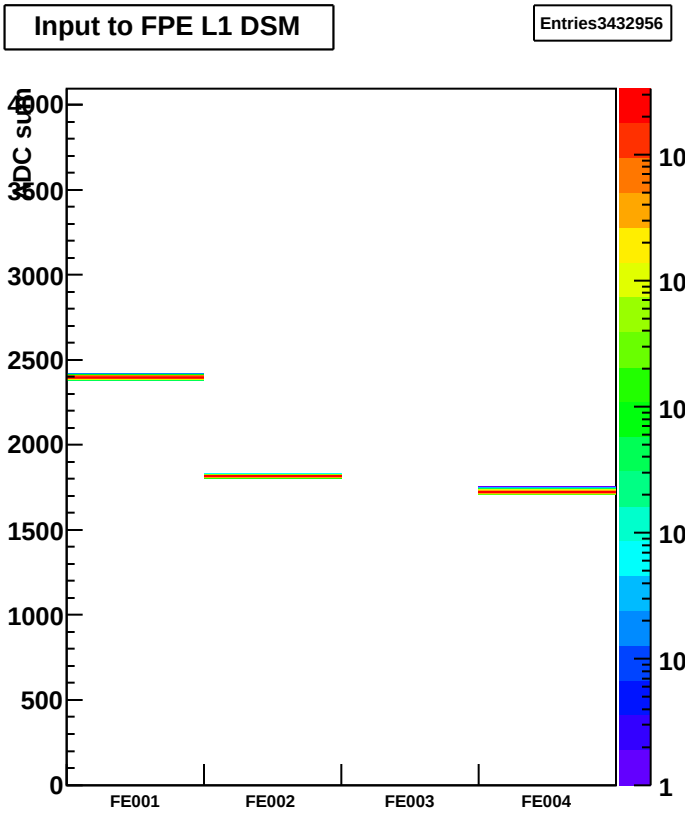

Input to FPD L2 DSM
Entries 5149434
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

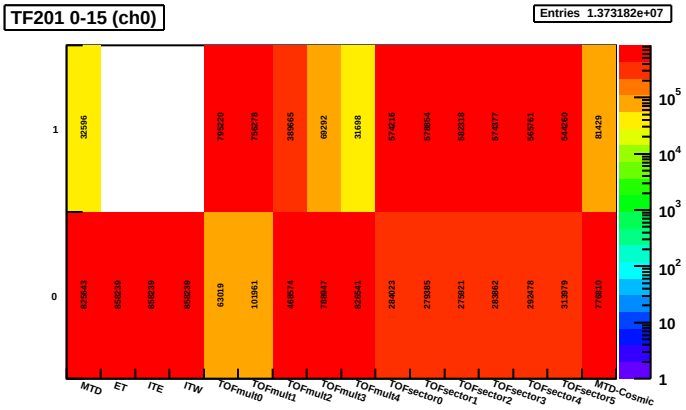

Input to FE001 QT board
Entries 2.746365e+07
ADC
channel

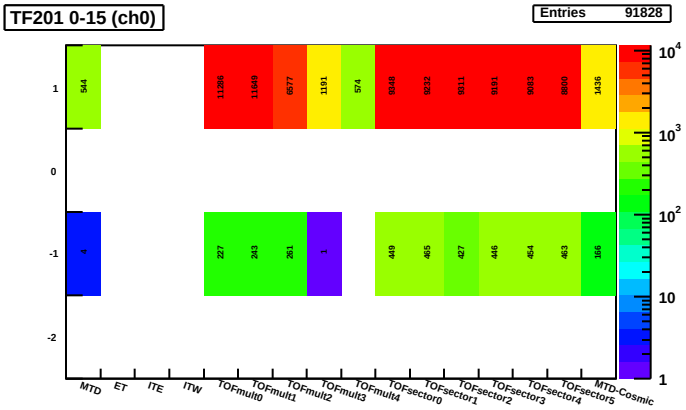

Input to FE002 QT board
Entries 2.746365e+07
ADC
channel

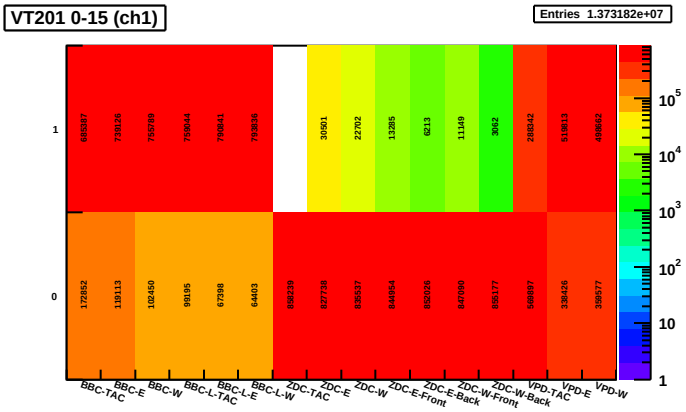

Input to FE003 QT board
Entries 2.746365e+07
ADC
channel

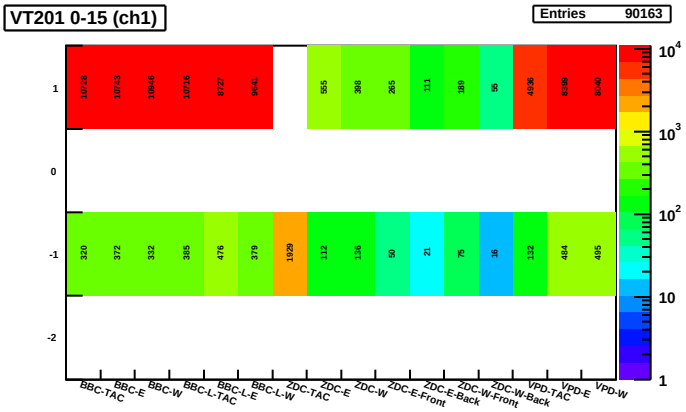

Input to FE004 QT board
Entries 2.746365e+07
ADC
channel

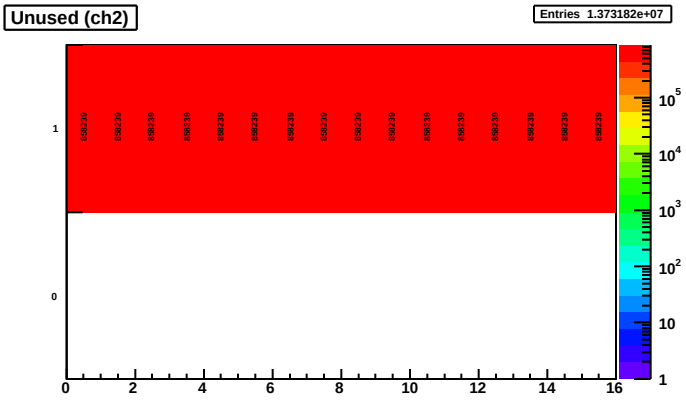

Input to FPE L1 DSM
Entries3432956
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 1.373182e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 91828
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 1.373182e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 90163
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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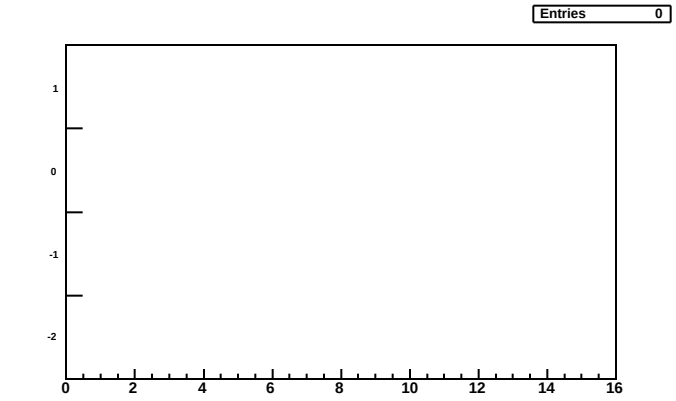
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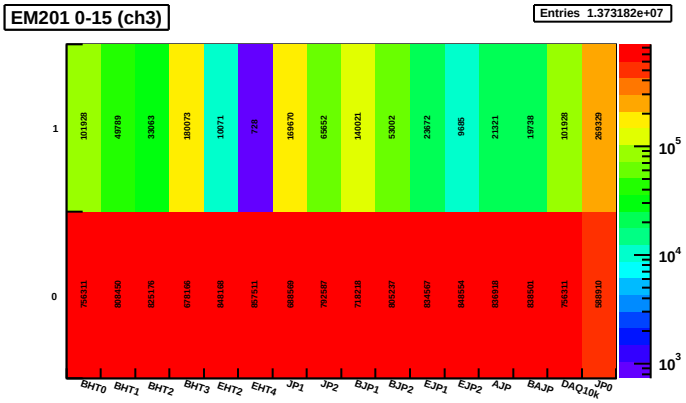
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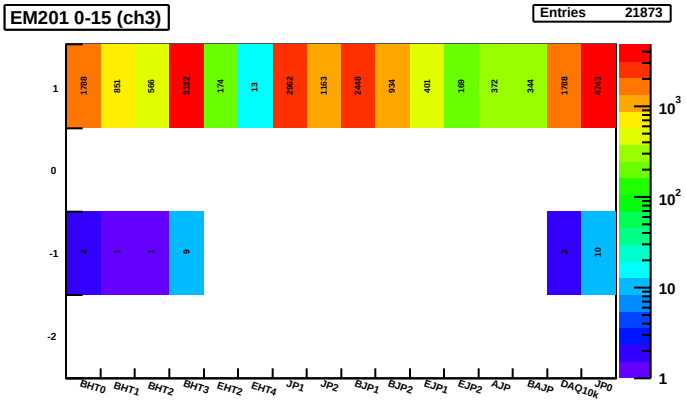
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Entries 21873
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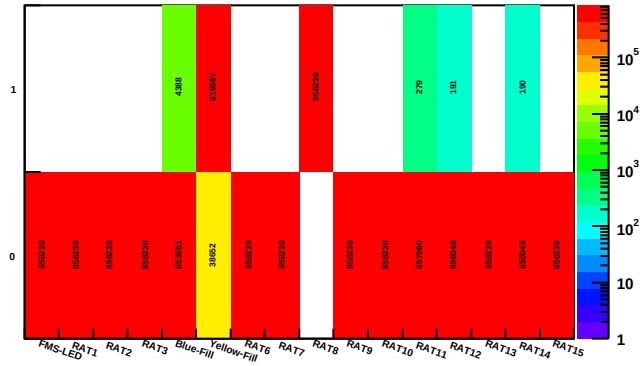
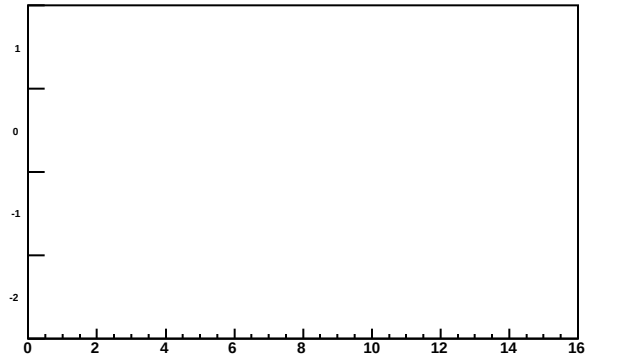
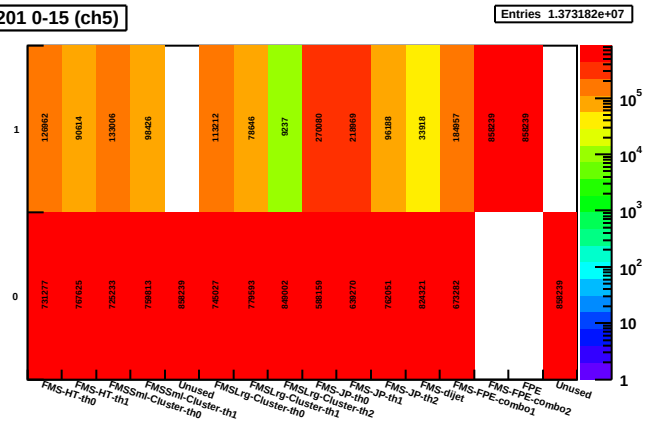
RAT board (ch4)





FP201 0-15 (ch5)



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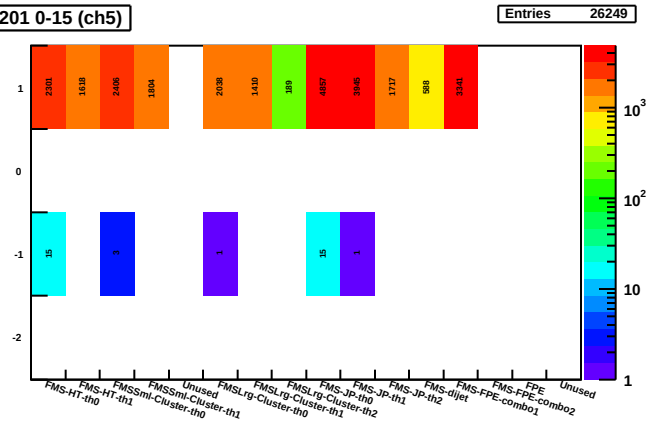
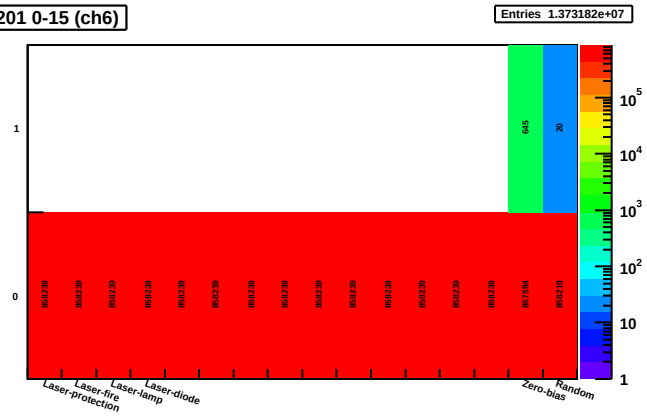
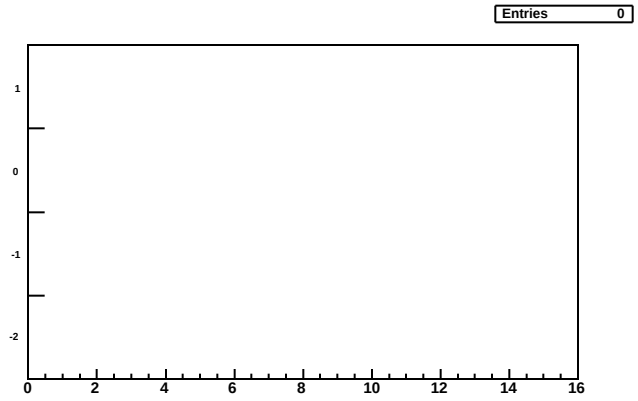
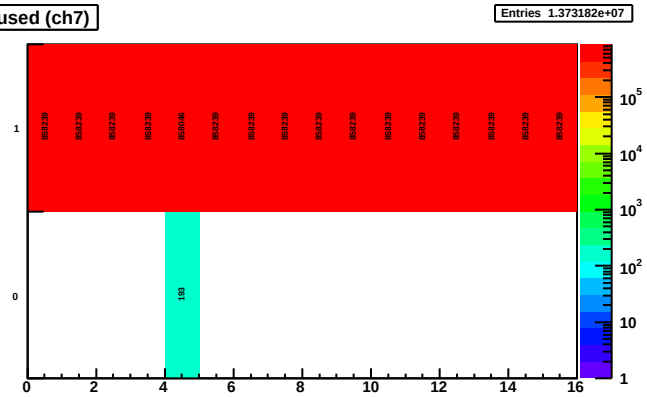
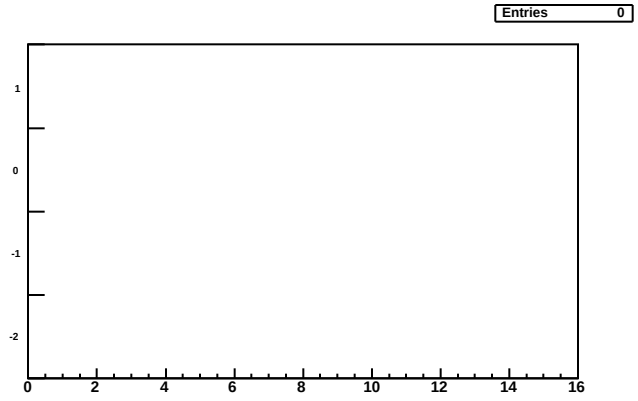


ST201 0-15 (ch6)





Unused (ch7)