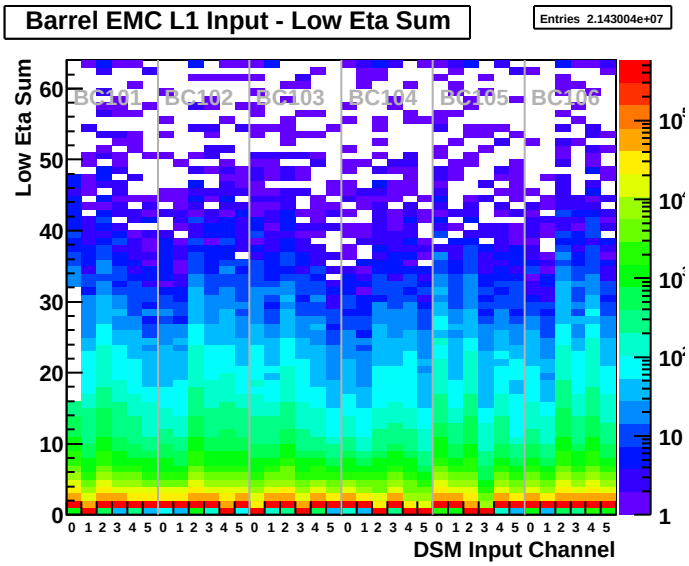
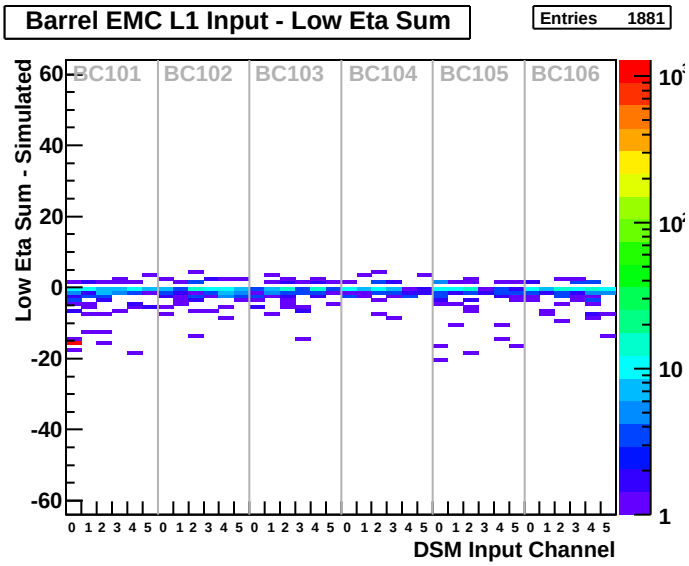
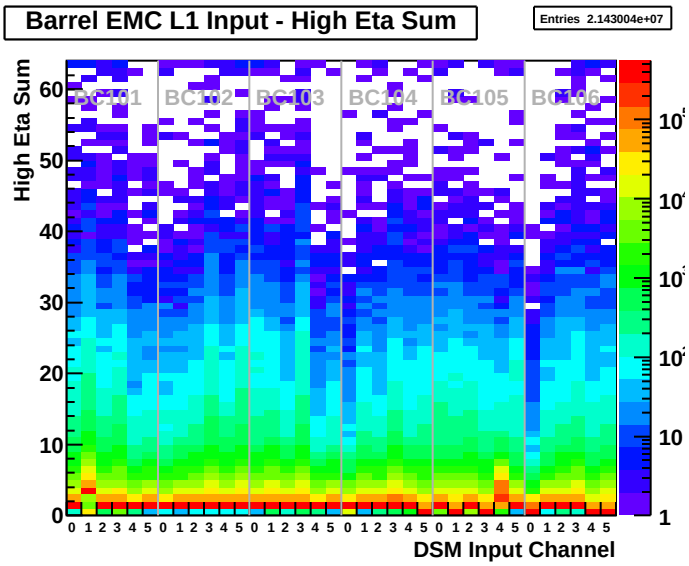
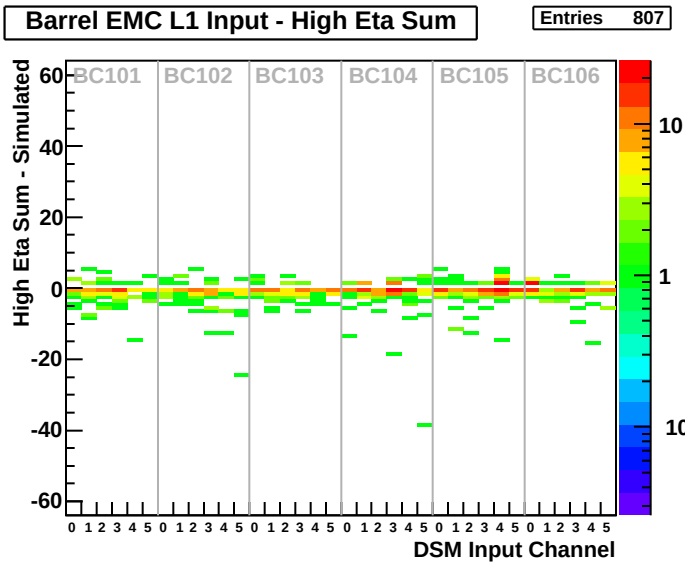
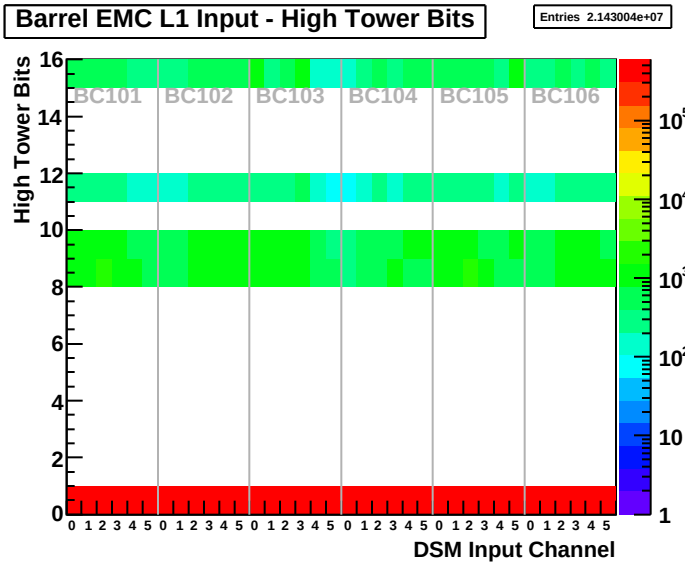
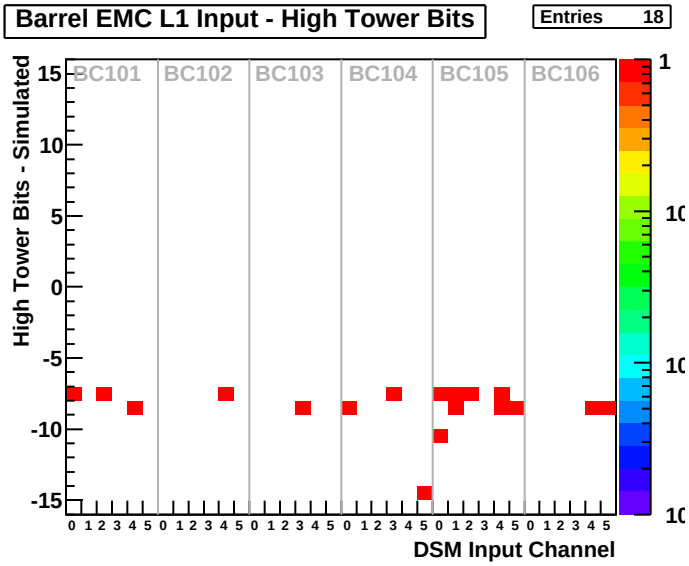

Barrel EMC L1 Input - Low Eta Sum
Entries 2.143004e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

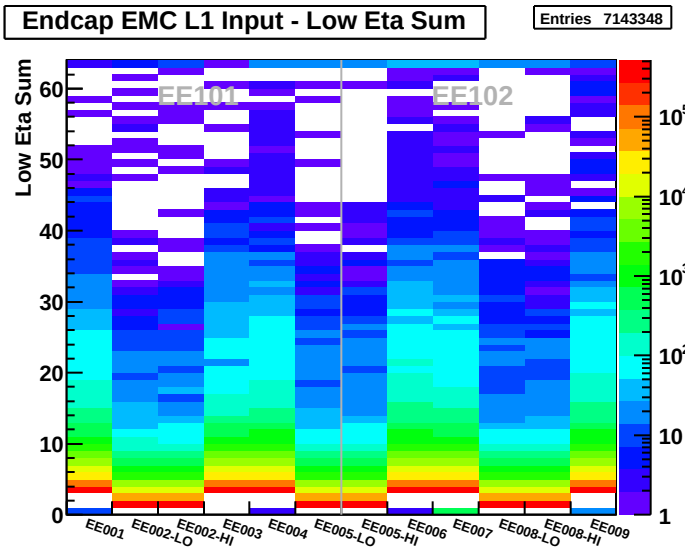

Barrel EMC L1 Input - Low Eta Sum
Entries 1881
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

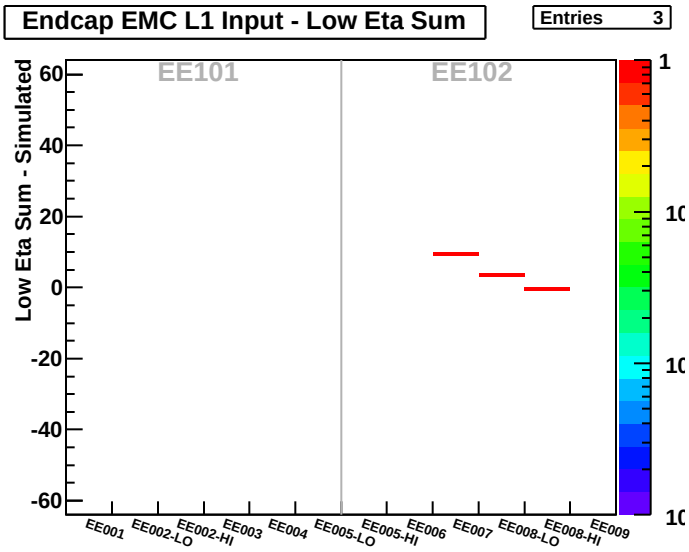

Barrel EMC L1 Input - High Eta Sum
Entries 2.143004e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

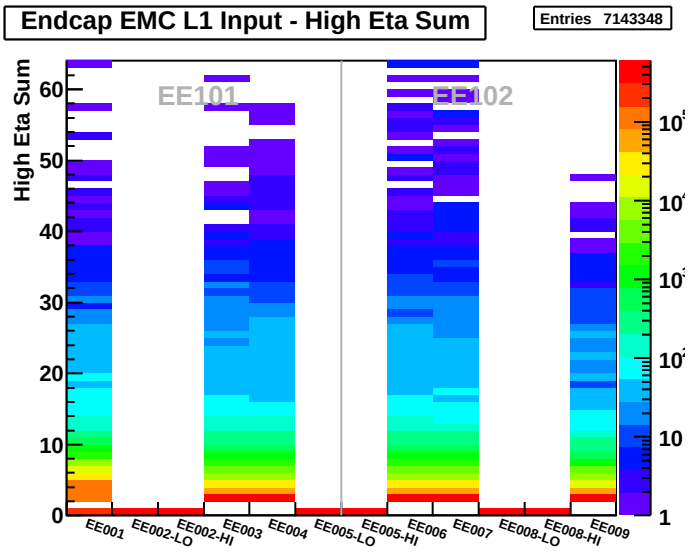

Barrel EMC L1 Input - High Eta Sum
Entries 807
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

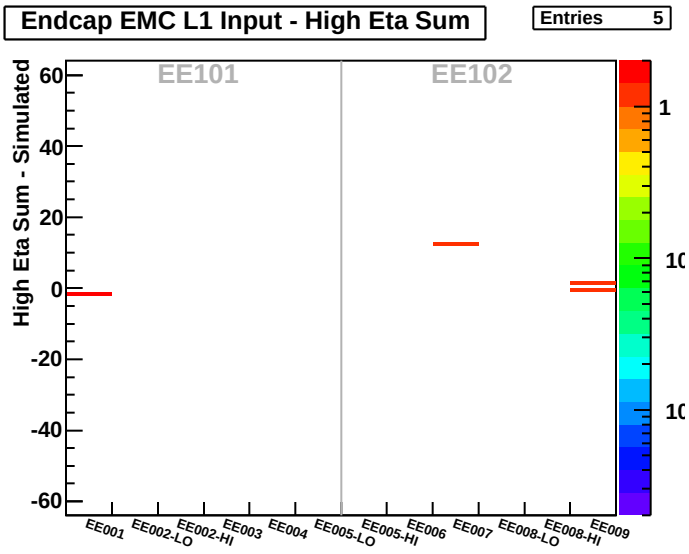

Barrel EMC L1 Input - High Tower Bits
Entries 2.143004e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

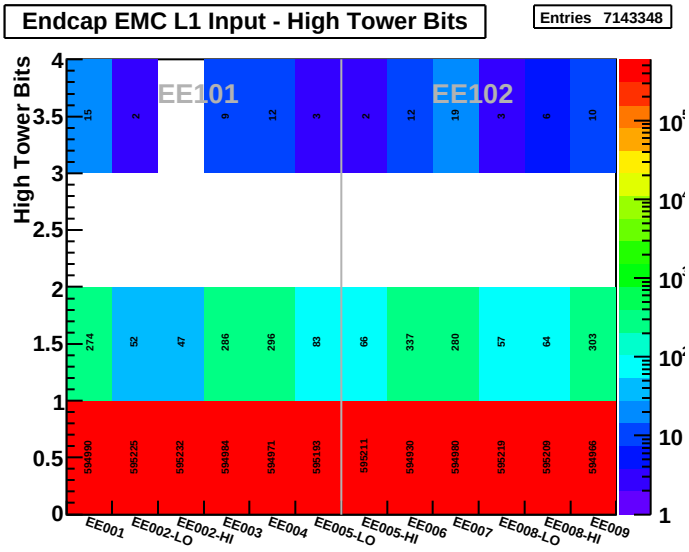

Barrel EMC L1 Input - High Tower Bits
Entries 18
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

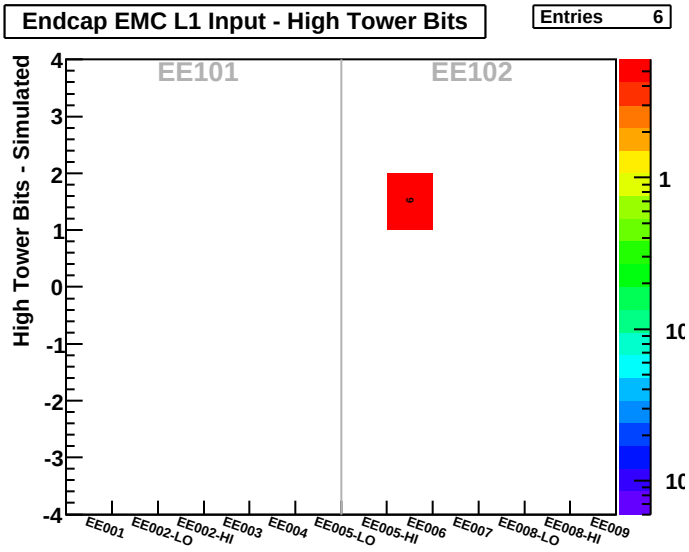

Endcap EMC L1 Input - Low Eta Sum
Entries 7143348
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

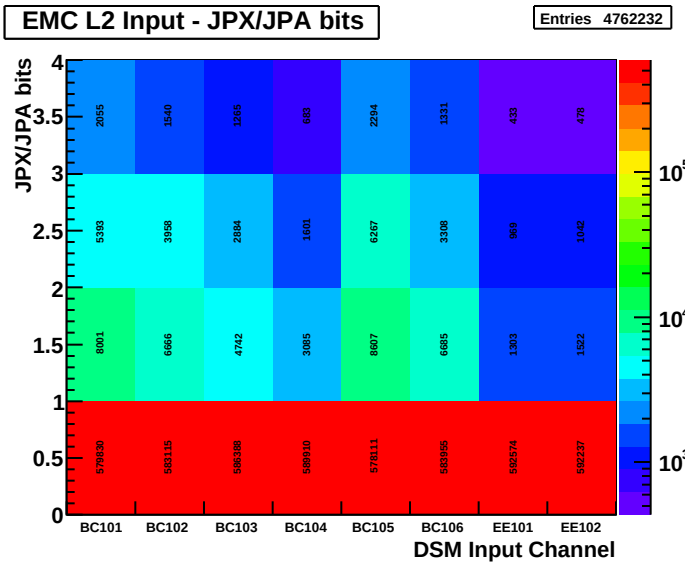

Endcap EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

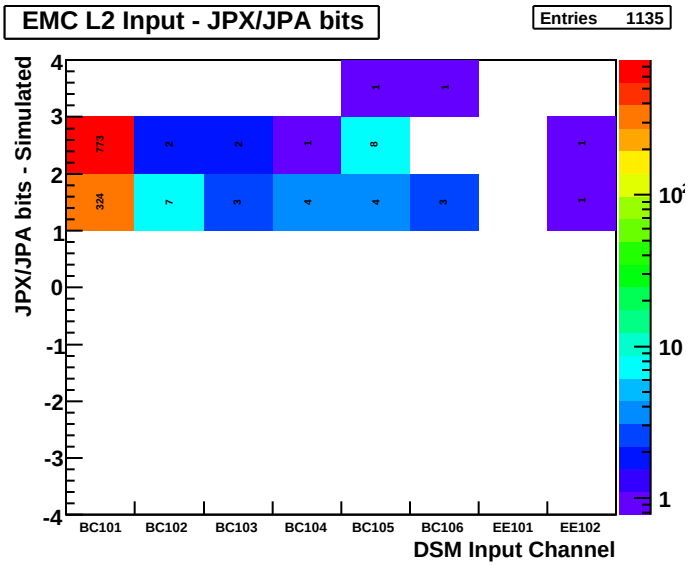

Endcap EMC L1 Input - High Eta Sum
Entries 7143348
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

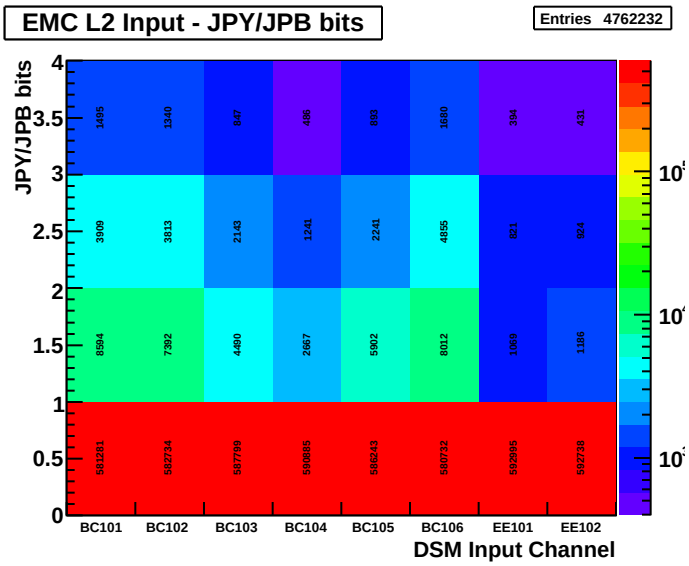

Endcap EMC L1 Input - High Eta Sum
Entries 5
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

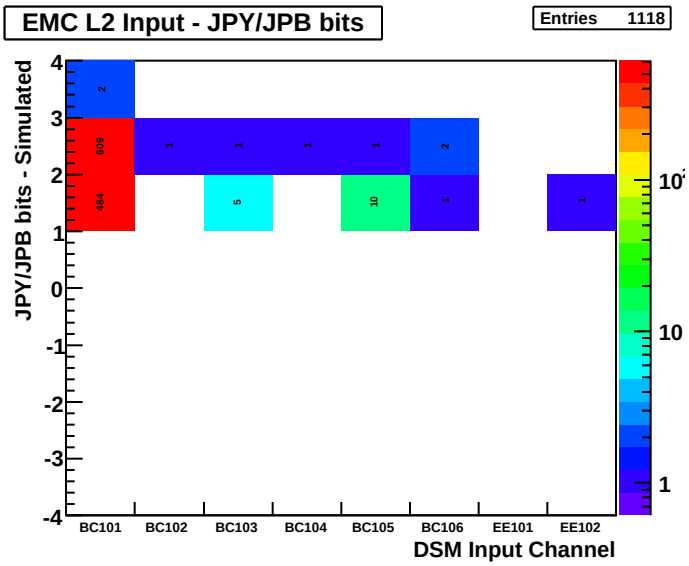

Endcap EMC L1 Input - High Tower Bits
Entries 7143348
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

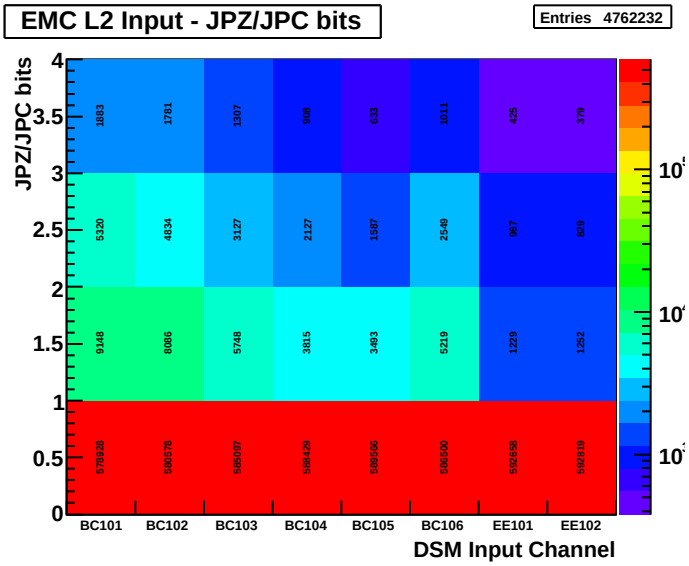

Endcap EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

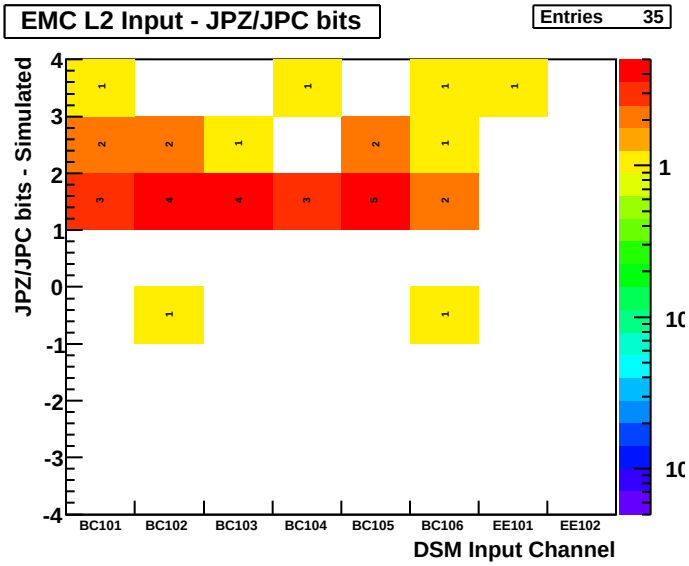

EMC L2 Input - JPX/JPA bits
Entries 4762232
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

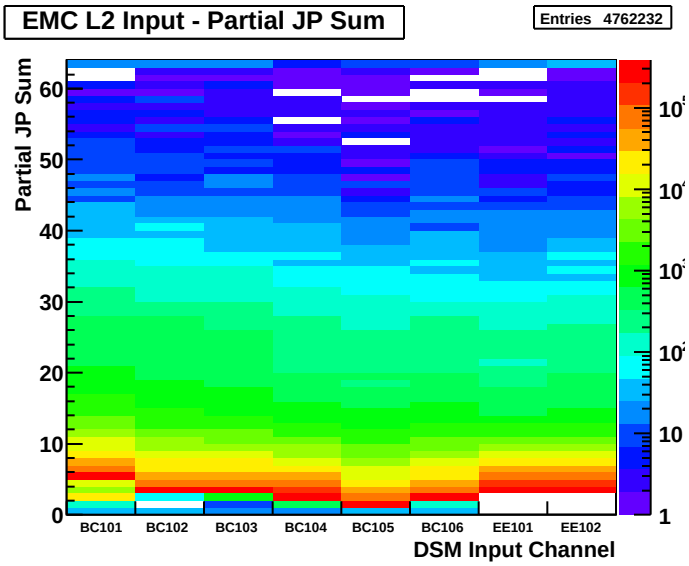

EMC L2 Input - JPX/JPA bits
Entries 1135
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

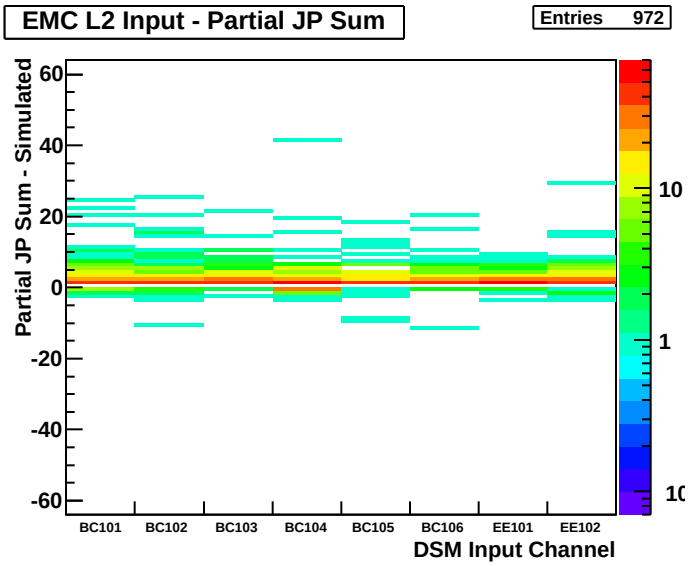

EMC L2 Input - JPY/JPB bits
Entries 4762232
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

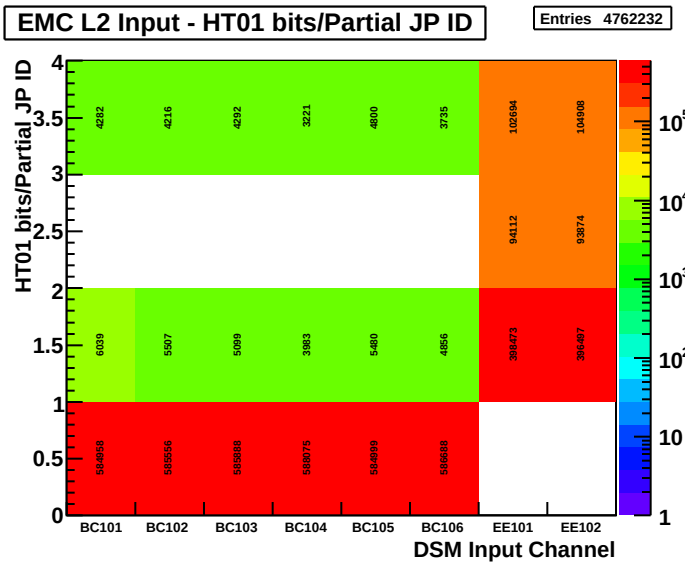

EMC L2 Input - JPY/JPB bits
Entries 1118
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

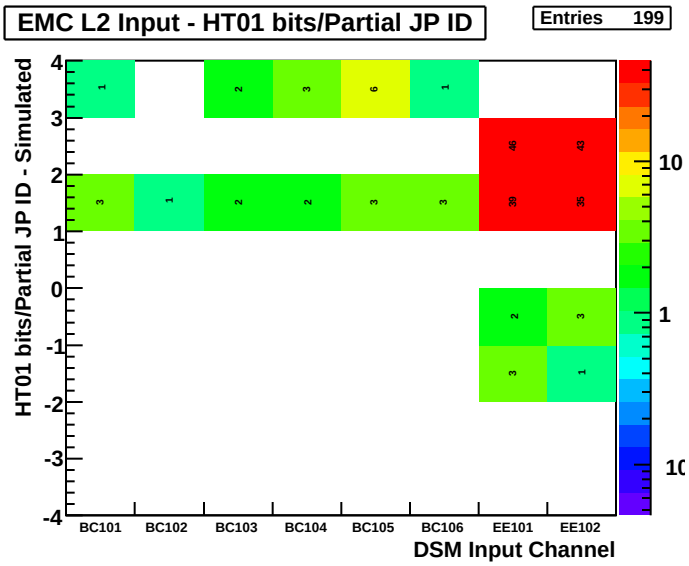

EMC L2 Input - JPZ/JPC bits
Entries 4762232
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

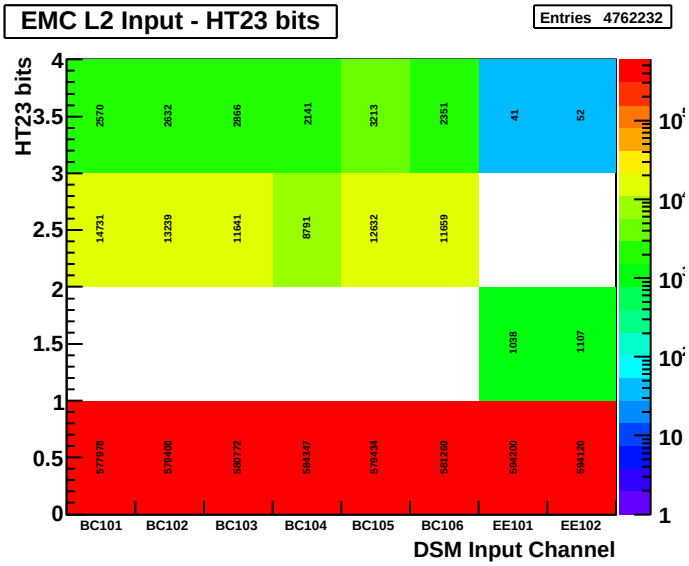

EMC L2 Input - JPZ/JPC bits
Entries 35
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

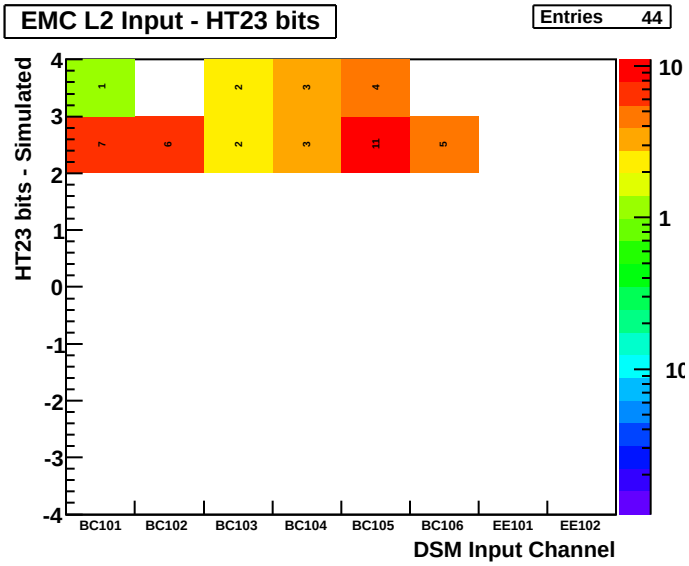

EMC L2 Input - Partial JP Sum
Entries 4762232
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

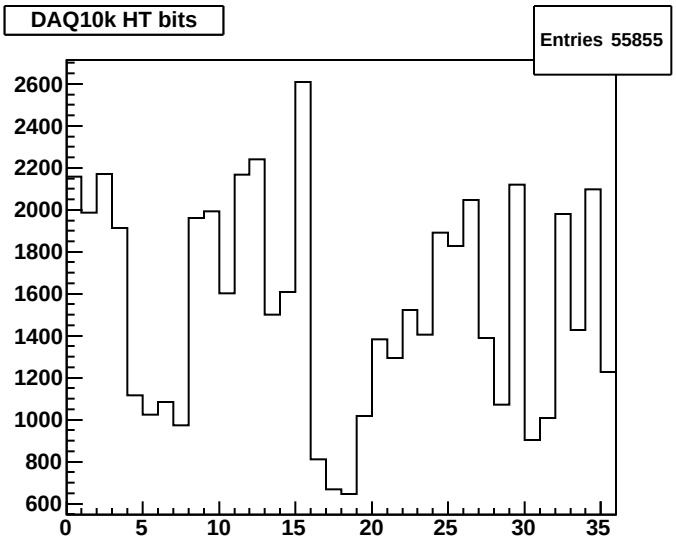

EMC L2 Input - Partial JP Sum
Entries 972
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

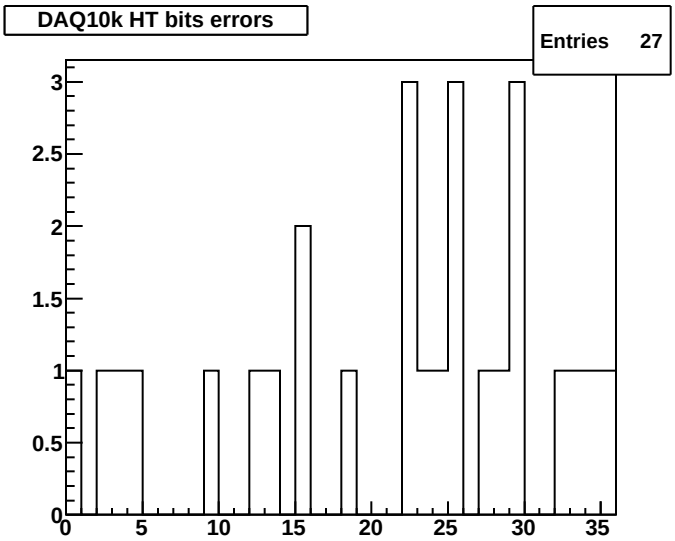

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4762232
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

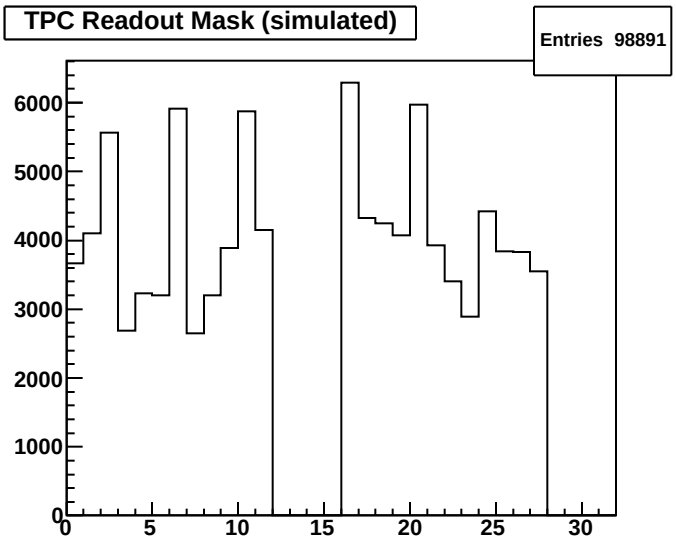

EMC L2 Input - HT01 bits/Partial JP ID
Entries 199
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

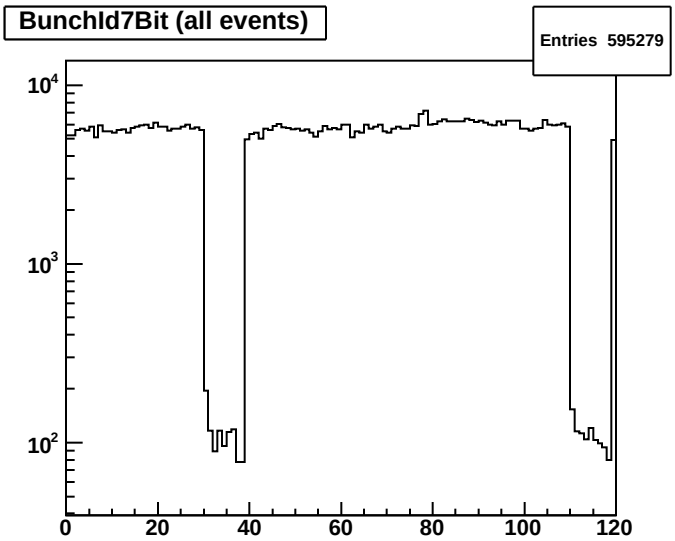

EMC L2 Input - HT23 bits
Entries 4762232
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

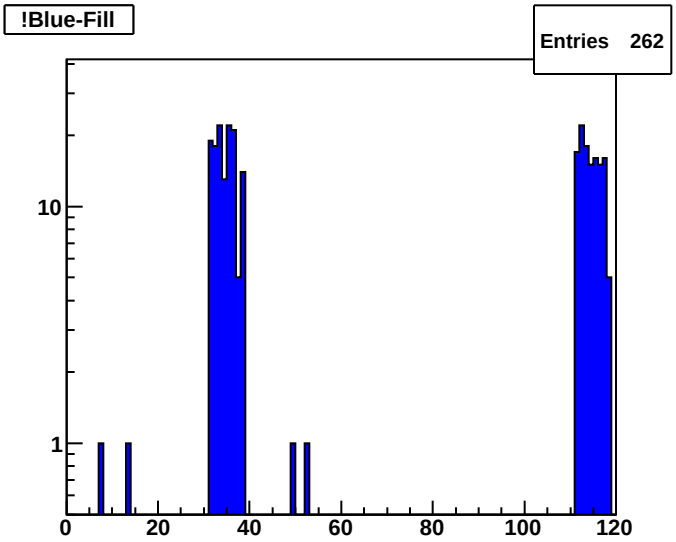

EMC L2 Input - HT23 bits
Entries 44
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

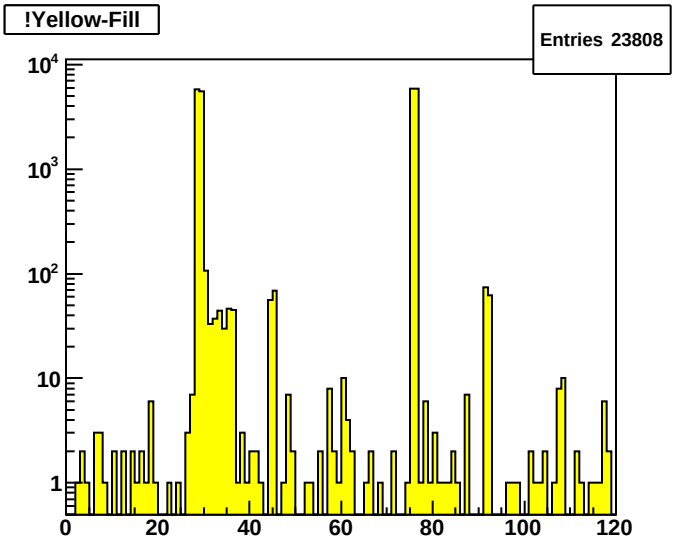

DAQ10k HT bits
Entries 55855

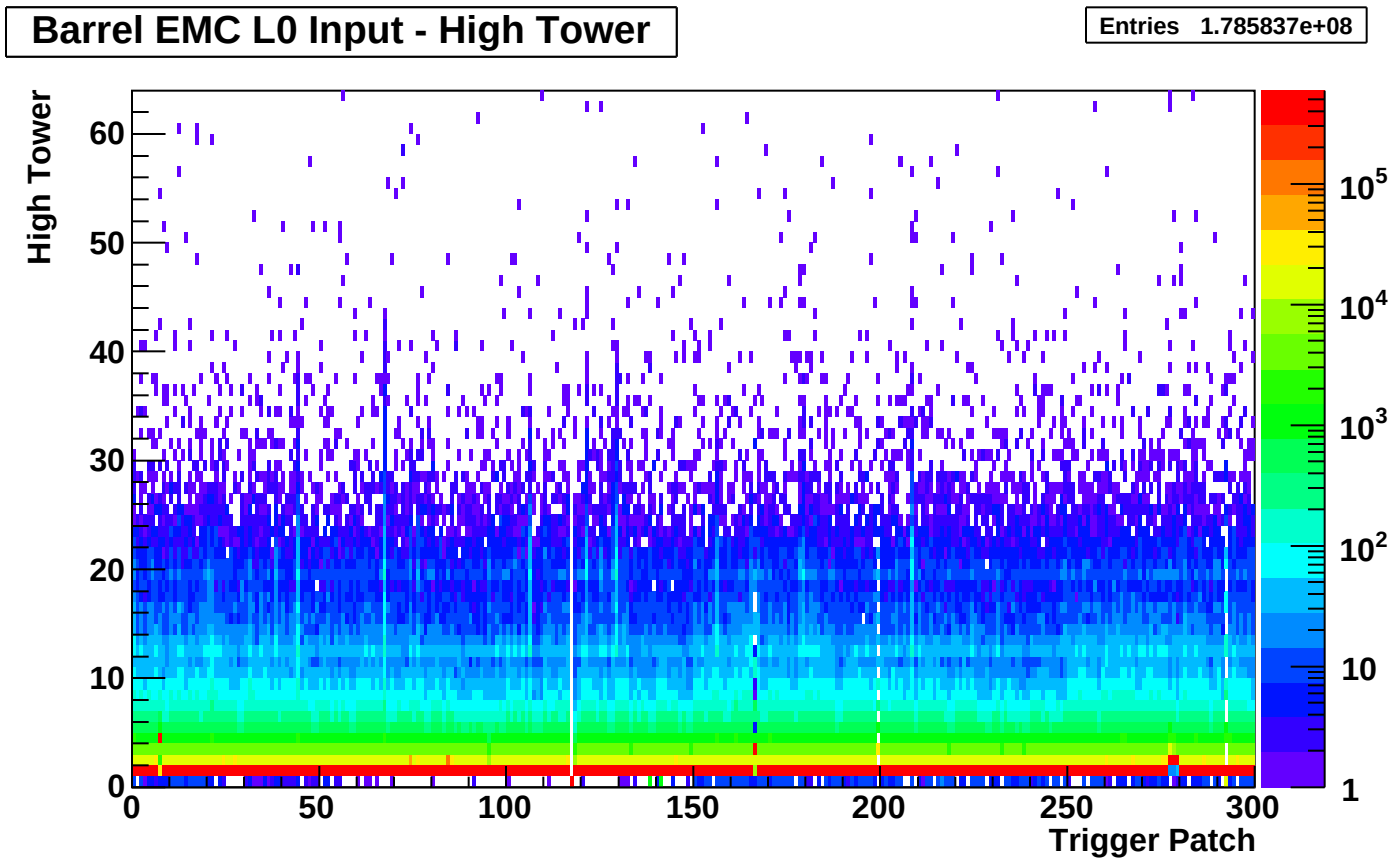

DAQ10k HT bits errors
Entries 27

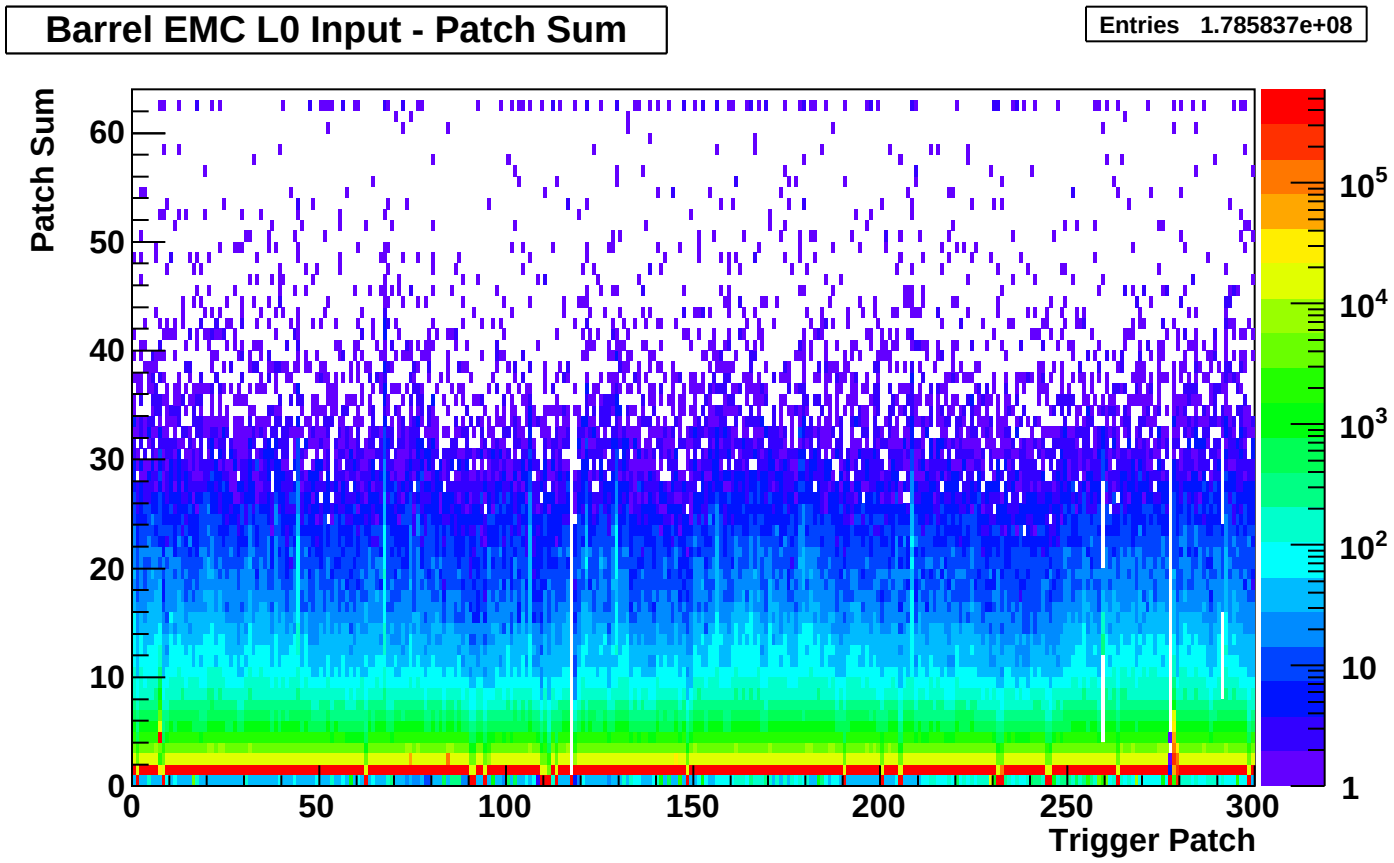

TPC Readout Mask (simulated)
Entries 98891

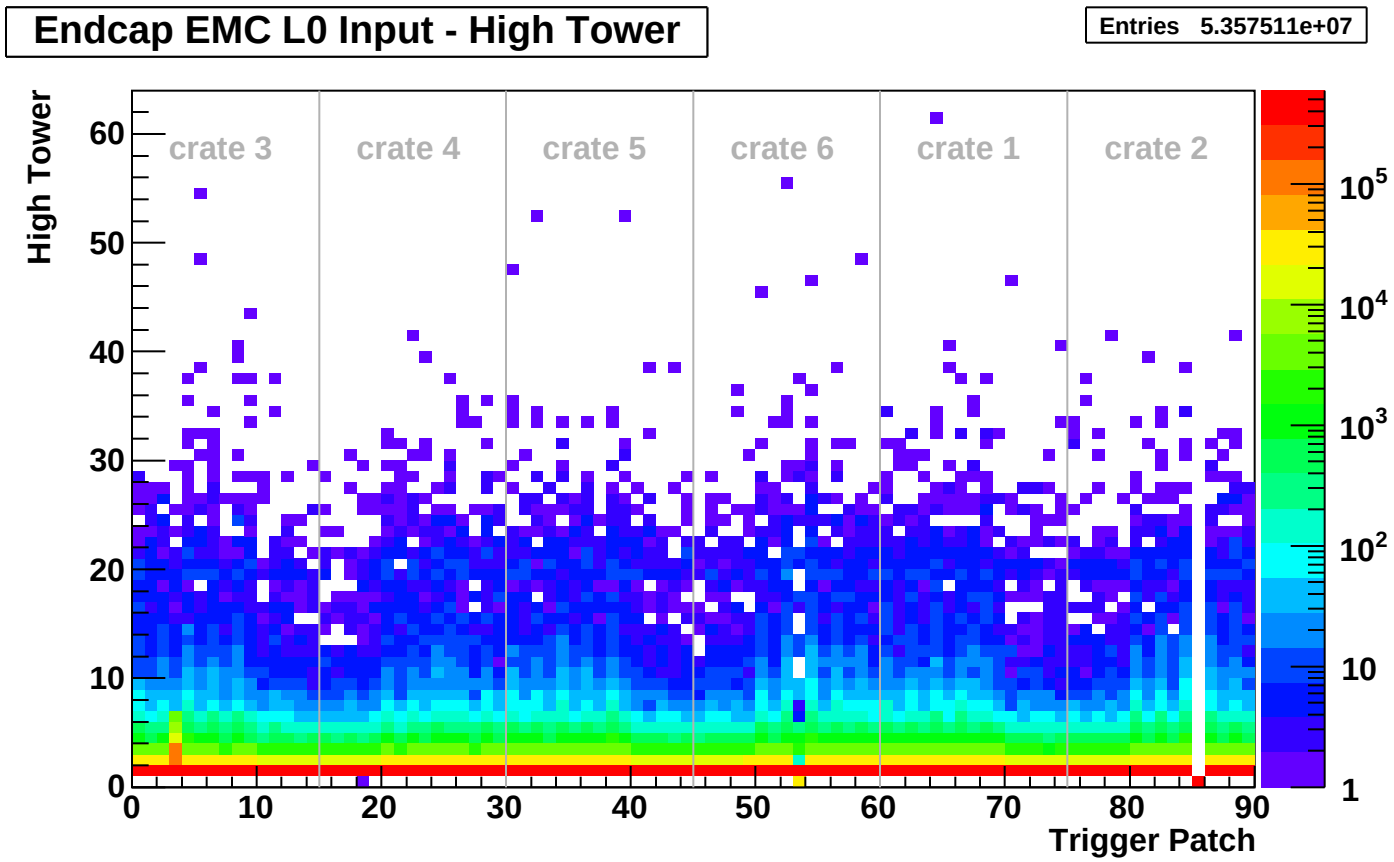

BunchId7Bit (all events)
Entries 595279

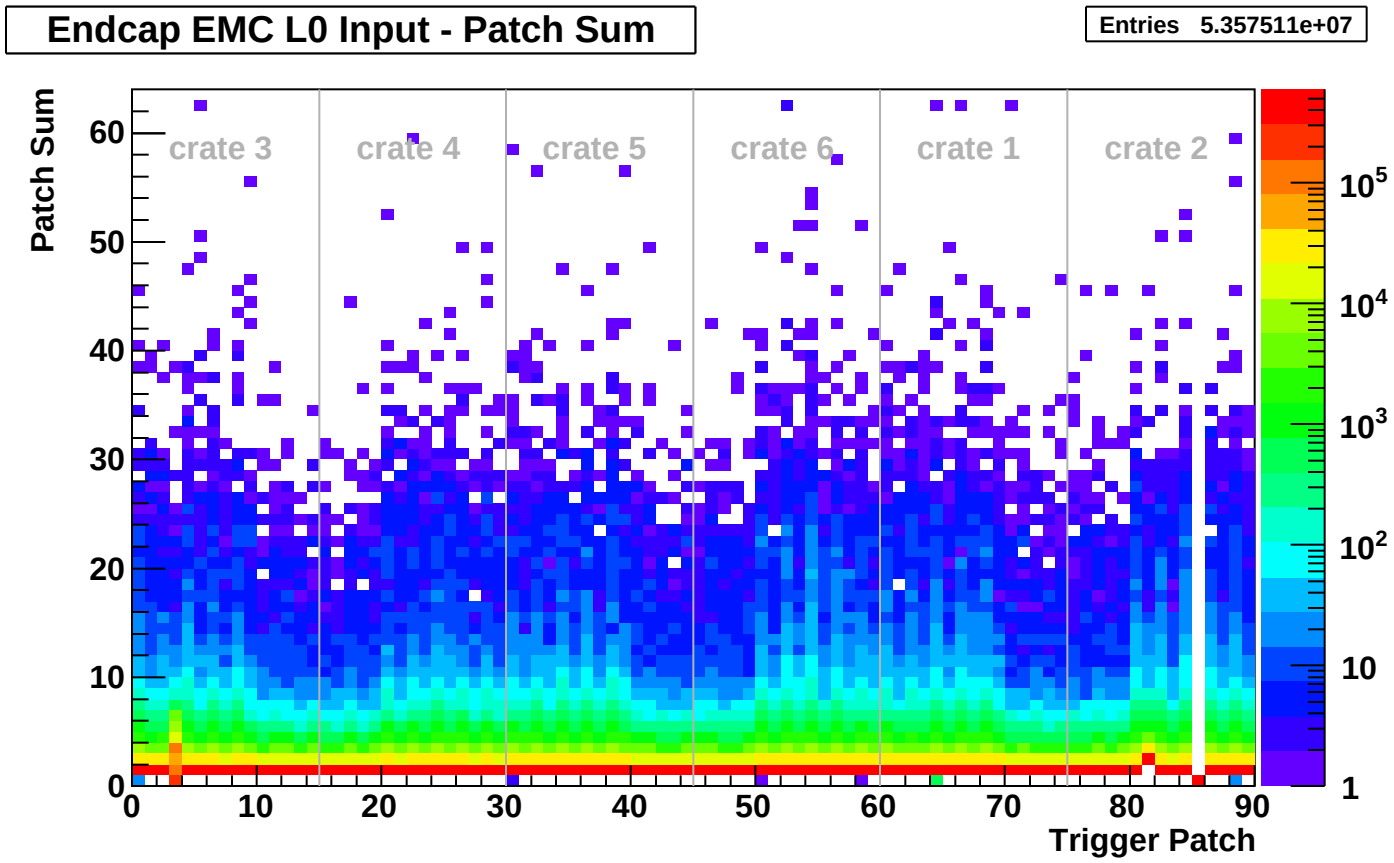

!Blue-Fill
Entries 262

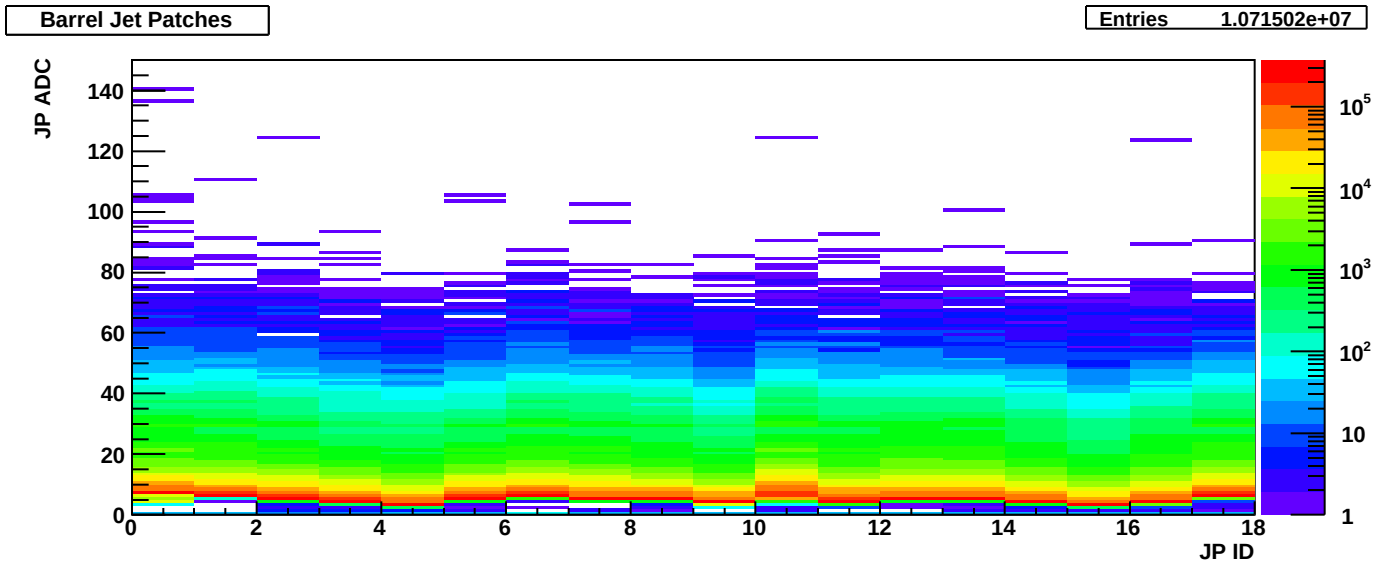

!Yellow-Fill
Entries 23808

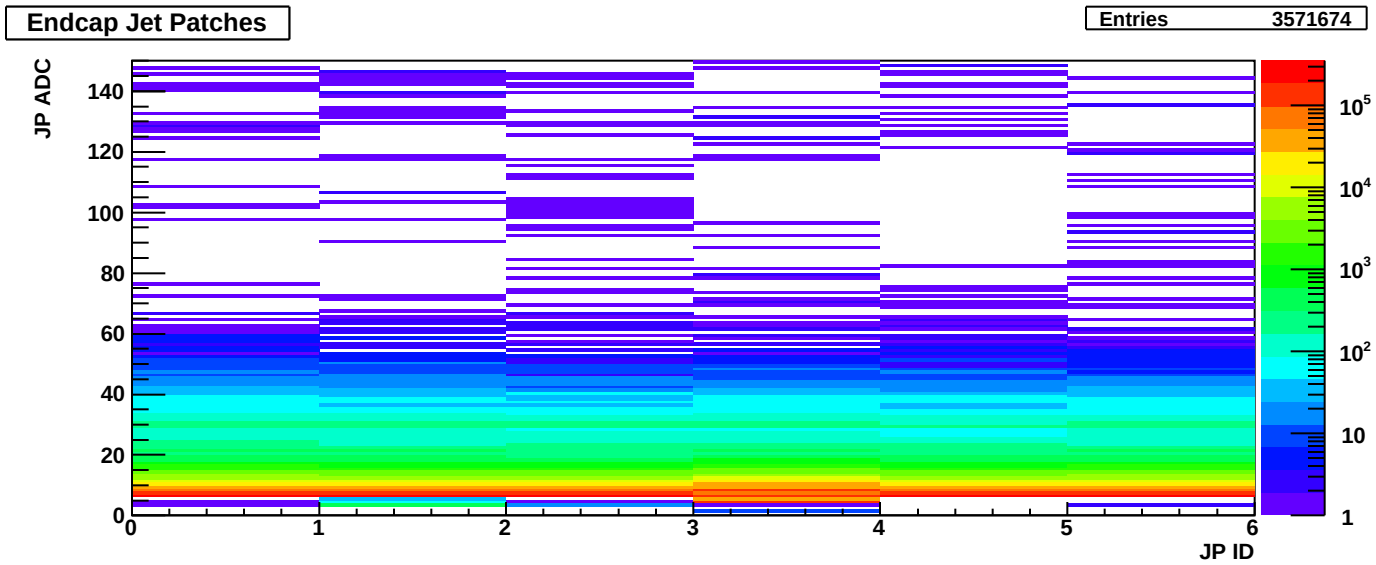

Barrel EMC L0 Input - High Tower
Entries 1.785837e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

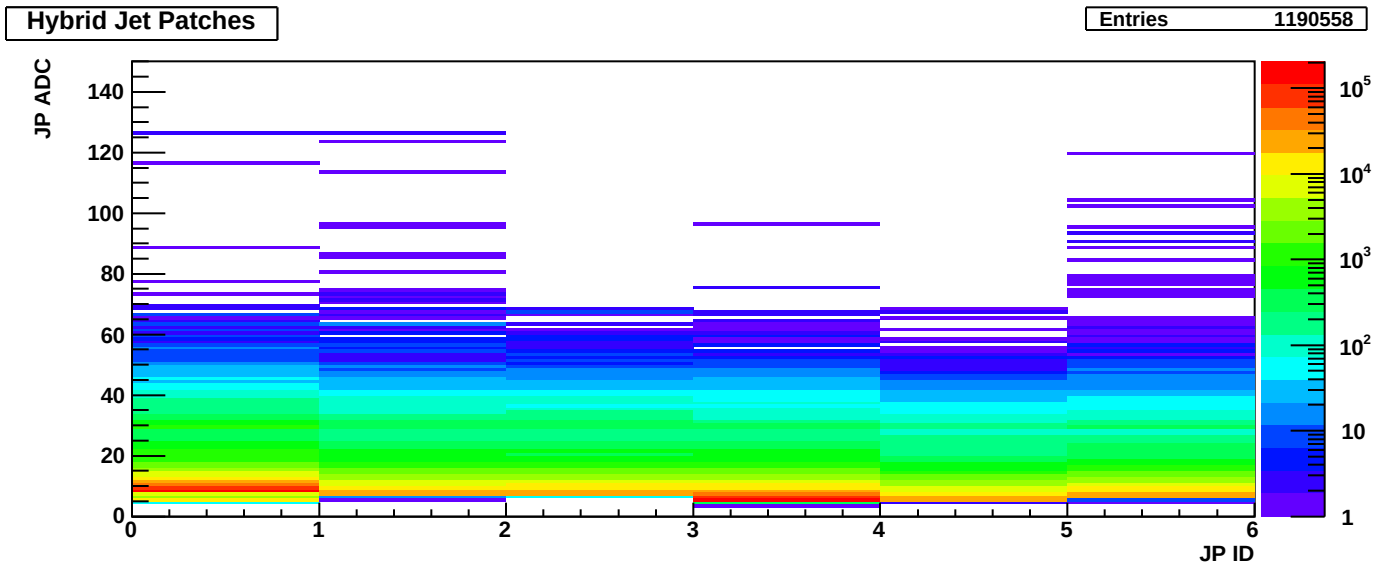

Barrel EMC L0 Input - Patch Sum
Entries 1.785837e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

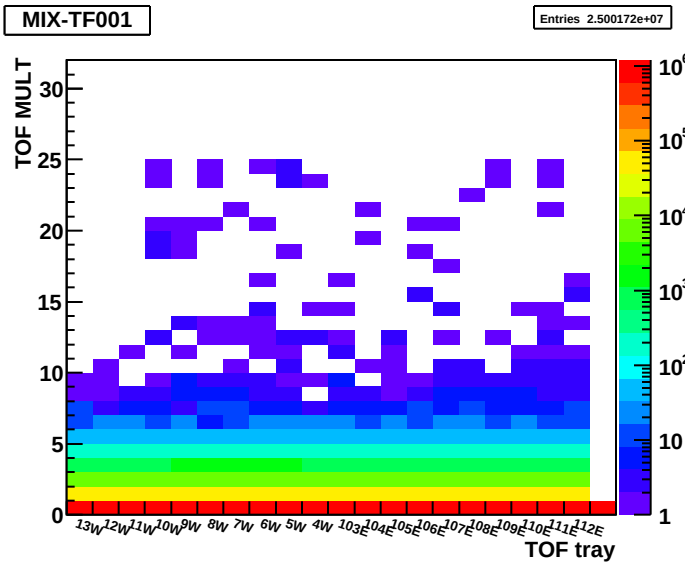

Endcap EMC L0 Input - High Tower
Entries 5.357511e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

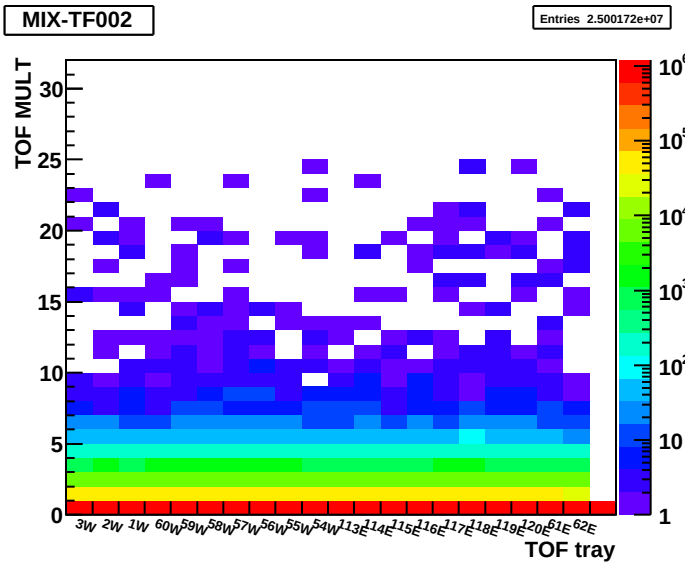

Endcap EMC L0 Input - Patch Sum
Entries 5.357511e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

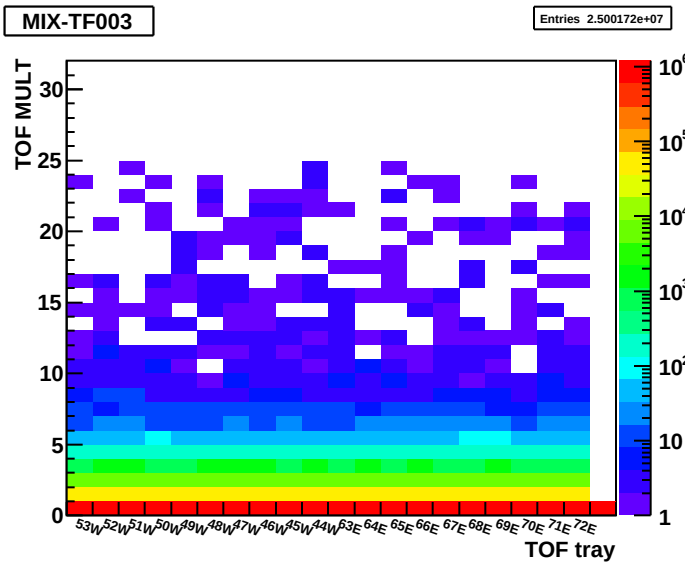

Barrel Jet Patches
Entries 1.071502e+07
JP ADC
JP ID

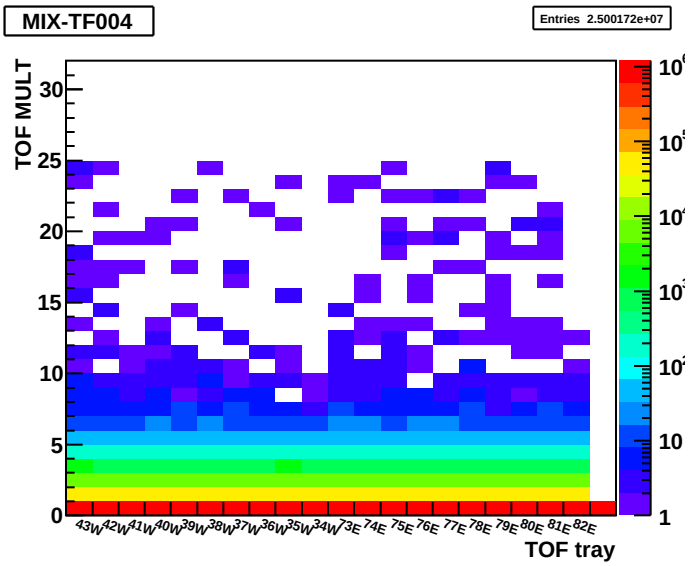

Endcap Jet Patches
Entries 3571674
JP ADC
JP ID

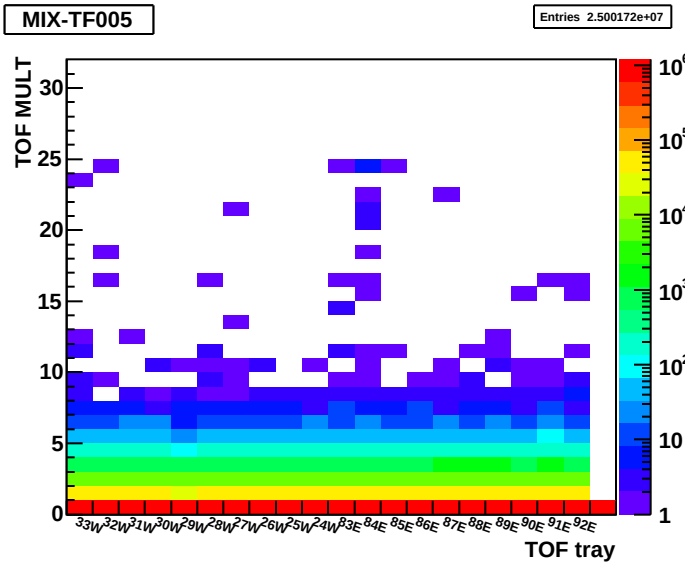

Hybrid Jet Patches
Entries 1190558
JP ADC
JP ID

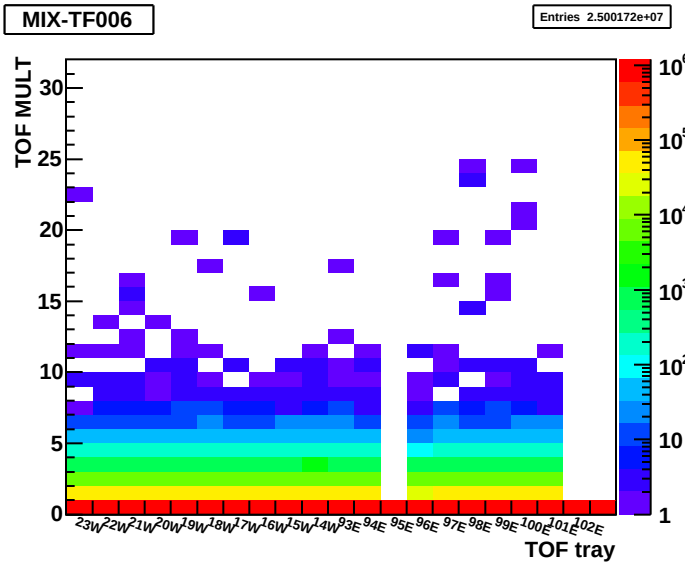

MIX-TF001
Entries 2.500172e+07
TOF MULT
TOF tray

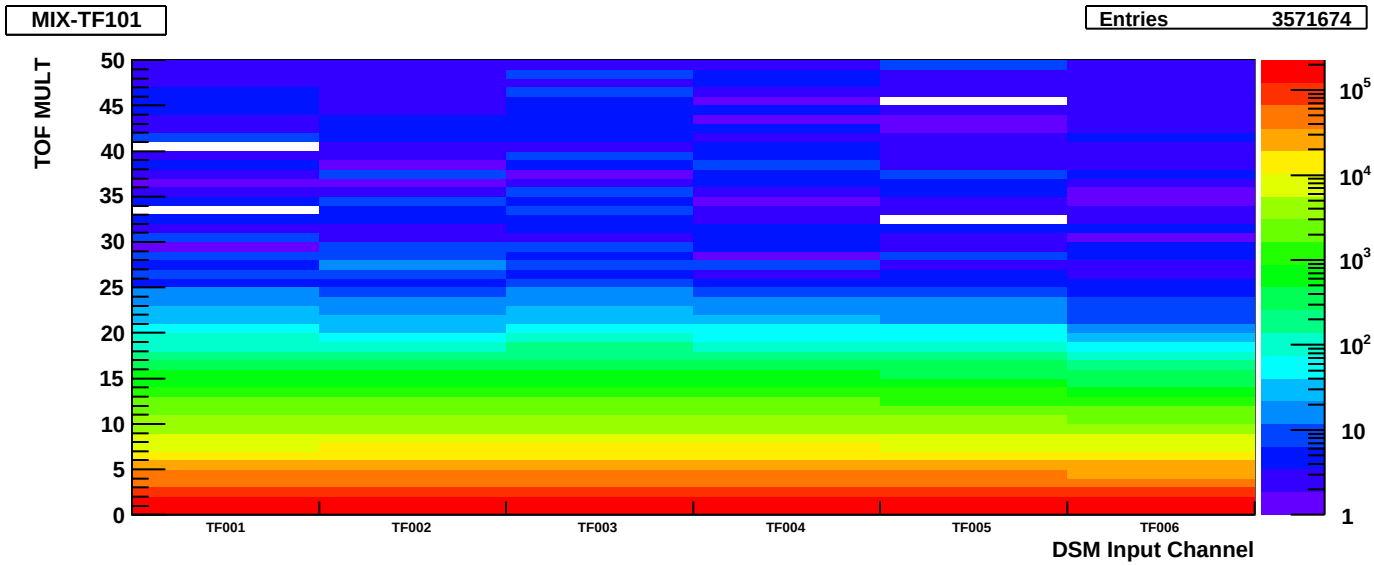

MIX-TF002
Entries 2.500172e+07
TOF MULT
TOF tray

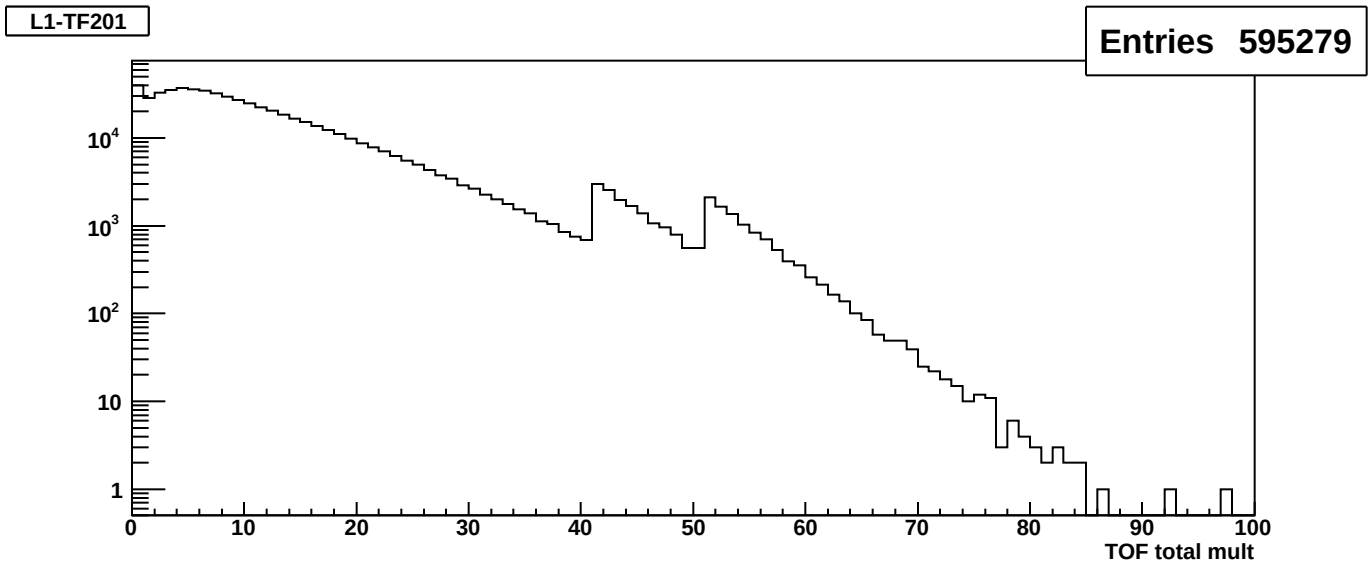

MIX-TF003
Entries 2.500172e+07
TOF MULT
TOF tray

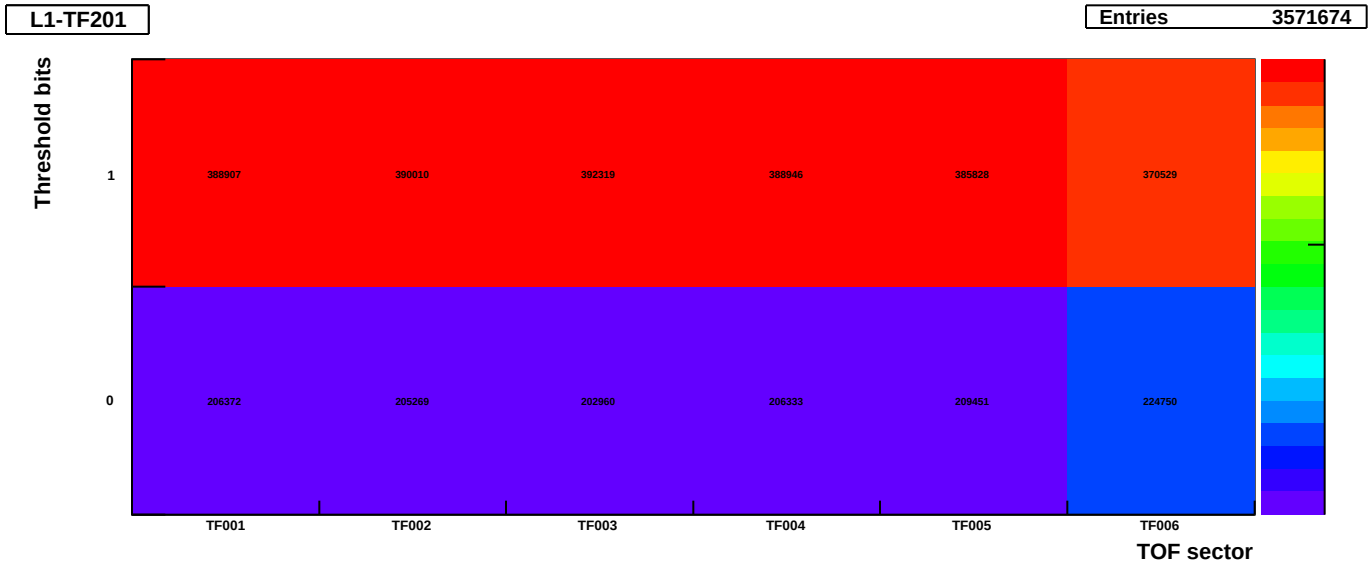

MIX-TF004
Entries 2.500172e+07
TOF MULT
TOF tray

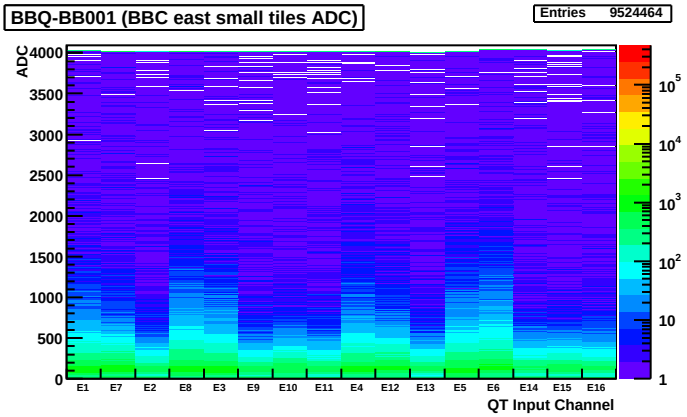

MIX-TF005
Entries 2.500172e+07
TOF MULT
TOF tray

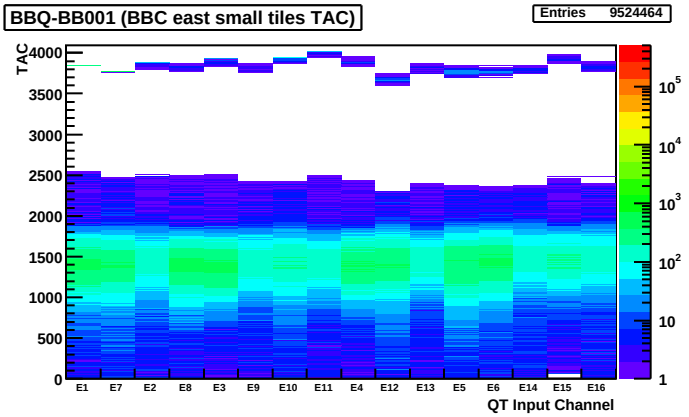

MIX-TF006
Entries 2.500172e+07
TOF MULT
TOF tray

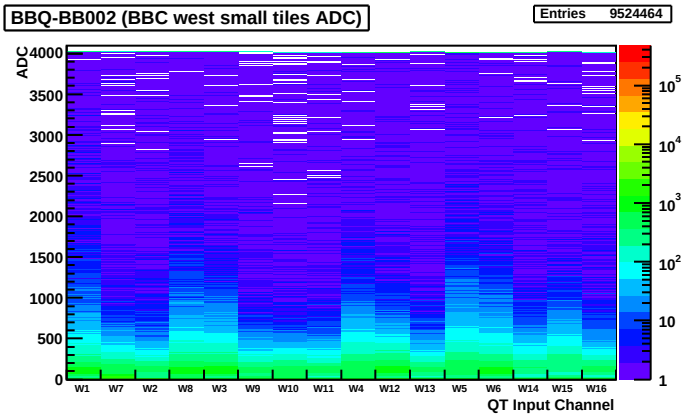

MIX-TF101
Entries 3571674
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

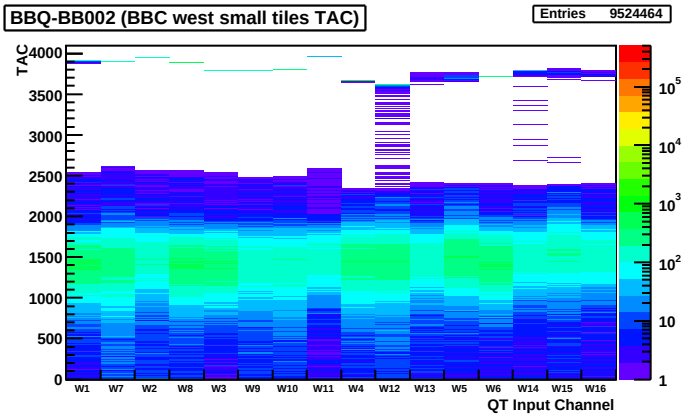

L1-TF201
Entries 595279
TOF total mult

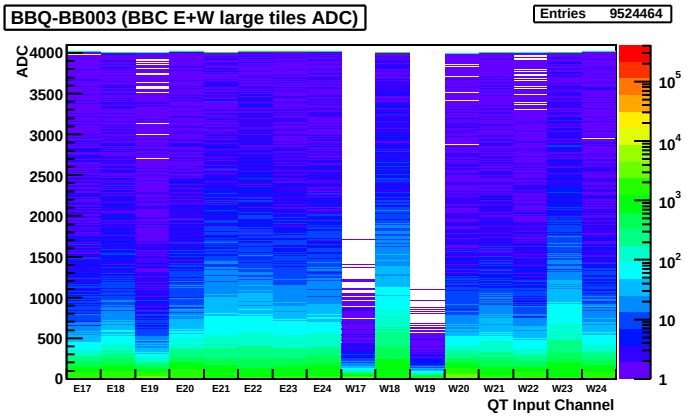

L1-TF201
Entries 3571674
Threshold bits
388907
390010
392319
388946
385828
370529
206372
205269
202960
206333
209451
224750
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

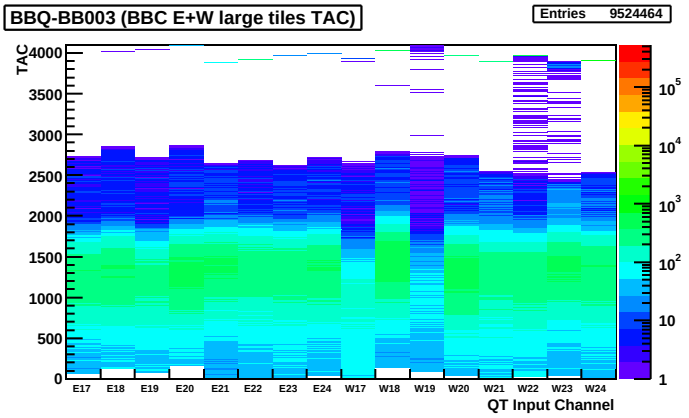

BBQ-BB001 (BBC east small tiles ADC)
Entries 9524464
ADC
QT Input Channel

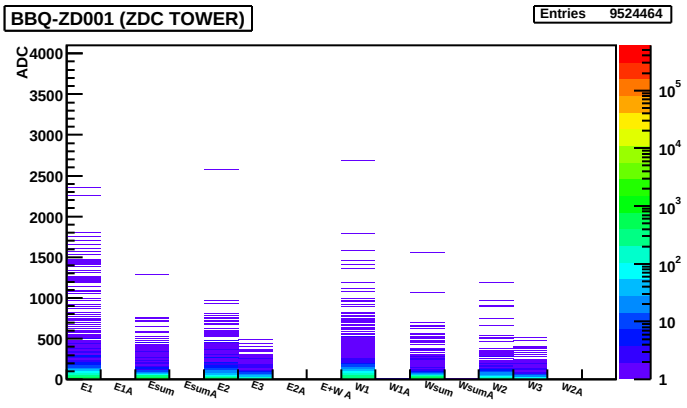

BBQ-BB001 (BBC east small tiles TAC)
Entries 9524464
TAC
QT Input Channel

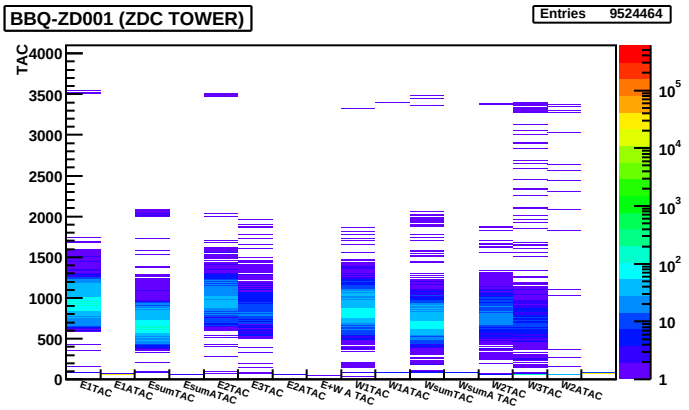

BBQ-BB002 (BBC west small tiles ADC)
Entries 9524464
ADC
QT Input Channel

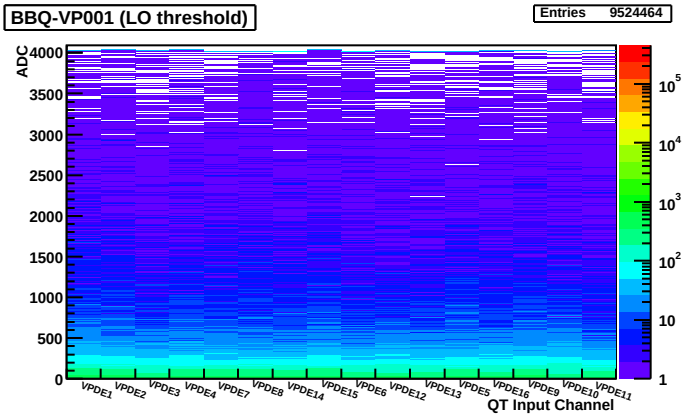

BBQ-BB002 (BBC west small tiles TAC)
Entries 9524464
TAC
QT Input Channel

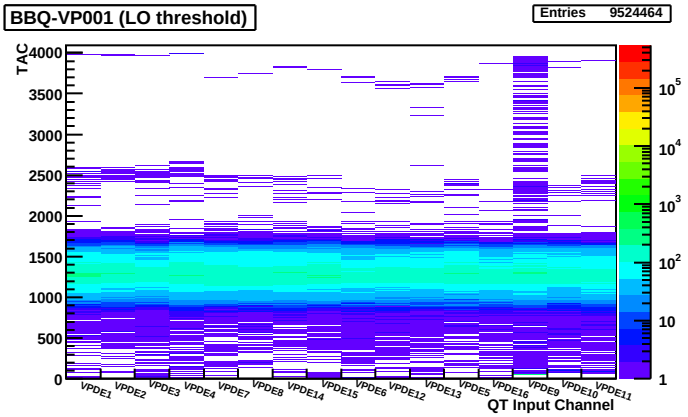

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9524464
ADC
QT Input Channel

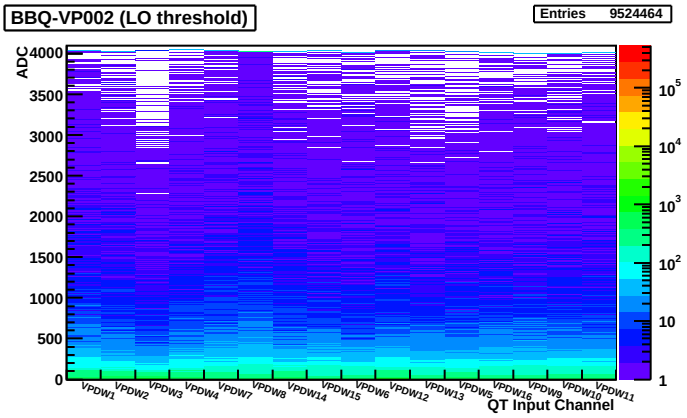

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9524464
TAC
QT Input Channel

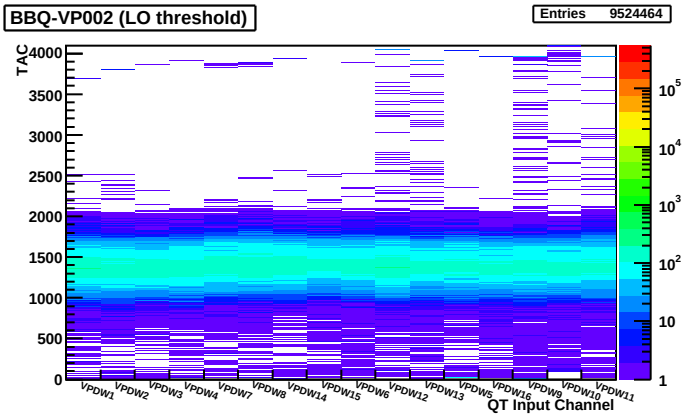

BBQ-ZD001 (ZDC TOWER)
Entries 9524464
ADC

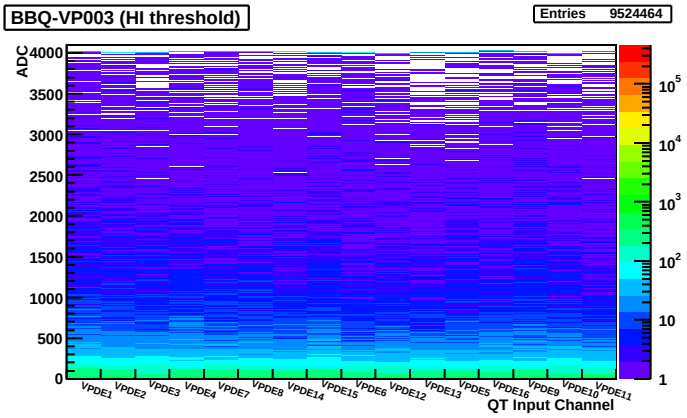

BBQ-ZD001 (ZDC TOWER)
Entries 9524464
TAC

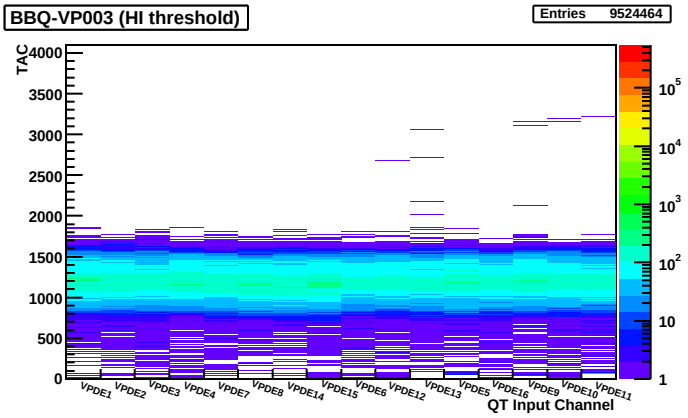

BBQ-VP001 (LO threshold)
Entries 9524464
ADC
QT Input Channel

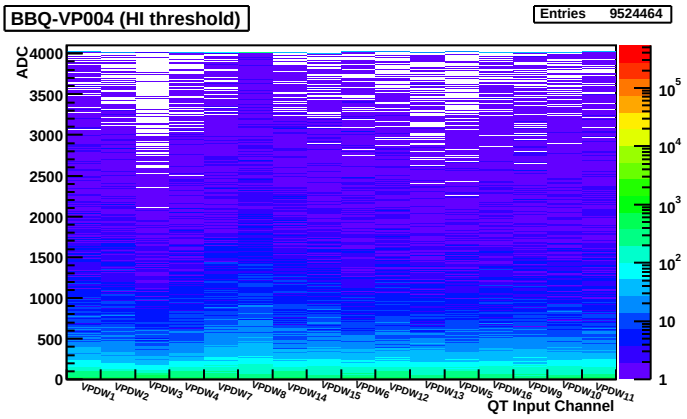

BBQ-VP001 (LO threshold)
Entries 9524464
TAC
QT Input Channel

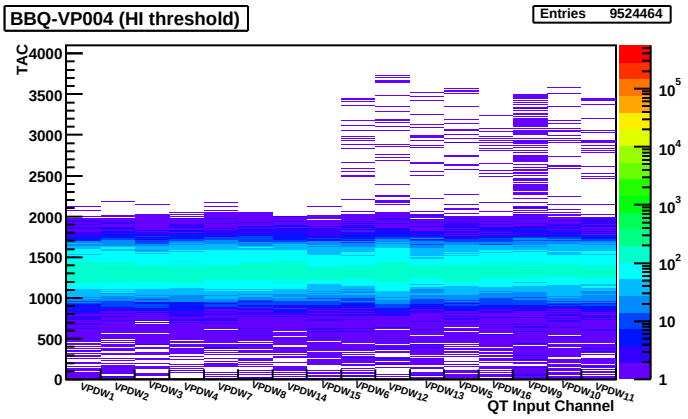

BBQ-VP002 (LO threshold)
Entries 9524464
ADC
QT Input Channel

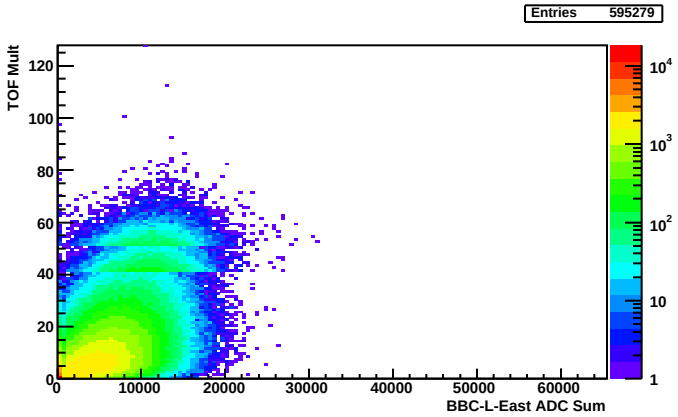

BBQ-VP002 (LO threshold)
Entries 9524464
TAC
QT Input Channel

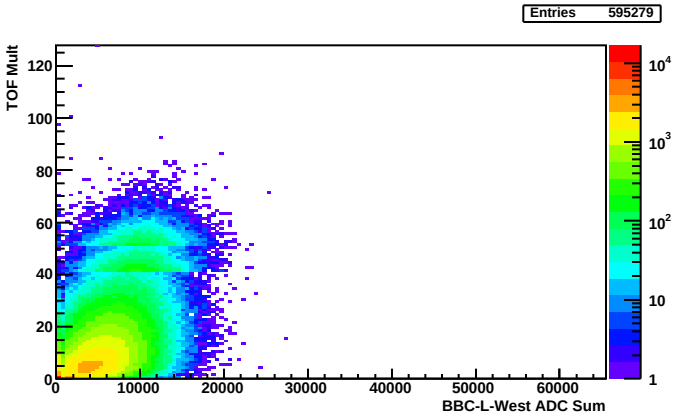

BBQ-VP003 (HI threshold)
Entries 9524464
ADC
QT Input Channel

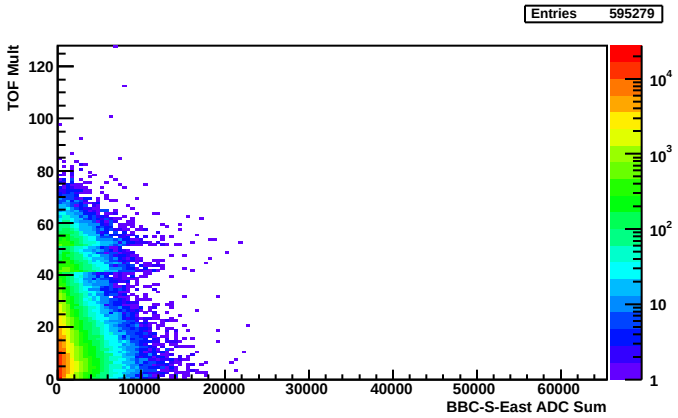

BBQ-VP003 (HI threshold)
Entries 9524464
TAC
QT Input Channel

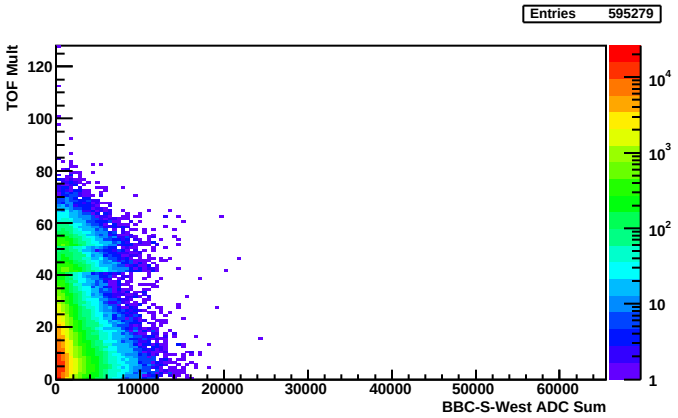

BBQ-VP004 (HI threshold)
Entries 9524464
ADC
QT Input Channel

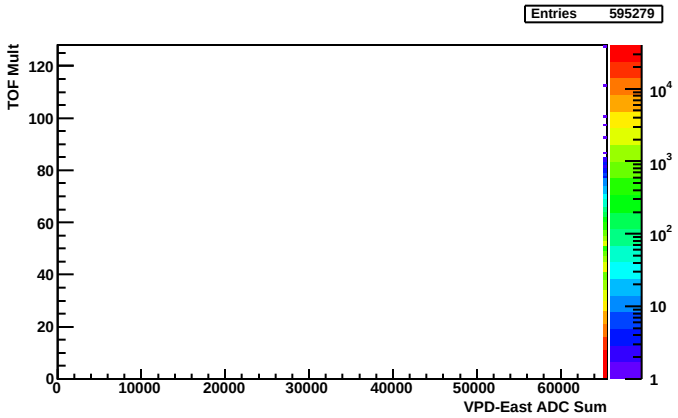

BBQ-VP004 (HI threshold)
Entries 9524464
TAC
QT Input Channel

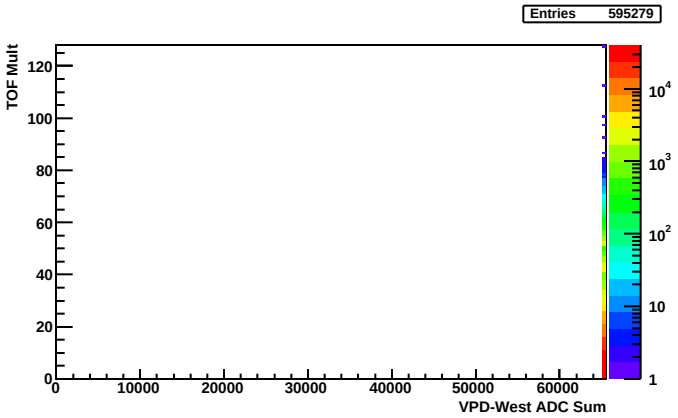

Entries 595279
TOF Mult
BBC-L-East ADC Sum

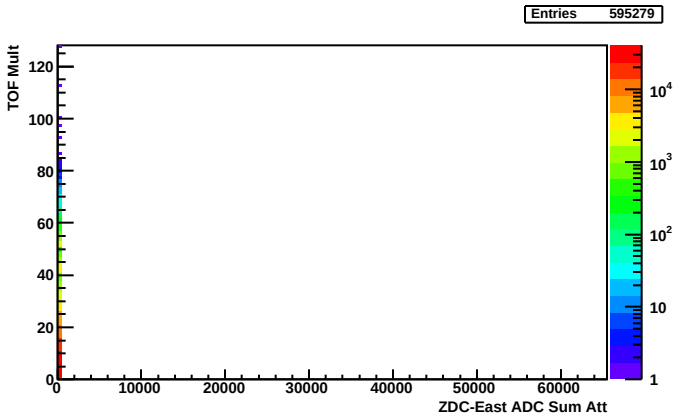

Entries 595279
TOF Mult
BBC-L-West ADC Sum

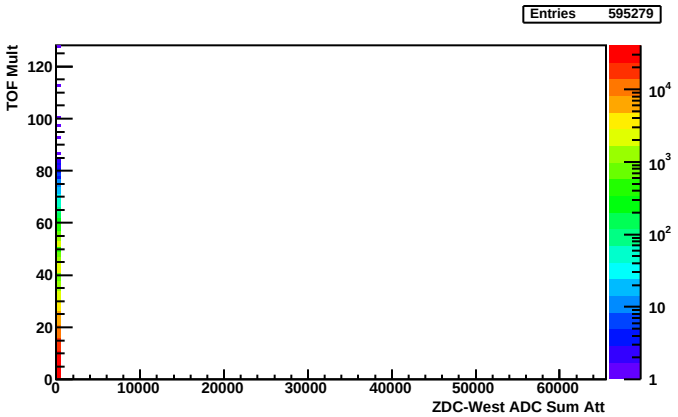

Entries 595279
TOF Mult
BBC-S-East ADC Sum

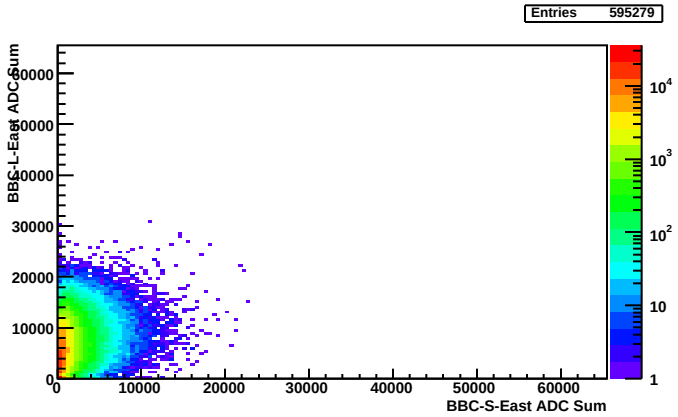

Entries 595279
TOF Mult
BBC-S-West ADC Sum

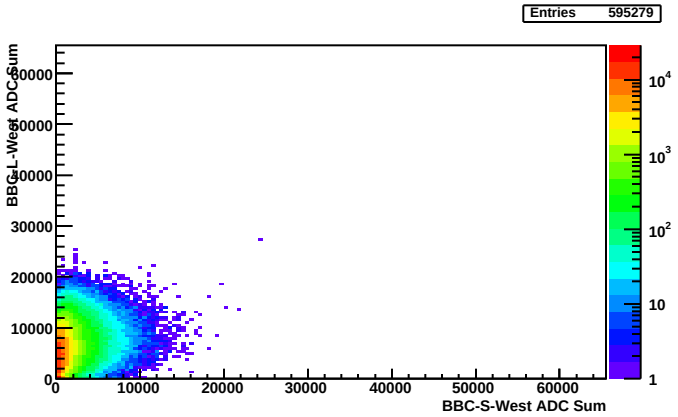

Entries 595279
TOF Mult
VPD-East ADC Sum

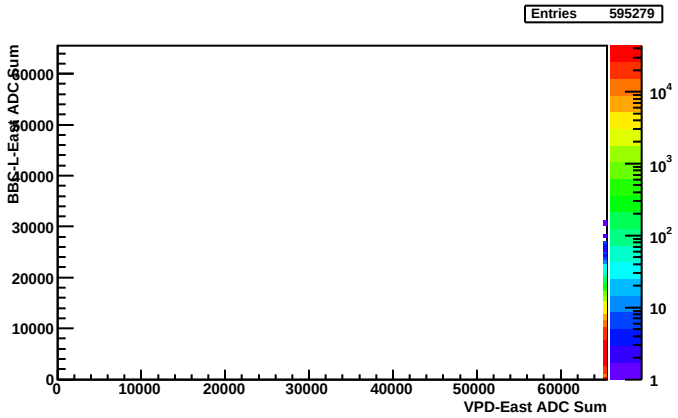

Entries 595279
TOF Mult
VPD-West ADC Sum


Entries 595279
TOF Mult
ZDC-East ADC Sum Att


Entries 595279
TOF Mult
ZDC-West ADC Sum Att


Entries 595279
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 595279
BBC-L-West ADC Sum
BBC-S-West ADC Sum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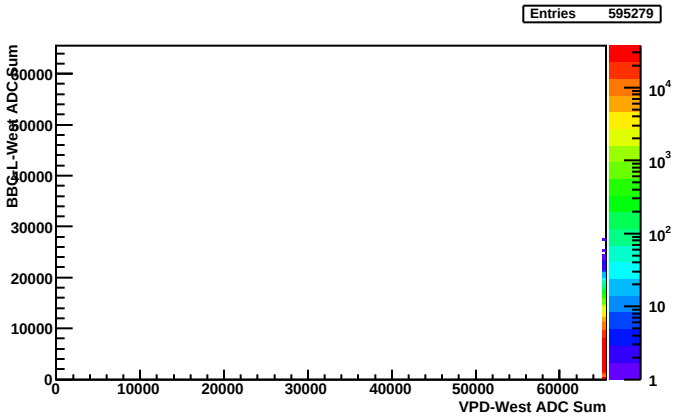
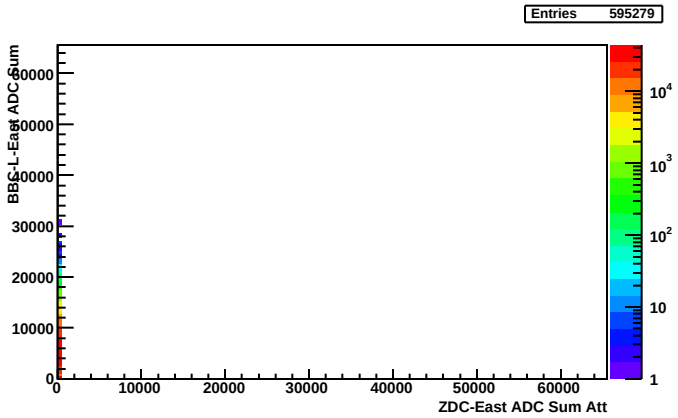
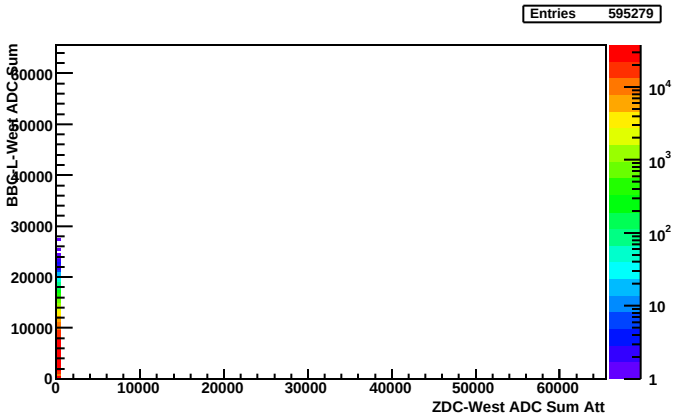
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 595279
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 595279
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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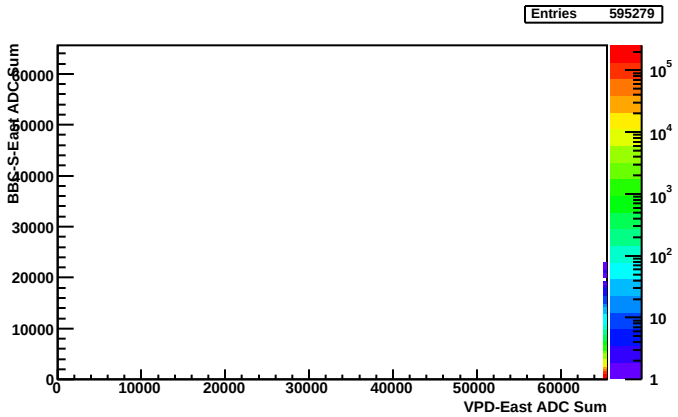
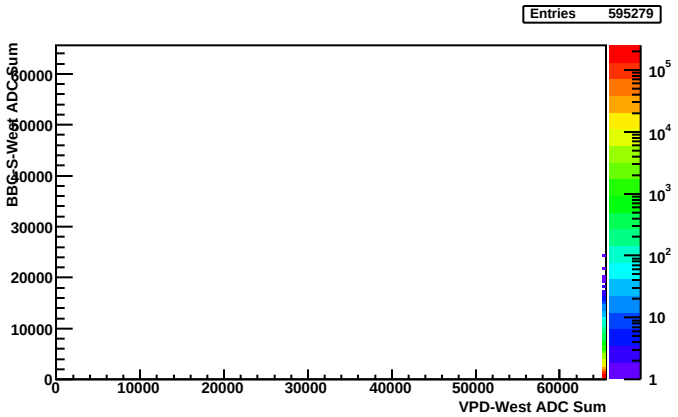
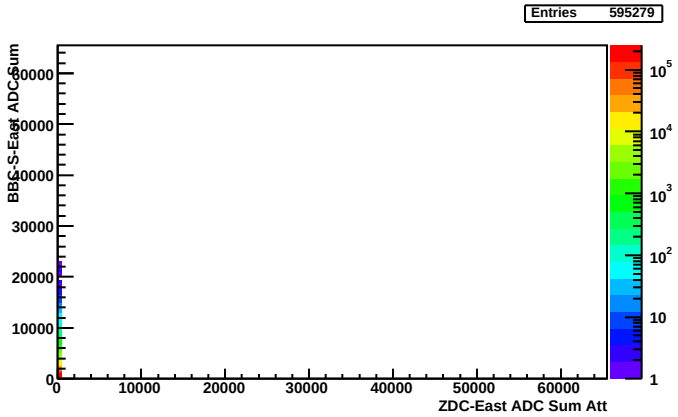
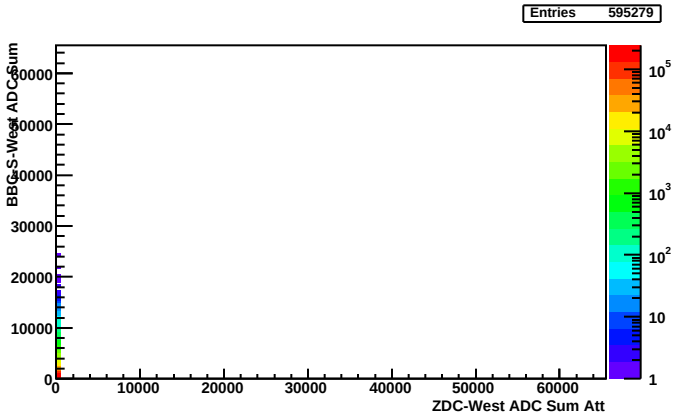
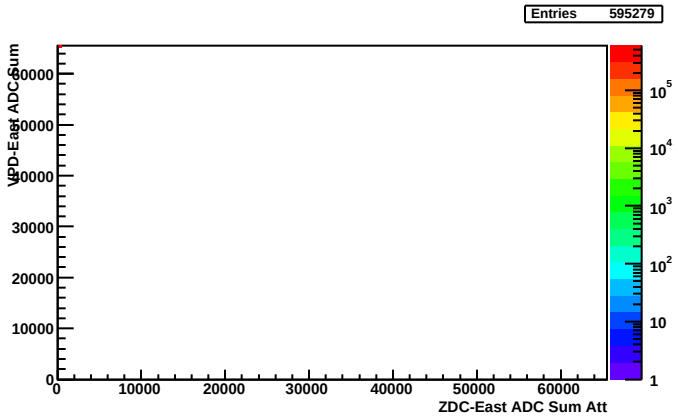
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

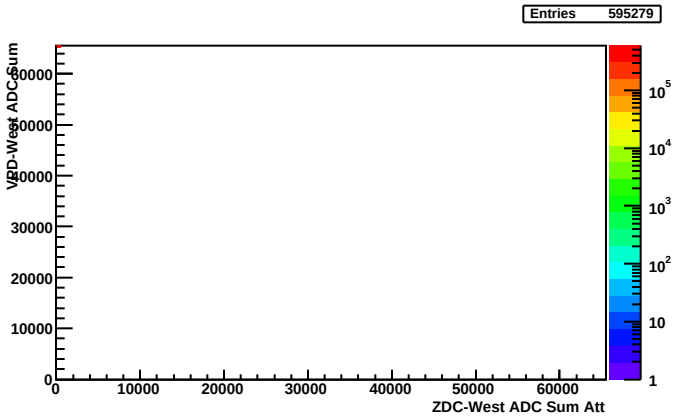

Entries 595279
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 595279
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 595279
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 595279
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 595279
VPD-East ADC Sum
ZDC-East ADC Sum Att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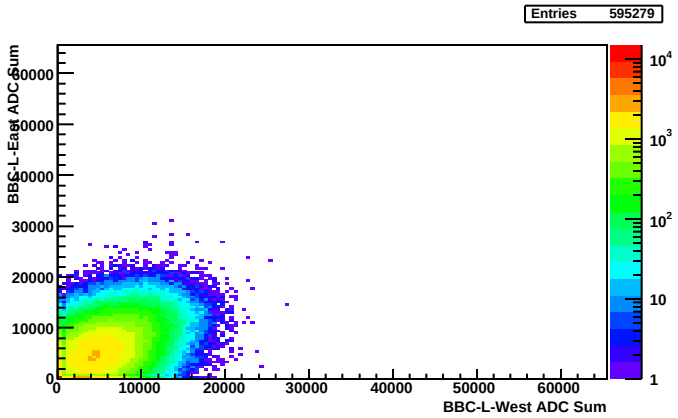
VPD-West ADC Sum
ZDC-West ADC Sum Att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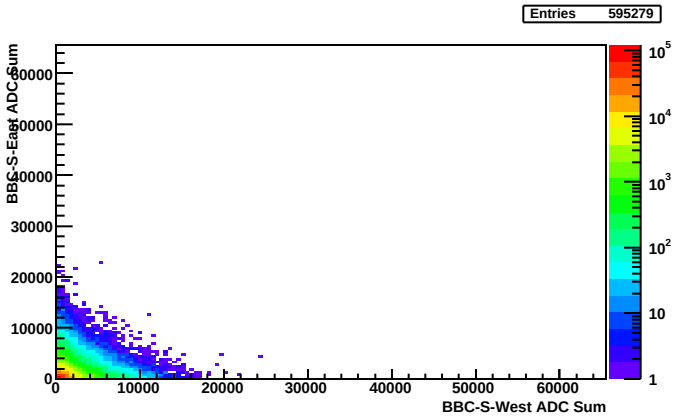
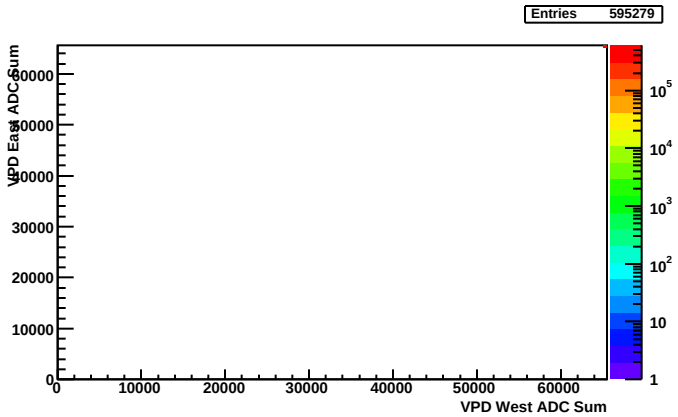
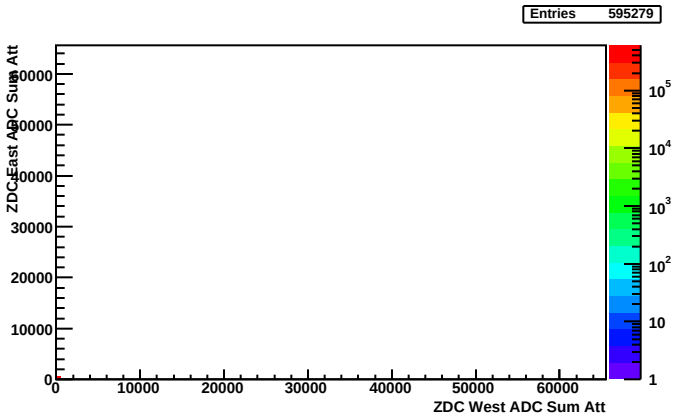
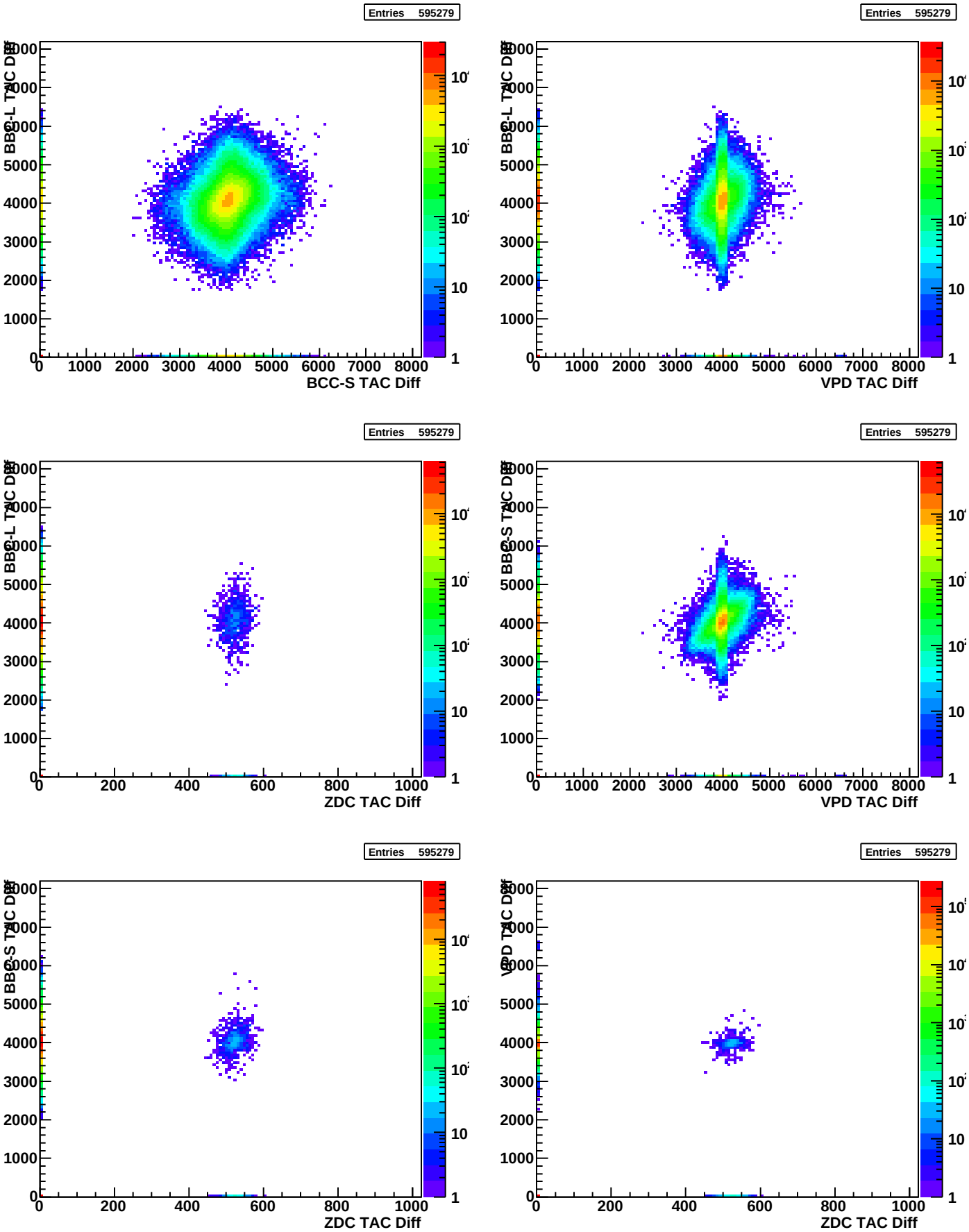
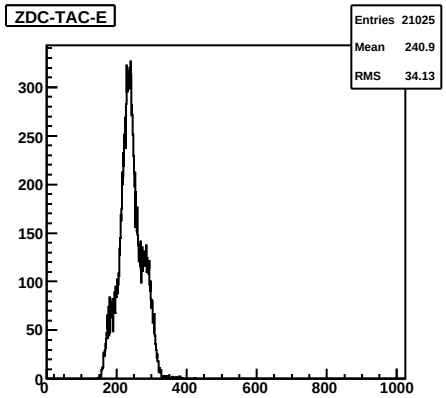
BBC-L-East ADC Sum
BBC-L-West ADC Sum

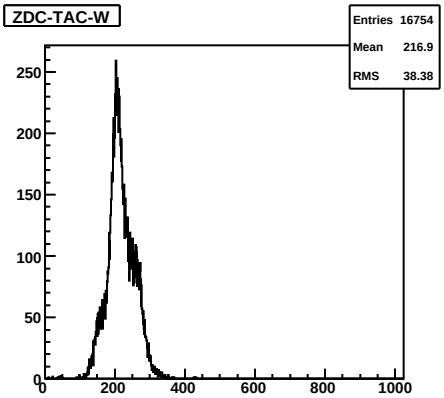

Entries 595279
BBC-S-East ADC Sum
BBC-S-West ADC Sum

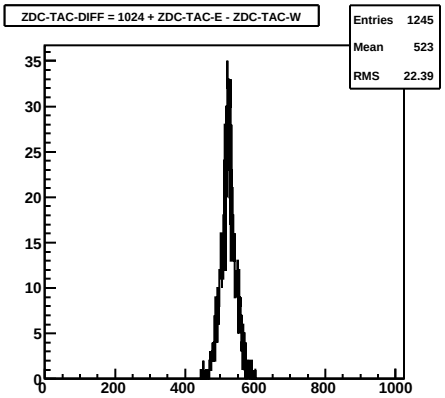

Entries 595279
VPD East ADC Sum
VPD West ADC Sum

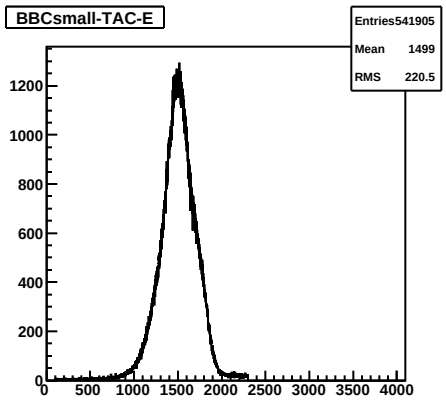

Entries 595279
ZDC East ADC Sum Att
ZDC West ADC Sum Att

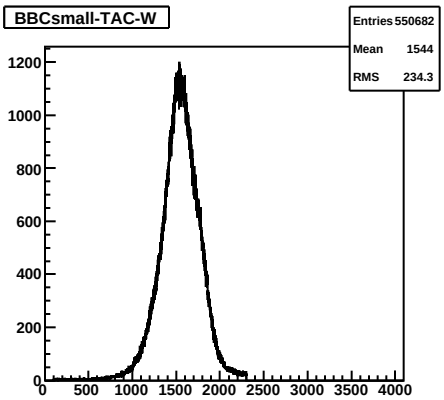

Entries 595279
BBC-L TAC Diff
BCC-S TAC Diff
Entries 595279
BBC-L TAC Diff
VPD TAC Diff
Entries 595279
BBC-L TAC Diff
ZDC TAC Diff
Entries 595279
BBC-S TAC Diff
VPD TAC Diff
Entries 595279
BBC-S TAC Diff
ZDC TAC Diff
Entries 595279
VPD TAC Diff
ZDC TAC Diff

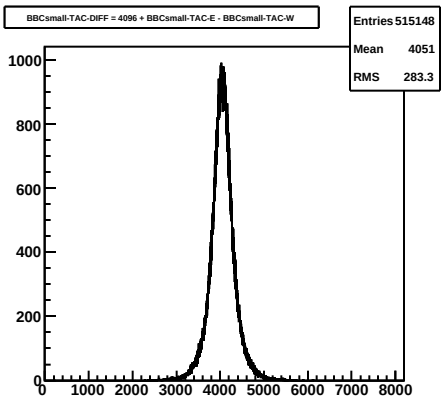

ZDC-TAC-E
Entries 21025
Mean 240.9
RMS 34.13

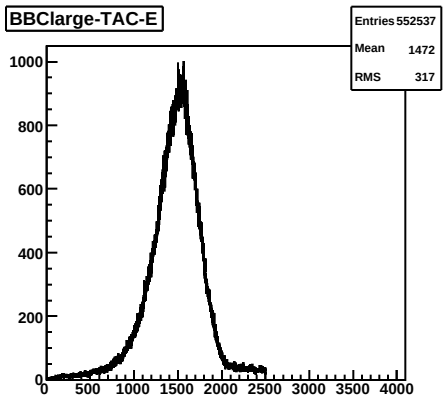

ZDC-TAC-W
Entries 16754
Mean 216.9
RMS 38.38

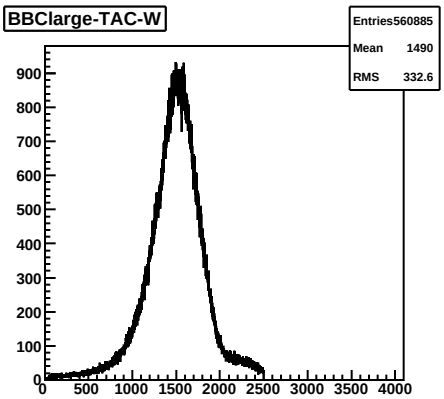

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 1245
Mean 523
RMS 22.39

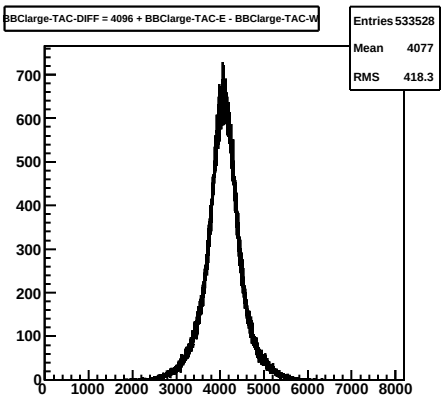

BBCsmall-TAC-E
Entries 541905
Mean 1499
RMS 220.5

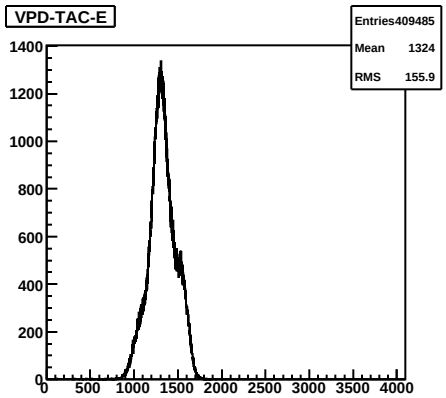

BBCsmall-TAC-W
Entries 550682
Mean 1544
RMS 234.3

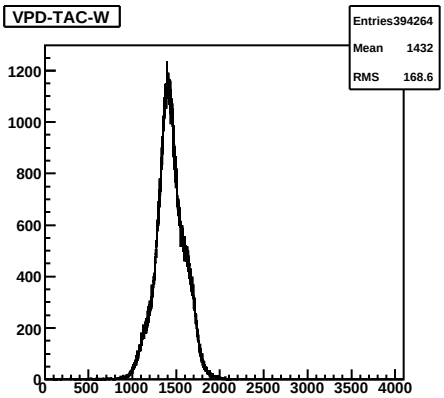

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 515148
Mean 4051
RMS 283.3

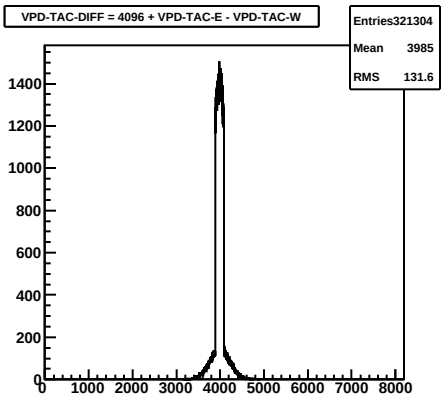

BBClarge-TAC-E
Entries 552537
Mean 1472
RMS 317

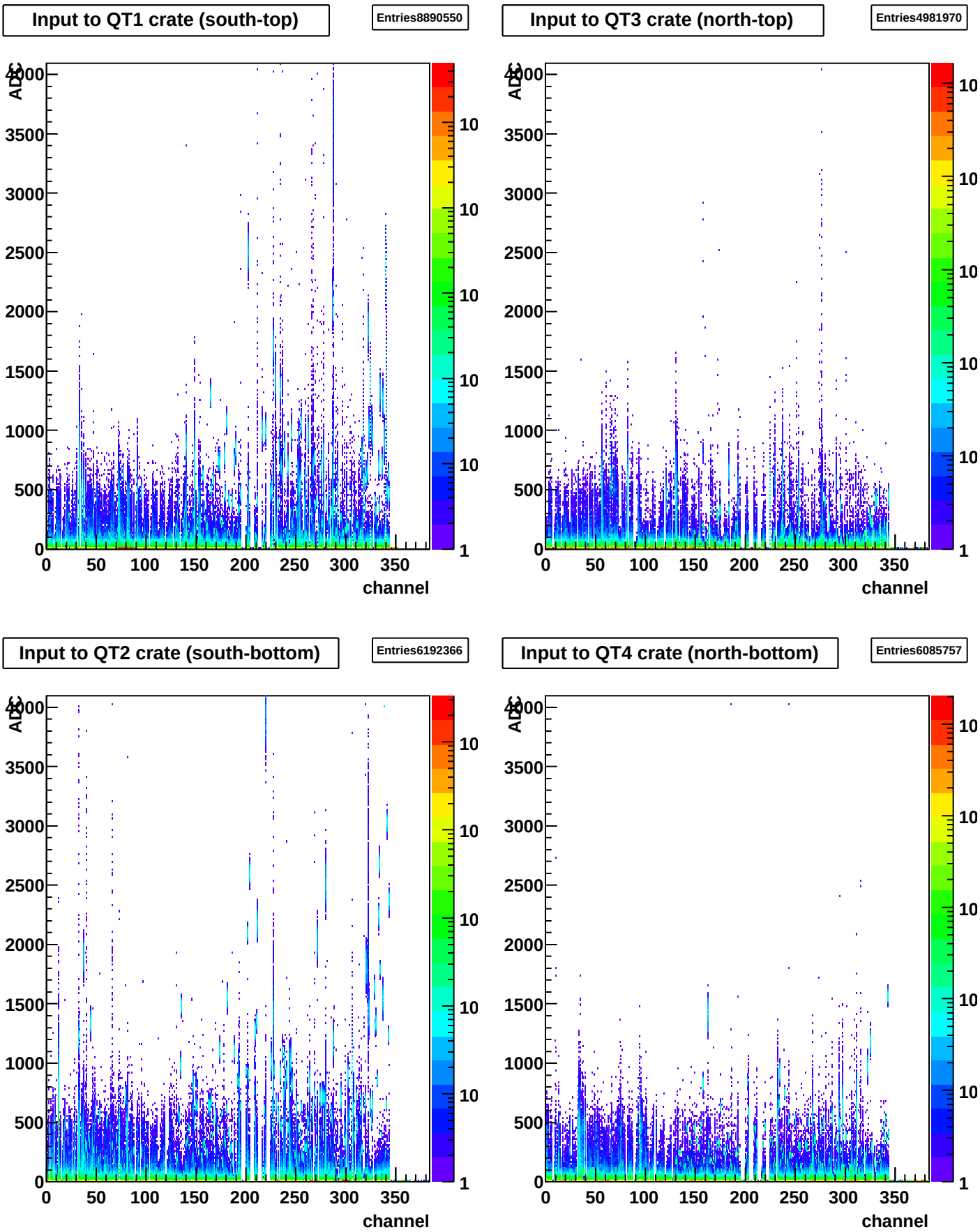

BBClarge-TAC-W
Entries 560885
Mean 1490
RMS 332.6

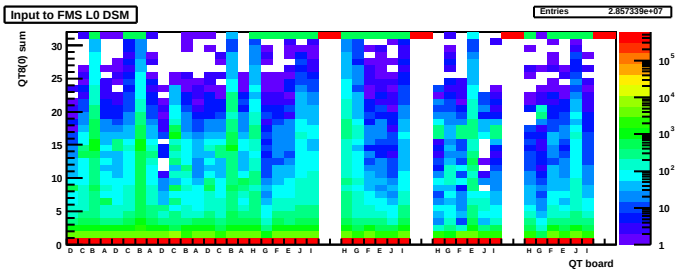

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 533528
Mean 4077
RMS 418.3

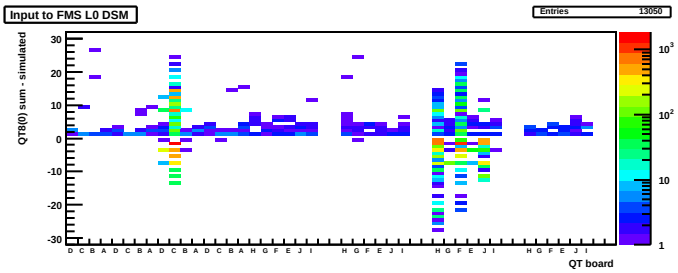

VPD-TAC-E
Entries 409485
Mean 1324
RMS 155.9

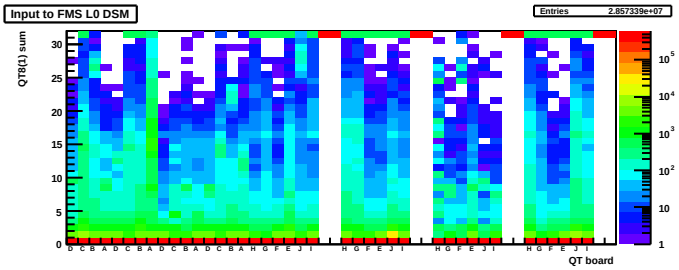

VPD-TAC-W
Entries 394264
Mean 1432
RMS 168.6

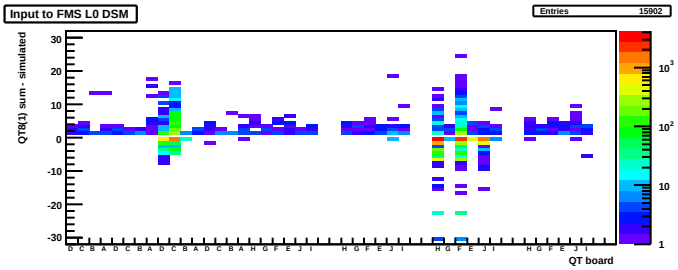

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 321304
Mean 3985
RMS 131.6

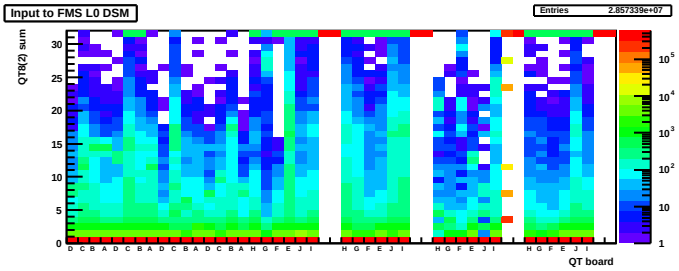

Input to QT1 crate (south-top)
Entries8890550
Input to QT3 crate (north-top)
Entries4981970
Input to QT2 crate (south-bottom)
Entries6192366
Input to QT4 crate (north-bottom)
Entries6085757
ADC
channel

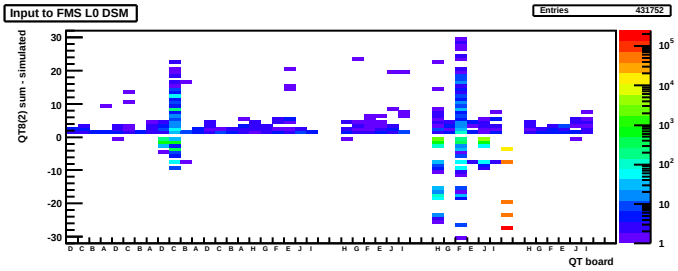

Input to FMS L0 DSM
Entries 2.857339e+07
QT8(0) sum
QT board

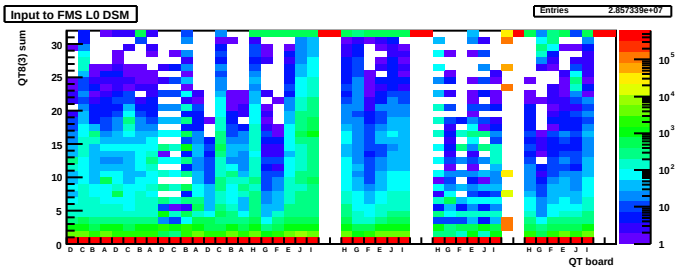

Input to FMS L0 DSM
Entries 13050
QT8(0) sum - simulated
QT board

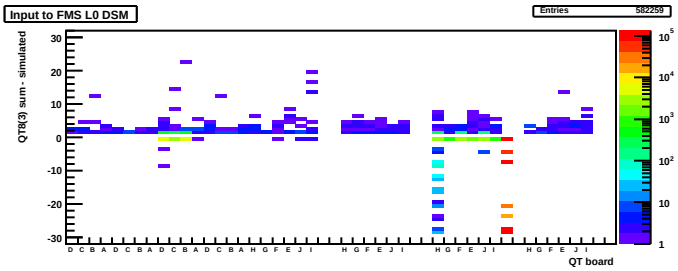

Input to FMS L0 DSM
Entries 2.857339e+07
QT8(1) sum
QT board

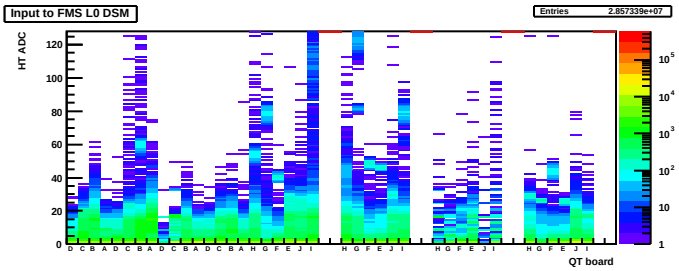

Input to FMS L0 DSM
Entries 15902
QT8(1) sum - simulated
QT board

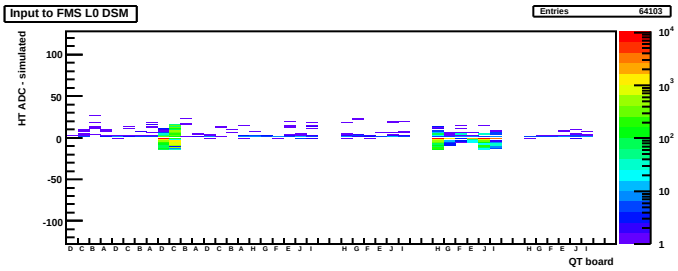

Input to FMS L0 DSM
Entries 2.857339e+07
QT8(2) sum
QT board

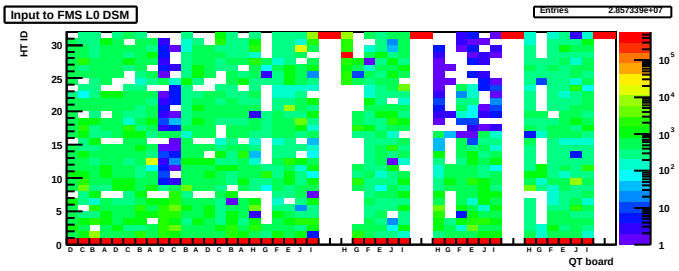

Input to FMS L0 DSM
Entries 431752
QT8(2) sum - simulated
QT board

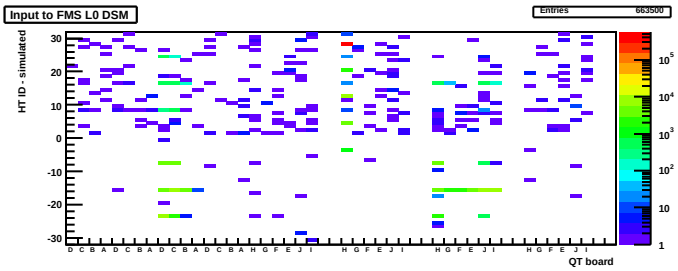

Input to FMS L0 DSM
Entries 2.857339e+07
QT8(3) sum
QT board

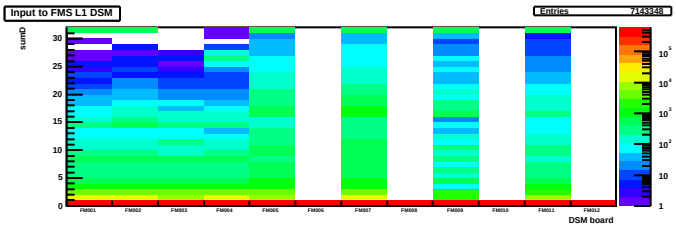

Input to FMS L0 DSM
Entries 582259
QT8(3) sum - simulated
QT board

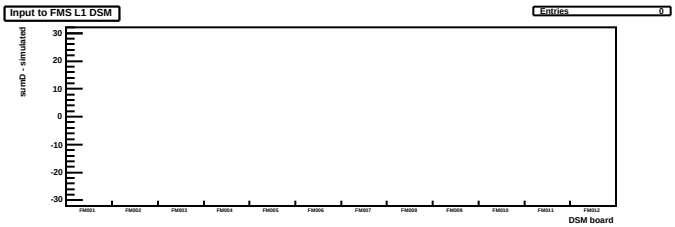

Input to FMS L0 DSM
Entries 2.857339e+07
HT ADC
QT board

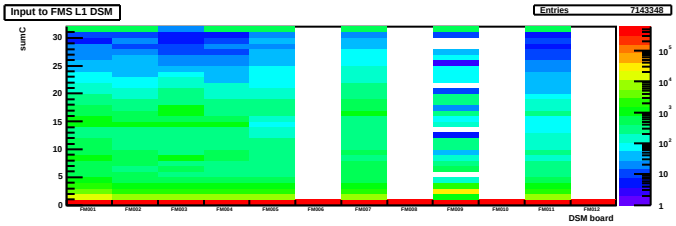

Input to FMS L0 DSM
Entries 64103
HT ADC - simulated
QT board

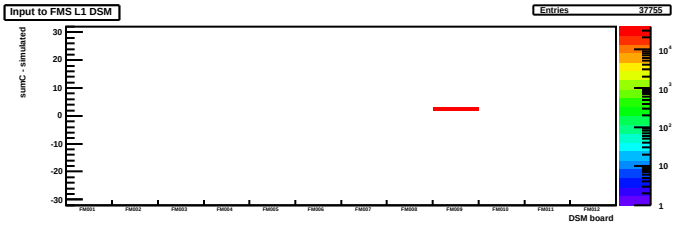

Input to FMS L0 DSM
Entries 2.857339e+07
HT ID
QT board

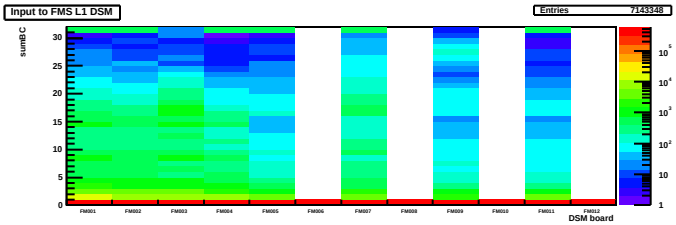

Input to FMS L0 DSM
Entries 663500
HT ID - simulated
QT board

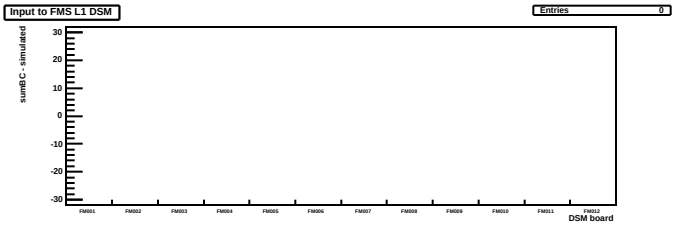

Input to FMS L1 DSM
Entries 7143348
sumD
DSM board

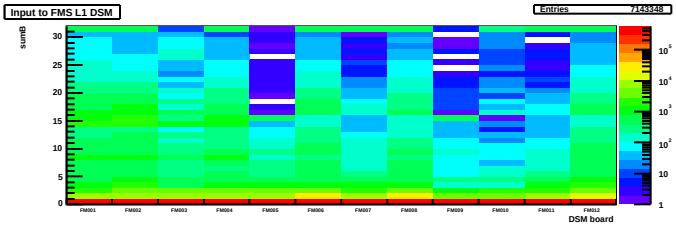

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

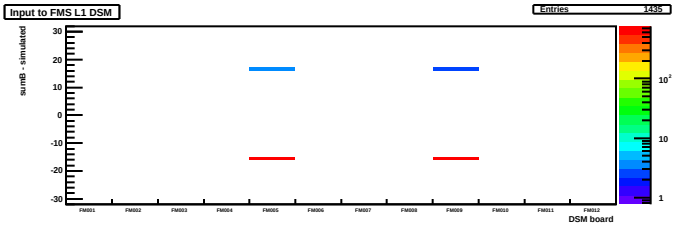

Input to FMS L1 DSM
Entries 7143348
sumC
DSM board

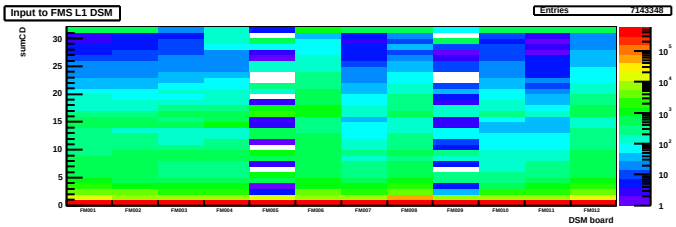

Input to FMS L1 DSM
Entries 37755
sumC - simulated
DSM board

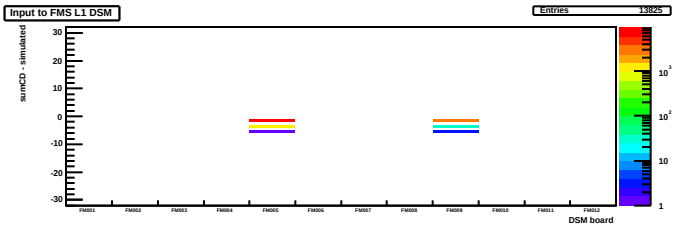

Input to FMS L1 DSM
Entries 7143348
sumBC
DSM board

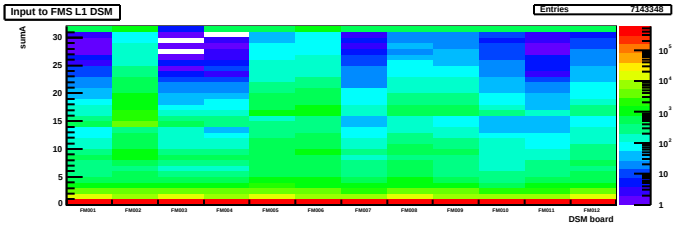

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

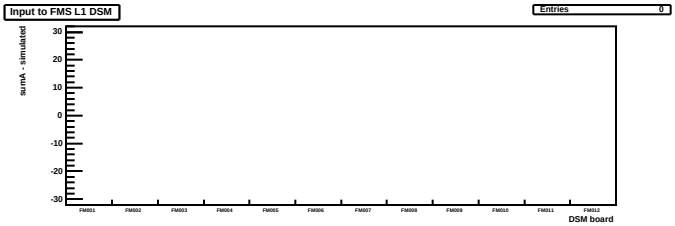

Input to FMS L1 DSM
Entries 7143348
sumB
DSM board

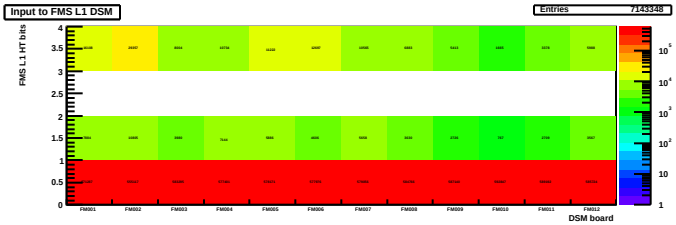

Input to FMS L1 DSM
Entries 1435
sumB - simulated
DSM board

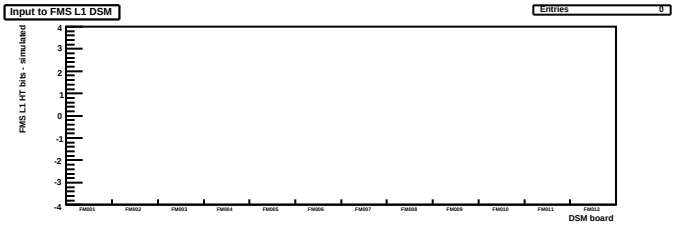

Input to FMS L1 DSM
Entries 7143348
sumCD
DSM board

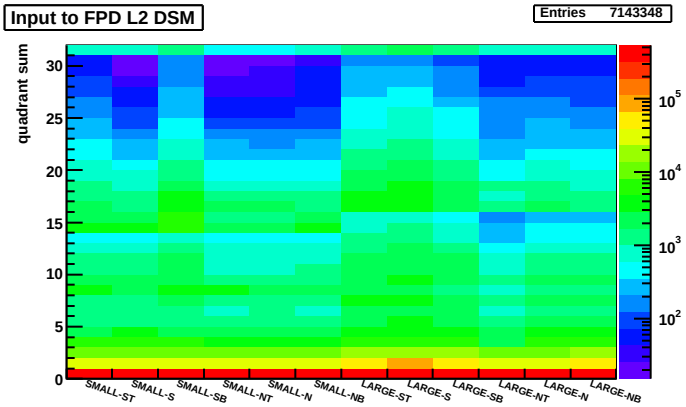

Input to FMS L1 DSM
Entries 13825
sumCD - simulated
DSM board

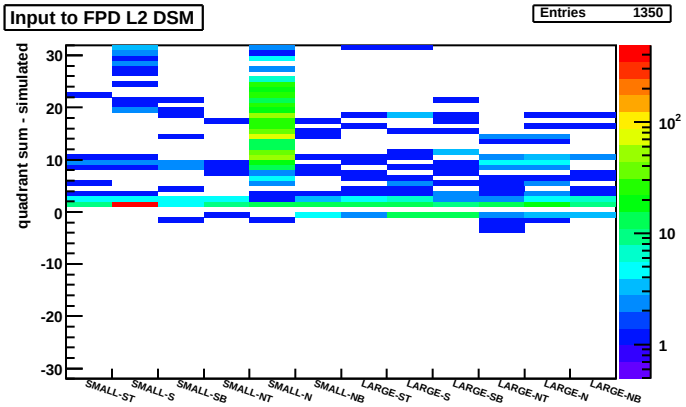

Input to FMS L1 DSM
Entries 7143348
sumA
DSM board

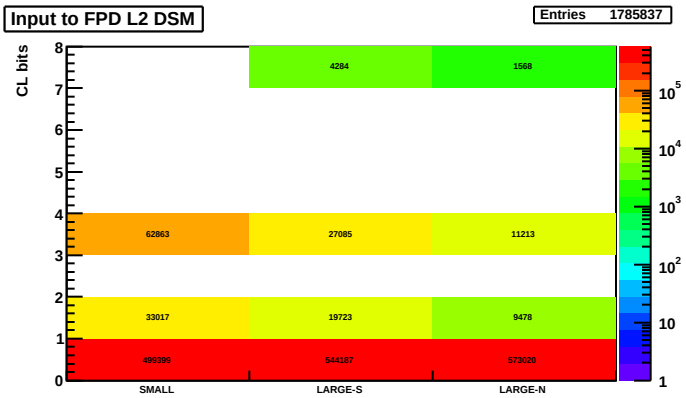

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

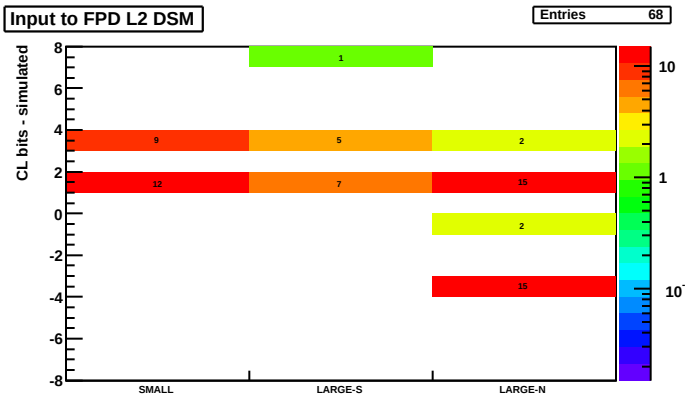

Input to FMS L1 DSM
Entries 7143348
FMS L1 HT bits
DSM board

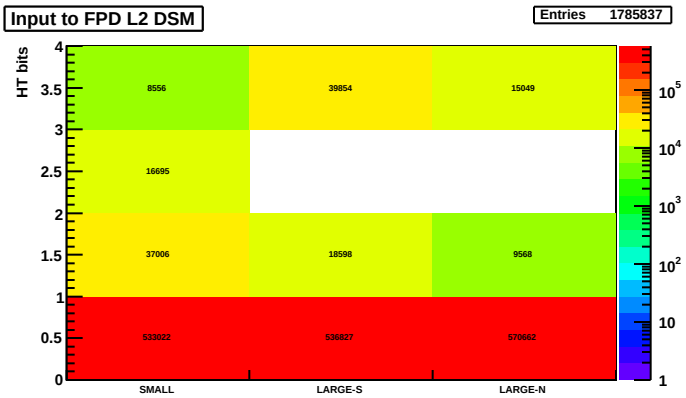

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

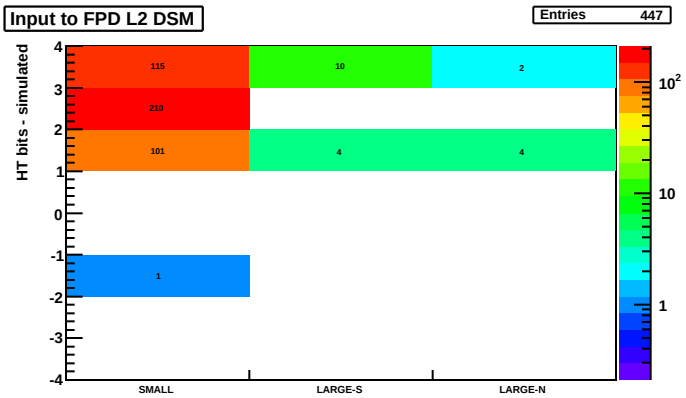

Input to FPD L2 DSM
Entries 7143348
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

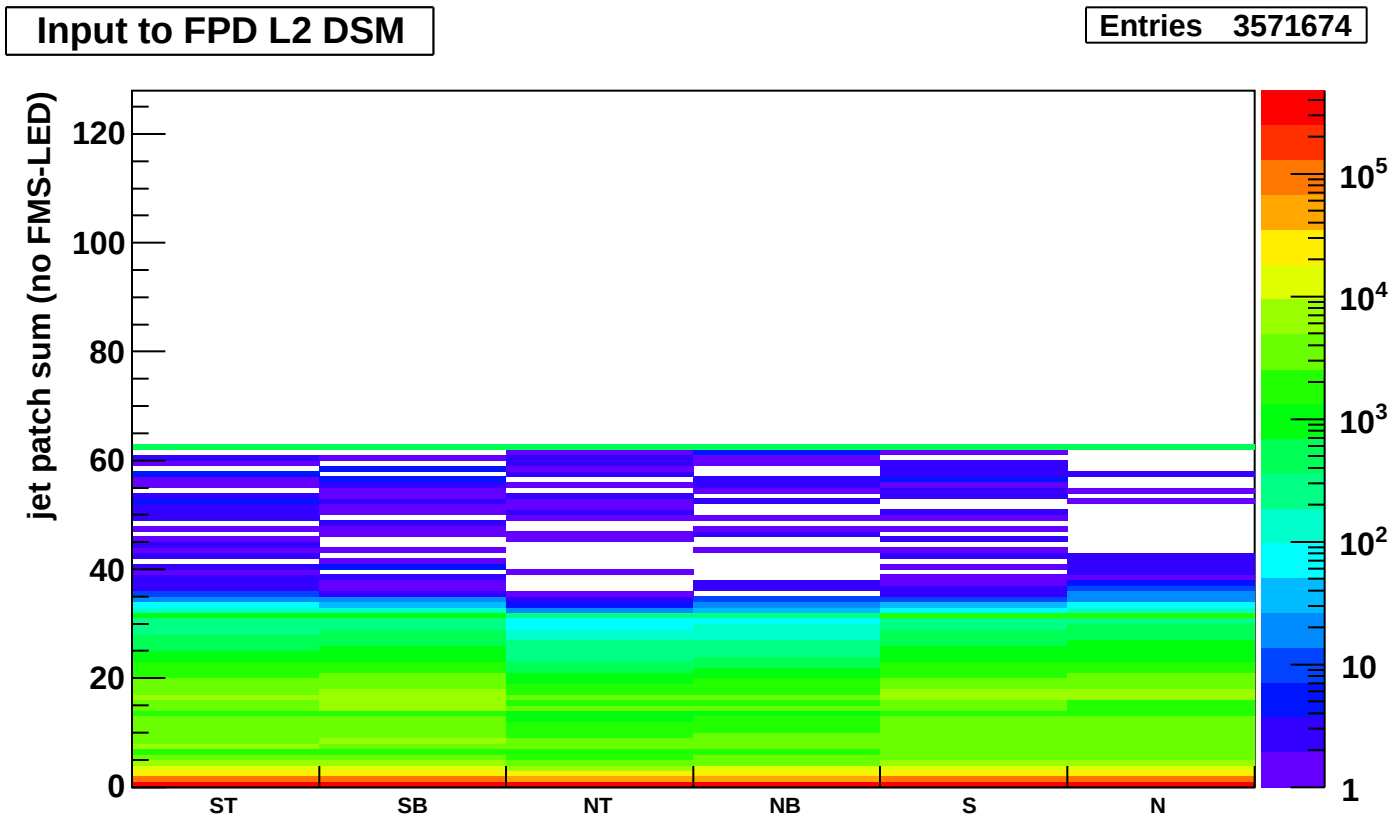

Input to FPD L2 DSM
Entries 1350
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

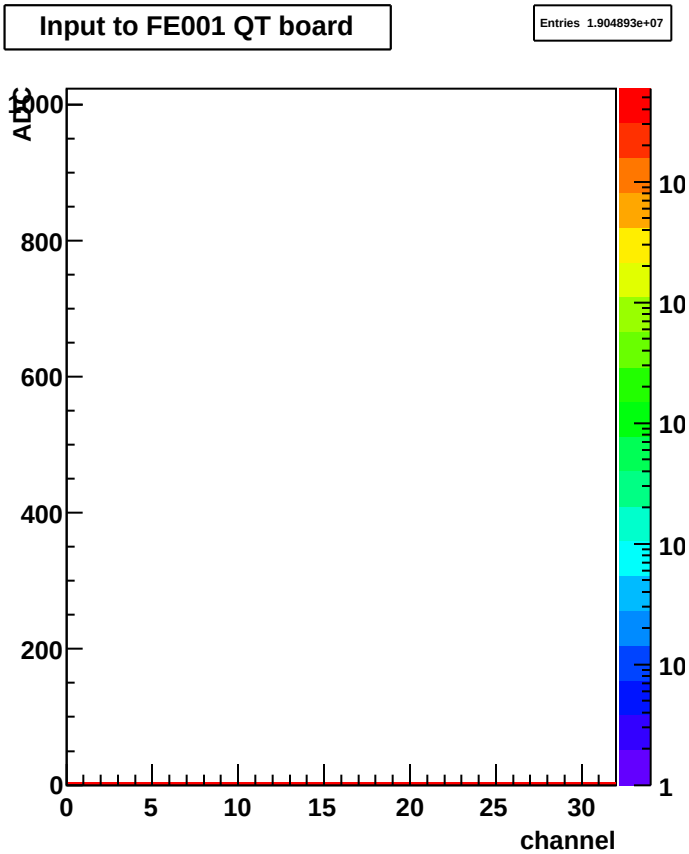

Input to FPD L2 DSM
Entries 1785837
CL bits
4284
1568
62863
27085
11213
33017
19723
9478
499399
544187
573020
SMALL
LARGE-S
LARGE-N

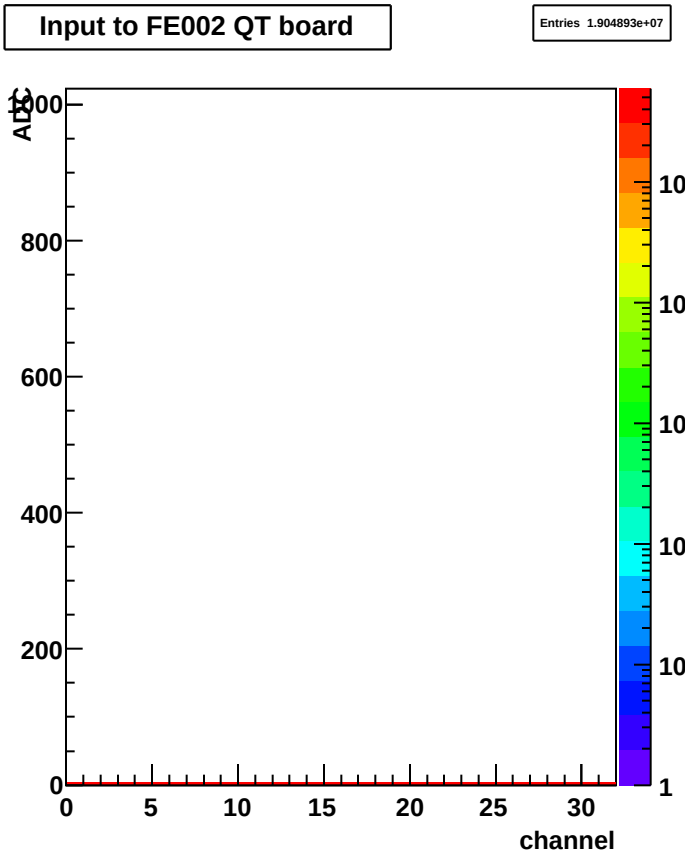

Input to FPD L2 DSM
Entries 68
CL bits - simulated
1
9
5
2
12
7
15
2
15
SMALL
LARGE-S
LARGE-N

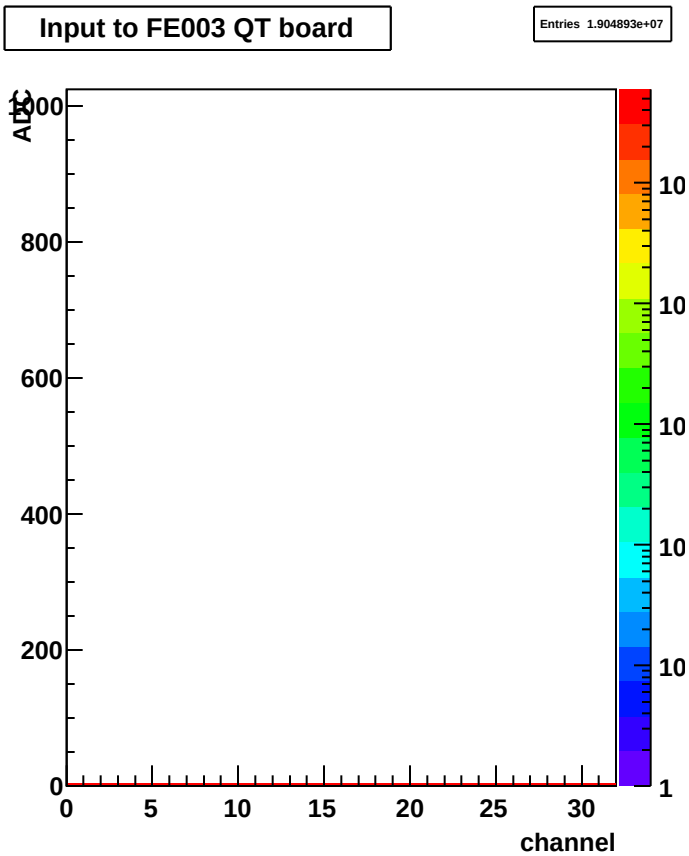

Input to FPD L2 DSM
Entries 1785837
HT bits
8556
39854
15049
16695
37006
18598
9568
533022
536827
570662
SMALL
LARGE-S
LARGE-N

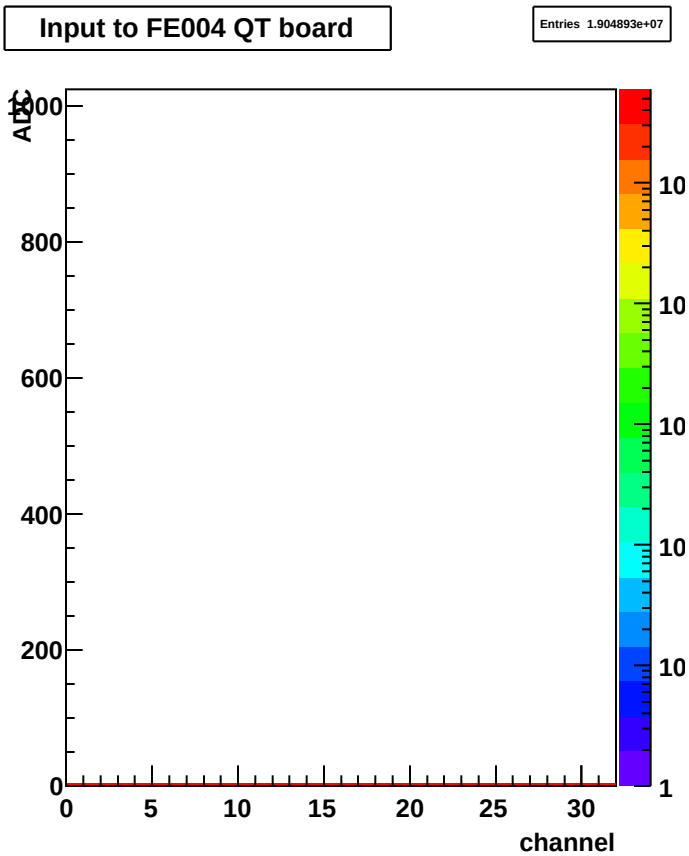

Input to FPD L2 DSM
Entries 447
HT bits - simulated
115
10
2
210
101
4
4
1
SMALL
LARGE-S
LARGE-N

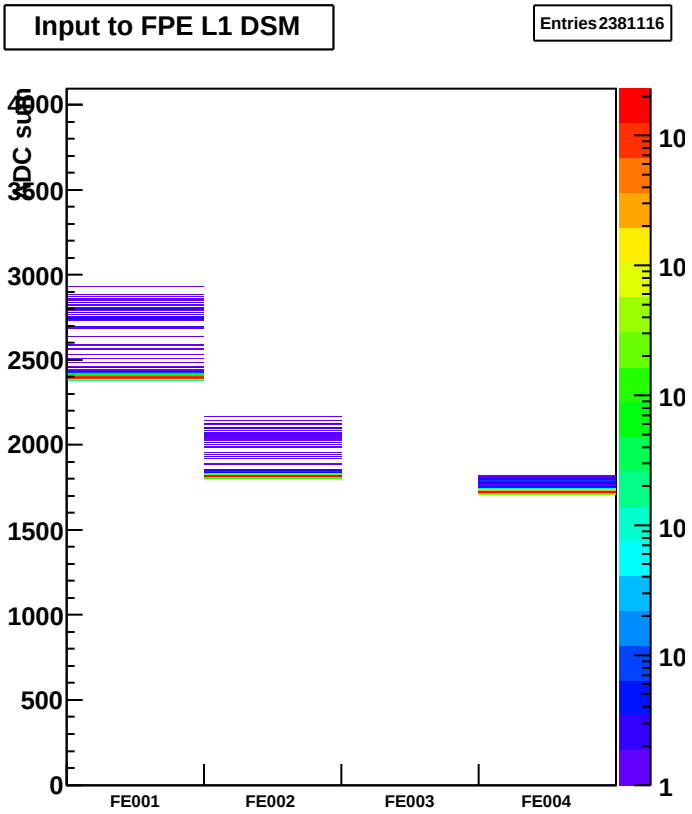

Input to FPD L2 DSM
Entries 3571674
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.904893e+07
ADC
channel

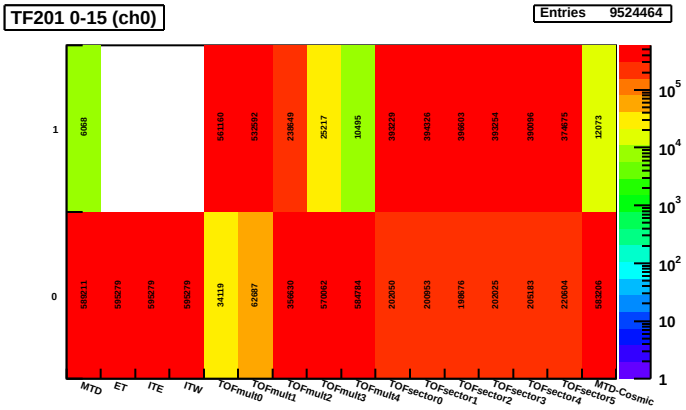

Input to FE002 QT board
Entries 1.904893e+07
ADC
channel


Input to FE003 QT board
Entries 1.904893e+07
ADC
channel

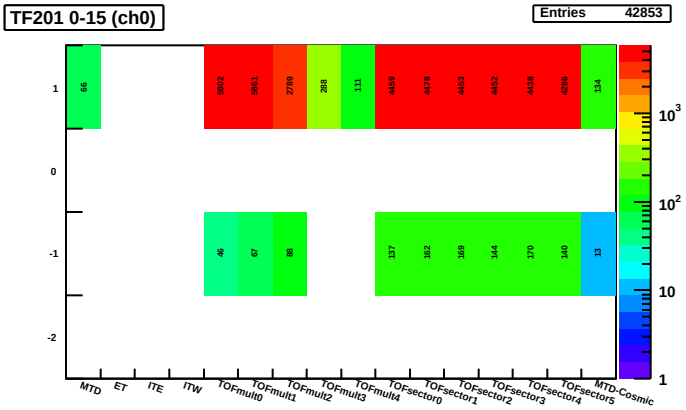

Input to FE004 QT board
Entries 1.904893e+07
ADC
channel


Input to FPE L1 DSM
Entries 2381116
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

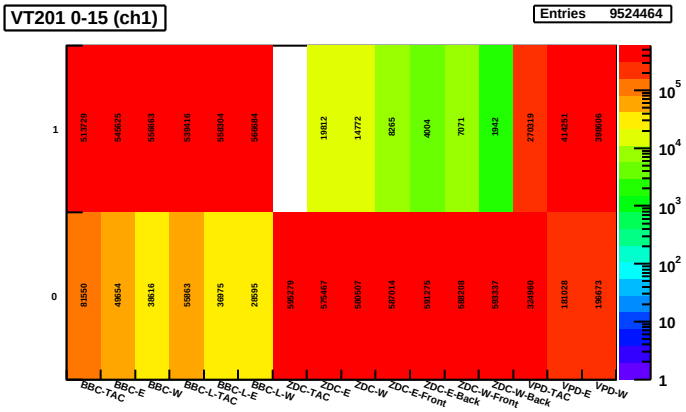
TF201 0-15 (ch0)



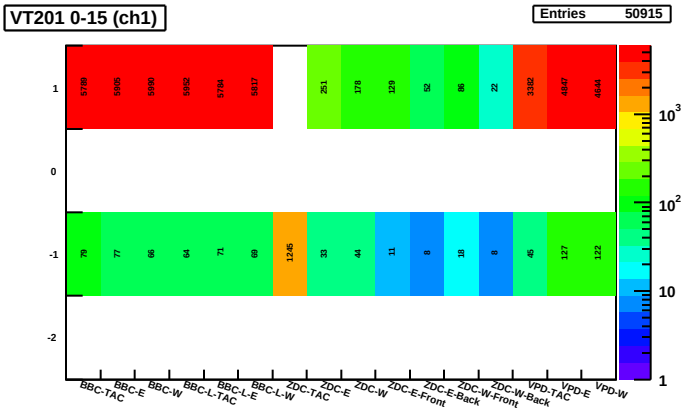
TF201 0-15 (ch0)



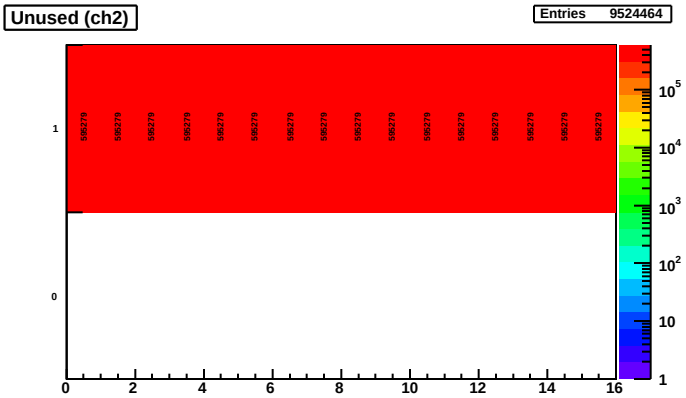
VT201 0-15 (ch1)

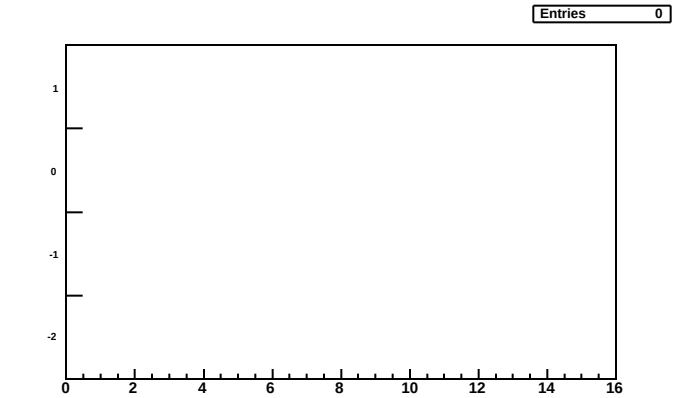


VT201 0-15 (ch1)

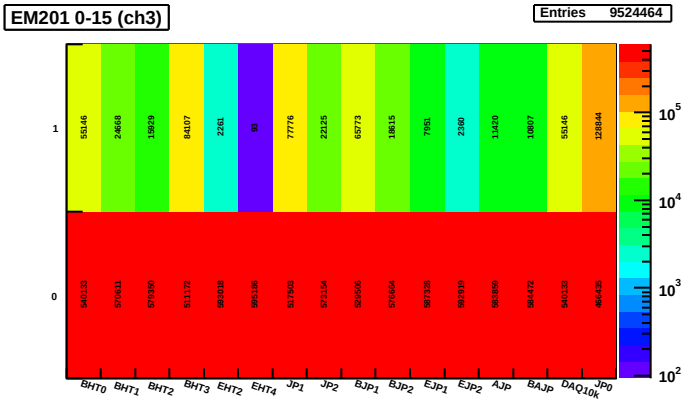


Unused (ch2)

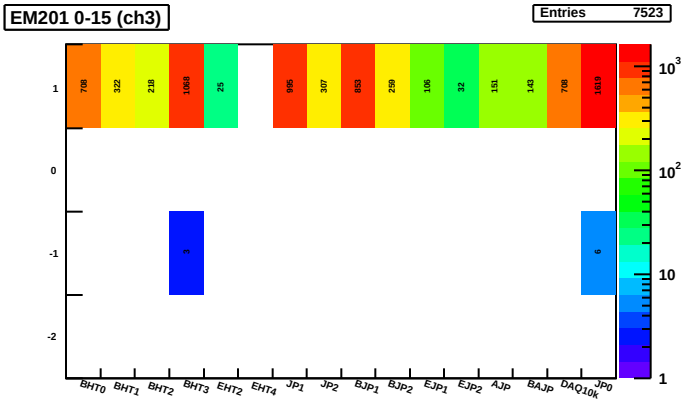




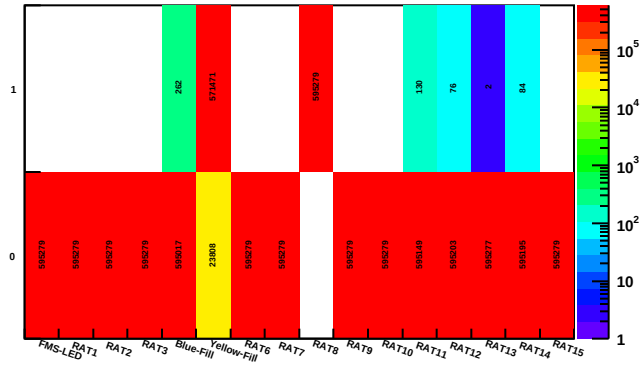
EM201 0-15 (ch3)

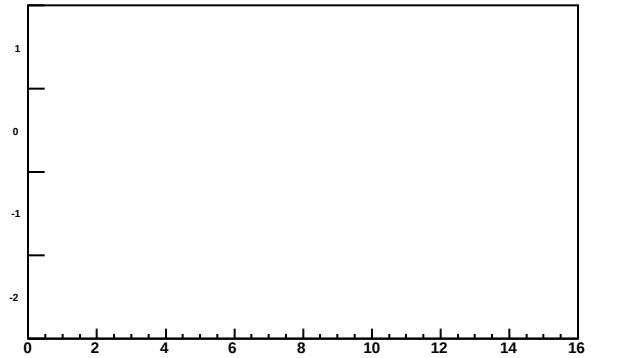


EM201 0-15 (ch3)

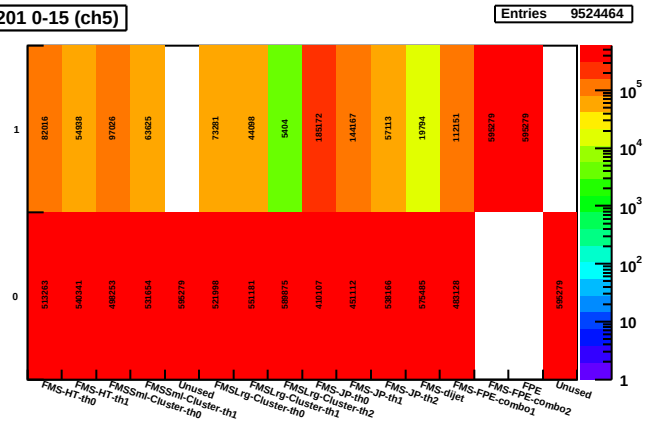


RAT board (ch4)





FP201 0-15 (ch5)



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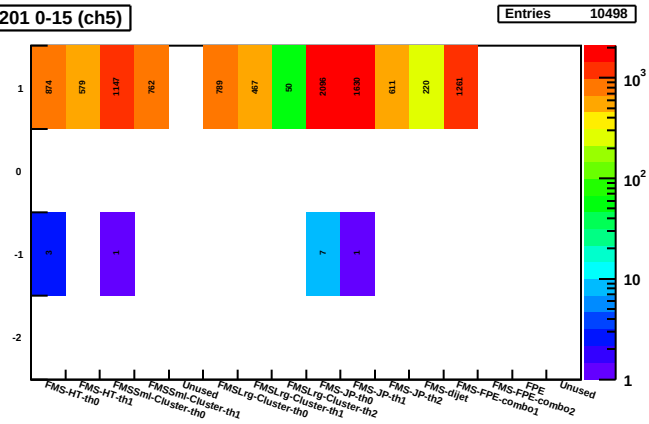

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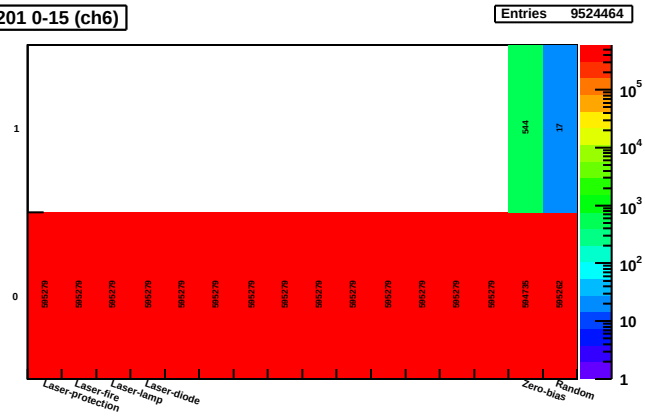

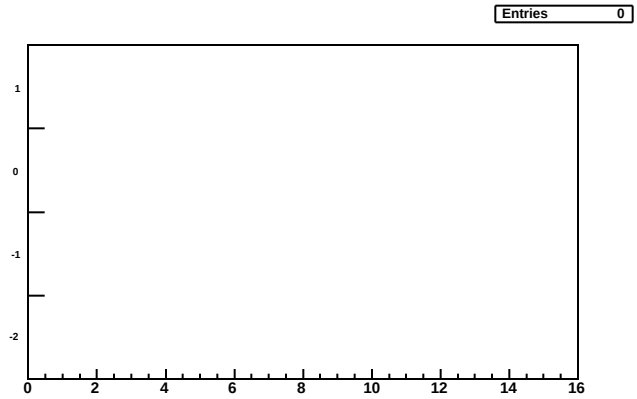
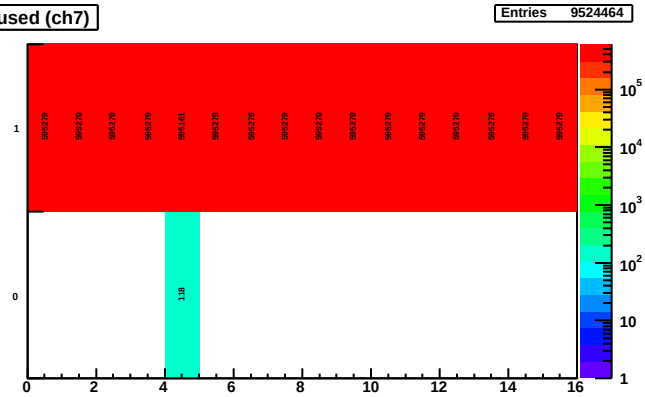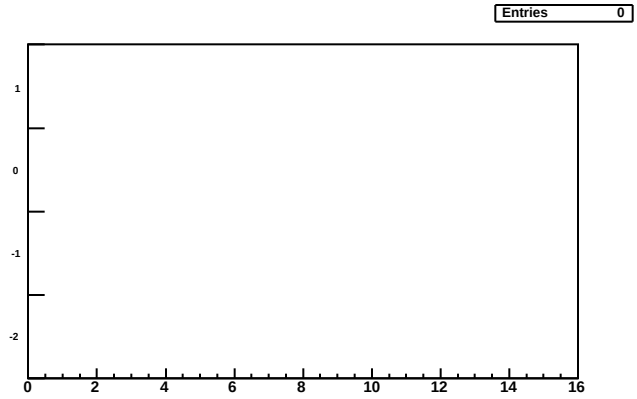
Unused (ch7)