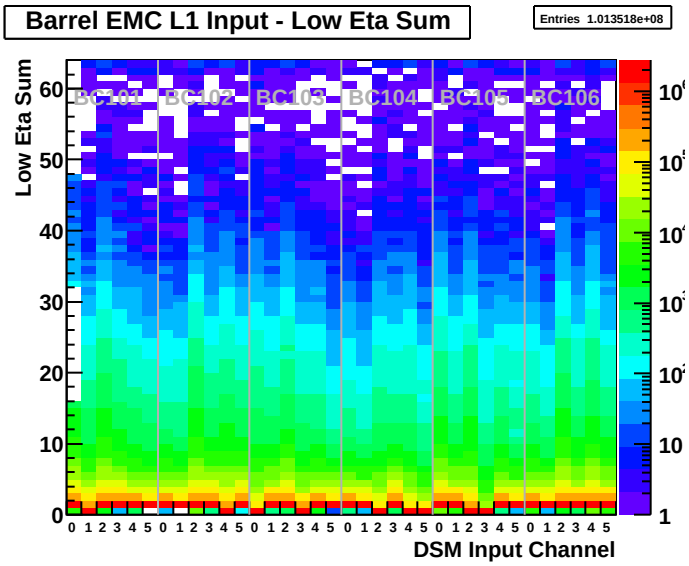
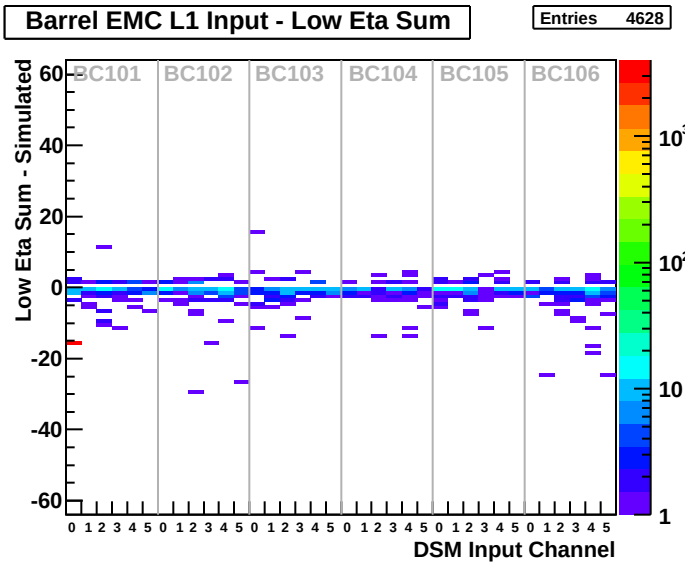
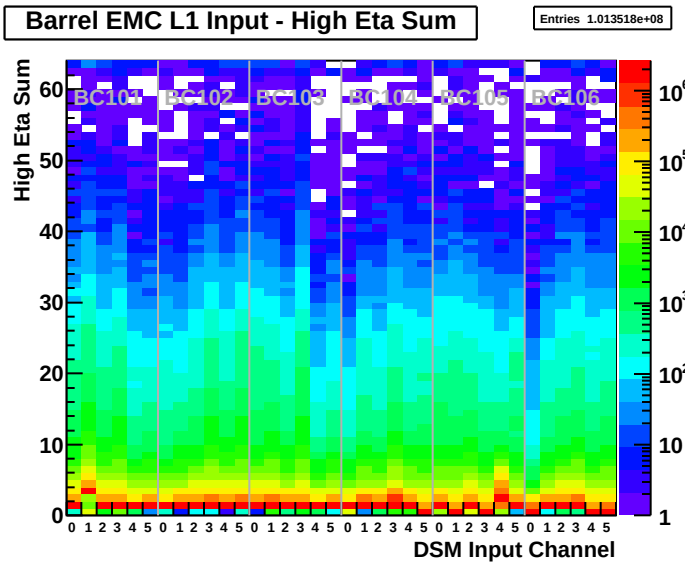
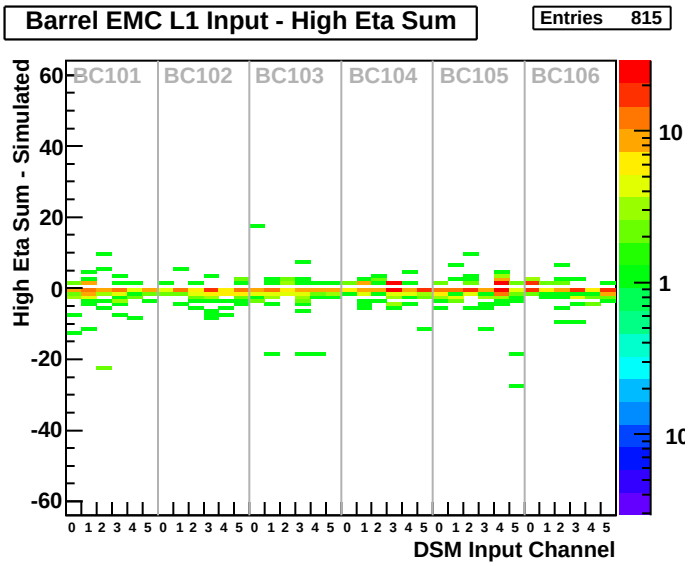
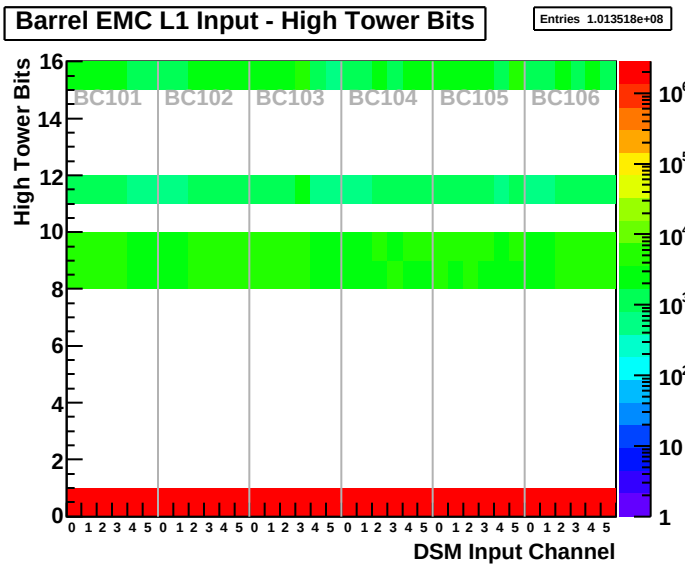
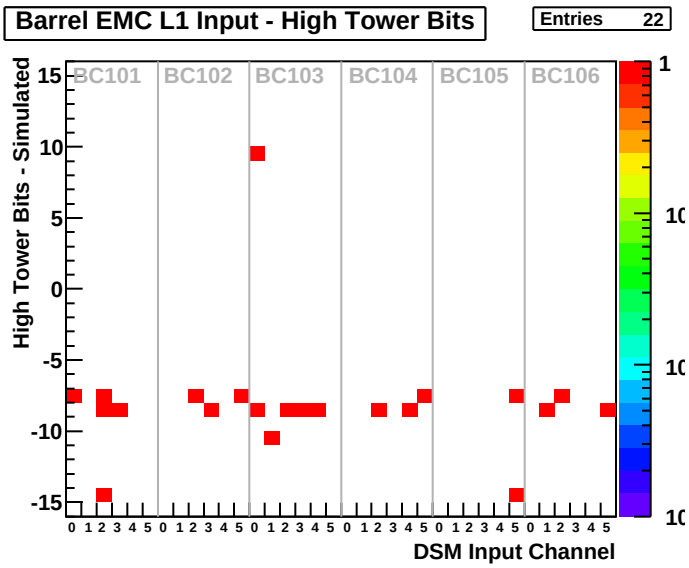

Barrel EMC L1 Input - Low Eta Sum
Entries 1.013518e+08
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

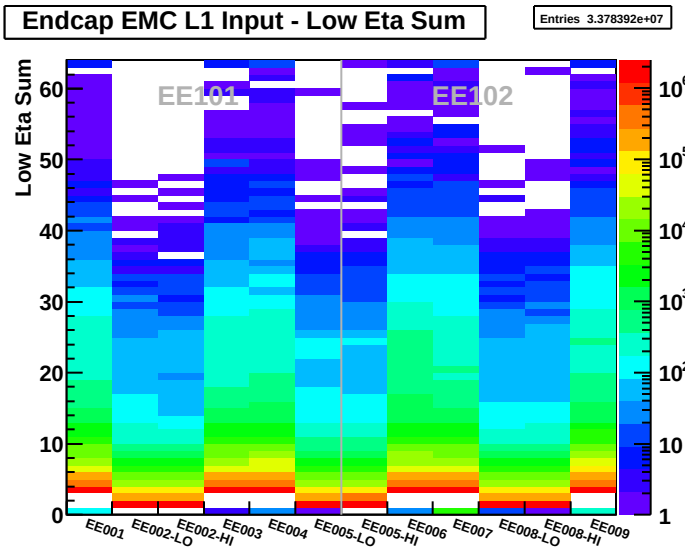

Barrel EMC L1 Input - Low Eta Sum
Entries 4628
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

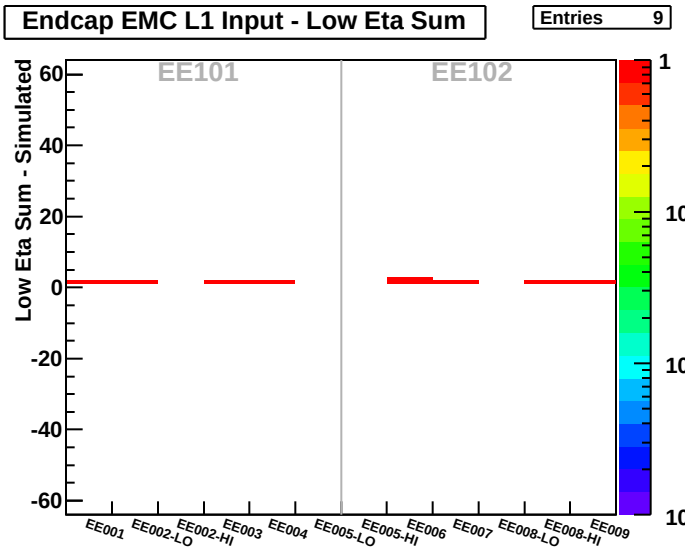

Barrel EMC L1 Input - High Eta Sum
Entries 1.013518e+08
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

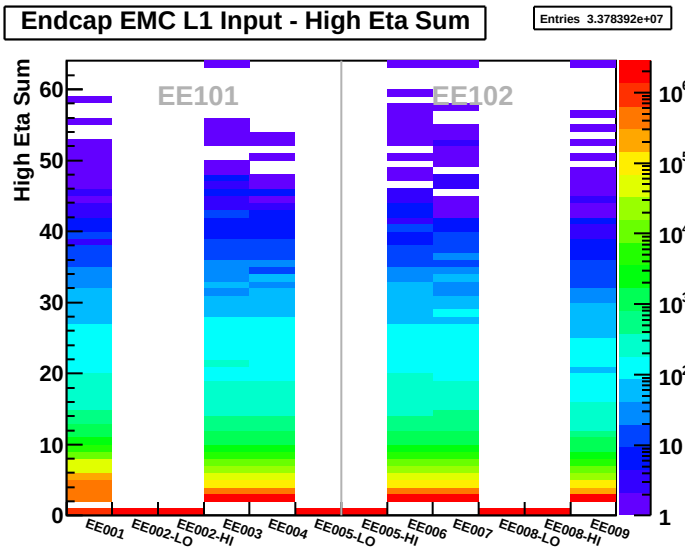

Barrel EMC L1 Input - High Eta Sum
Entries 815
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

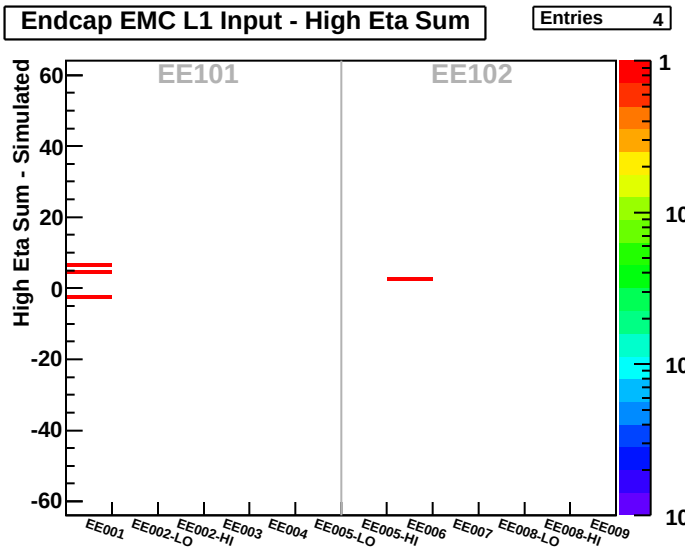

Barrel EMC L1 Input - High Tower Bits
Entries 1.013518e+08
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

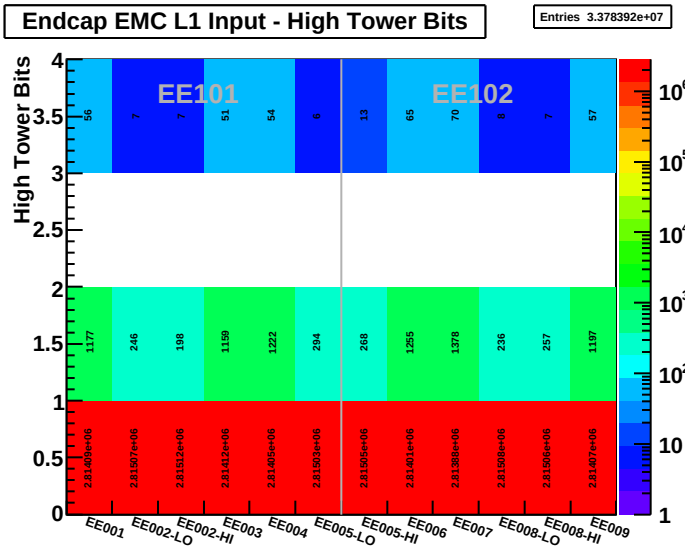

Barrel EMC L1 Input - High Tower Bits
Entries 22
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

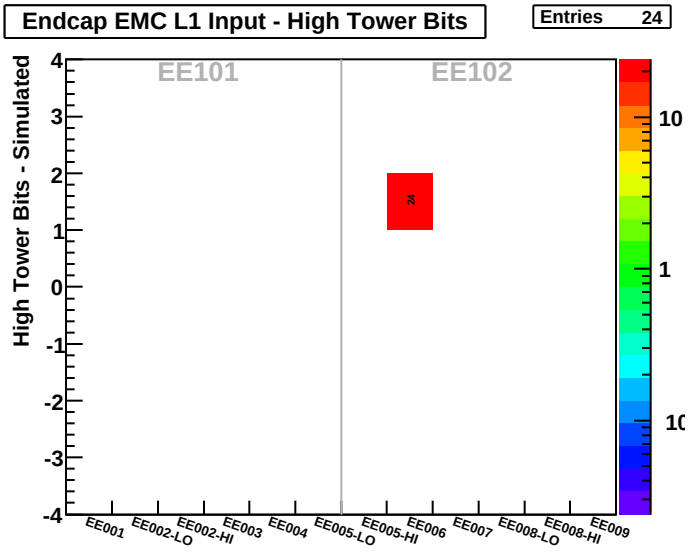

Endcap EMC L1 Input - Low Eta Sum
Entries 3.378392e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

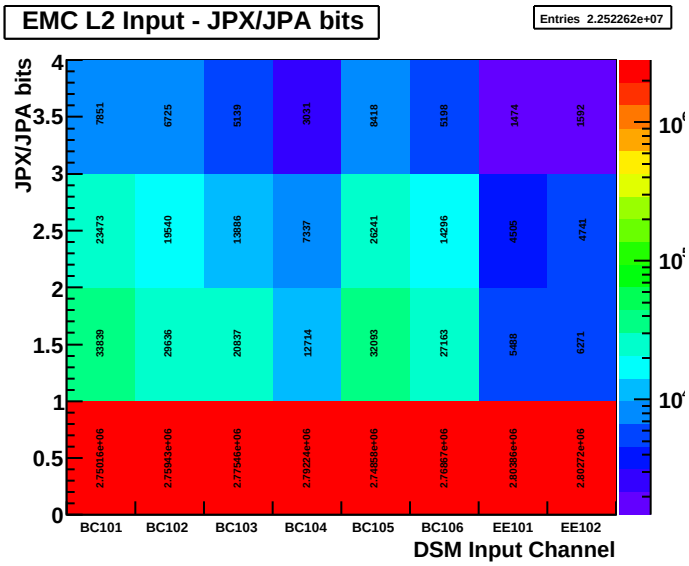

Endcap EMC L1 Input - Low Eta Sum
Entries 9
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

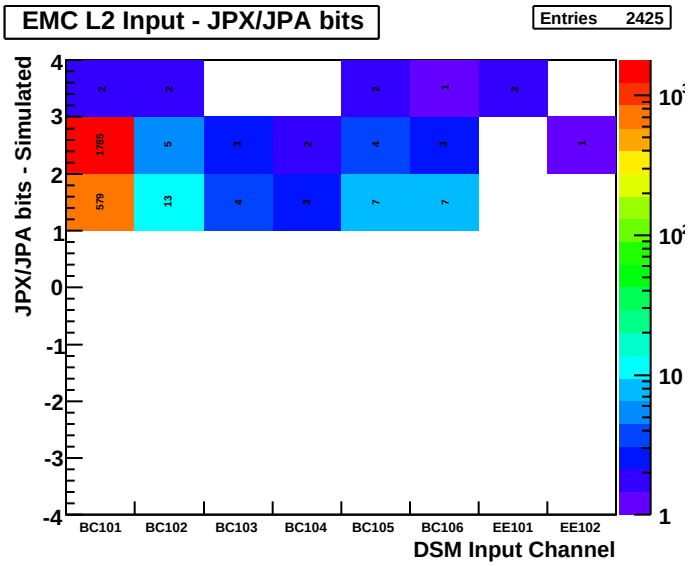

Endcap EMC L1 Input - High Eta Sum
Entries 3.378392e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

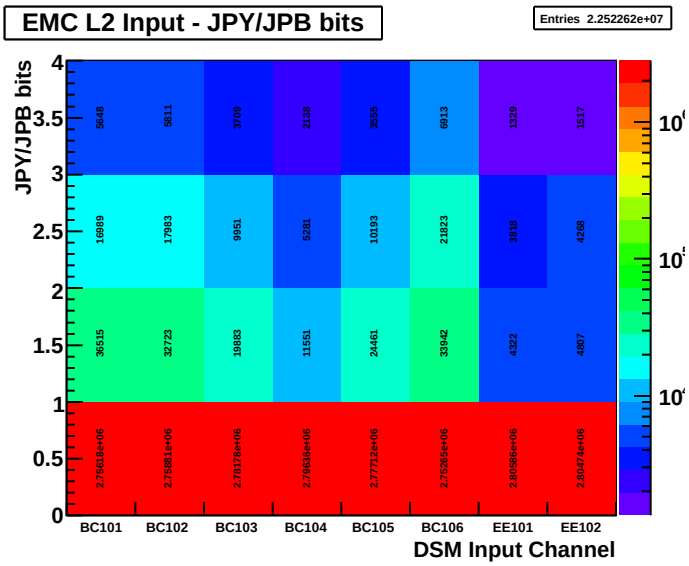

Endcap EMC L1 Input - High Eta Sum
Entries 4
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

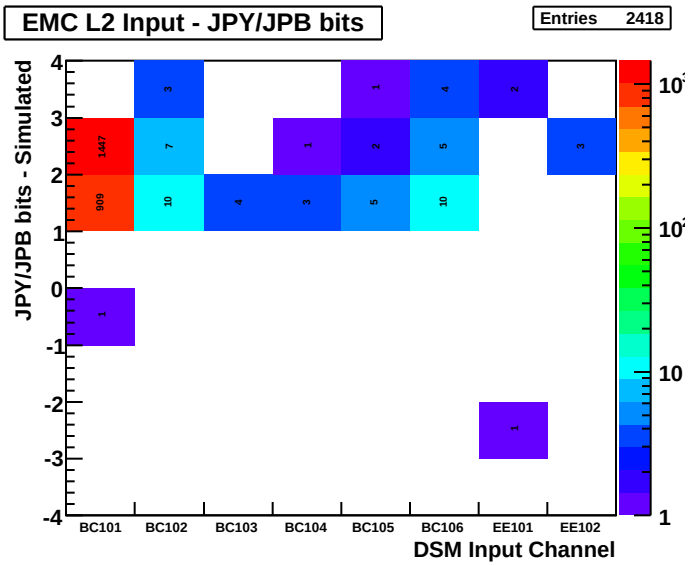

Endcap EMC L1 Input - High Tower Bits
Entries 3.378392e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

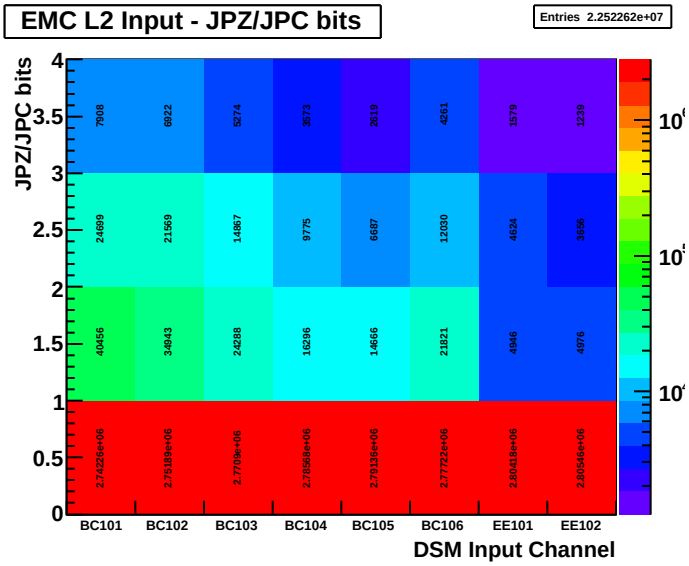

Endcap EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

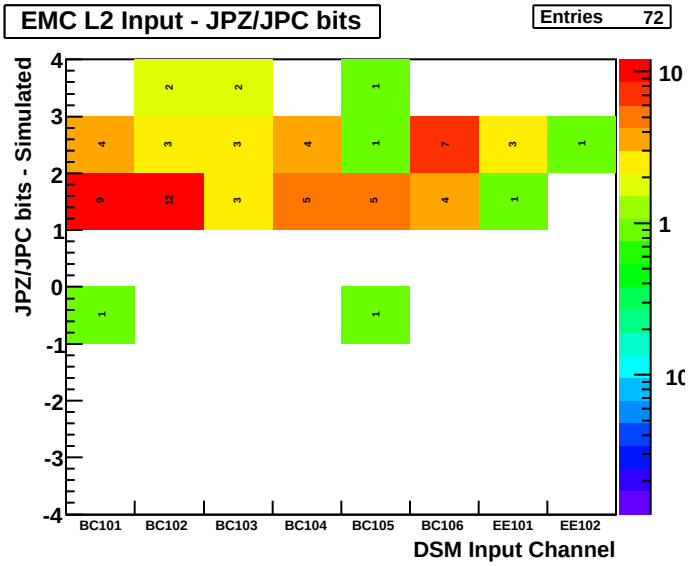

EMC L2 Input - JPX/JPA bits
Entries 2.252262e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

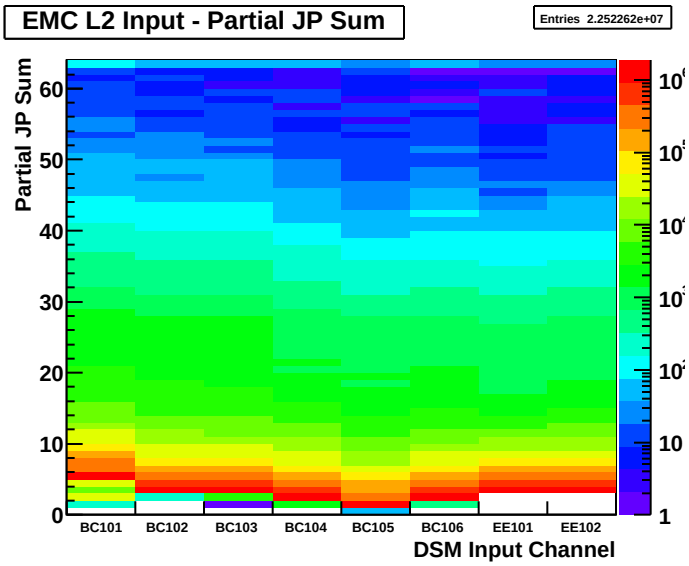

EMC L2 Input - JPX/JPA bits
Entries 2425
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

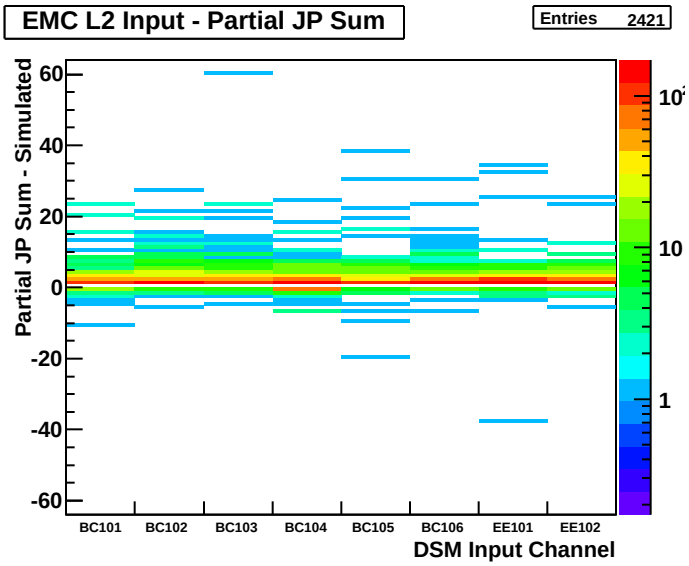

EMC L2 Input - JPY/JPB bits
Entries 2.252262e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

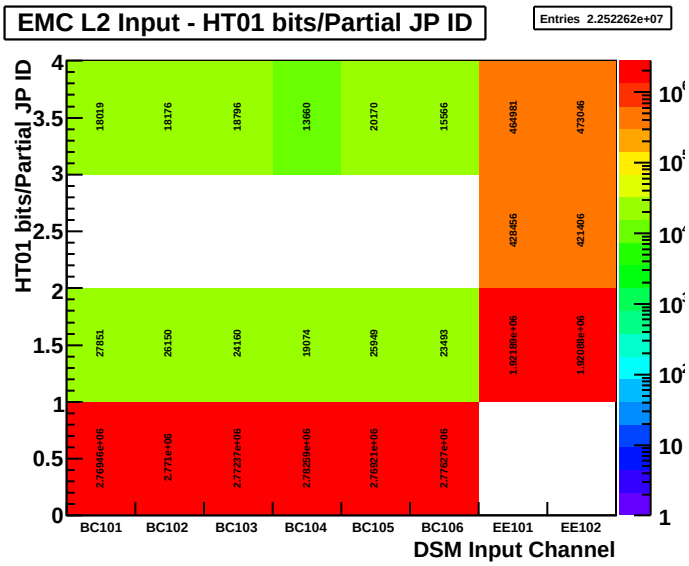

EMC L2 Input - JPY/JPB bits
Entries 2418
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

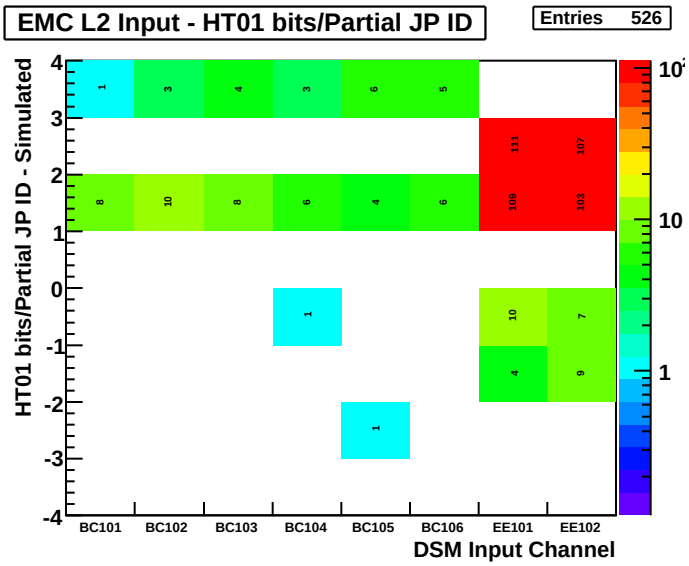

EMC L2 Input - JPZ/JPC bits
Entries 2.252262e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

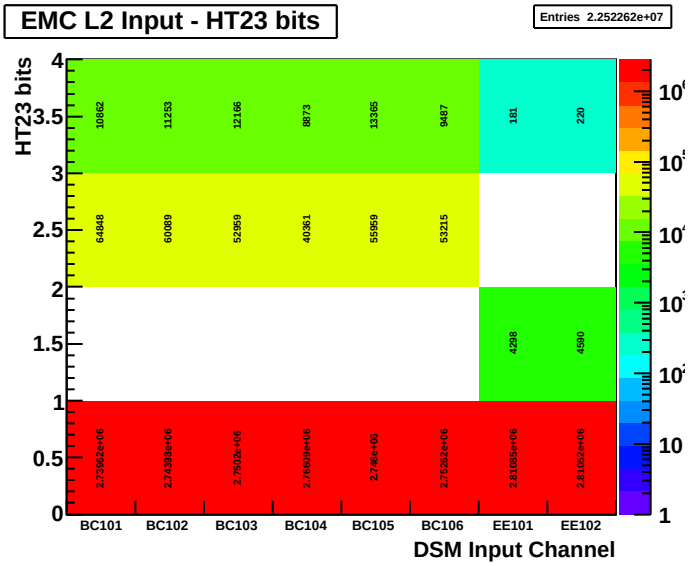

EMC L2 Input - JPZ/JPC bits
Entries 72
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

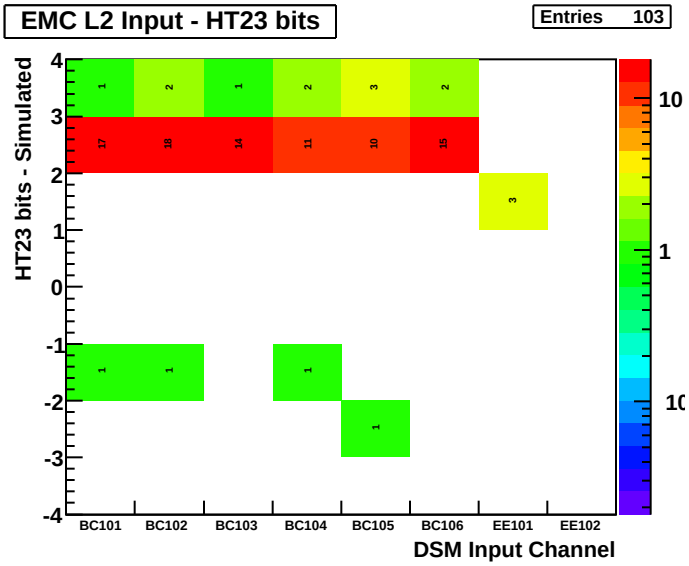

EMC L2 Input - Partial JP Sum
Entries 2.252262e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

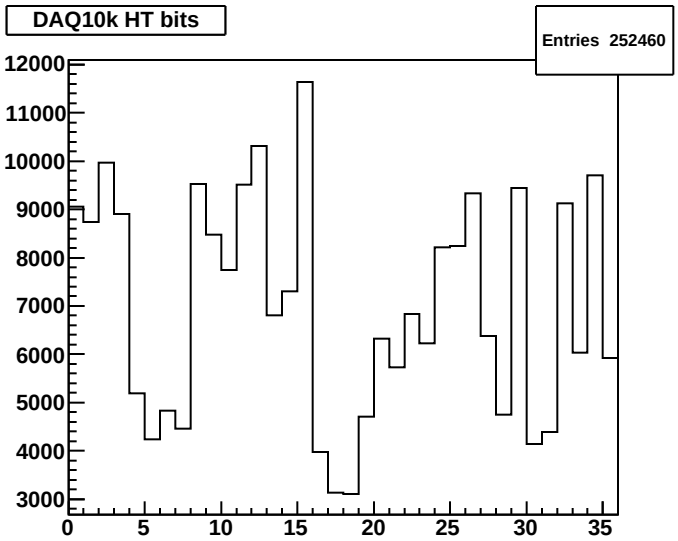

EMC L2 Input - Partial JP Sum
Entries 2421
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

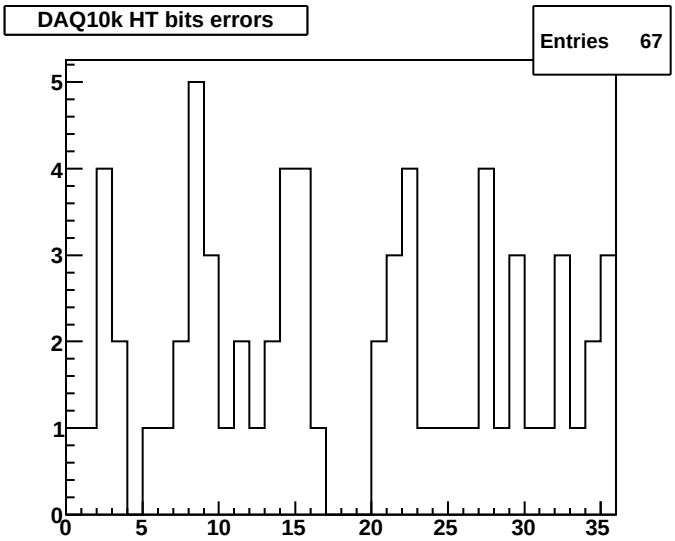

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2.252262e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

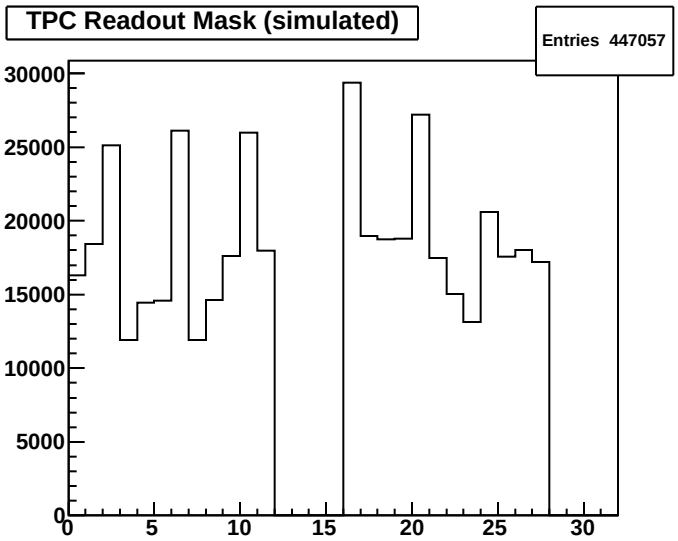

EMC L2 Input - HT01 bits/Partial JP ID
Entries 526
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

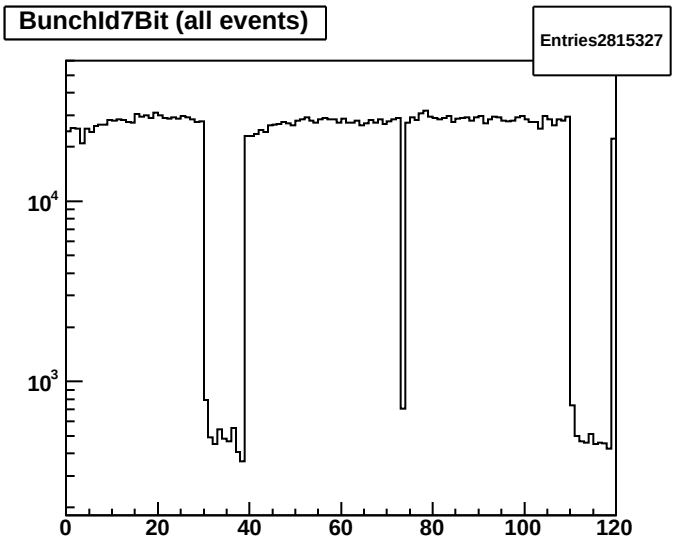

EMC L2 Input - HT23 bits
Entries 2.252262e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

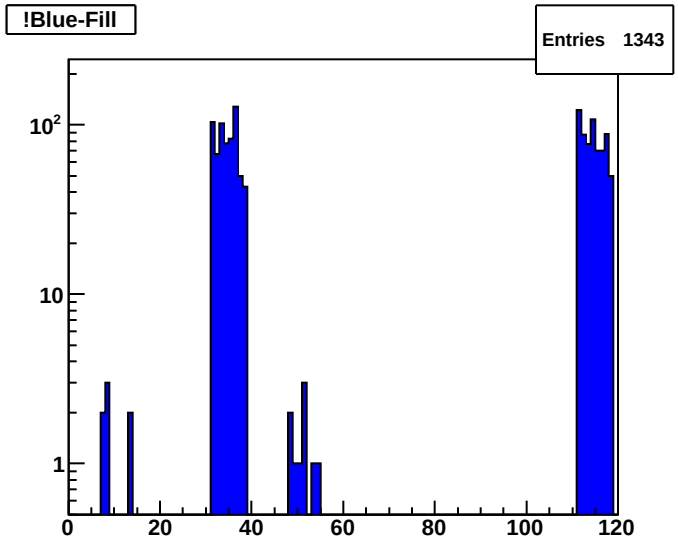

EMC L2 Input - HT23 bits
Entries 103
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

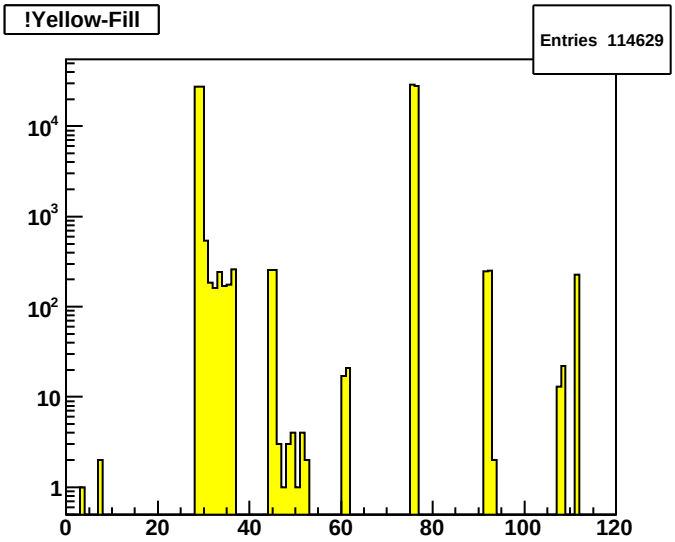

DAQ10k HT bits
Entries 252460

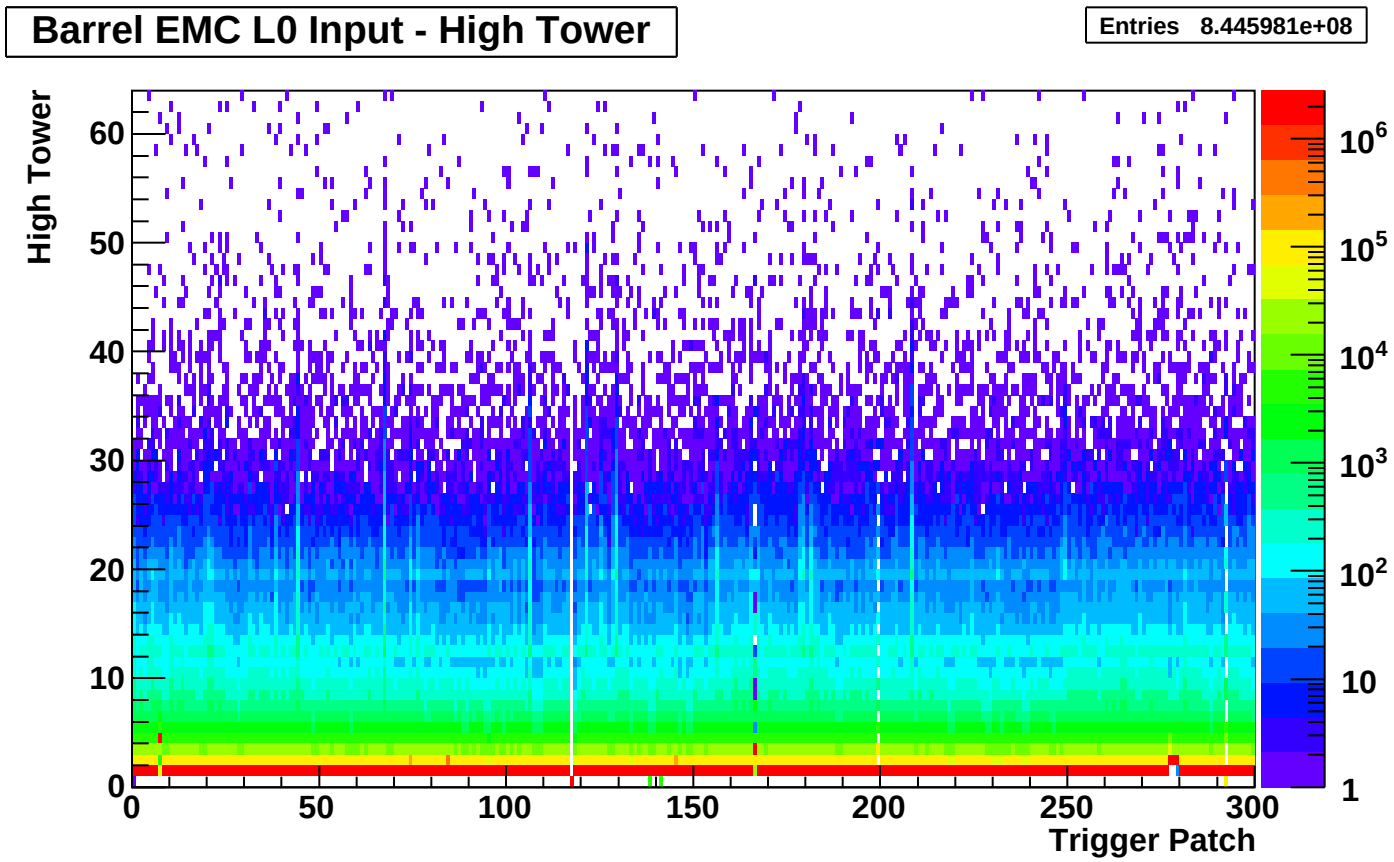

DAQ10k HT bits errors
Entries 67

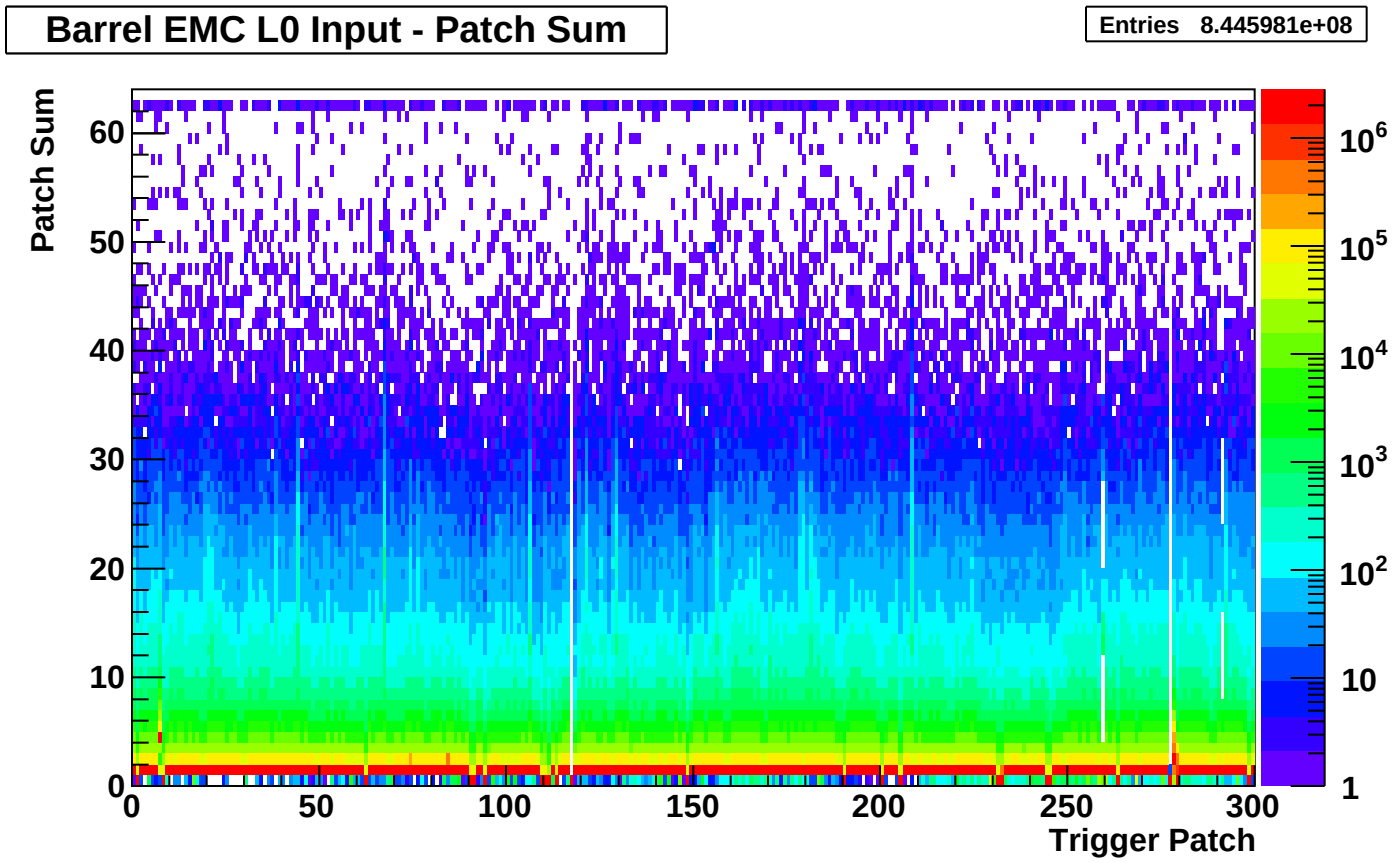

TPC Readout Mask (simulated)
Entries 447057

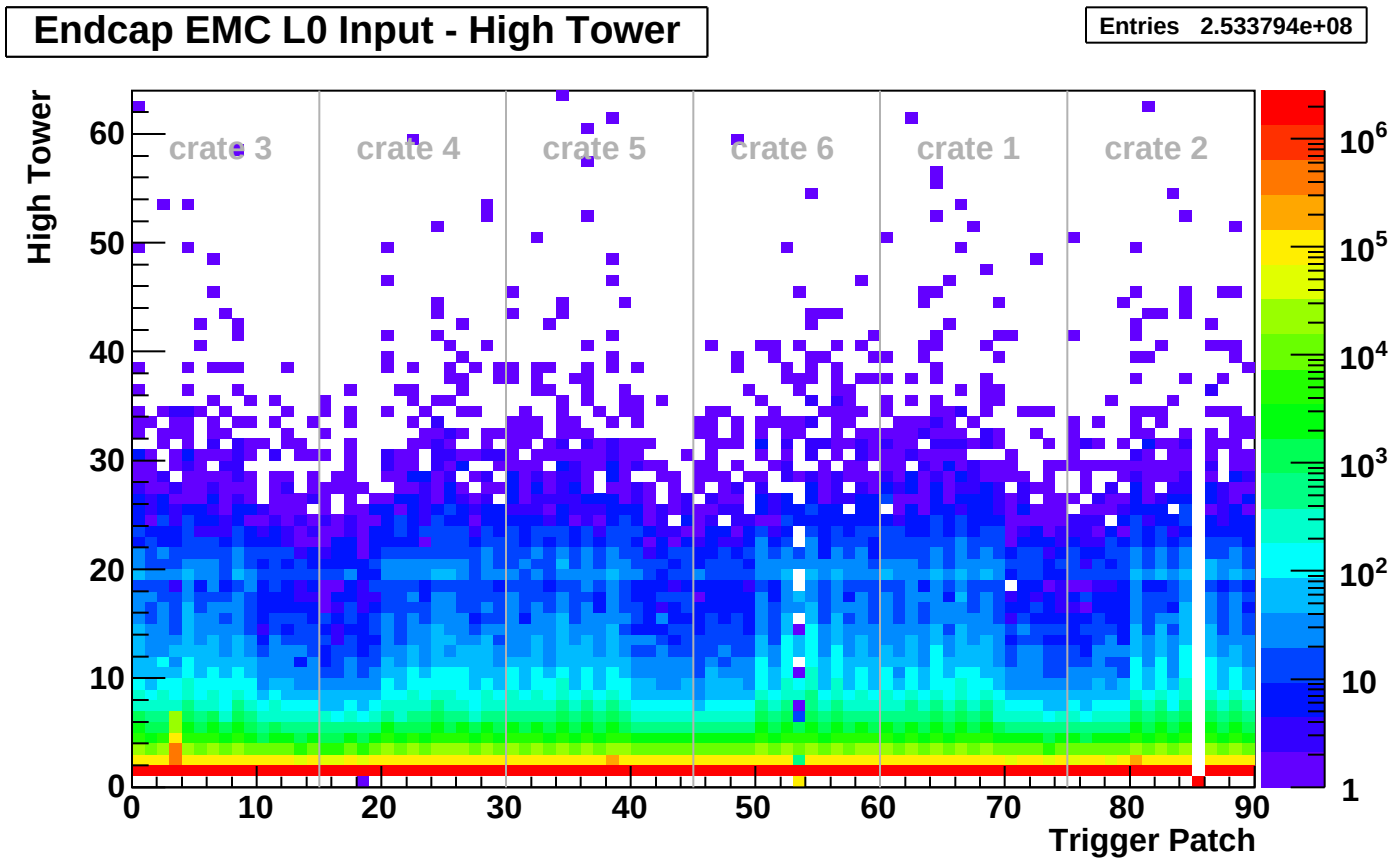

BunchId7Bit (all events)
Entries2815327

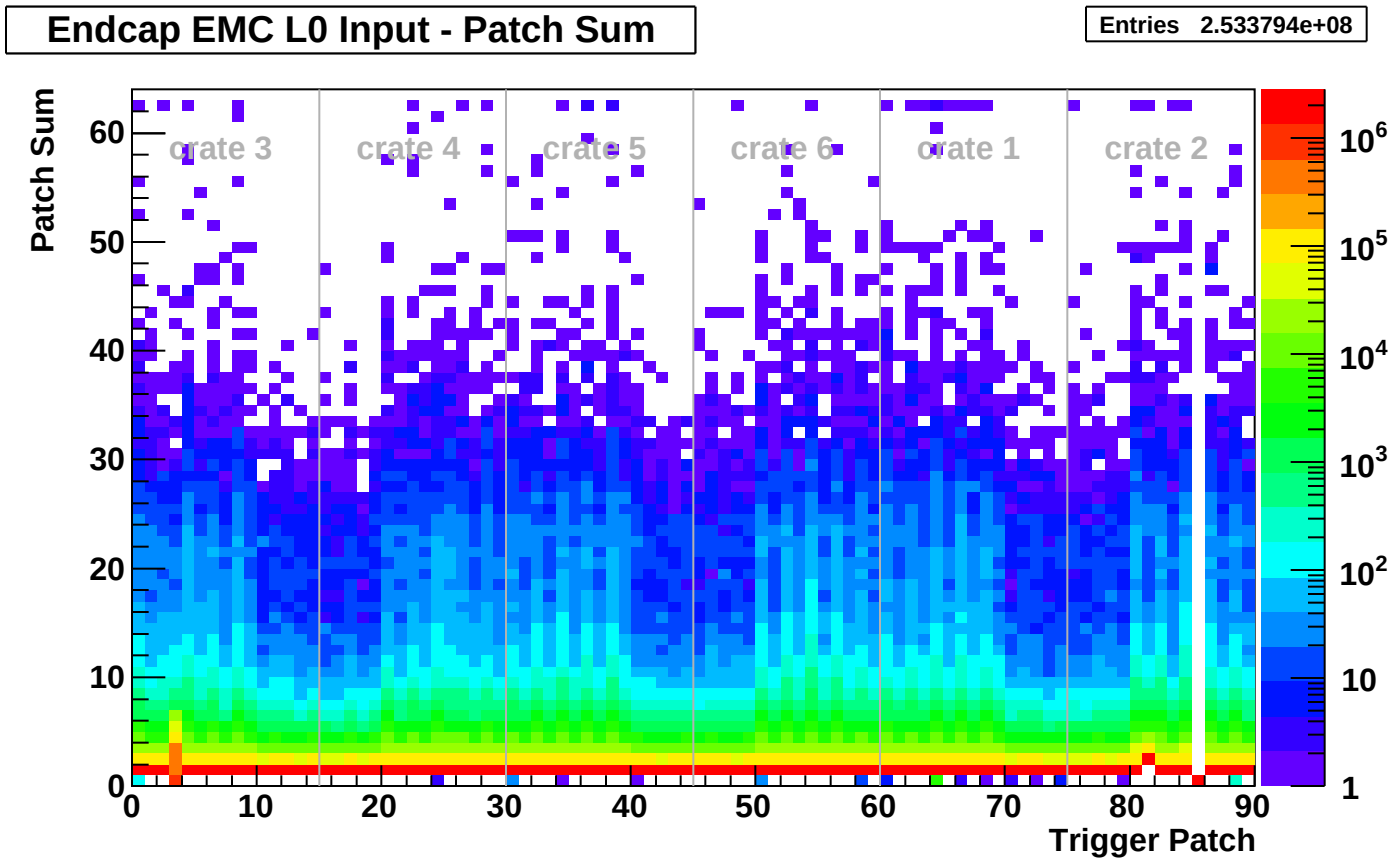

!Blue-Fill
Entries 1343

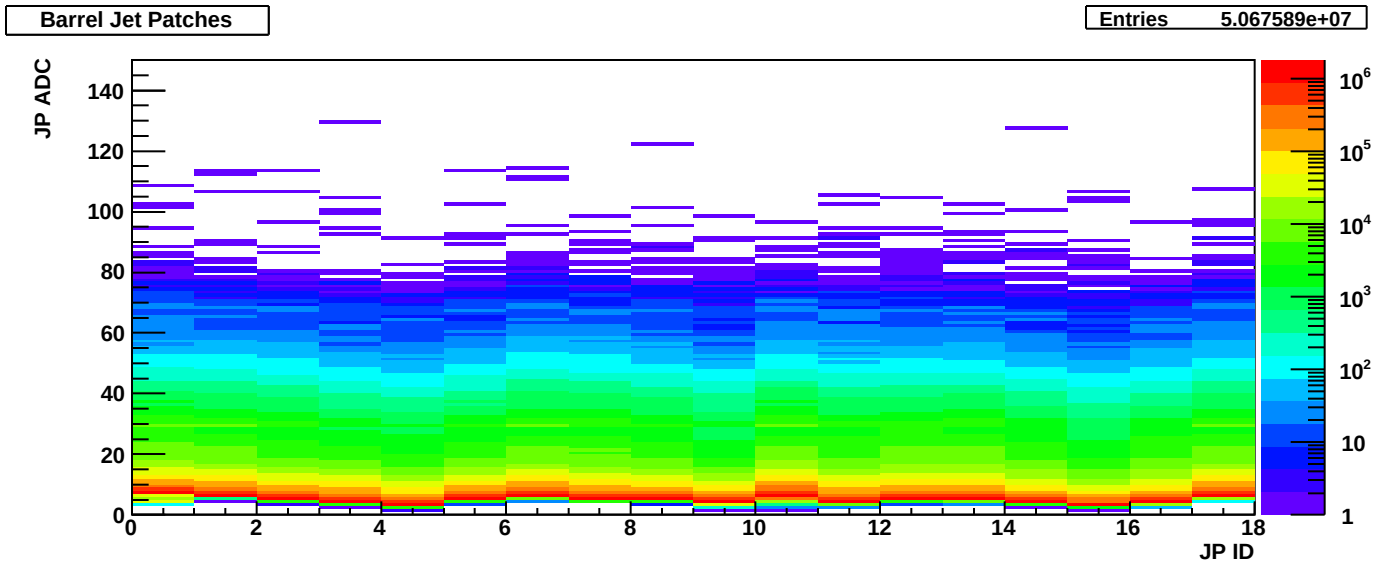

!Yellow-Fill
Entries 114629

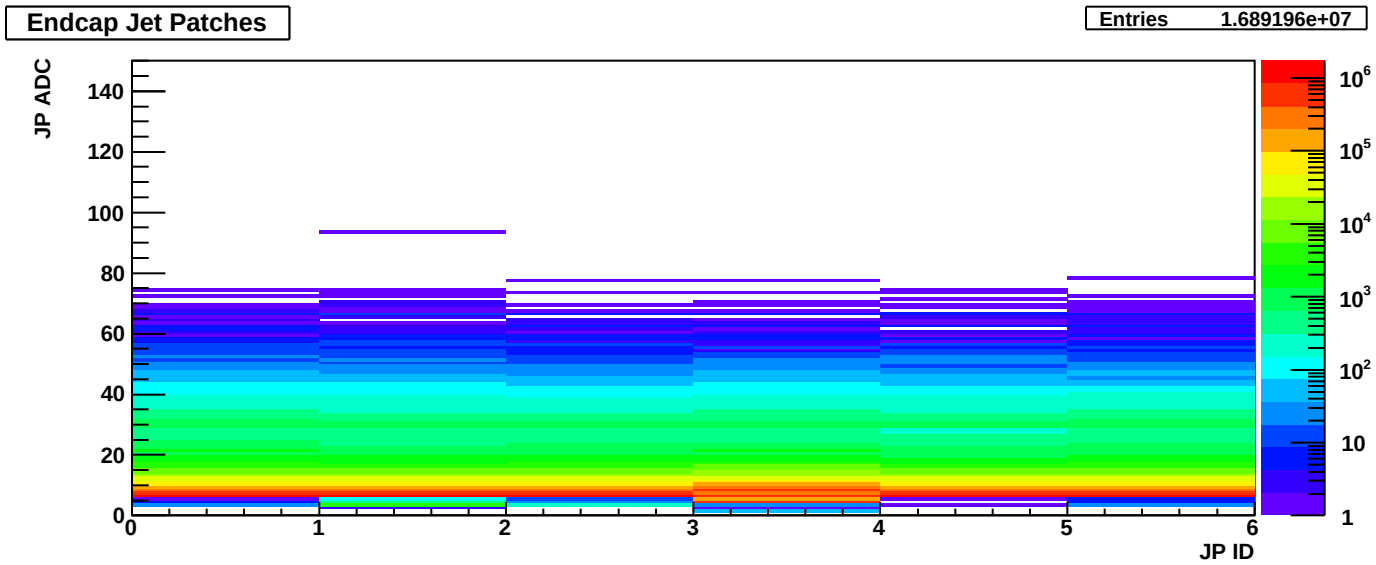

Barrel EMC L0 Input - High Tower
Entries 8.445981e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

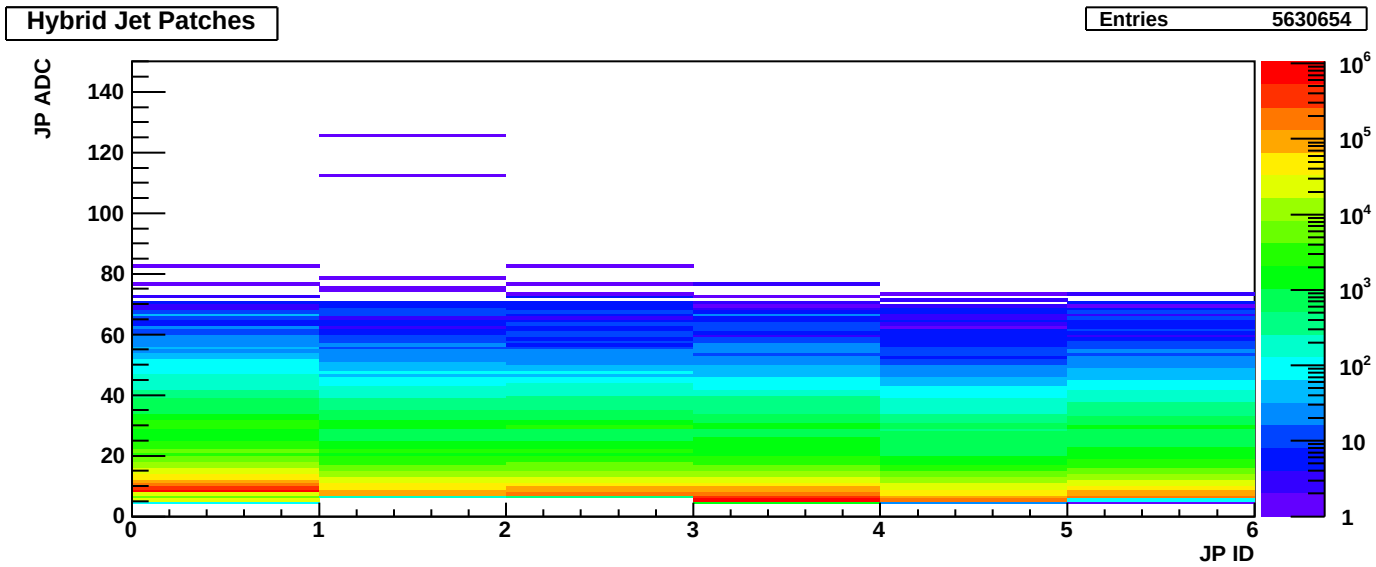

Barrel EMC L0 Input - Patch Sum
Entries 8.445981e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

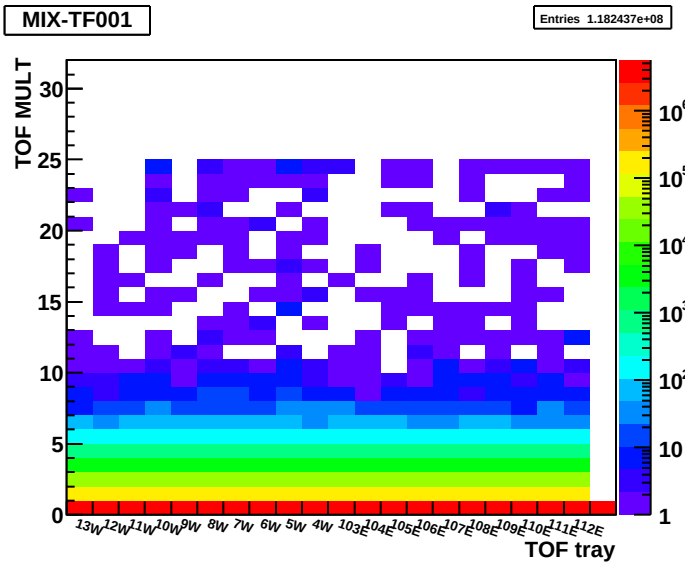

Endcap EMC L0 Input - High Tower
Entries 2.533794e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

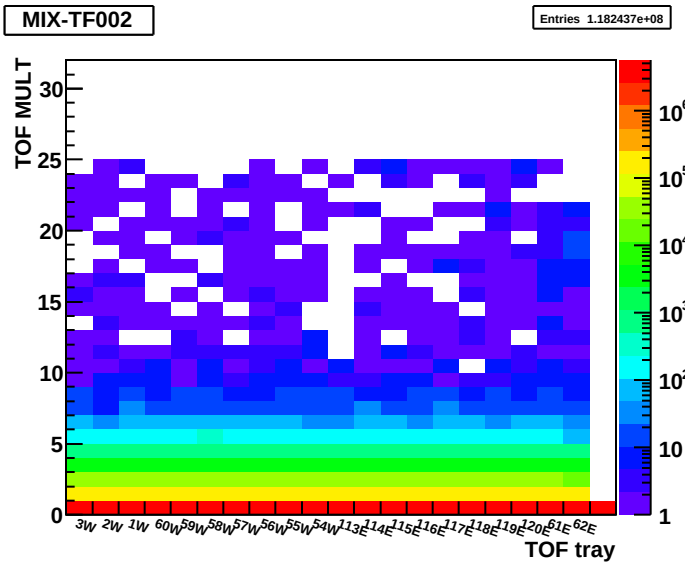

Endcap EMC L0 Input - Patch Sum
Entries 2.533794e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

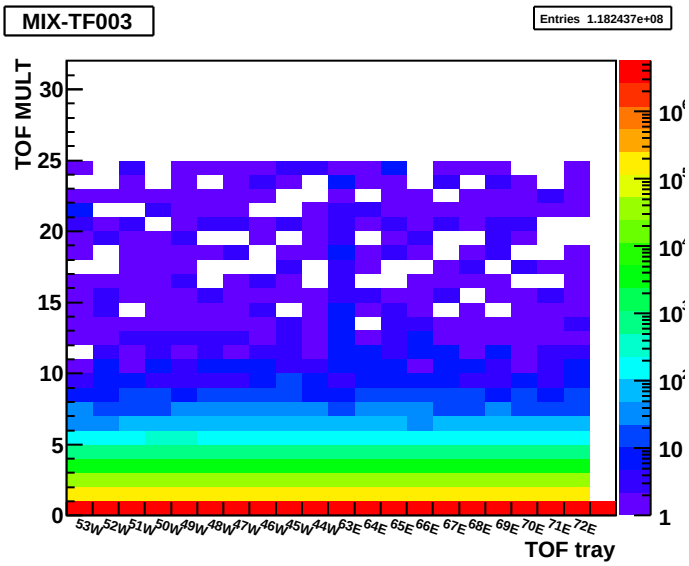

Barrel Jet Patches
Entries 5.067589e+07
JP ADC
JP ID

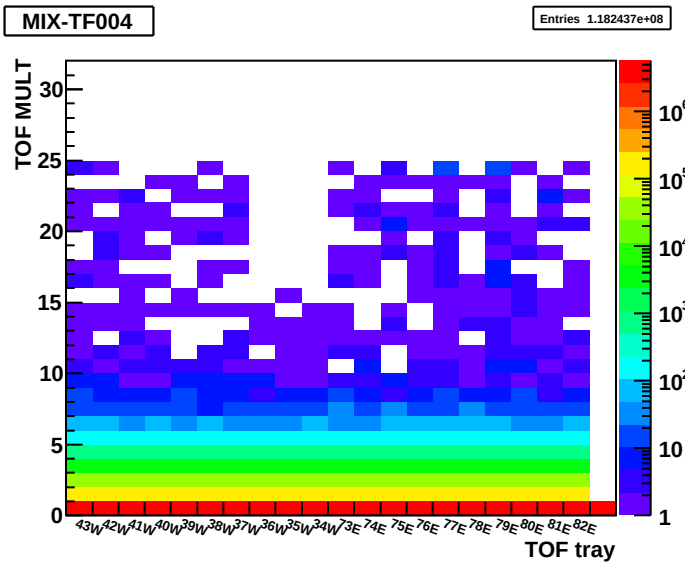

Endcap Jet Patches
Entries 1.689196e+07
JP ADC
JP ID

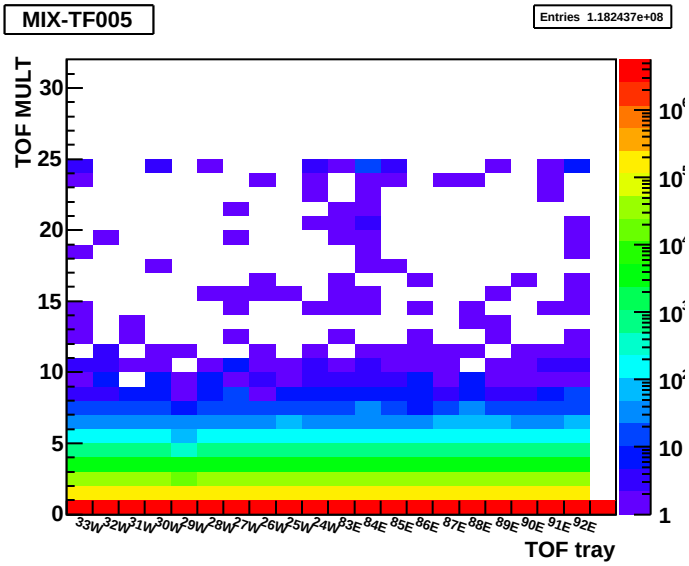

Hybrid Jet Patches
Entries 5630654
JP ADC
JP ID

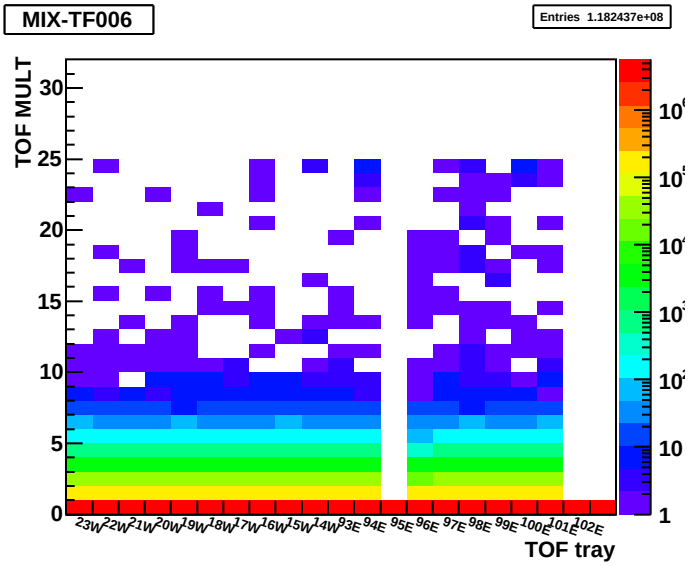

MIX-TF001
Entries 1.182437e+08
TOF MULT
TOF tray

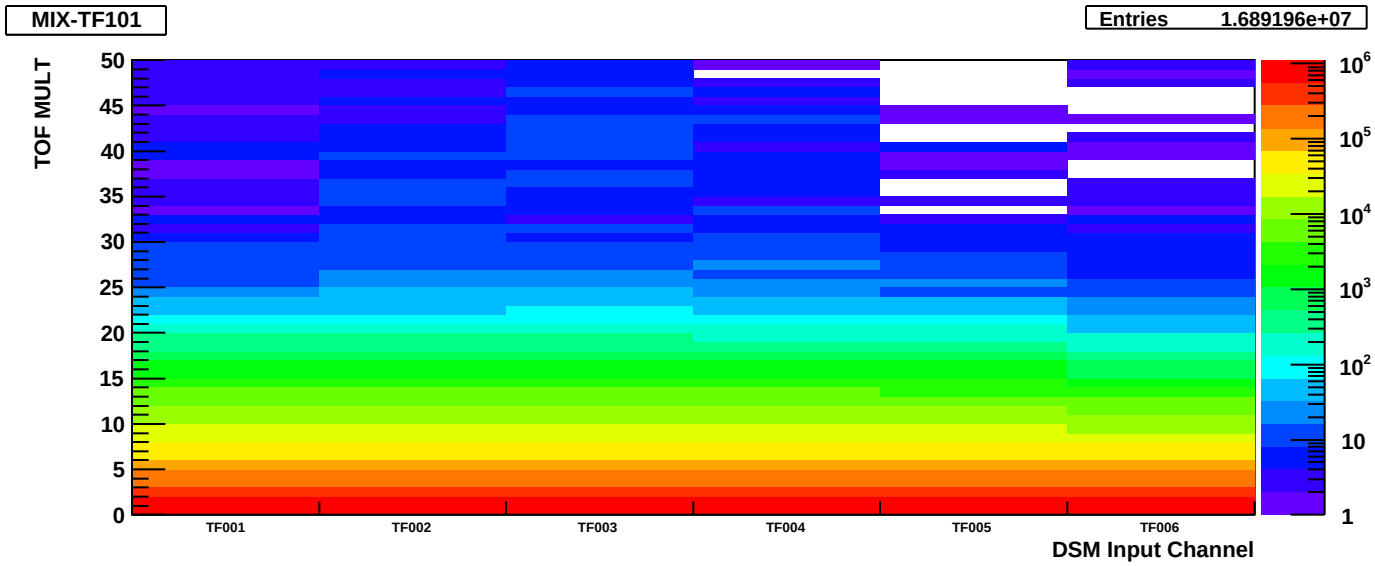

MIX-TF002
Entries 1.182437e+08
TOF MULT
TOF tray

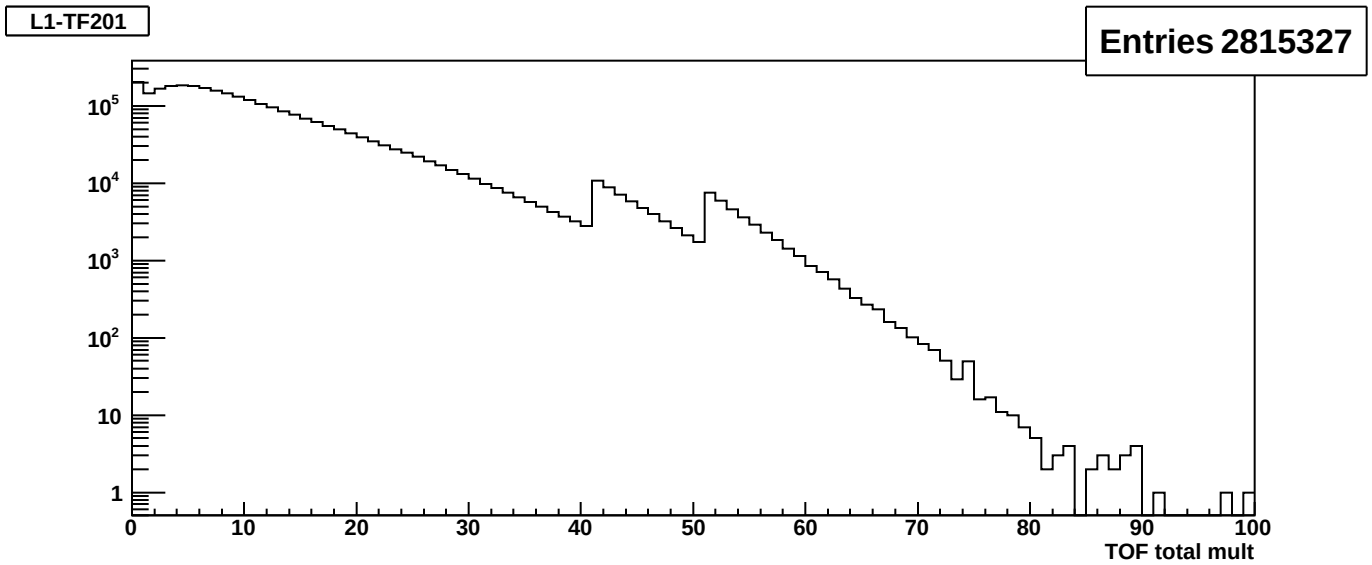

MIX-TF003
Entries 1.182437e+08
TOF MULT
TOF tray

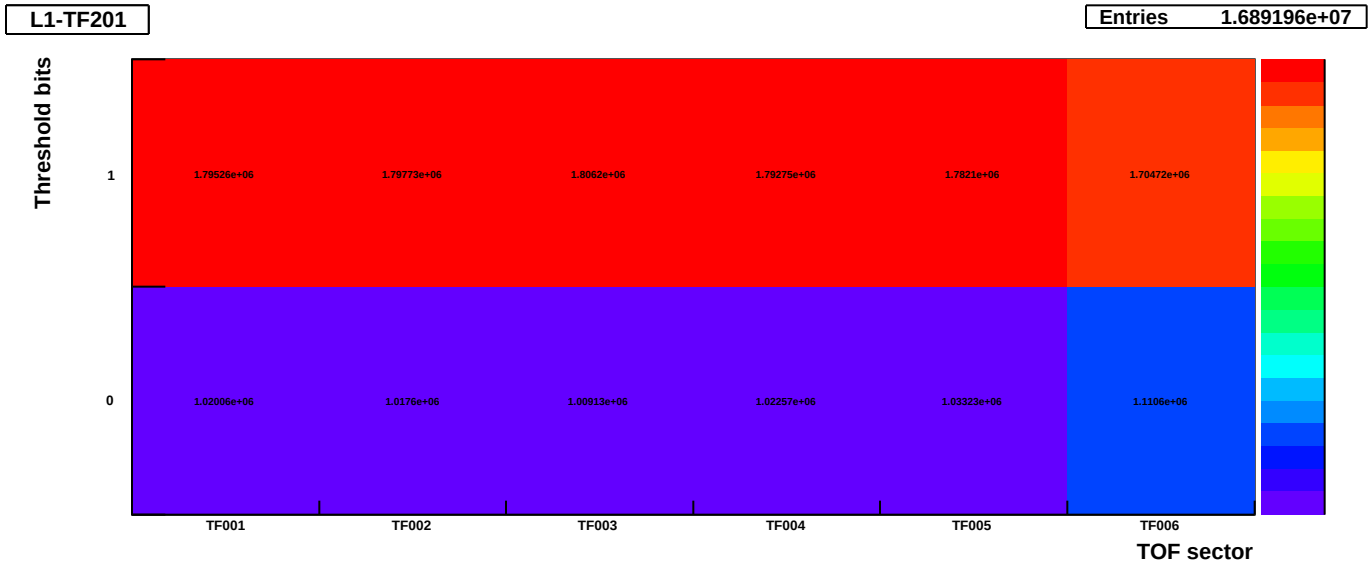

MIX-TF004
Entries 1.182437e+08
TOF MULT
TOF tray

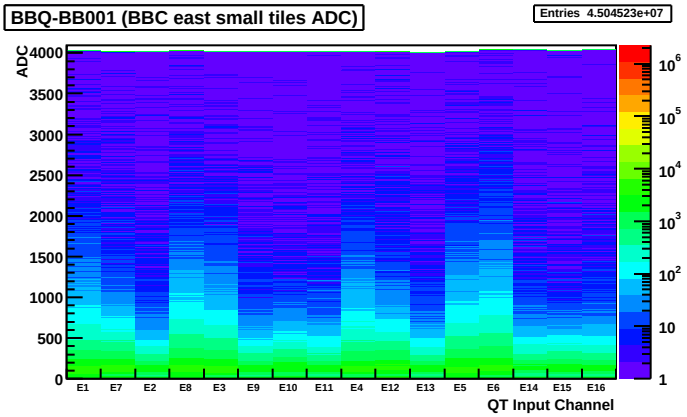

MIX-TF005
Entries 1.182437e+08
TOF MULT
TOF tray

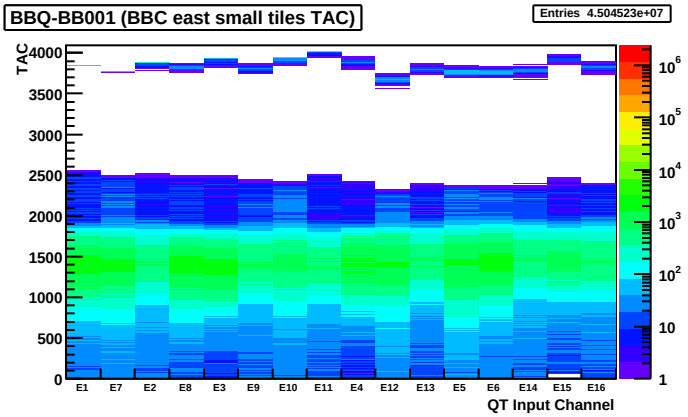

MIX-TF006
Entries 1.182437e+08
TOF MULT
TOF tray

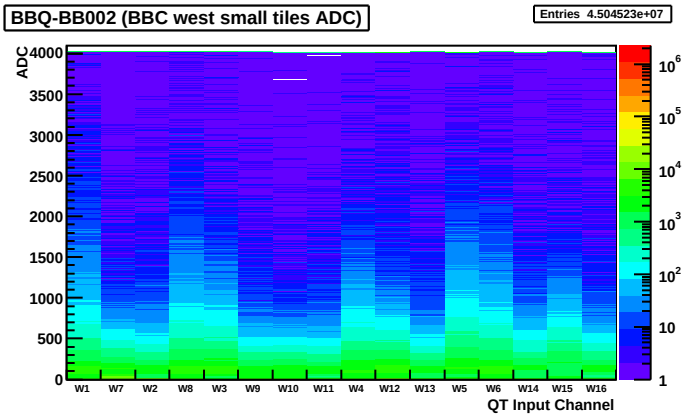

MIX-TF101
Entries 1.689196e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^6
10^5
10^4
10^3
10^2
10
1

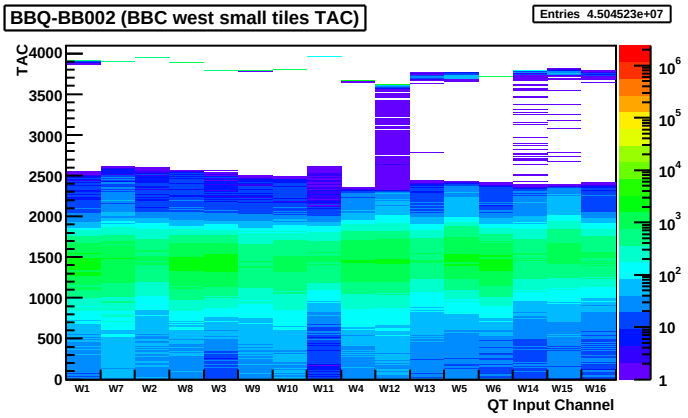

L1-TF201
Entries 2815327
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

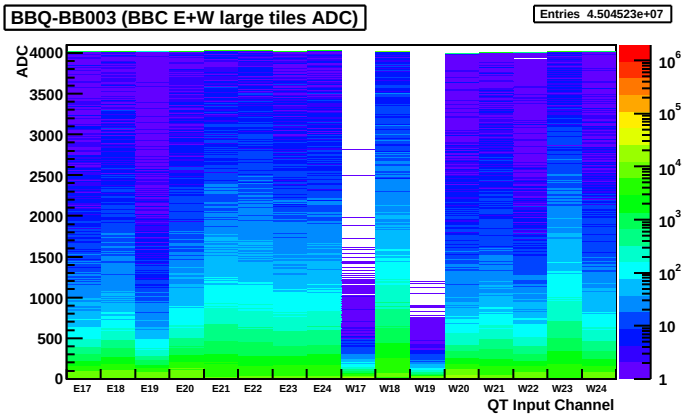

L1-TF201
Entries 1.689196e+07
Threshold bits
1
0
1.79526e+06
1.79773e+06
1.8062e+06
1.79275e+06
1.7821e+06
1.70472e+06
1.02006e+06
1.0176e+06
1.00913e+06
1.02257e+06
1.03323e+06
1.1106e+06
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

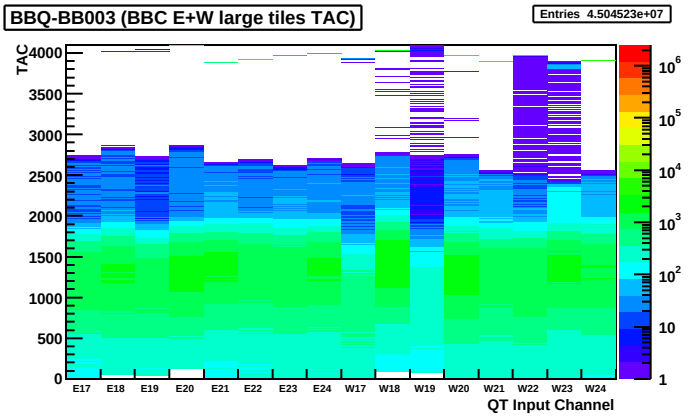

BBQ-BB001 (BBC east small tiles ADC)
Entries 4.504523e+07
ADC
QT Input Channel

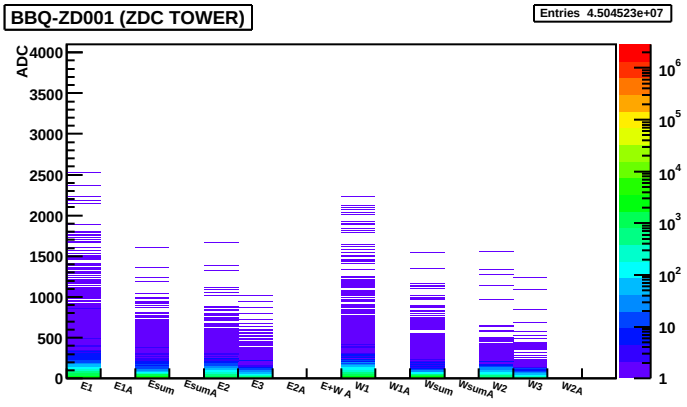

BBQ-BB001 (BBC east small tiles TAC)
Entries 4.504523e+07
TAC
QT Input Channel

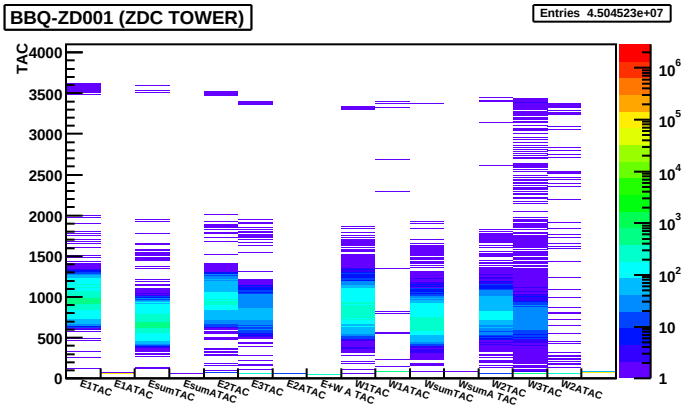

BBQ-BB002 (BBC west small tiles ADC)
Entries 4.504523e+07
ADC
QT Input Channel

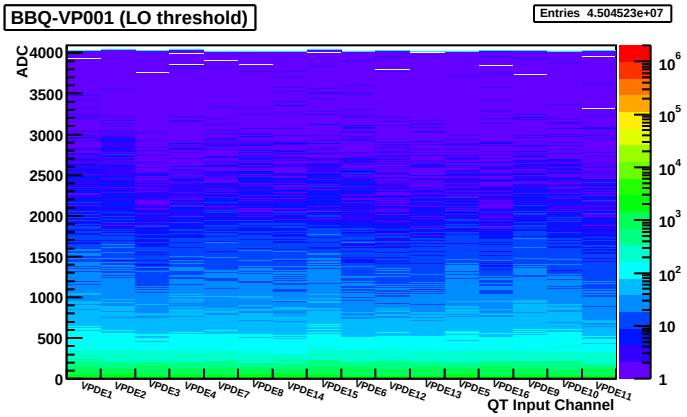

BBQ-BB002 (BBC west small tiles TAC)
Entries 4.504523e+07
TAC
QT Input Channel

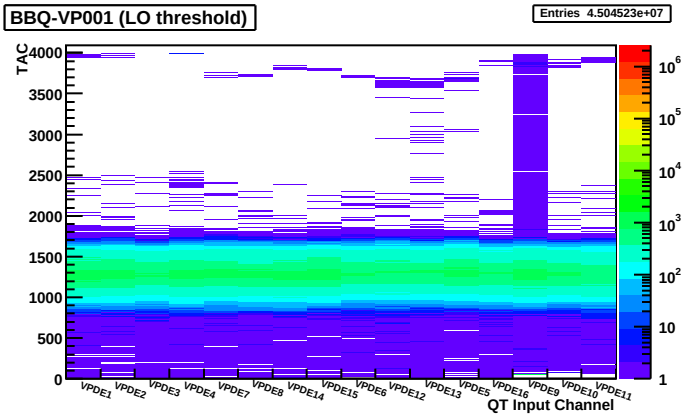

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4.504523e+07
ADC
QT Input Channel

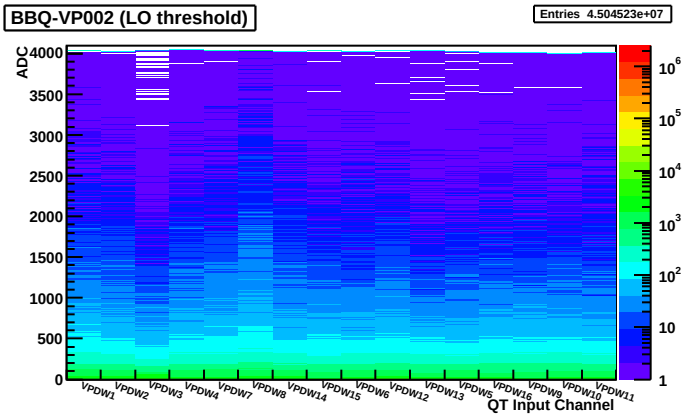

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4.504523e+07
TAC
QT Input Channel

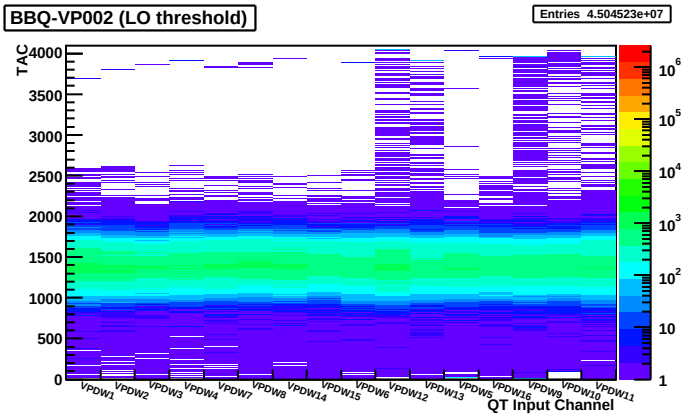

BBQ-ZD001 (ZDC TOWER)
Entries 4.504523e+07
ADC

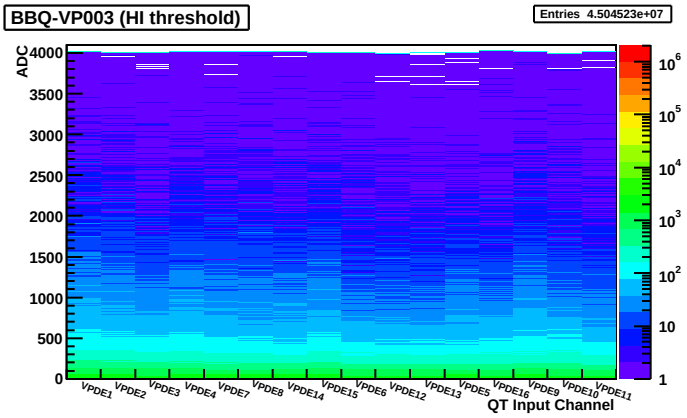

BBQ-ZD001 (ZDC TOWER)
Entries 4.504523e+07
TAC

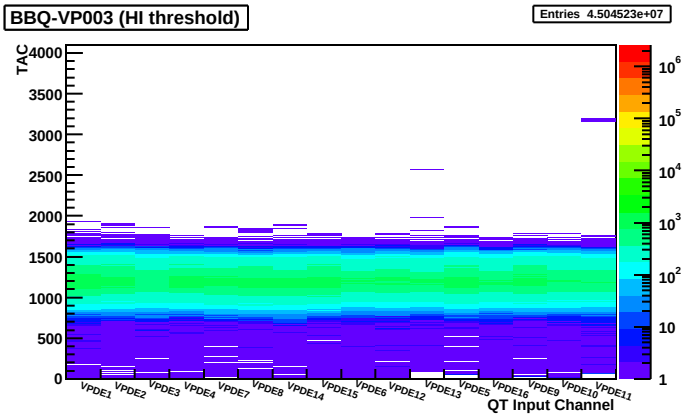

BBQ-VP001 (LO threshold)
Entries 4.504523e+07
ADC
QT Input Channel

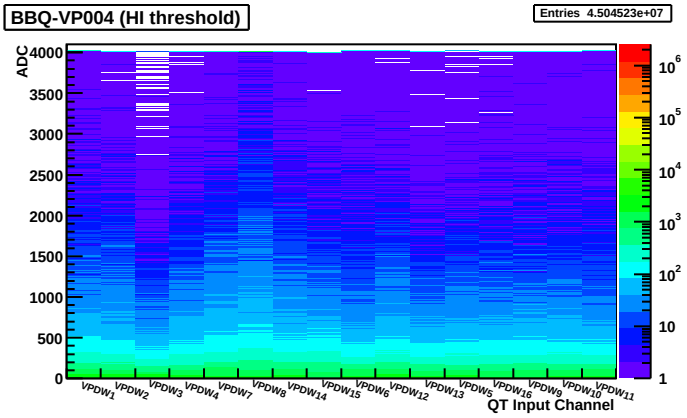

BBQ-VP001 (LO threshold)
Entries 4.504523e+07
TAC
QT Input Channel

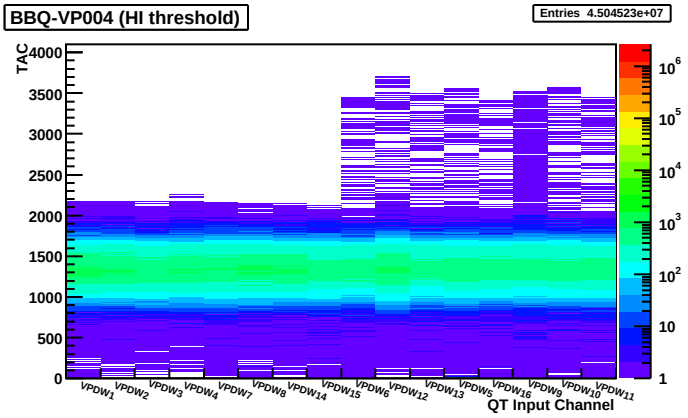

BBQ-VP002 (LO threshold)
Entries 4.504523e+07
ADC
QT Input Channel

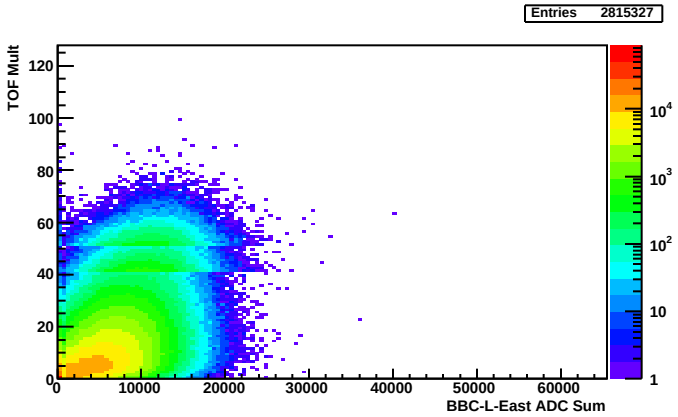

BBQ-VP002 (LO threshold)
Entries 4.504523e+07
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4.504523e+07
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4.504523e+07
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4.504523e+07
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4.504523e+07
TAC
QT Input Channel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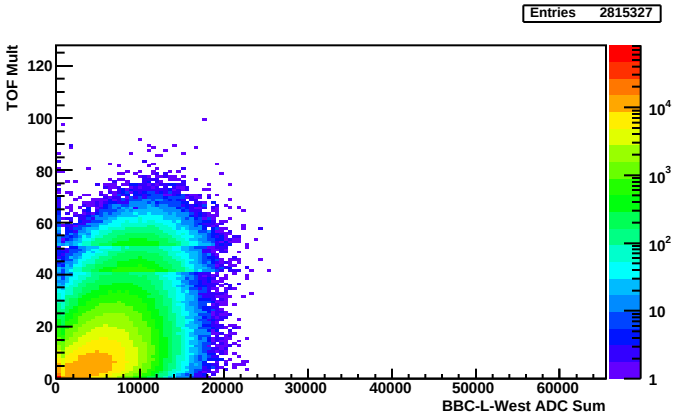
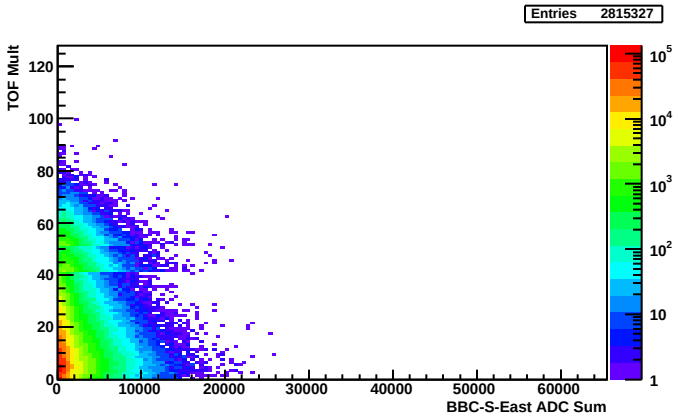
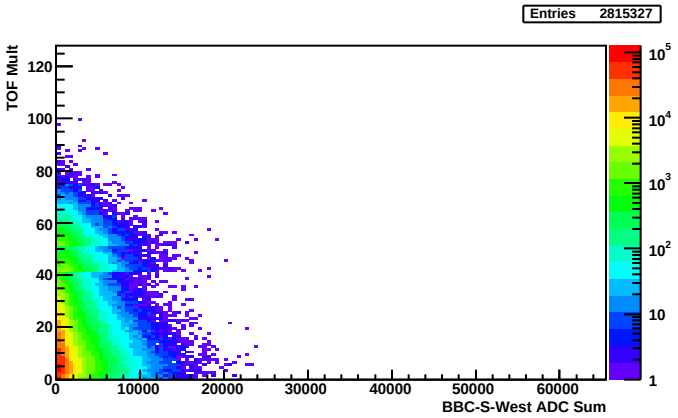
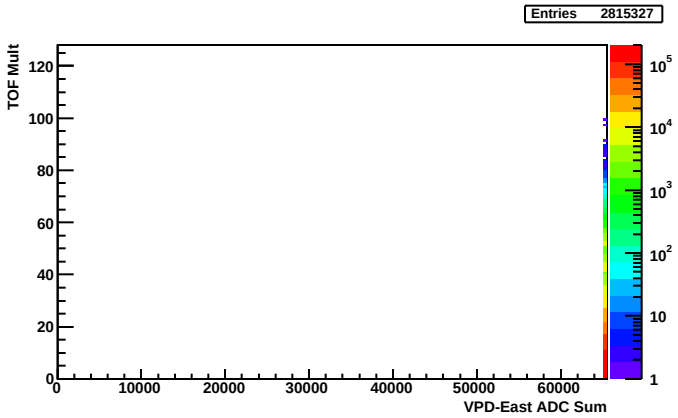
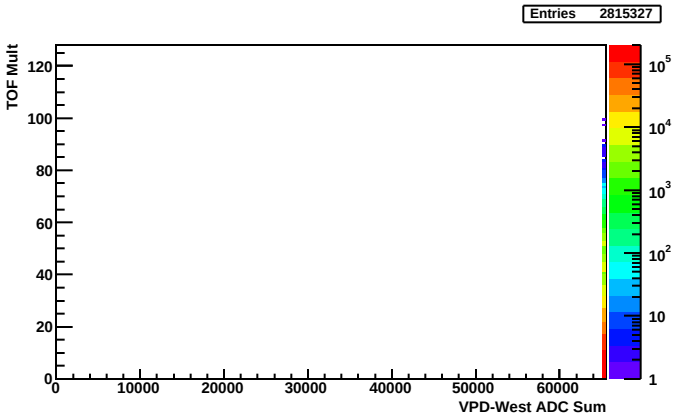
TOF Mult
BBC-L-East ADC Sum

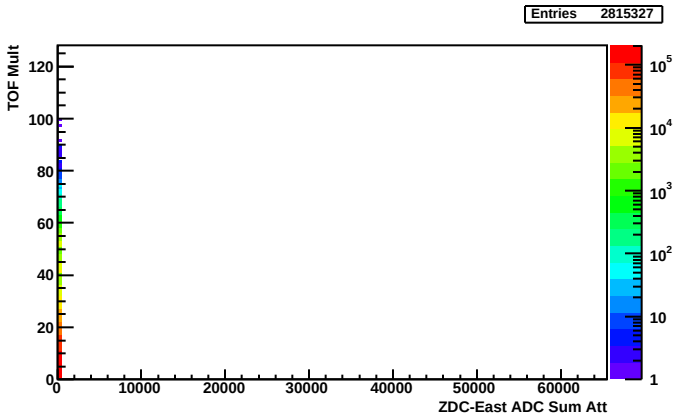

Entries 2815327
TOF Mult
BBC-L-West ADC Sum

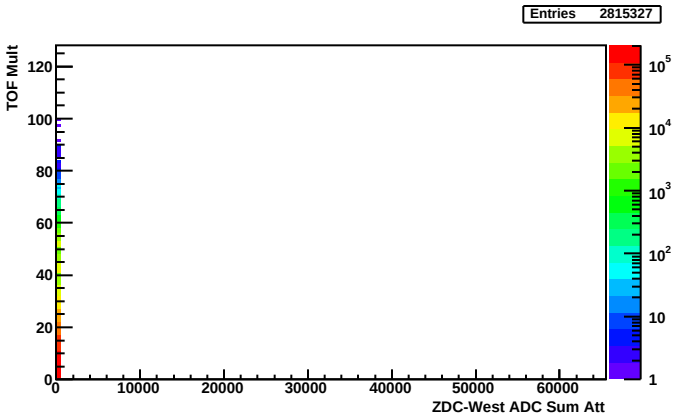

Entries 2815327
TOF Mult
BBC-S-East ADC Sum

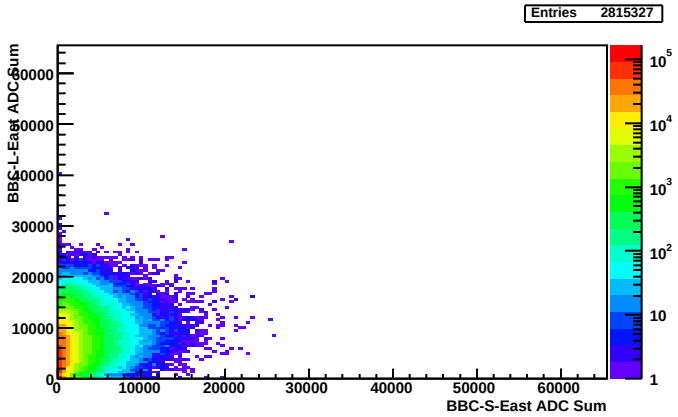

Entries 2815327
TOF Mult
BBC-S-West ADC Sum


Entries 2815327
TOF Mult
VPD-East ADC Sum


Entries 2815327
TOF Mult
VPD-West ADC Sum


Entries 2815327
TOF Mult
ZDC-East ADC Sum Att


Entries 2815327
TOF Mult
ZDC-West ADC Sum Att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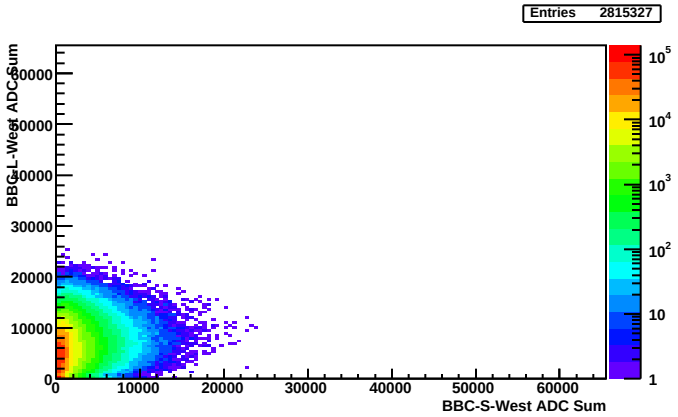
BBC-L-East ADC Sum
BBC-S-East ADC Sum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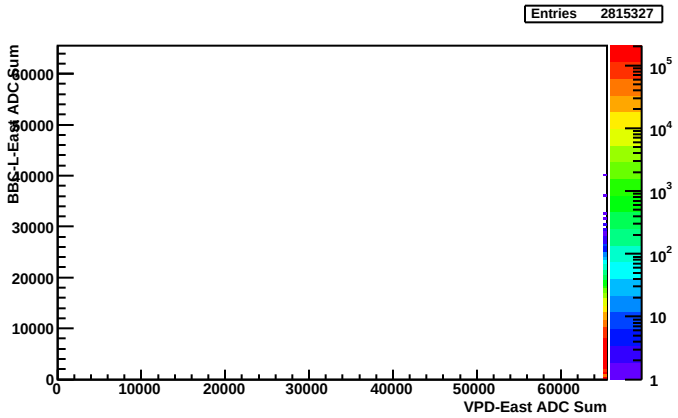
BBC-L-West ADC Sum
BBC-S-West ADC Sum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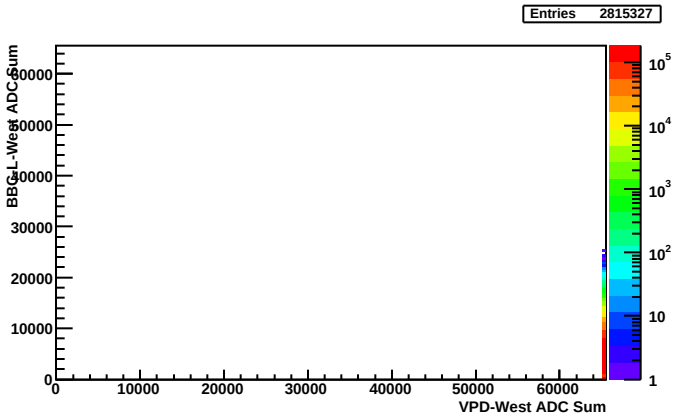
BBC-L-East ADC Sum
VPD-East ADC Sum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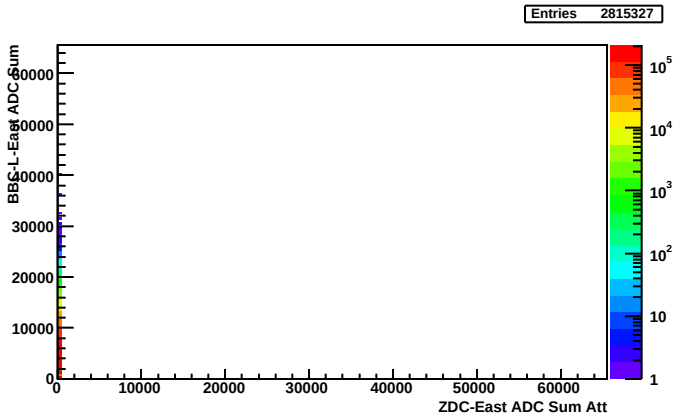
BBC-L-West ADC Sum
VPD-West ADC Sum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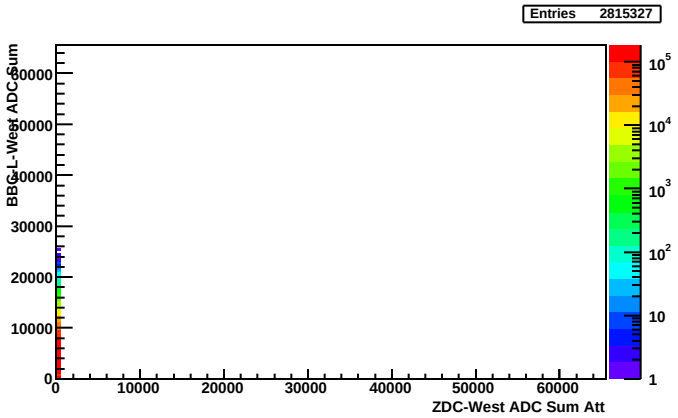
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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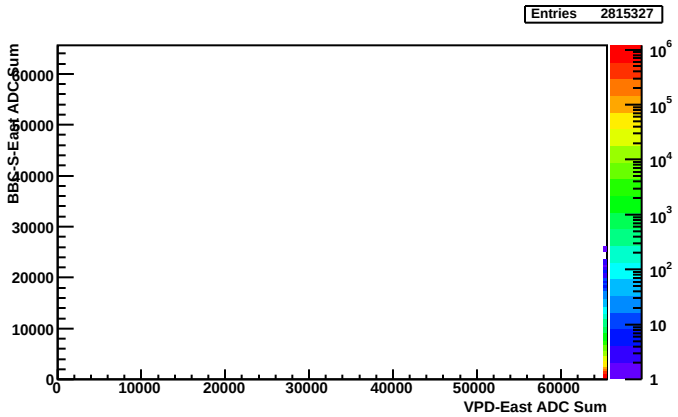
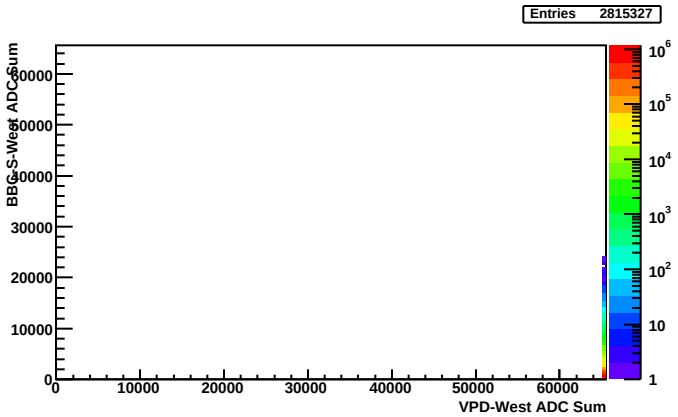
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2815327
BBC-S-East ADC Sum
VPD-East ADC Sum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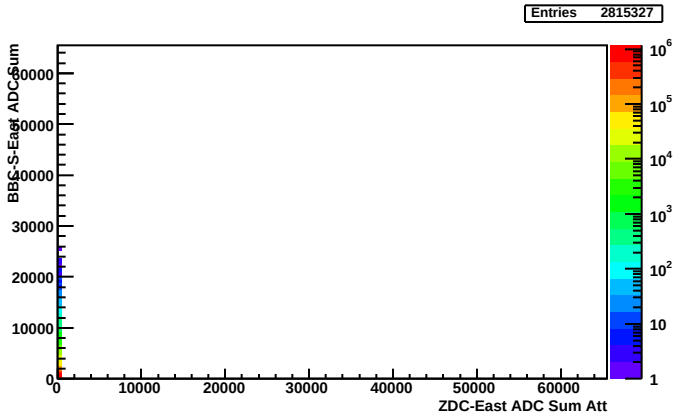
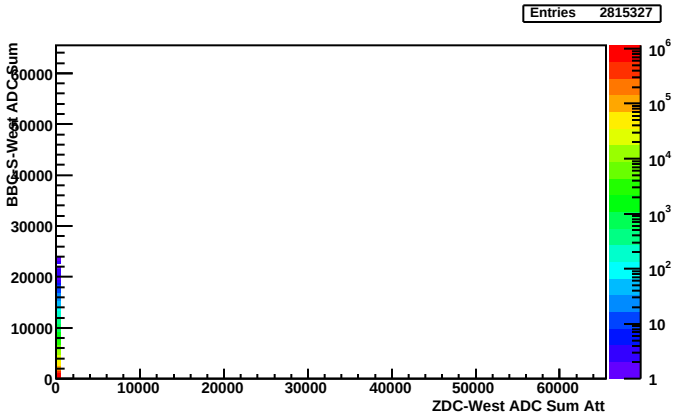
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2815327
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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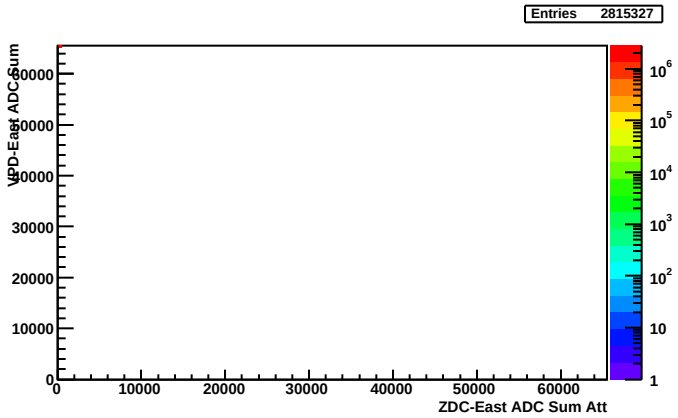
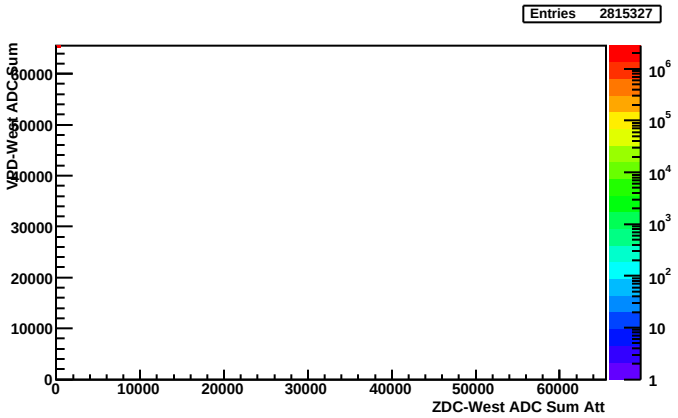
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 2815327
VPD-East ADC Sum
ZDC-East ADC Sum Att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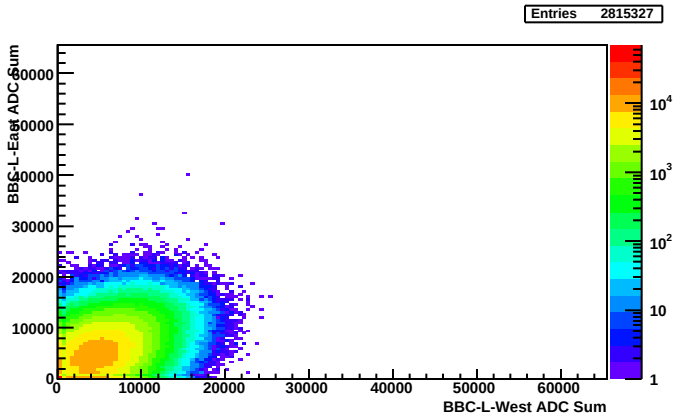
VPD-West ADC Sum
ZDC-West ADC Sum Att


Entries 2815327
BBC-L-East ADC Sum
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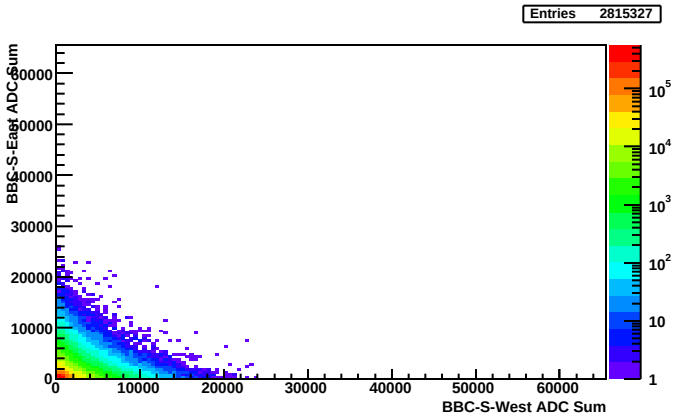
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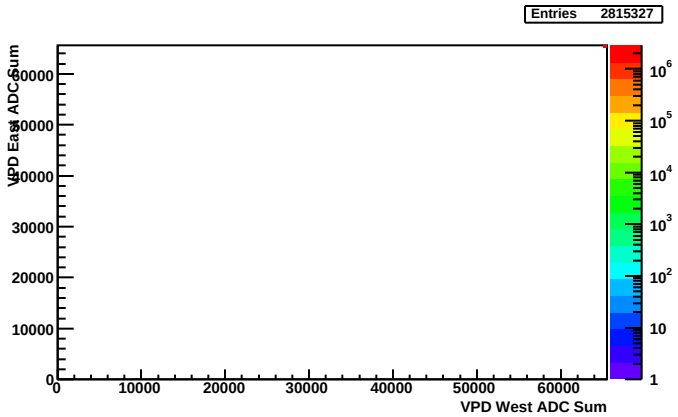
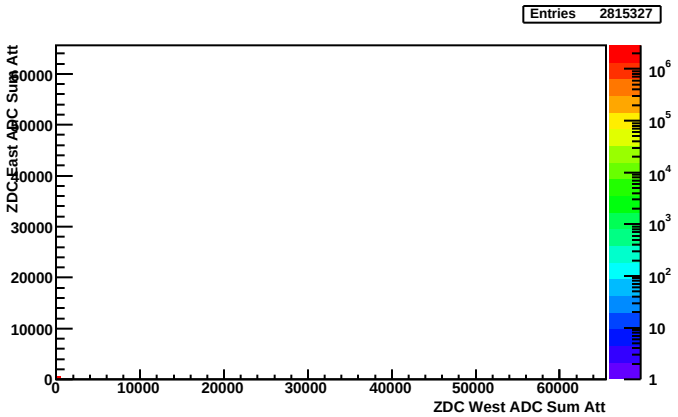
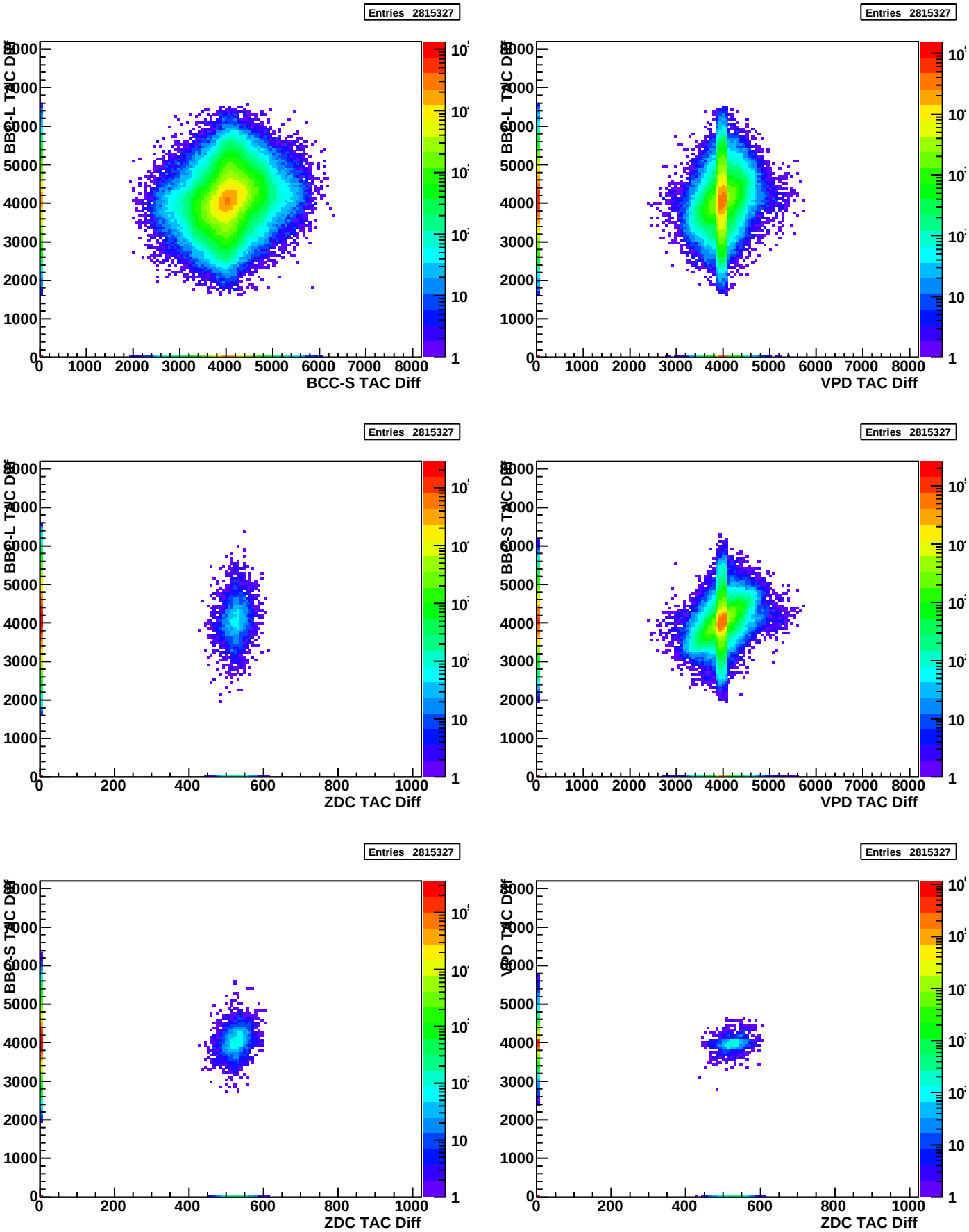
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 2815327
VPD East ADC Sum
VPD West ADC Sum


Entries 2815327
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2815327
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2815327
BBC-L TAC Diff
VPD TAC Diff
Entries 2815327
BBC-L TAC Diff
ZDC TAC Diff
Entries 2815327
BBC-S TAC Diff
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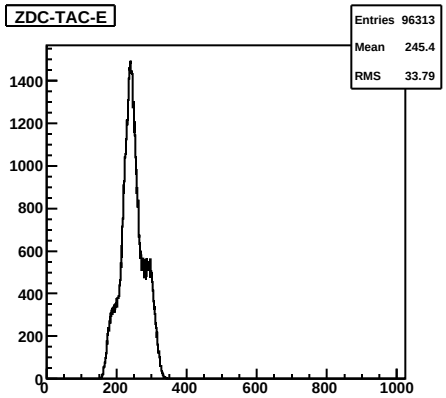
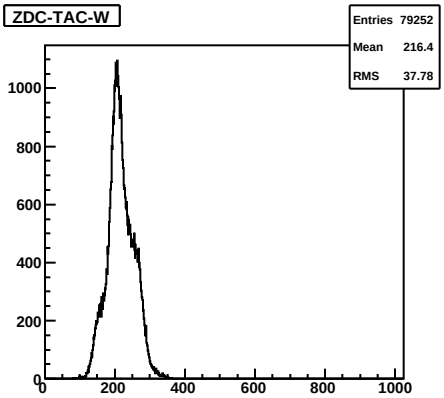
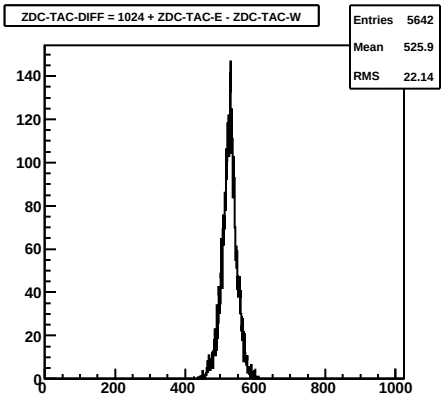
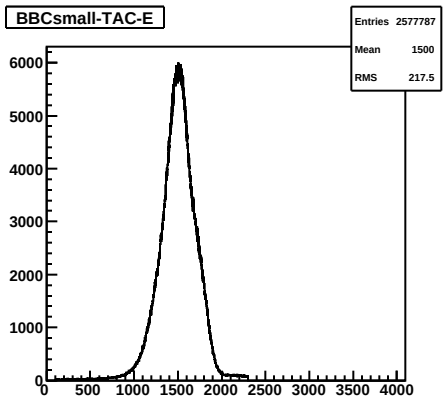
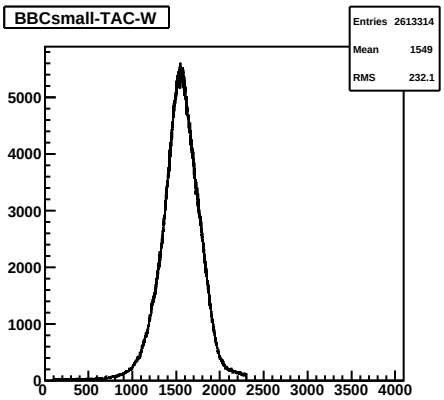
Entries 2815327
BBC-S TAC Diff
ZDC TAC Diff
Entries 2815327
VPD TAC Diff
ZDC TAC Diff

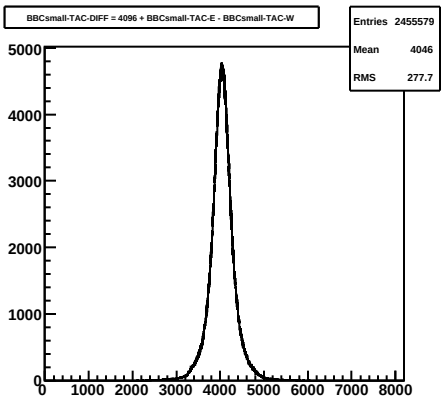

ZDC-TAC-E
Entries 96313
Mean 245.4
RMS 33.79

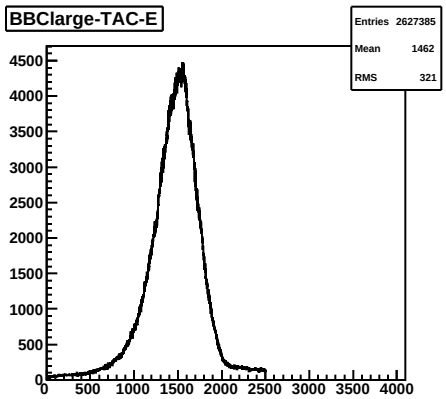

ZDC-TAC-W
Entries 79252
Mean 216.4
RMS 37.78

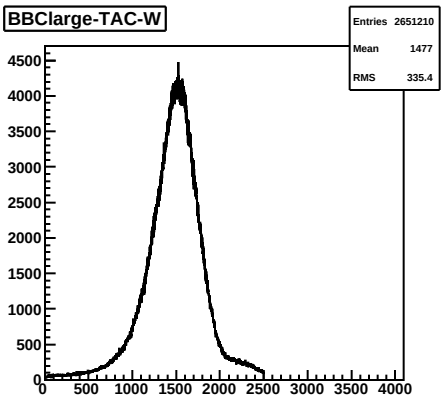

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5642
Mean 525.9
RMS 22.14

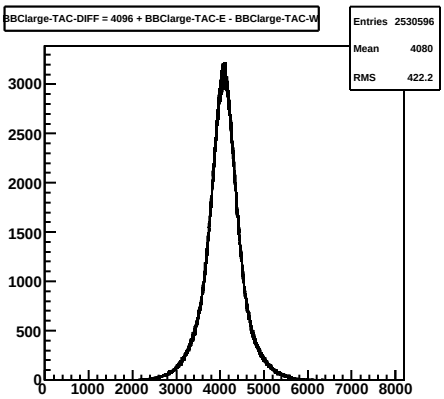

BBCsmall-TAC-E
Entries 2577787
Mean 1500
RMS 217.5

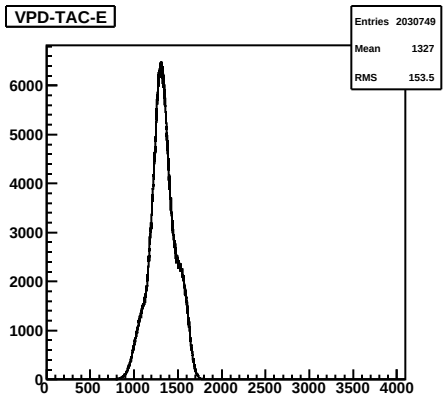

BBCsmall-TAC-W
Entries 2613314
Mean 1549
RMS 232.1

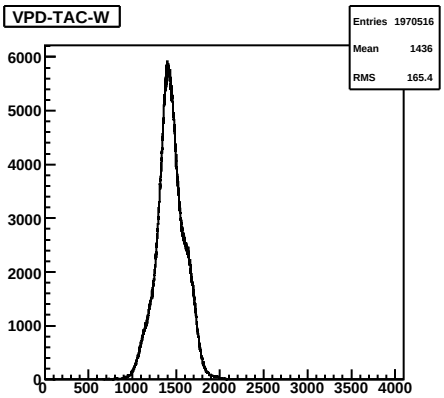

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2455579
Mean 4046
RMS 277.7

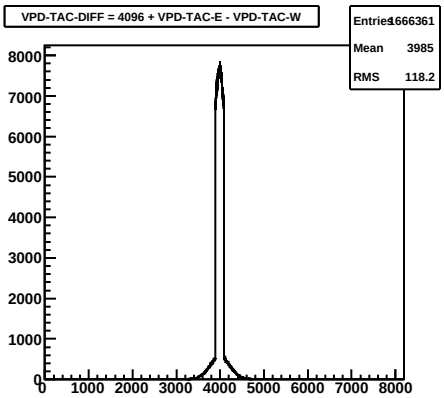

BBClarge-TAC-E
Entries 2627385
Mean 1462
RMS 321

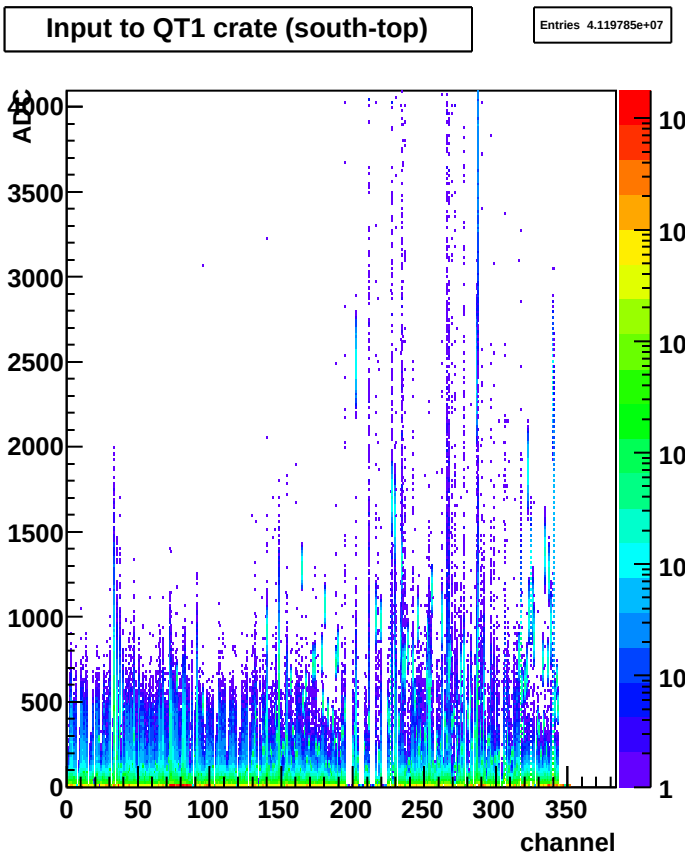

BBClarge-TAC-W
Entries 2651210
Mean 1477
RMS 335.4

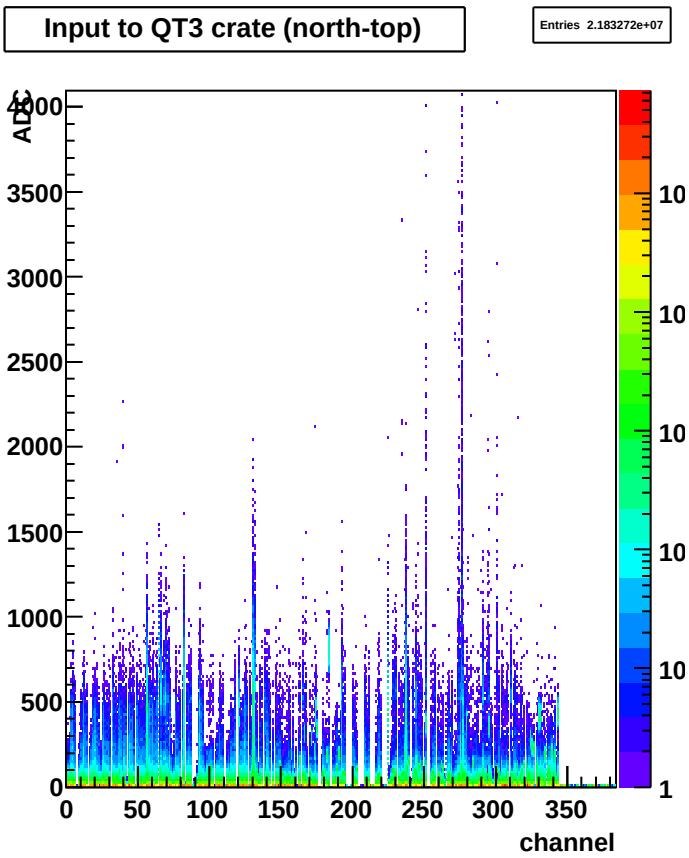

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2530596
Mean 4080
RMS 422.2

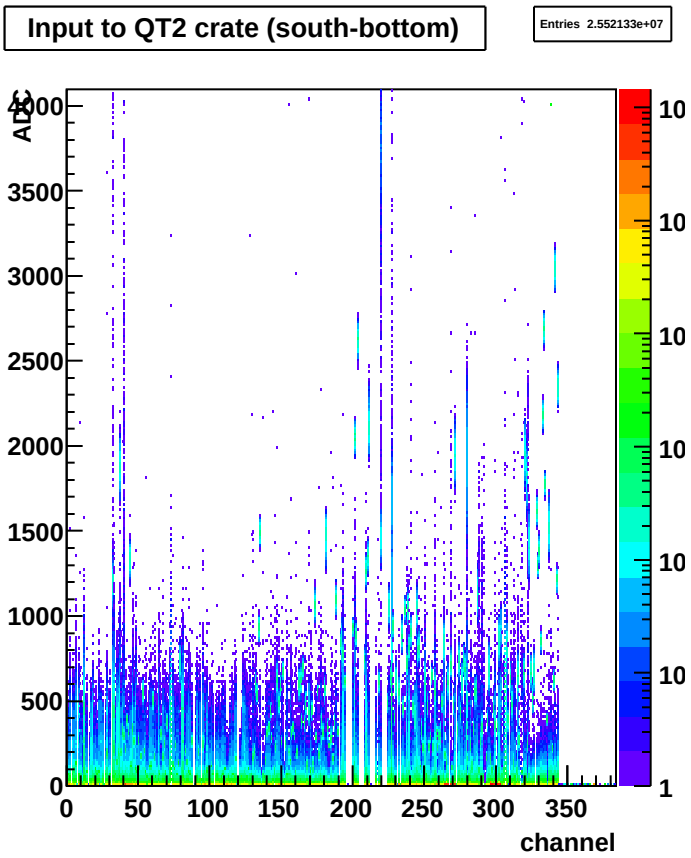

VPD-TAC-E
Entries 2030749
Mean 1327
RMS 153.5

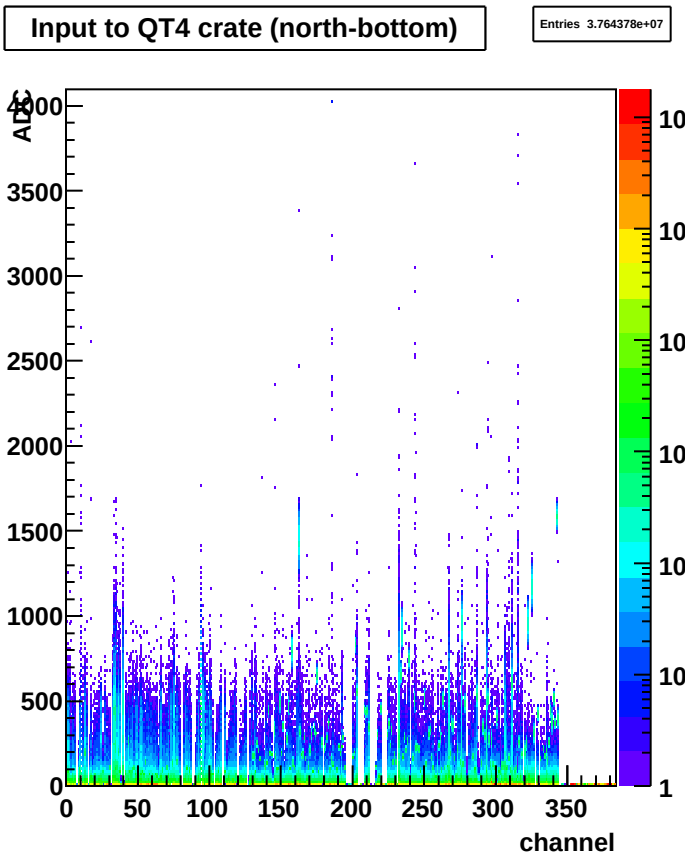

VPD-TAC-W
Entries 1970516
Mean 1436
RMS 165.4

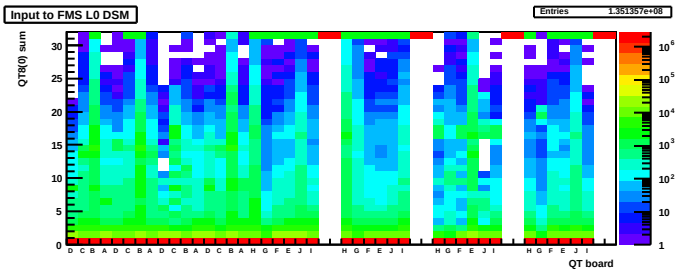

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1666361
Mean 3985
RMS 118.2

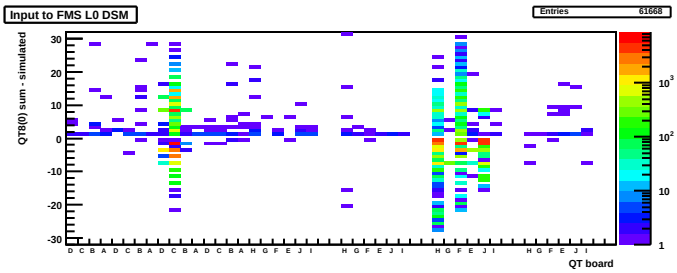

Input to QT1 crate (south-top)
Entries 4.119785e+07
ADC
channel

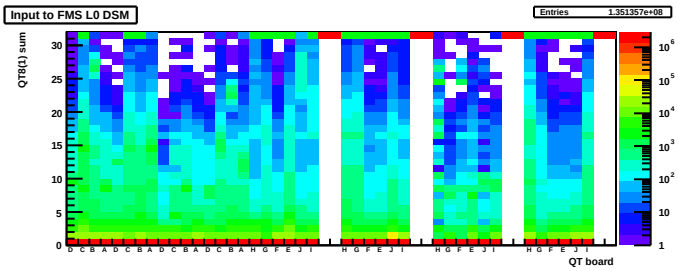

Input to QT3 crate (north-top)
Entries 2.183272e+07
ADC
channel

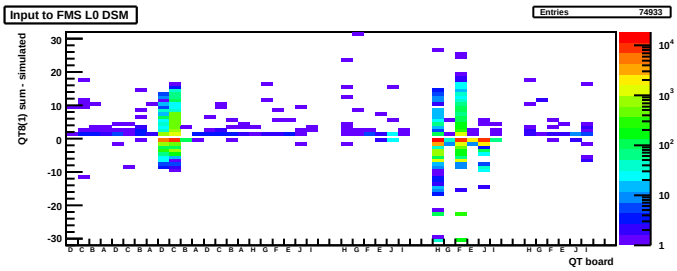

Input to QT2 crate (south-bottom)
Entries 2.552133e+07
ADC
channel

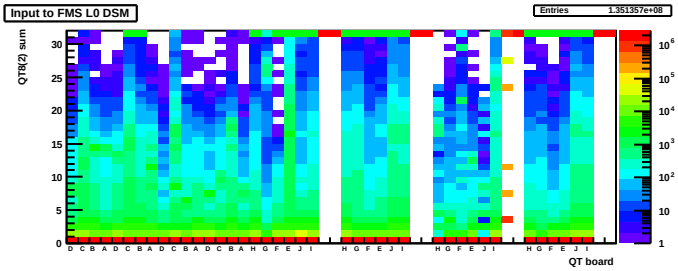

Input to QT4 crate (north-bottom)
Entries 3.764378e+07
ADC
channel

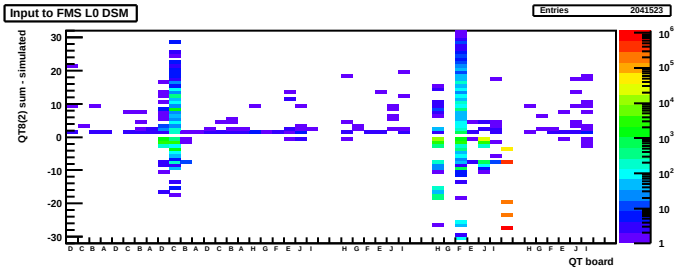

Input to FMS L0 DSM
Entries 1.351357e+08
QT8(0) sum
QT board

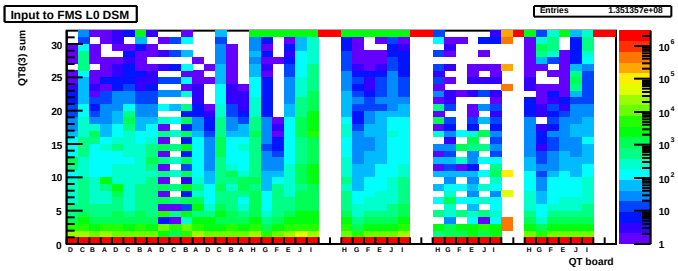

Input to FMS L0 DSM
Entries 61668
QT8(0) sum - simulated
QT board

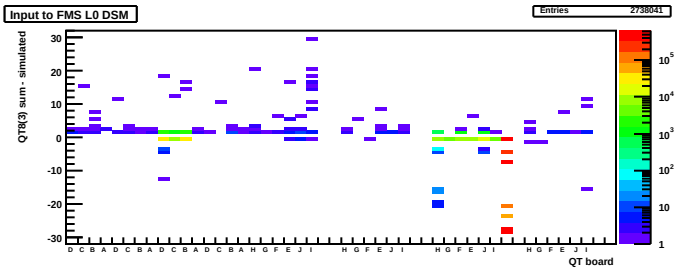

Input to FMS L0 DSM
Entries 1.351357e+08
QT8(1) sum
QT board

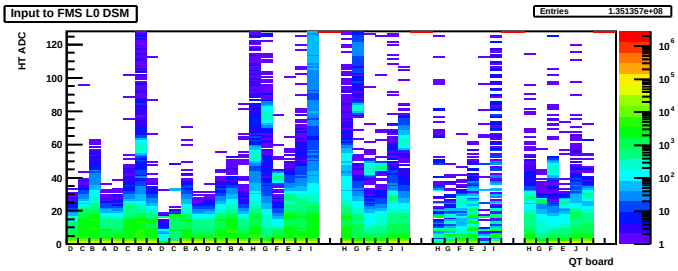

Input to FMS L0 DSM
Entries 74933
QT8(1) sum - simulated
QT board

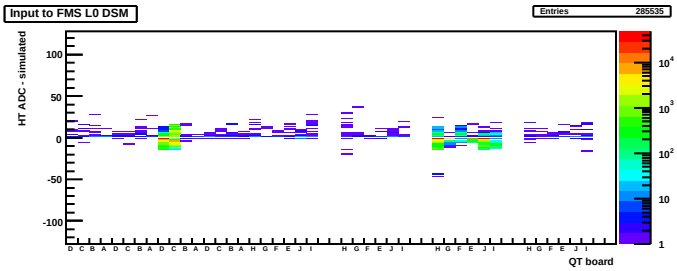

Input to FMS L0 DSM
Entries 1.351357e+08
QT8(2) sum
QT board

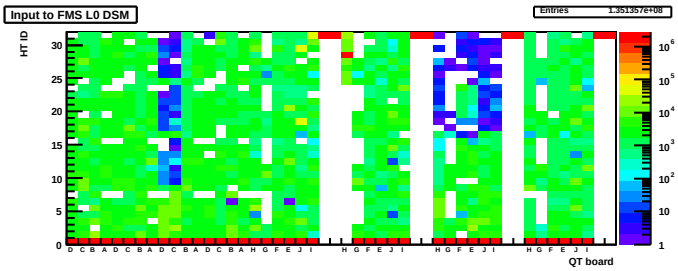

Input to FMS L0 DSM
Entries 2041523
QT8(2) sum - simulated
QT board

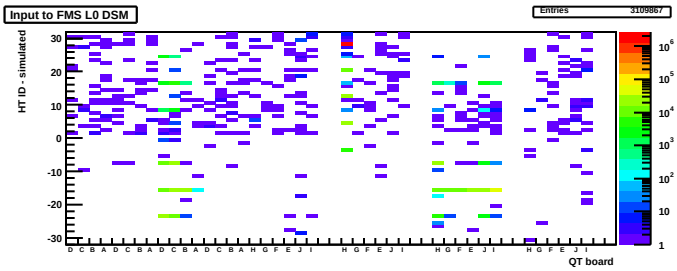

Input to FMS L0 DSM
Entries 1.351357e+08
QT8(3) sum
QT board

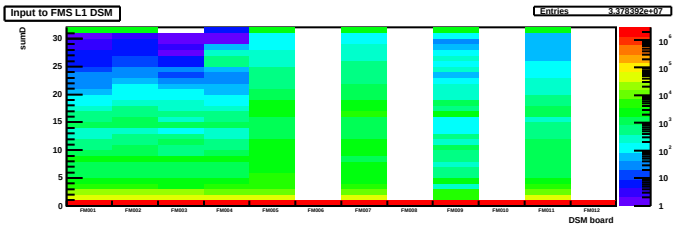

Input to FMS L0 DSM
Entries 2738041
QT8(3) sum - simulated
QT board

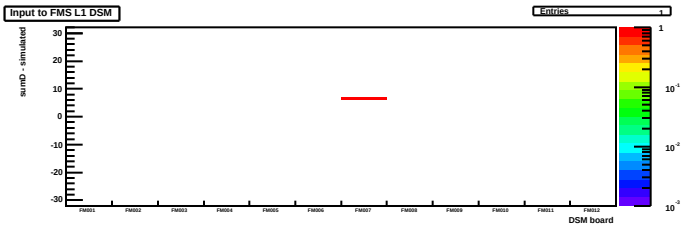

Input to FMS L0 DSM
Entries 1.351357e+08
HT ADC
QT board

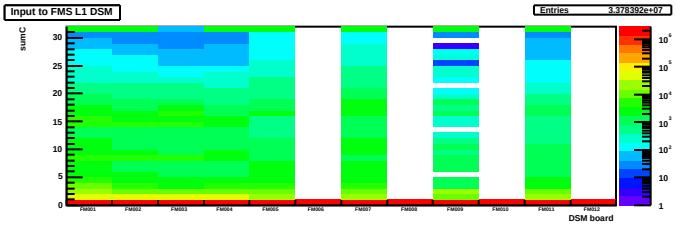

Input to FMS L0 DSM
Entries 285535
HT ADC - simulated
QT board

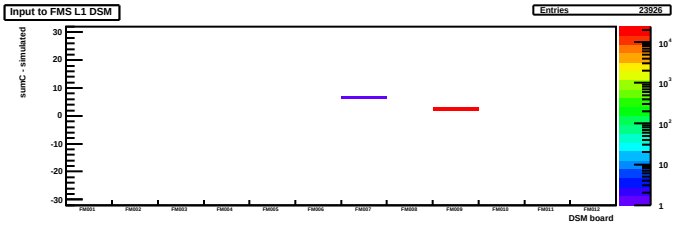

Input to FMS L0 DSM
Entries 1.351357e+08
HT ID
QT board

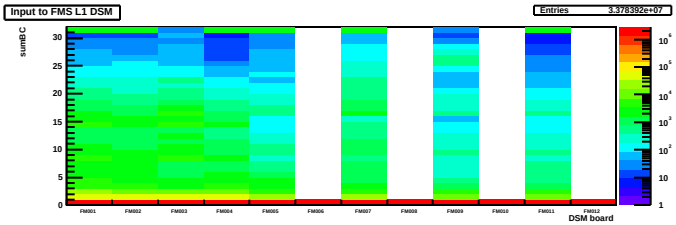

Input to FMS L0 DSM
Entries 3109867
HT ID - simulated
QT board

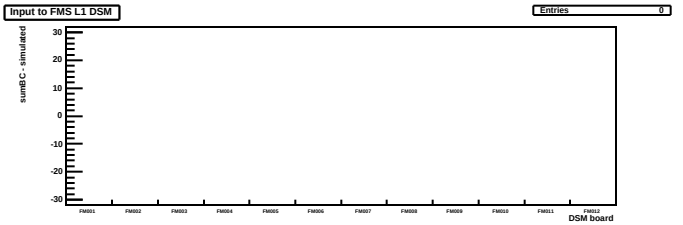

Input to FMS L1 DSM
Entries 3.378392e+07
sumD
DSM board

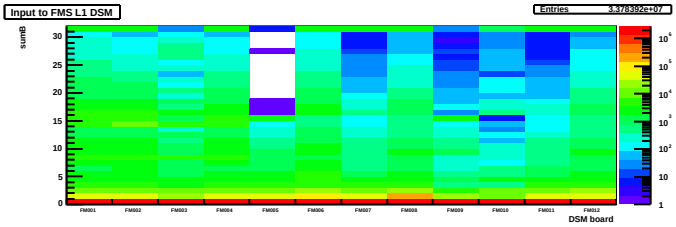

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

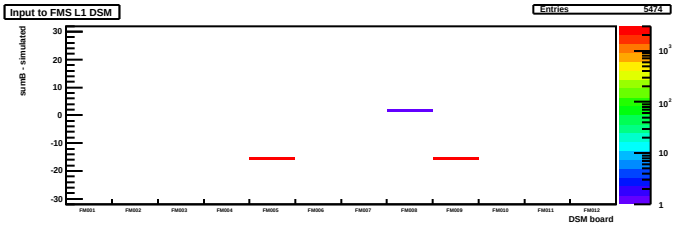

Input to FMS L1 DSM
Entries 3.378392e+07
sumC
DSM board

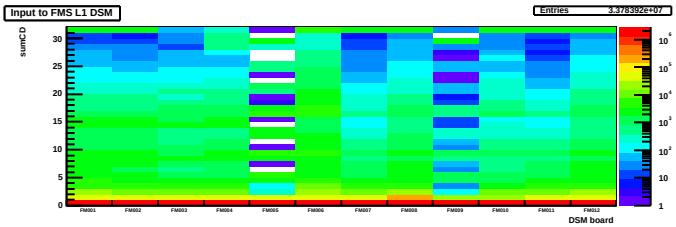

Input to FMS L1 DSM
Entries 23926
sumC - simulated
DSM board

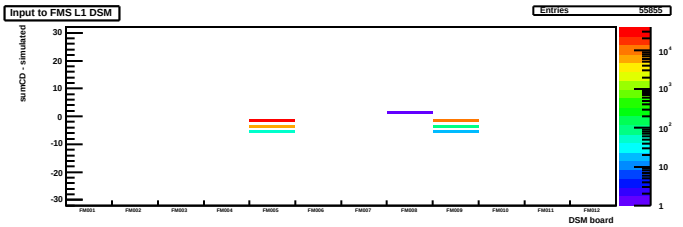

Input to FMS L1 DSM
Entries 3.378392e+07
sumBC
DSM board

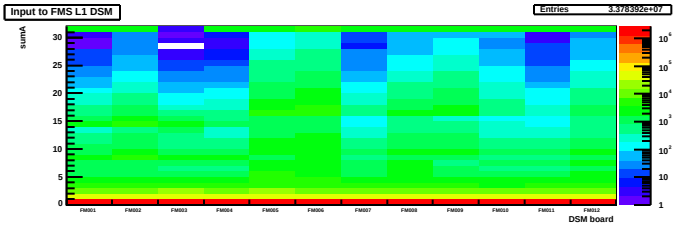

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

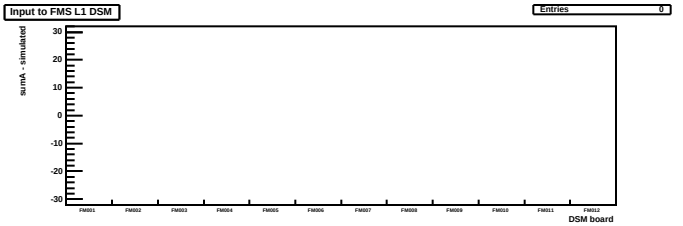

Input to FMS L1 DSM
Entries 3.378392e+07
sumB
DSM board

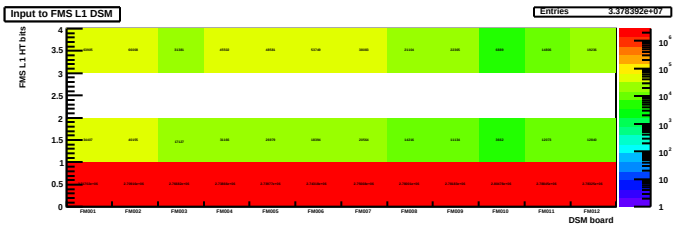

Input to FMS L1 DSM
Entries 5474
sumB - simulated
DSM board

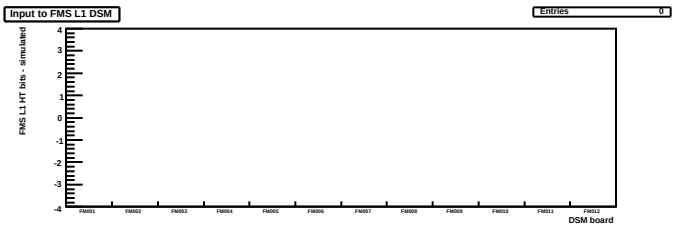

Input to FMS L1 DSM
Entries 3.378392e+07
sumCD
DSM board

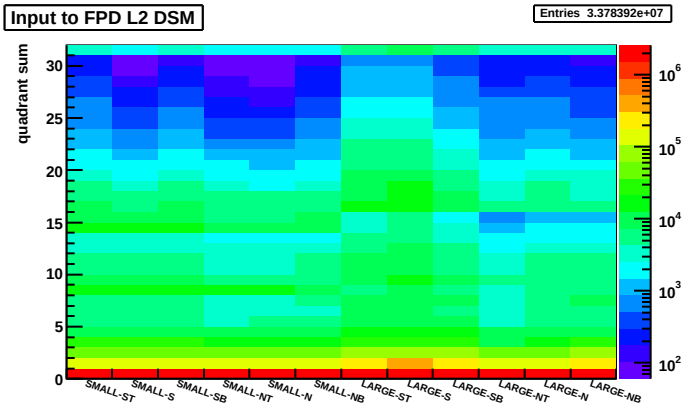

Input to FMS L1 DSM
Entries 55855
sumCD - simulated
DSM board

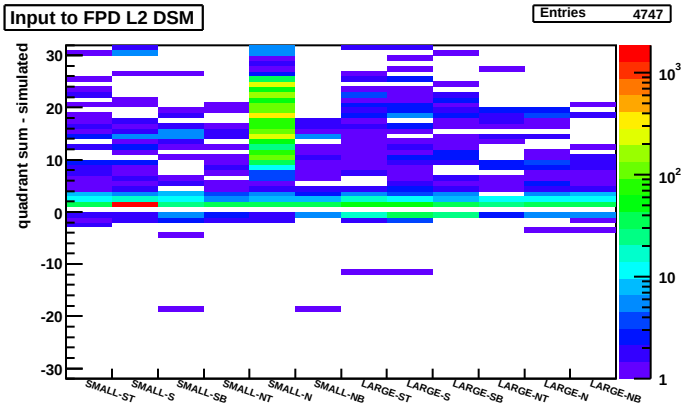

Input to FMS L1 DSM
Entries 3.378392e+07
sumA
DSM board

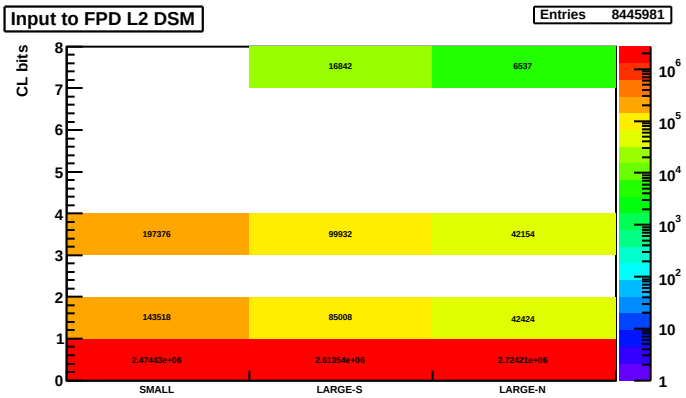

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

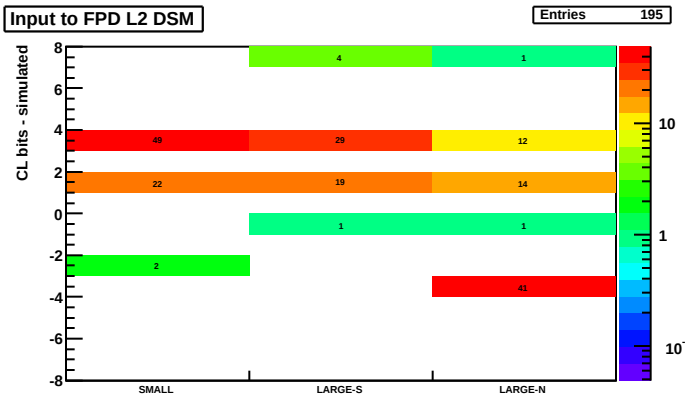

Input to FMS L1 DSM
Entries 3.378392e+07
FMS L1 HT bits
DSM board

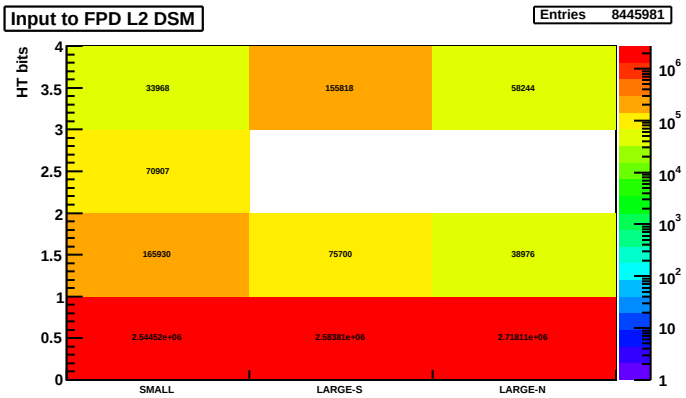

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

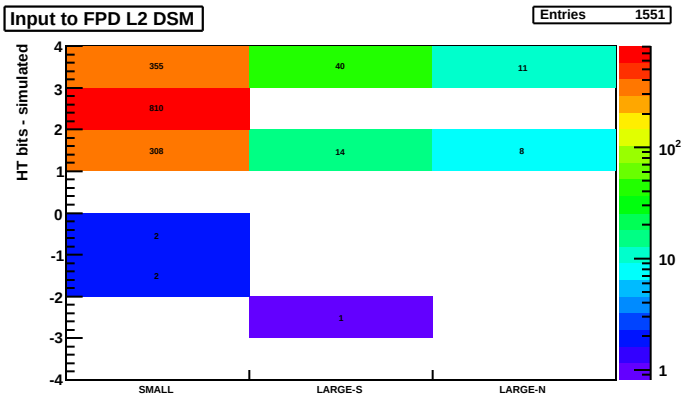

Input to FPD L2 DSM
Entries 3.378392e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

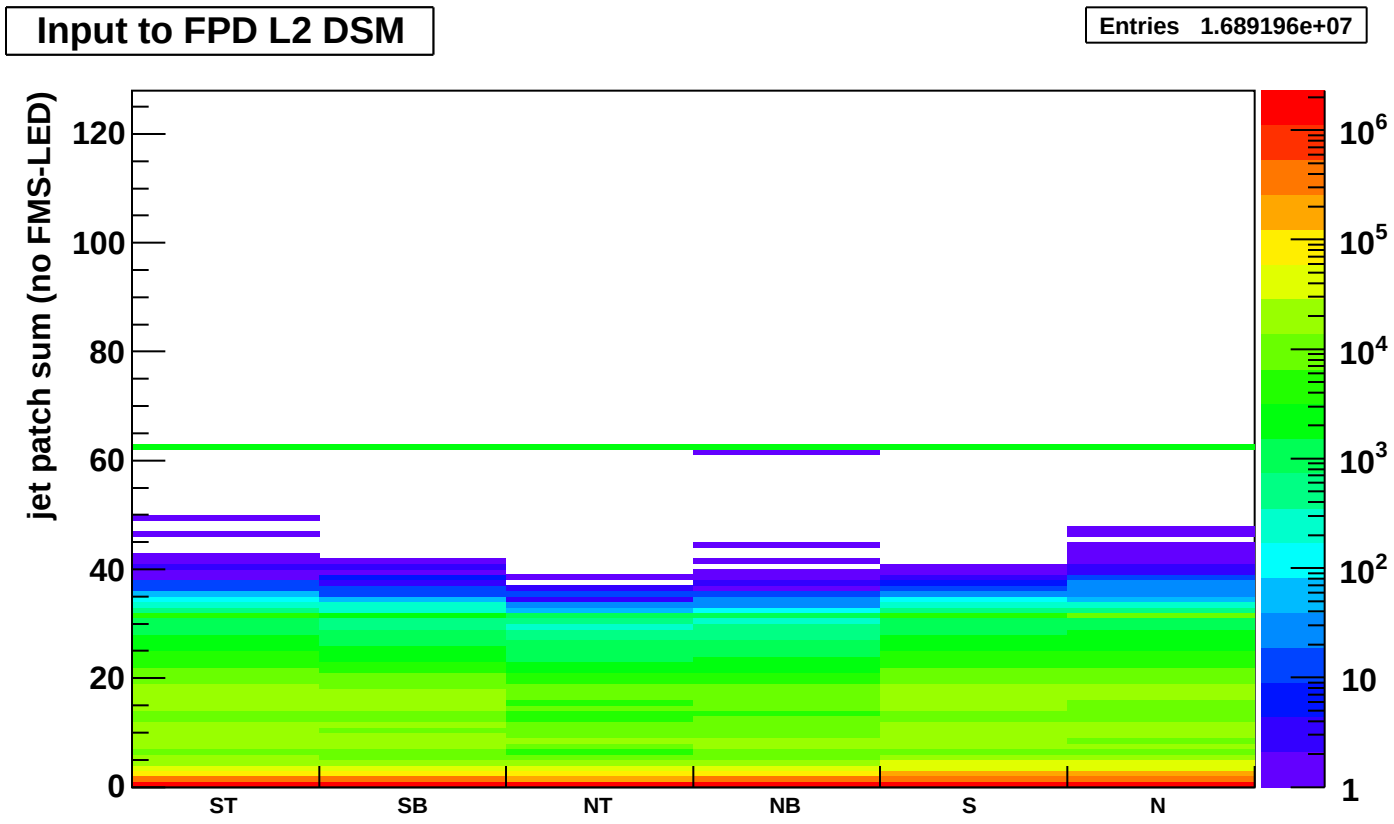

Input to FPD L2 DSM
Entries 4747
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

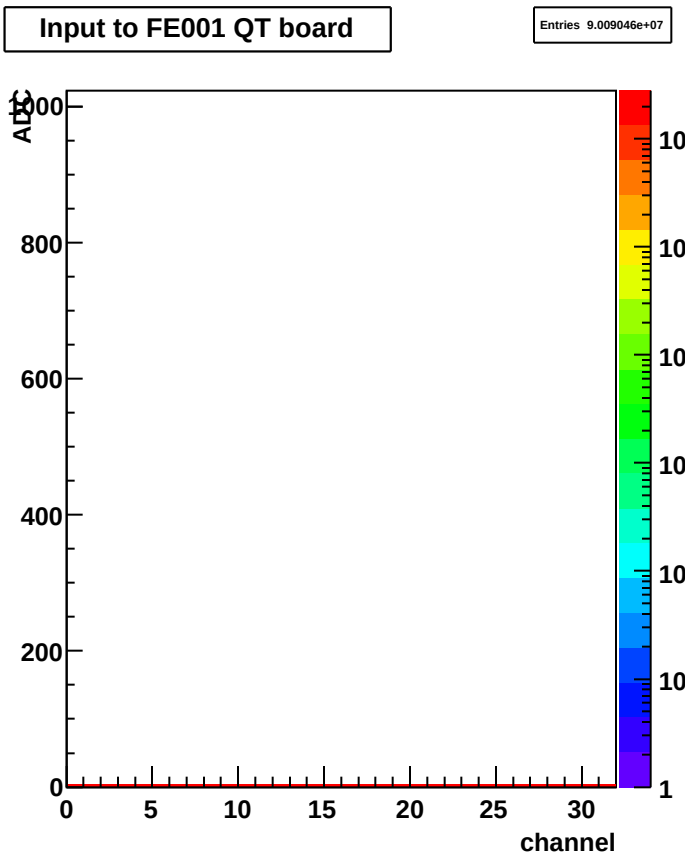

Input to FPD L2 DSM
Entries 8445981
CL bits
16842
6537
197376
99932
42154
143518
85008
42424
2.47443e+06
2.61354e+06
2.72421e+06
SMALL
LARGE-S
LARGE-N

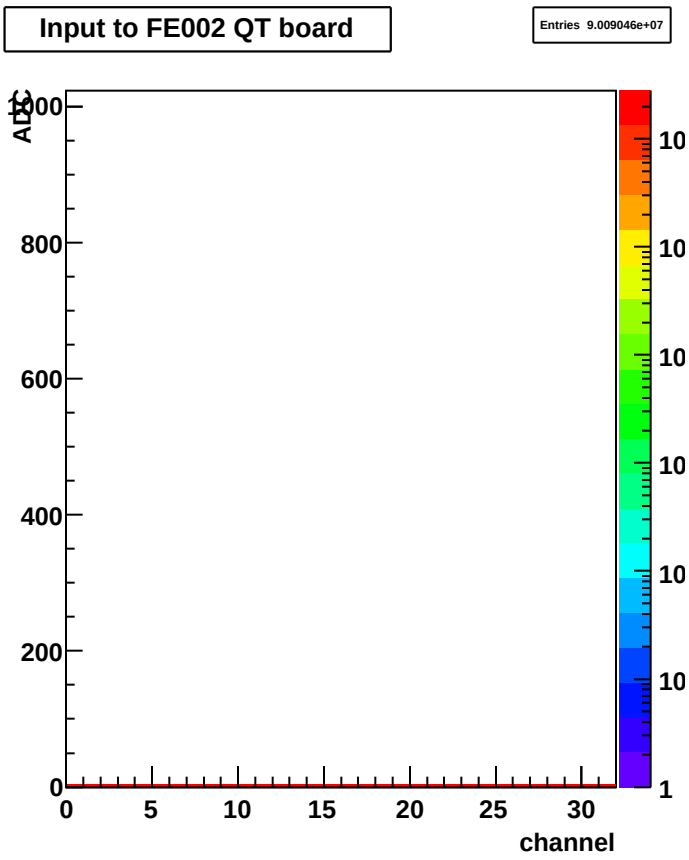

Input to FPD L2 DSM
Entries 195
CL bits - simulated
4
1
49
29
12
22
19
14
1
1
2
41
SMALL
LARGE-S
LARGE-N

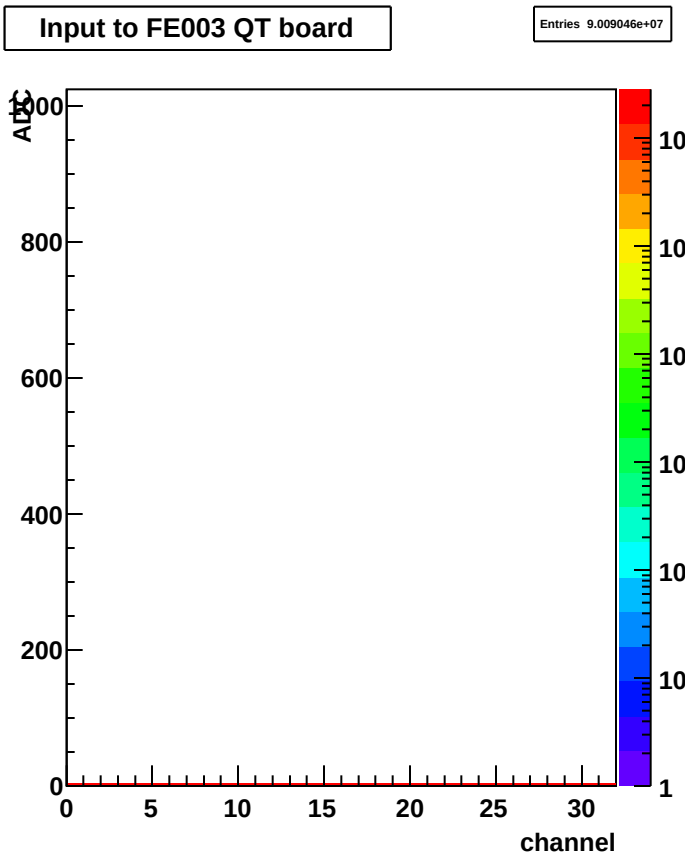

Input to FPD L2 DSM
Entries 8445981
HT bits
33968
155818
58244
70907
165930
75700
38976
2.54452e+06
2.58381e+06
2.71811e+06
SMALL
LARGE-S
LARGE-N

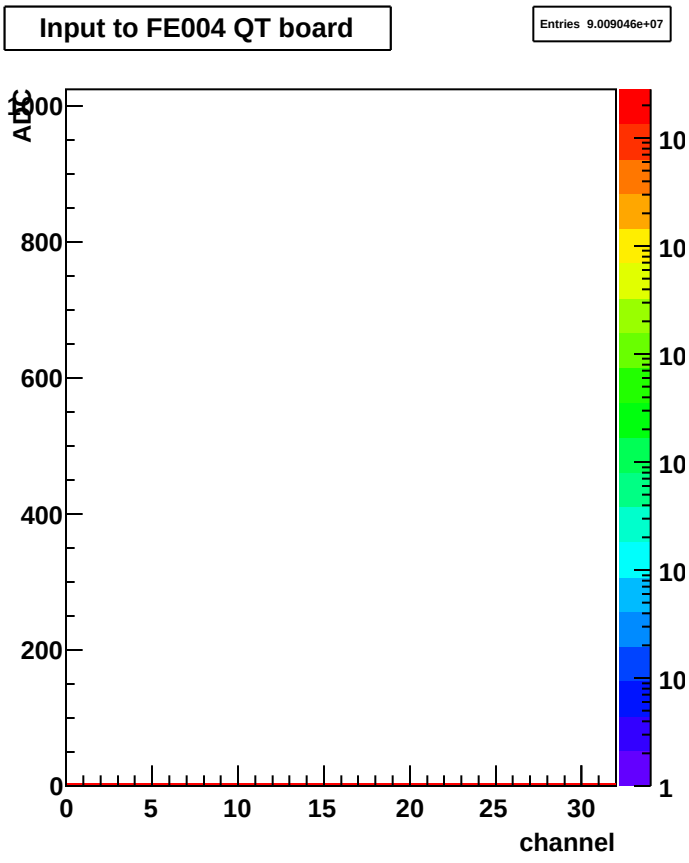

Input to FPD L2 DSM
Entries 1551
HT bits - simulated
355
40
11
810
308
14
8
2
2
1
SMALL
LARGE-S
LARGE-N

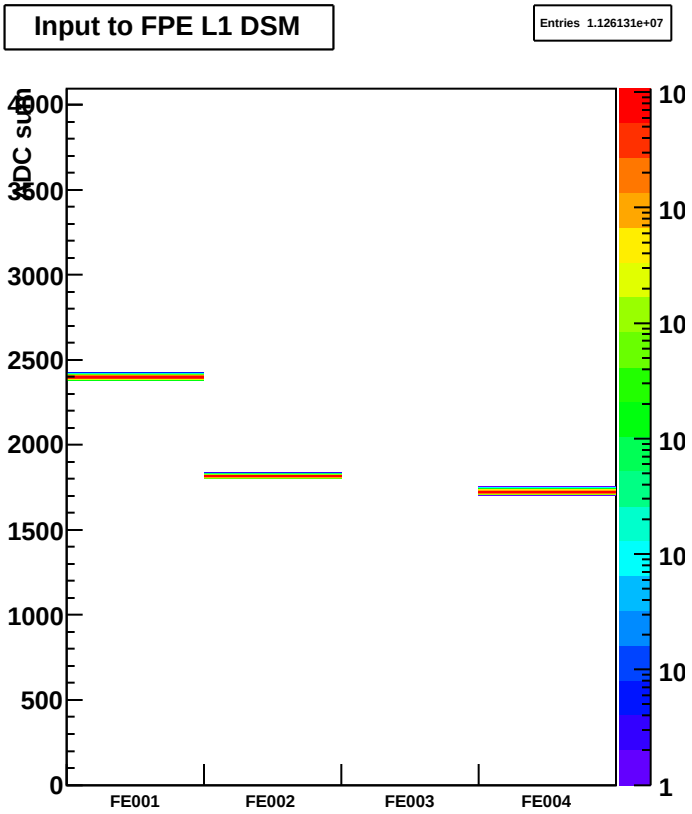

Input to FPD L2 DSM
Entries 1.689196e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 9.009046e+07
ADC
channel

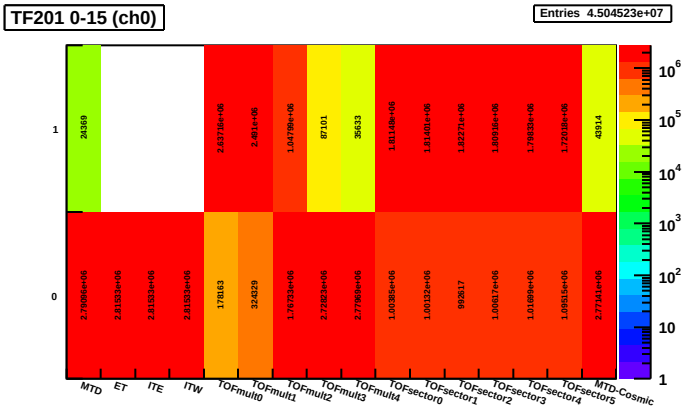

Input to FE002 QT board
Entries 9.009046e+07
ADC
channel


Input to FE003 QT board
Entries 9.009046e+07
ADC
channel

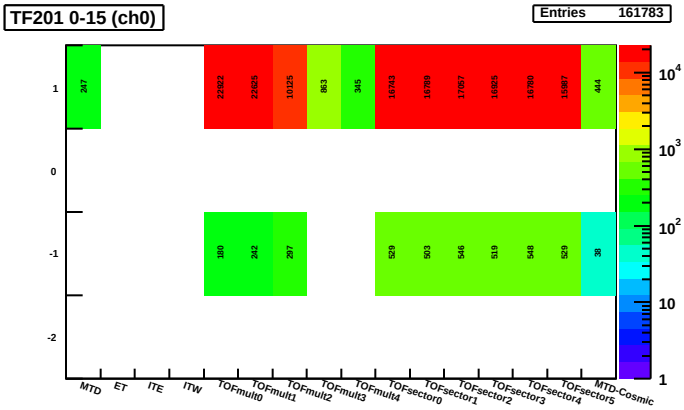

Input to FE004 QT board
Entries 9.009046e+07
ADC
channel


Input to FPE L1 DSM
Entries 1.126131e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
10
1

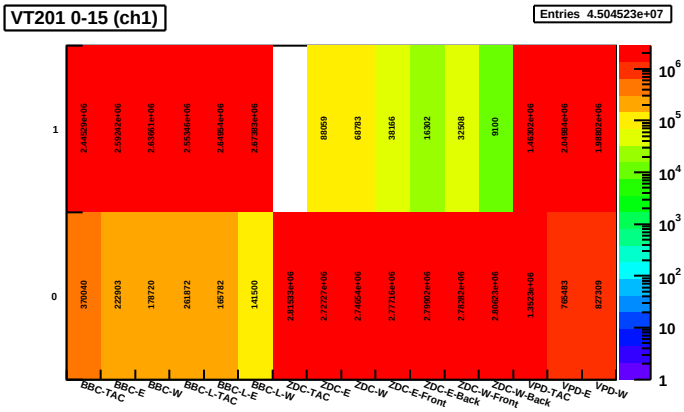
TF201 0-15 (ch0)



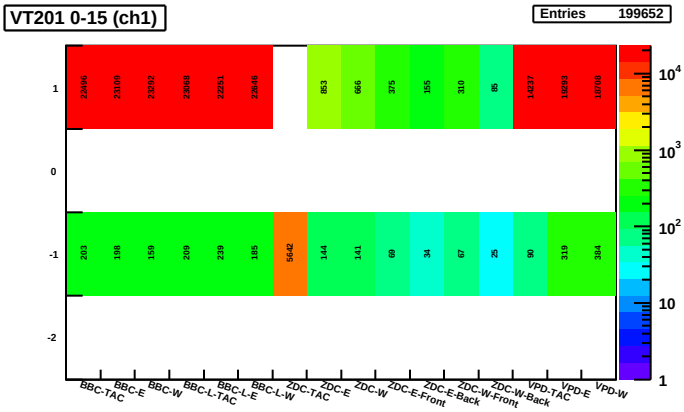
TF201 0-15 (ch0)



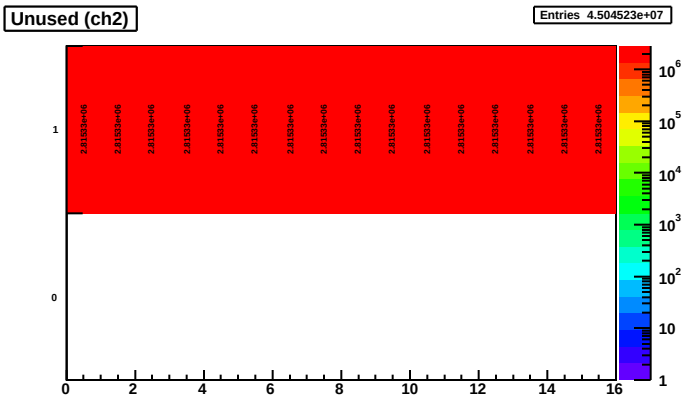
VT201 0-15 (ch1)

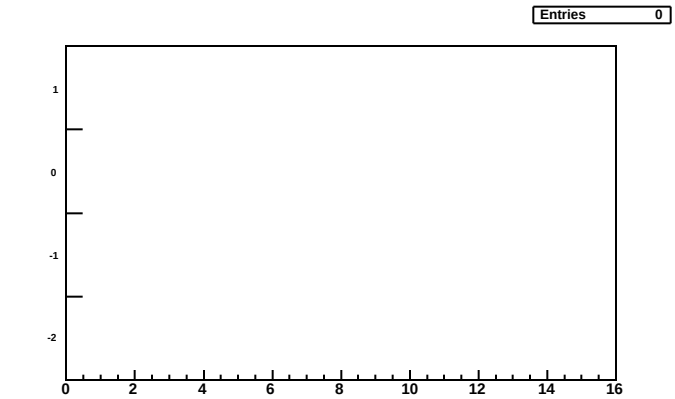


VT201 0-15 (ch1)

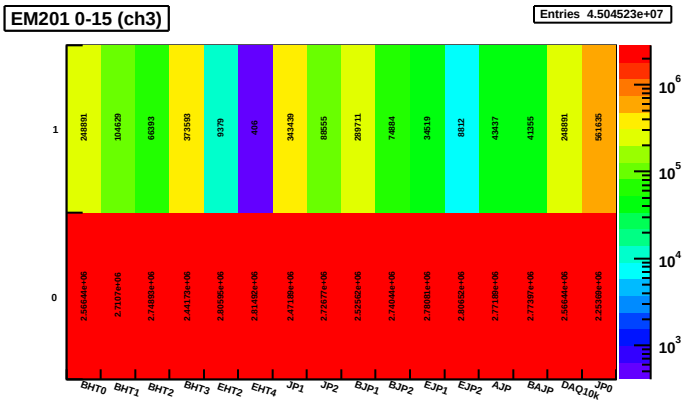


Unused (ch2)





EM201 0-15 (ch3)



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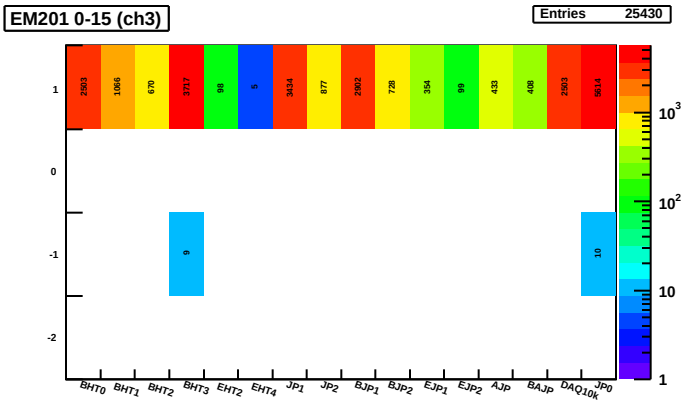
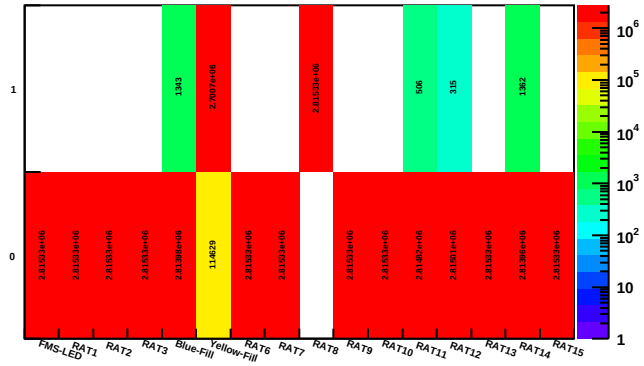
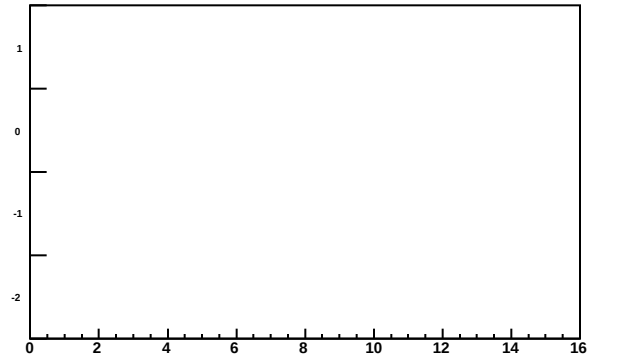
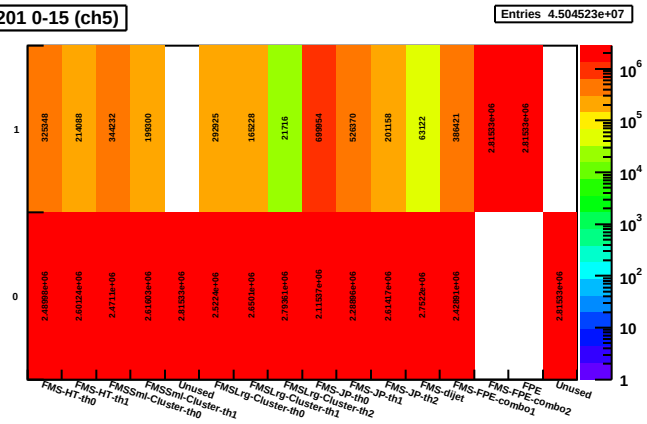


RAT board (ch4)





FP201 0-15 (ch5)



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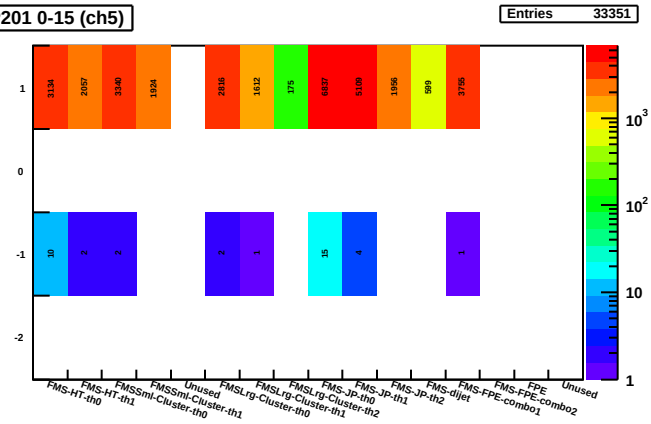
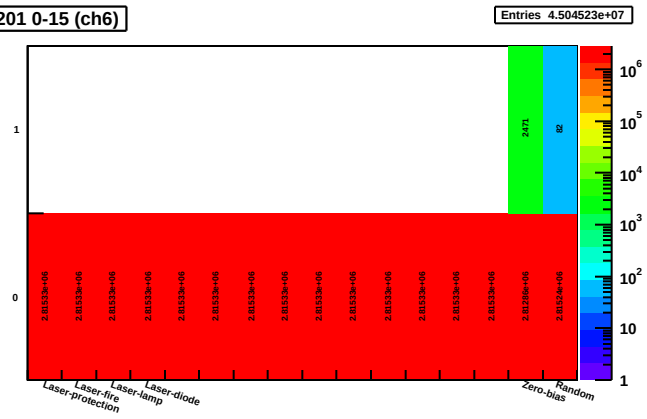
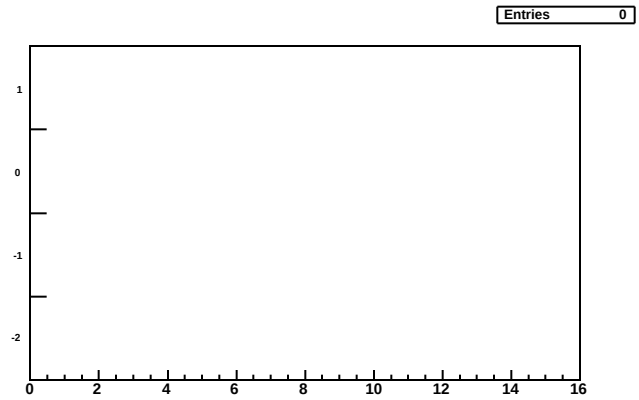
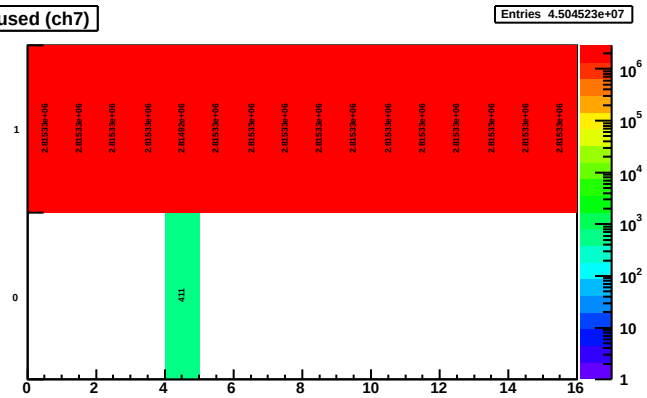
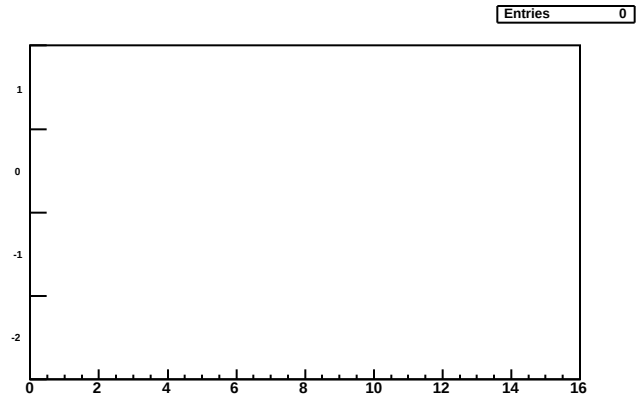


ST201 0-15 (ch6)





Unused (ch7)