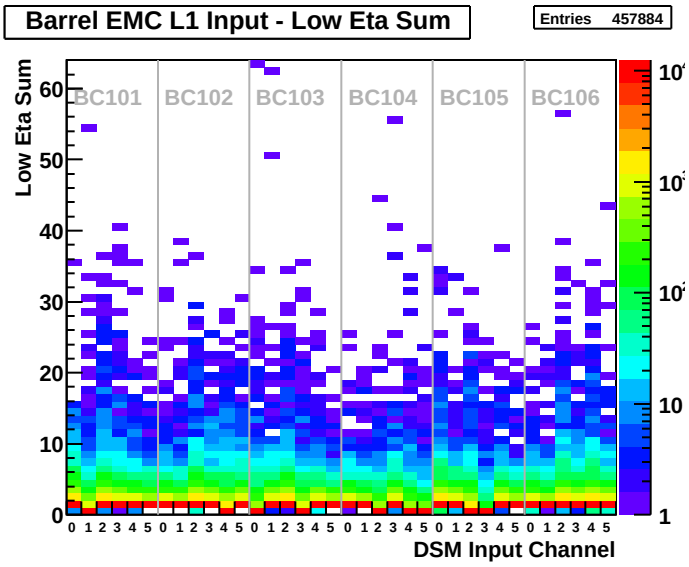
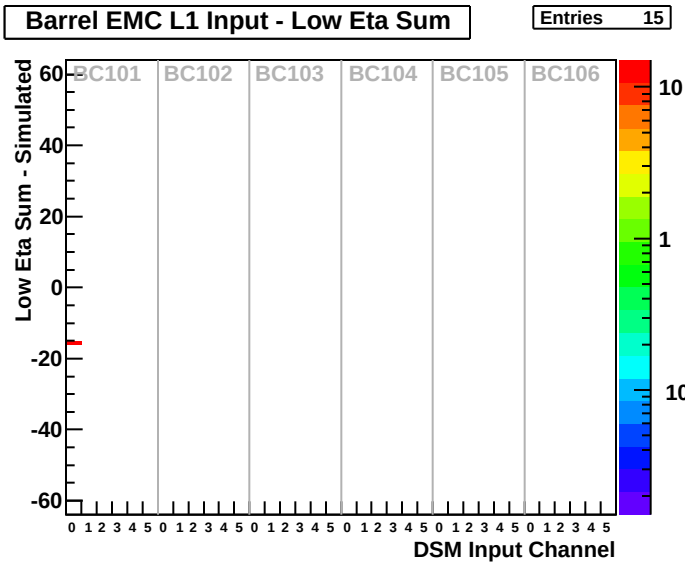
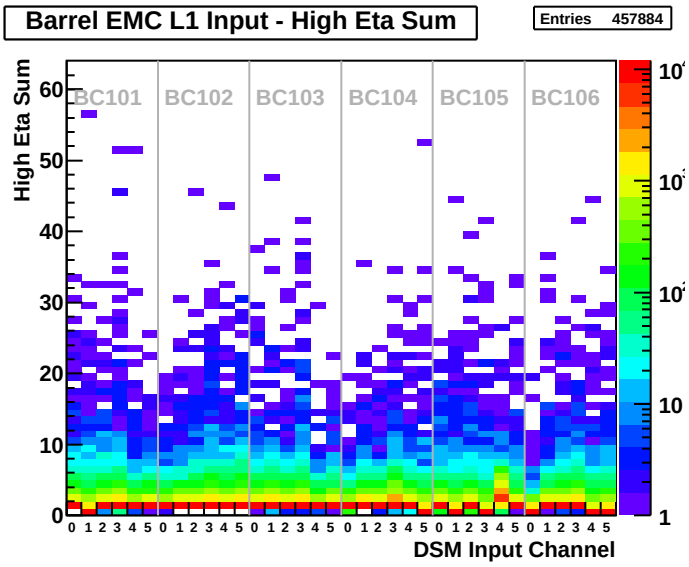
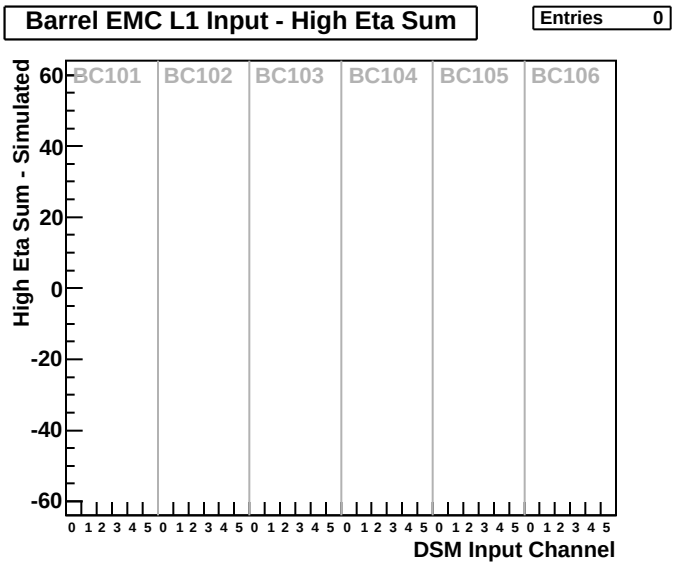
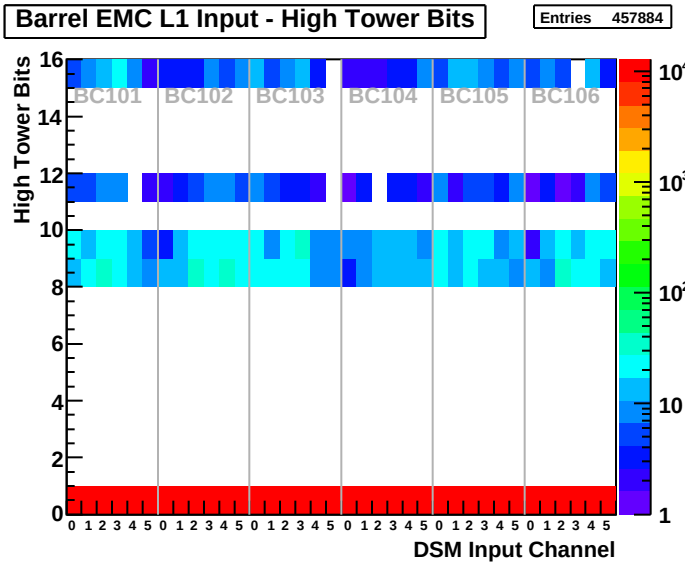
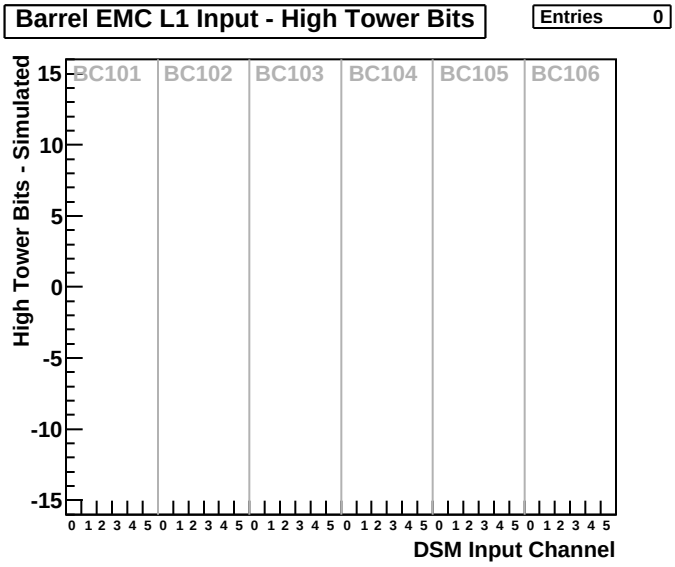

Barrel EMC L1 Input - Low Eta Sum
Entries 457884
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

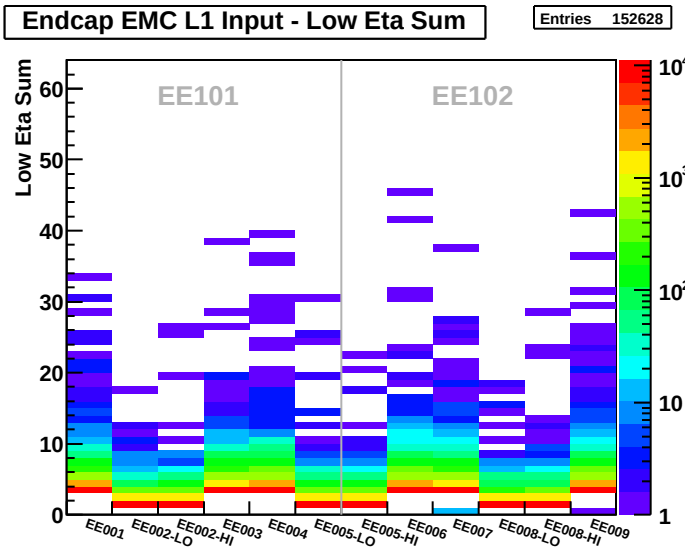

Barrel EMC L1 Input - Low Eta Sum
Entries 15
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

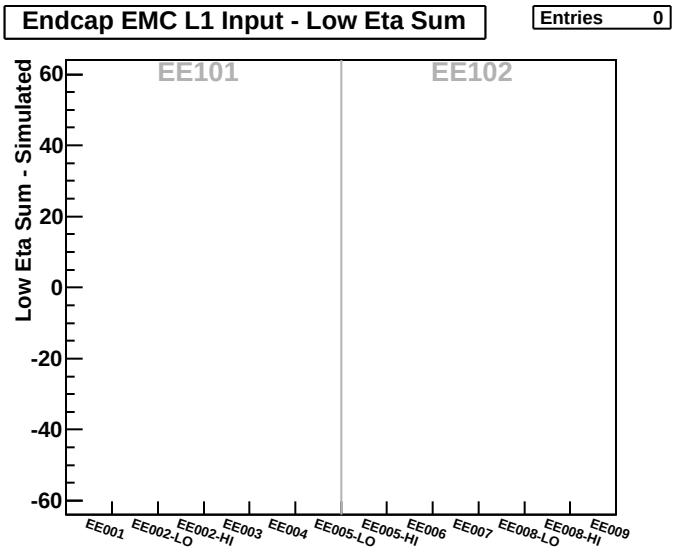

Barrel EMC L1 Input - High Eta Sum
Entries 457884
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

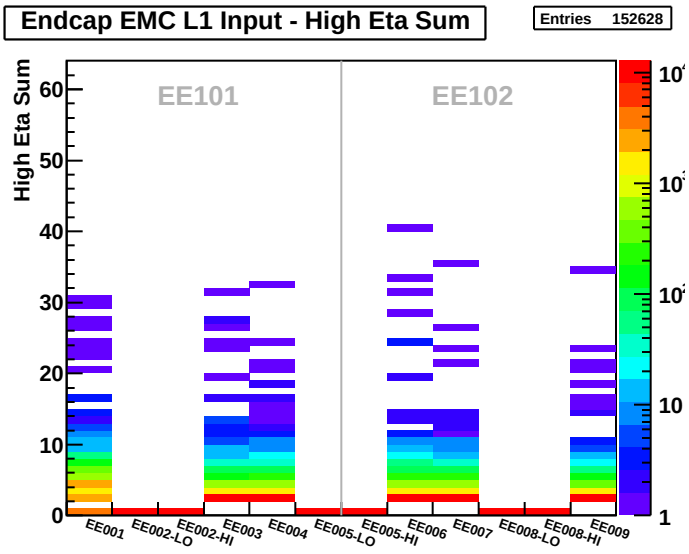

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

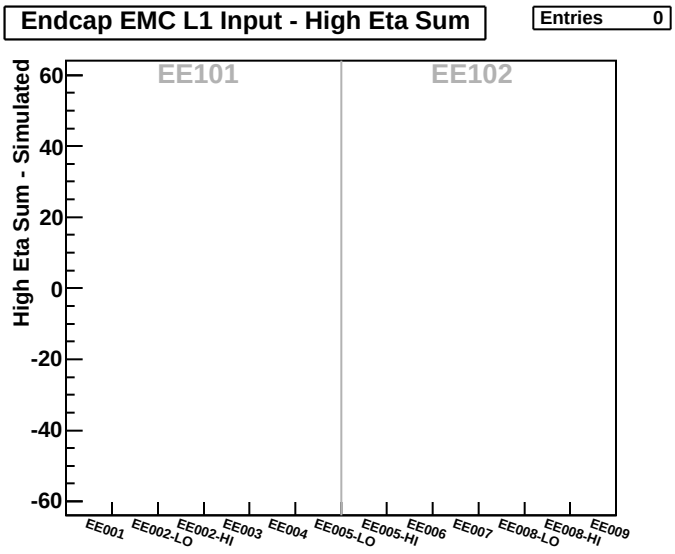

Barrel EMC L1 Input - High Tower Bits
Entries 457884
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

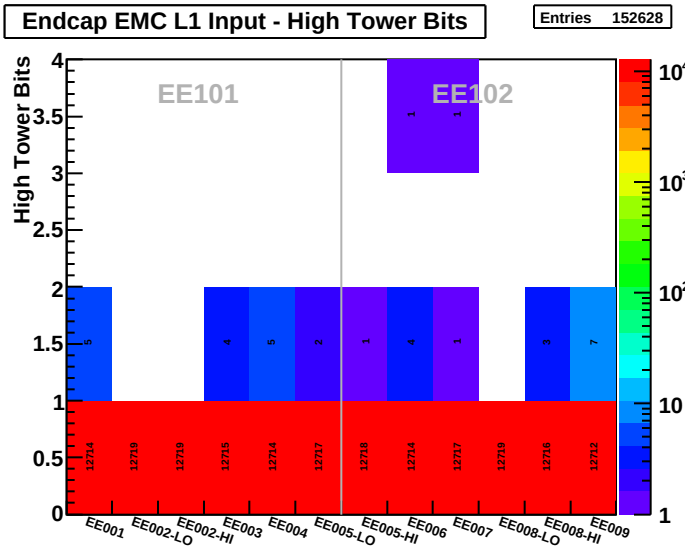

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

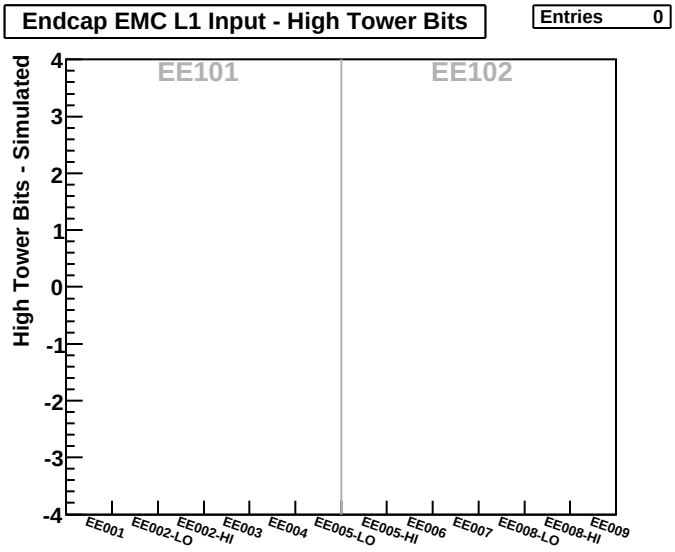

Endcap EMC L1 Input - Low Eta Sum
Entries 152628
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

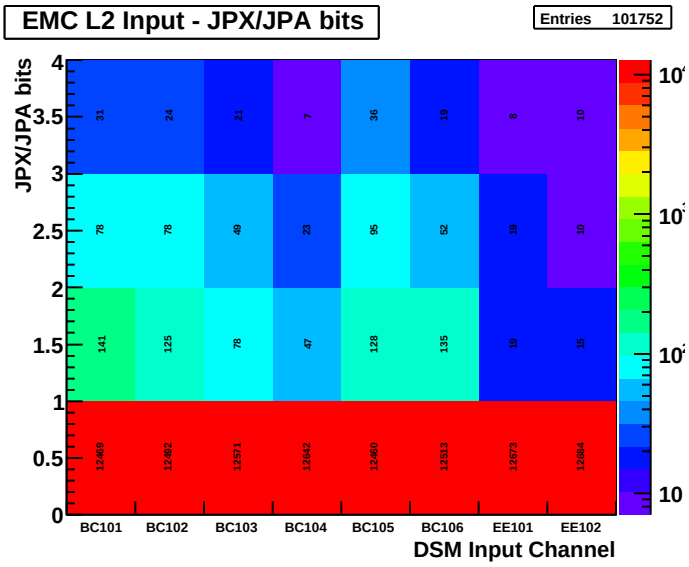

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

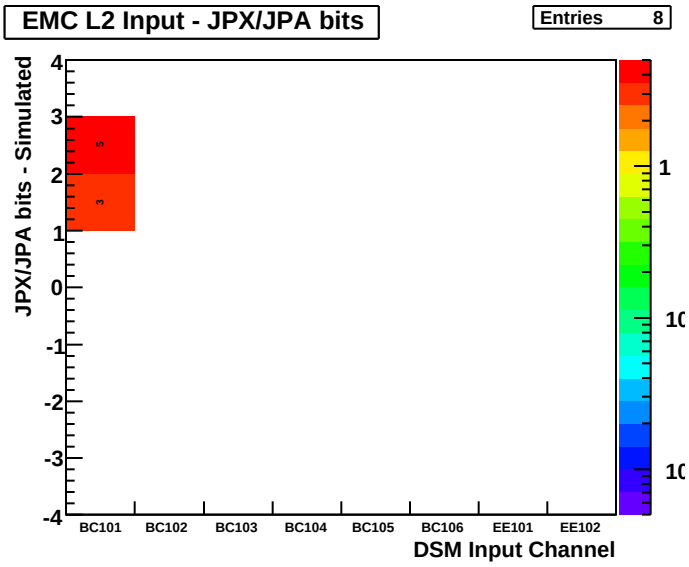

Endcap EMC L1 Input - High Eta Sum
Entries 152628
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

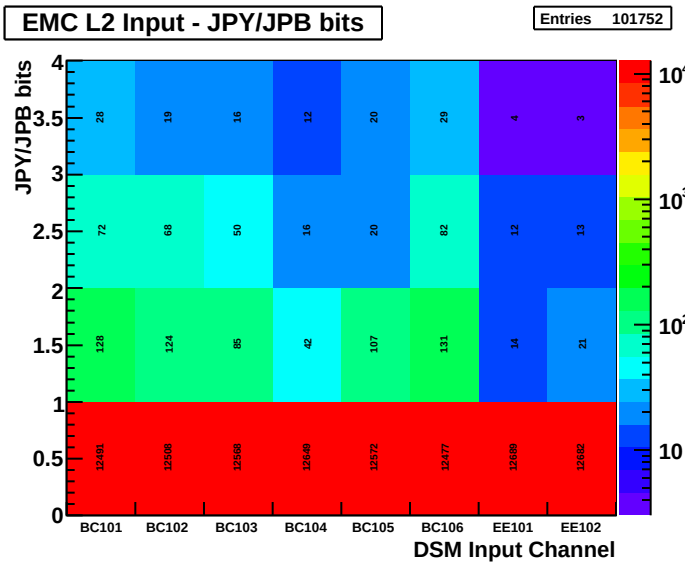

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

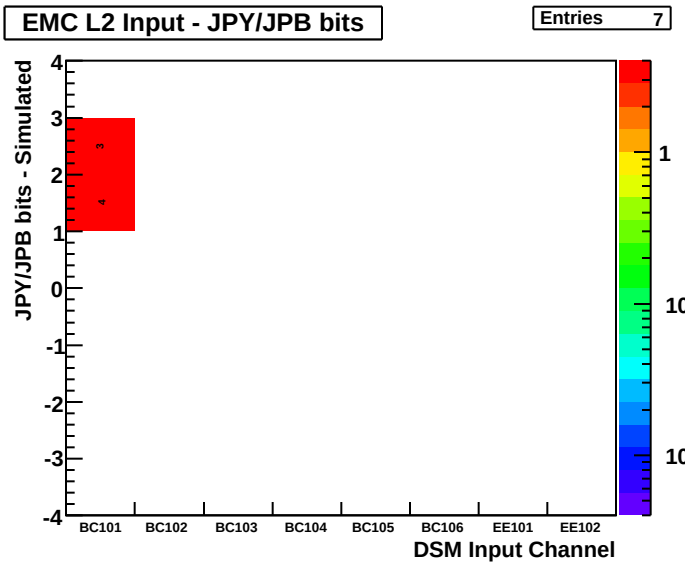

Endcap EMC L1 Input - High Tower Bits
Entries 152628
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

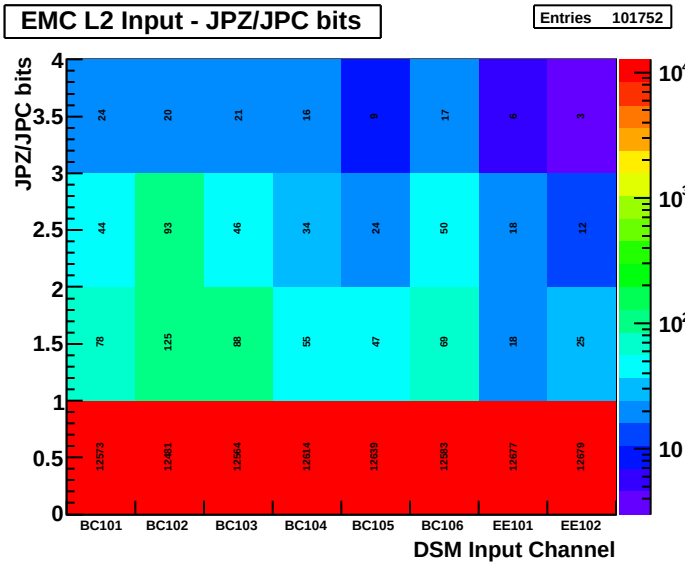

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

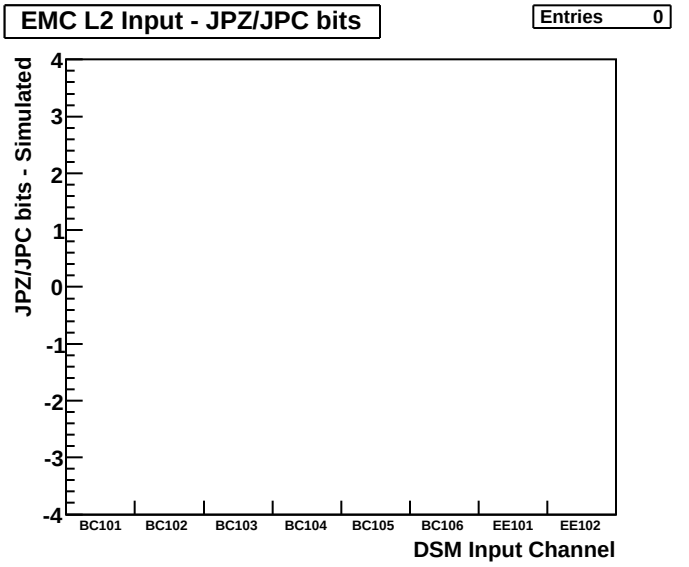

EMC L2 Input - JPX/JPA bits
Entries 101752
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

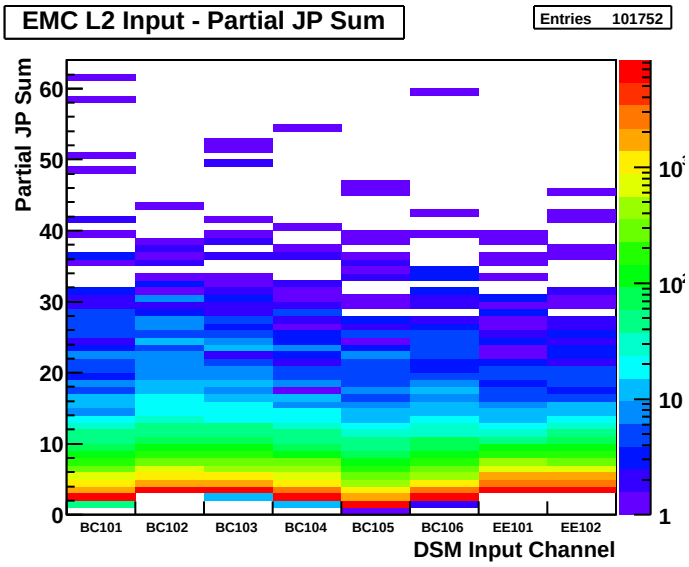

EMC L2 Input - JPX/JPA bits
Entries 8
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

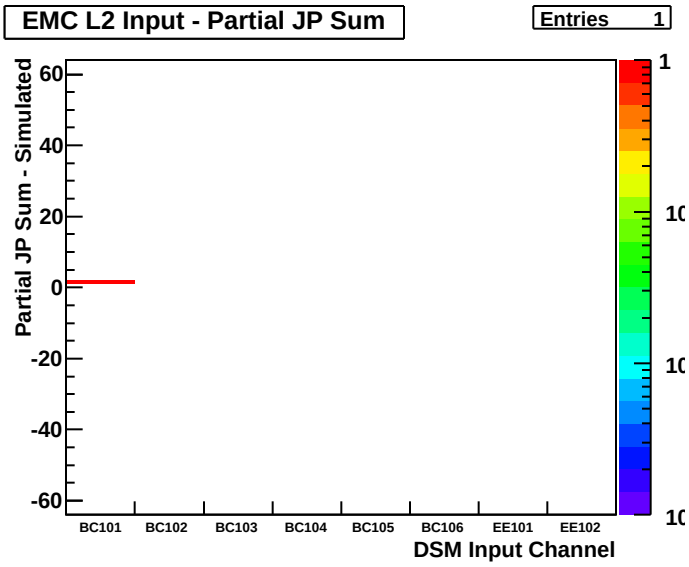

EMC L2 Input - JPY/JPB bits
Entries 101752
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

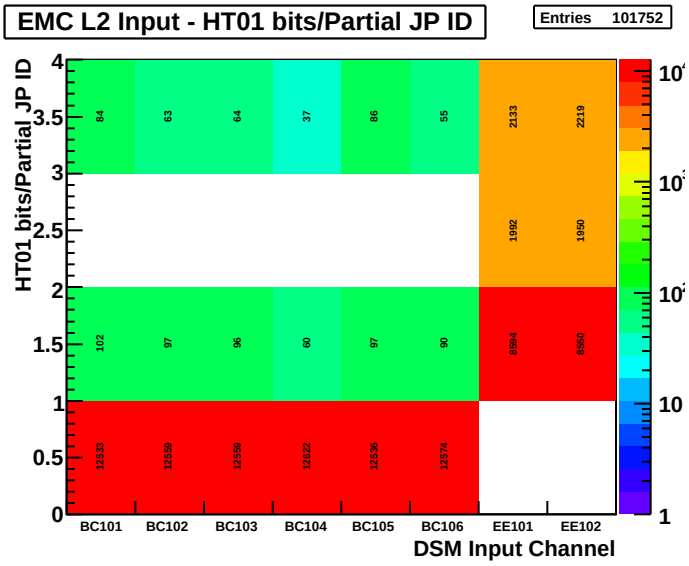

EMC L2 Input - JPY/JPB bits
Entries 7
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

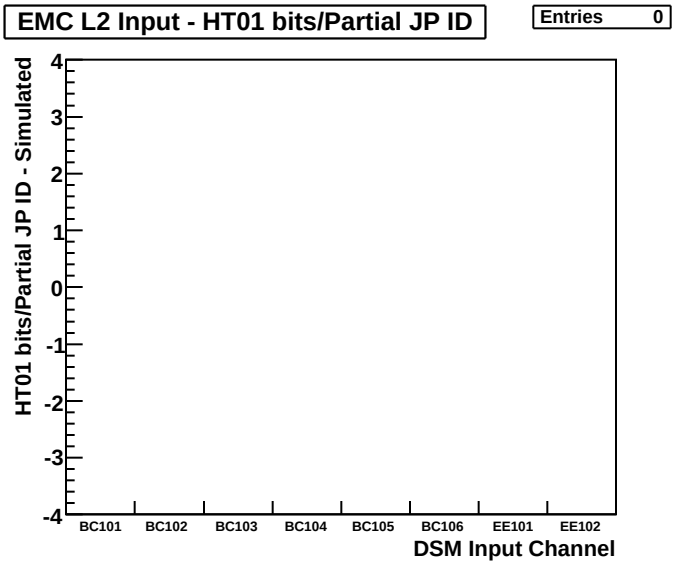

EMC L2 Input - JPZ/JPC bits
Entries 101752
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

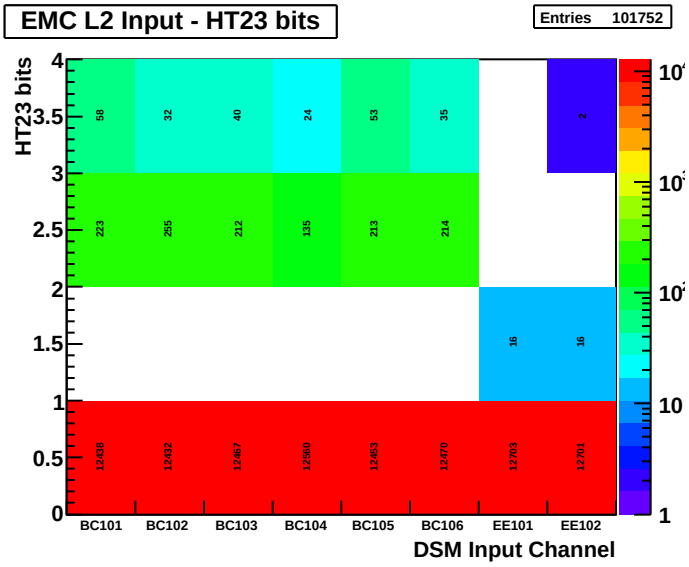

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

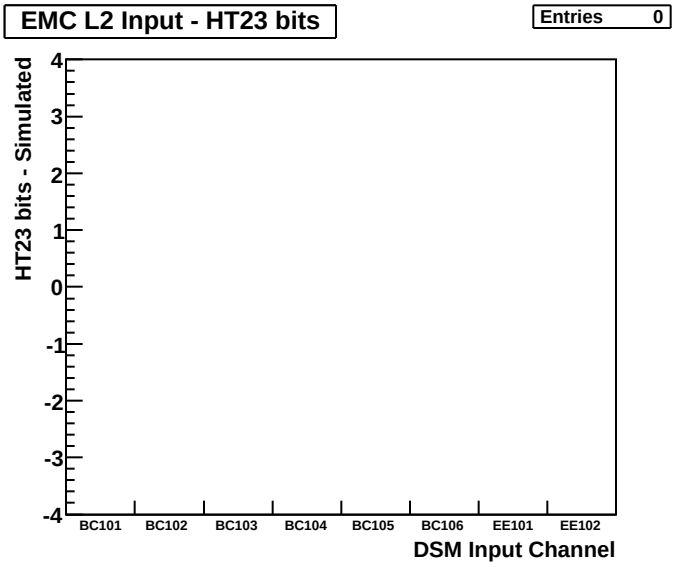

EMC L2 Input - Partial JP Sum
Entries 101752
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

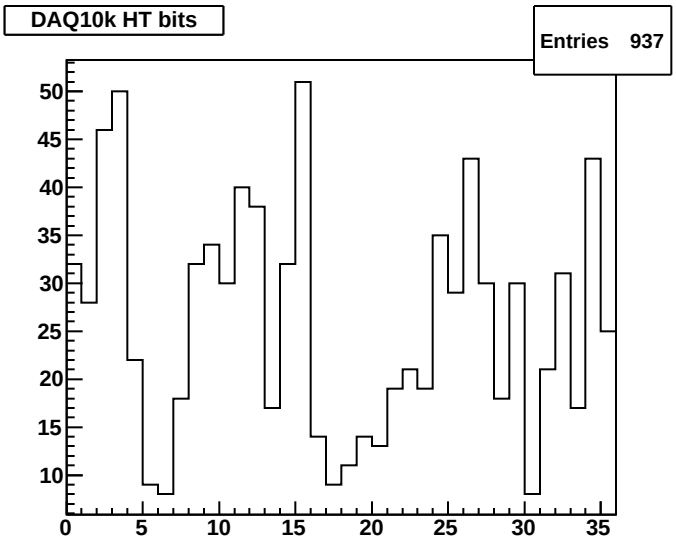

EMC L2 Input - Partial JP Sum
Entries 1
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

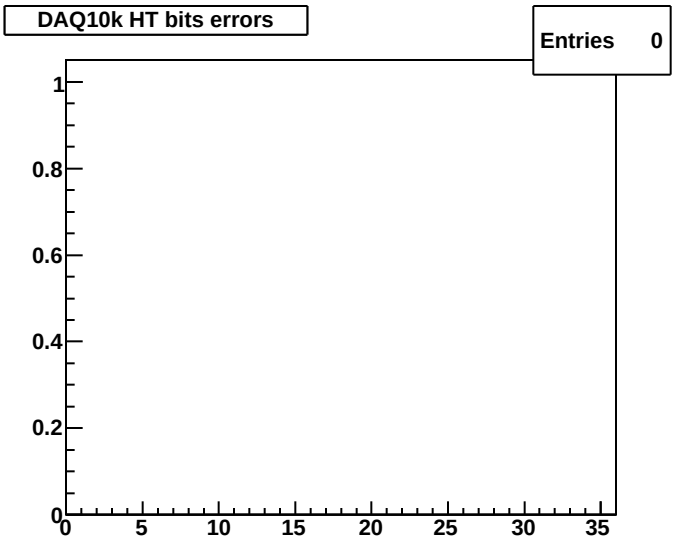

EMC L2 Input - HT01 bits/Partial JP ID
Entries 101752
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

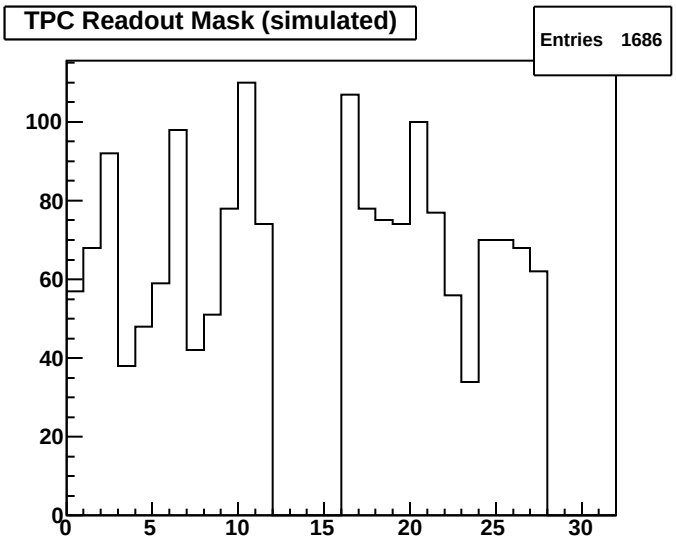

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

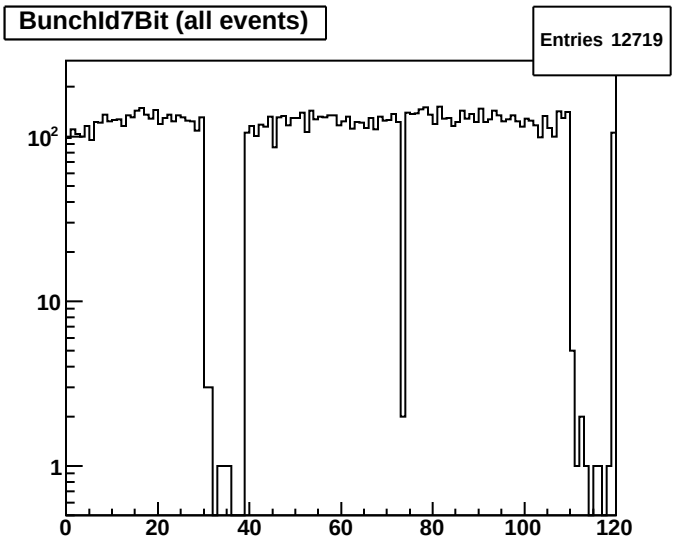

EMC L2 Input - HT23 bits
Entries 101752
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

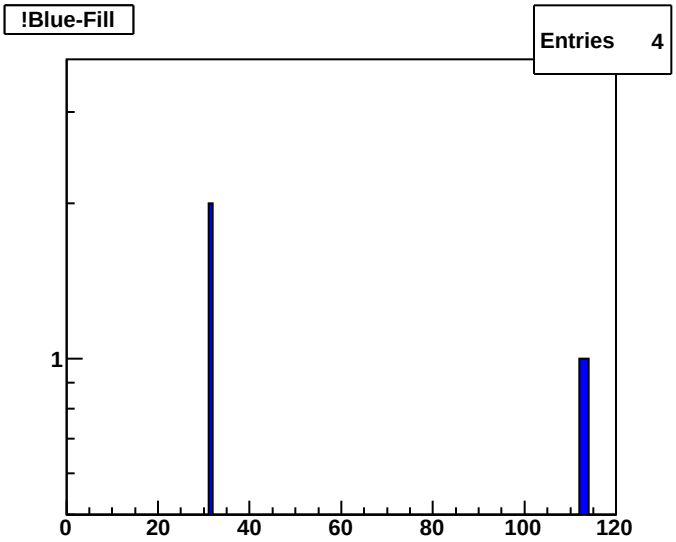

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

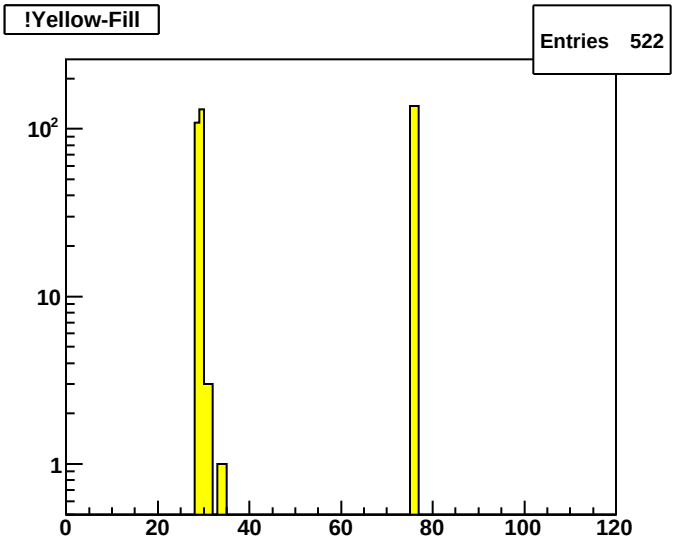

DAQ10k HT bits
Entries 937

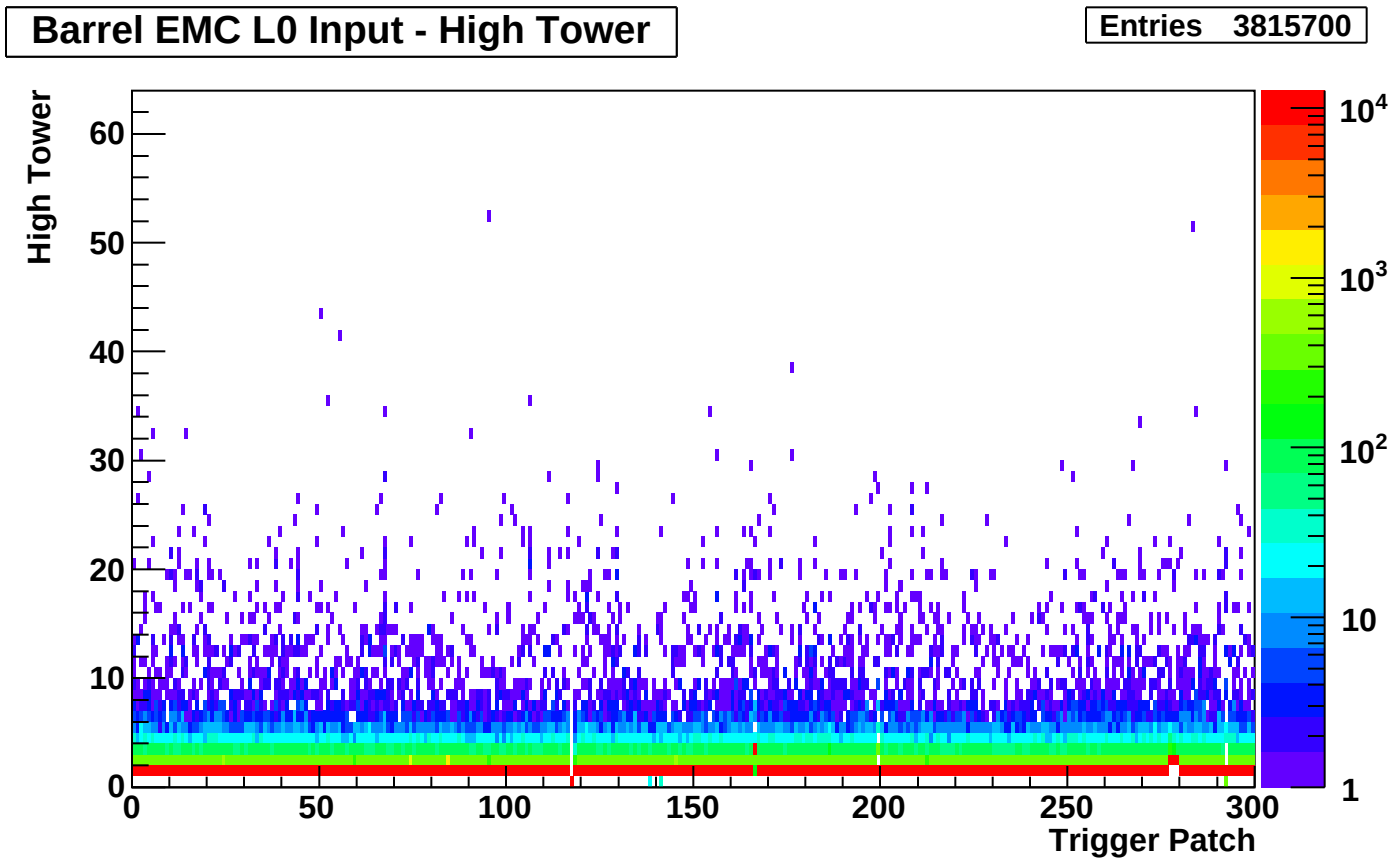

DAQ10k HT bits errors
Entries 0

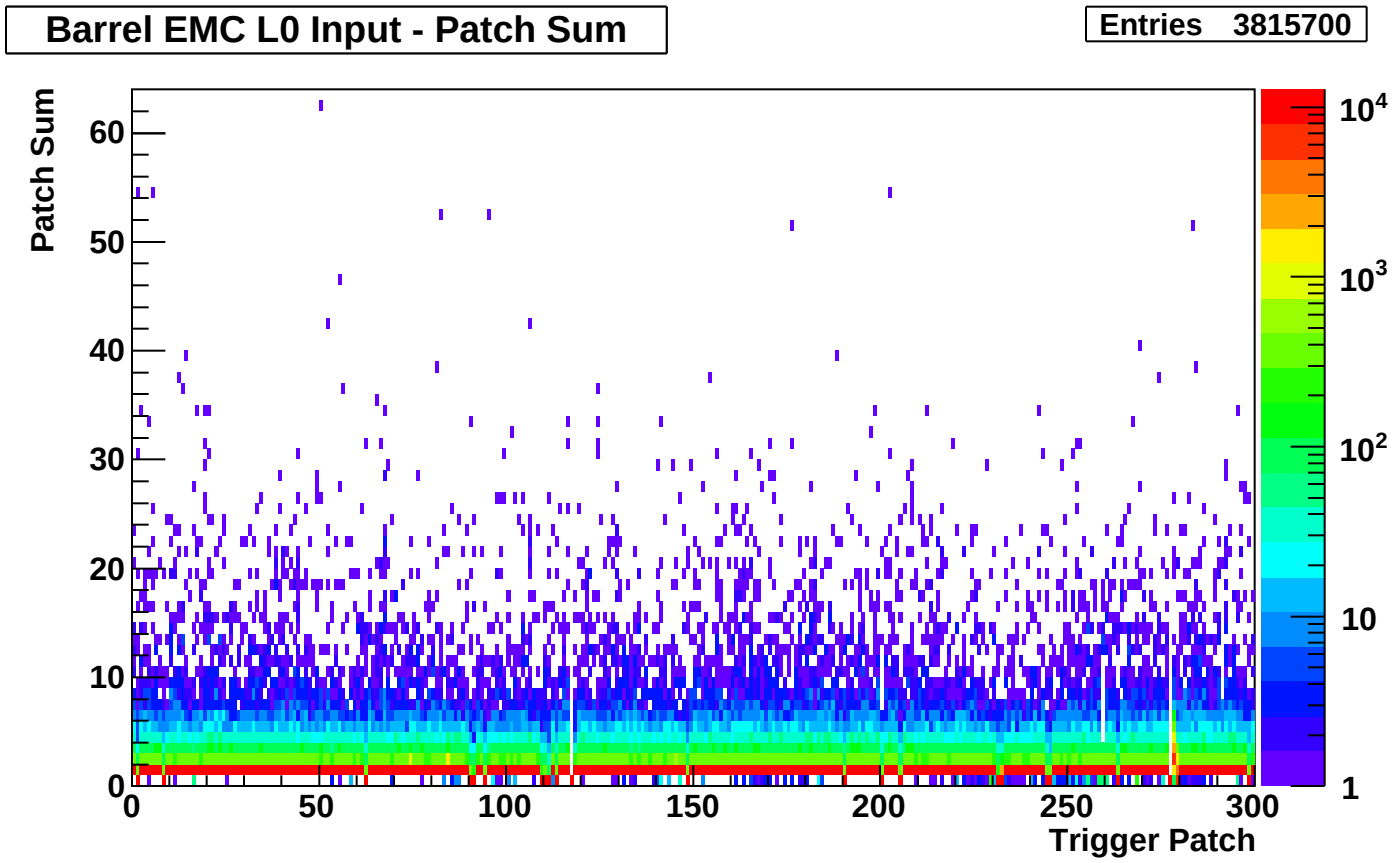

TPC Readout Mask (simulated)
Entries 1686

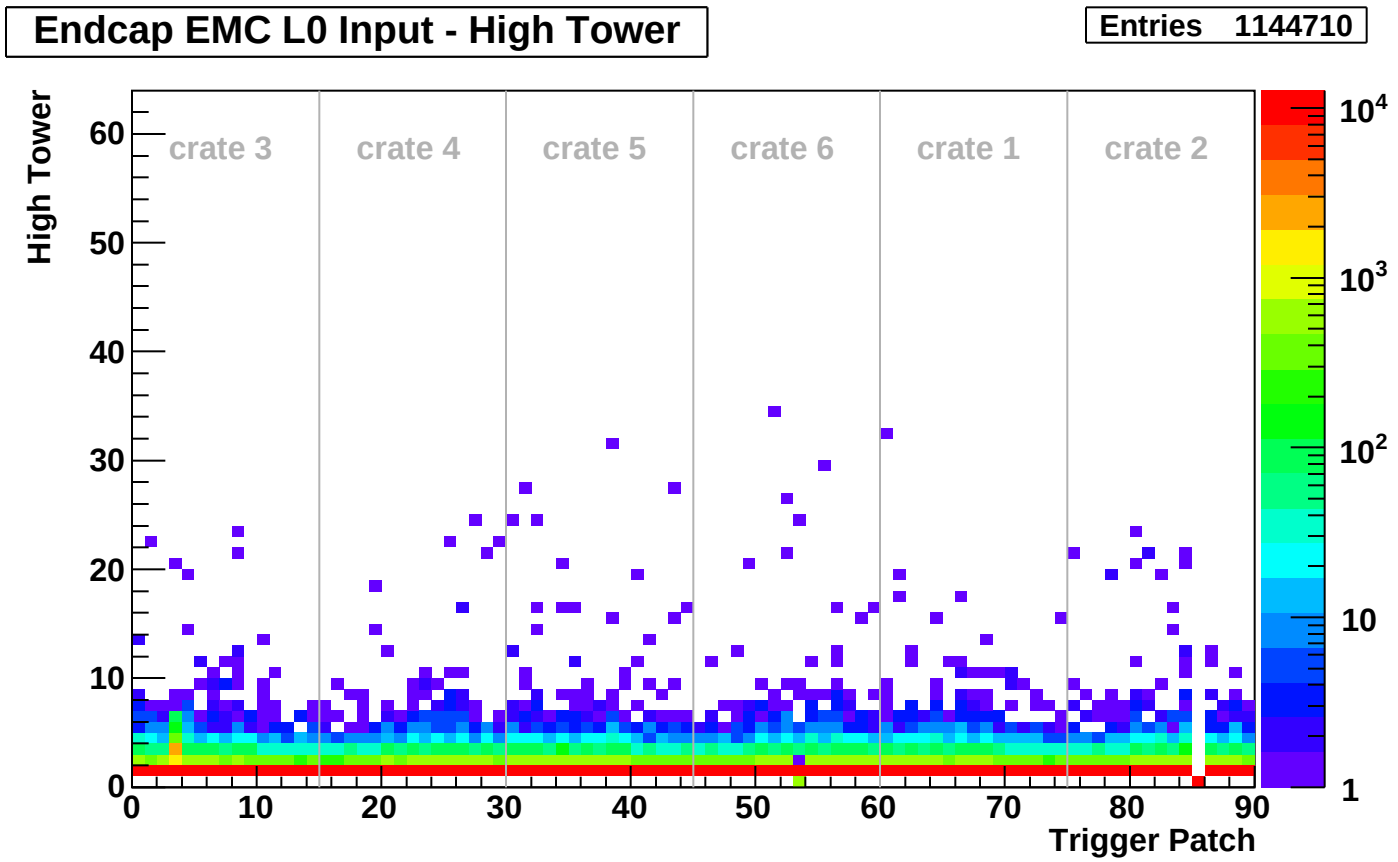

BunchId7Bit (all events)
Entries 12719

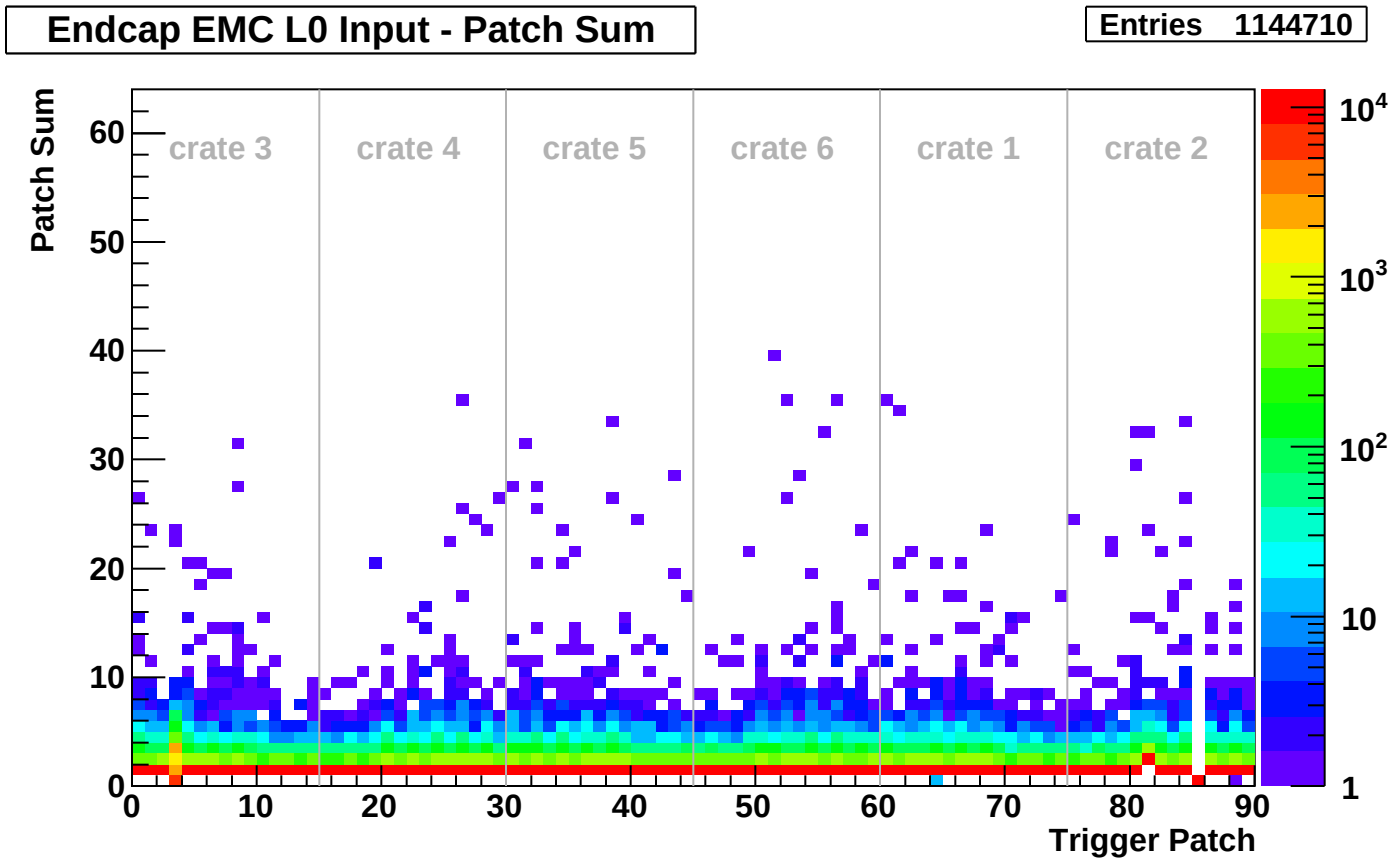

!Blue-Fill
Entries 4

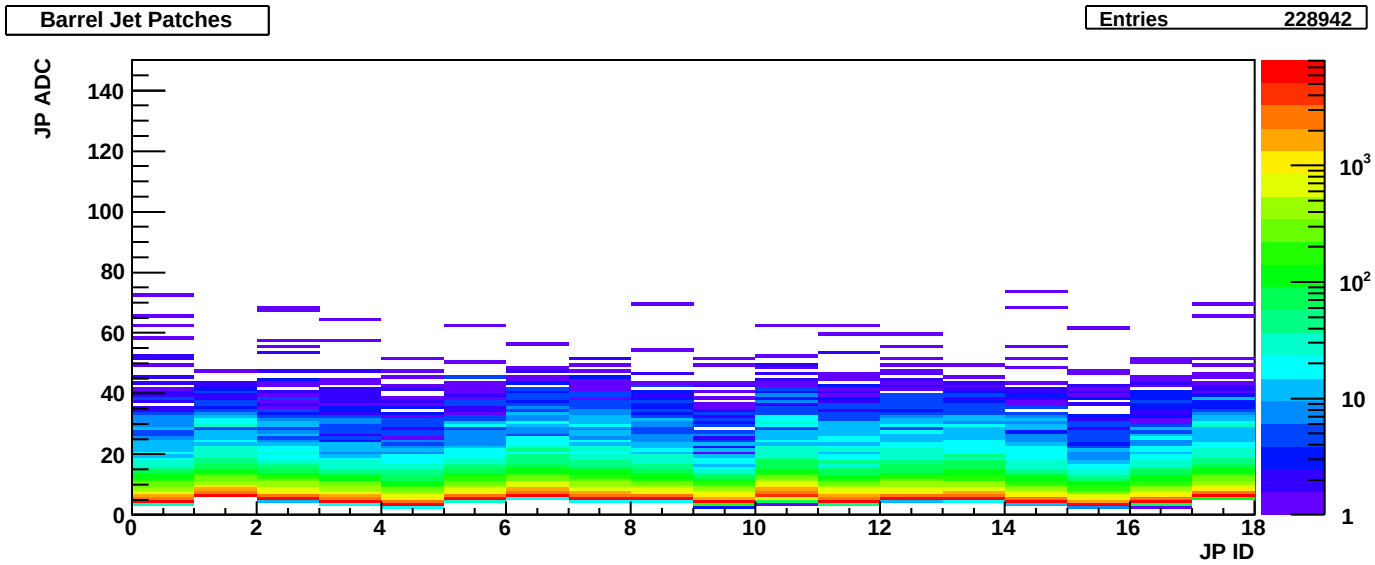

!Yellow-Fill
Entries 522

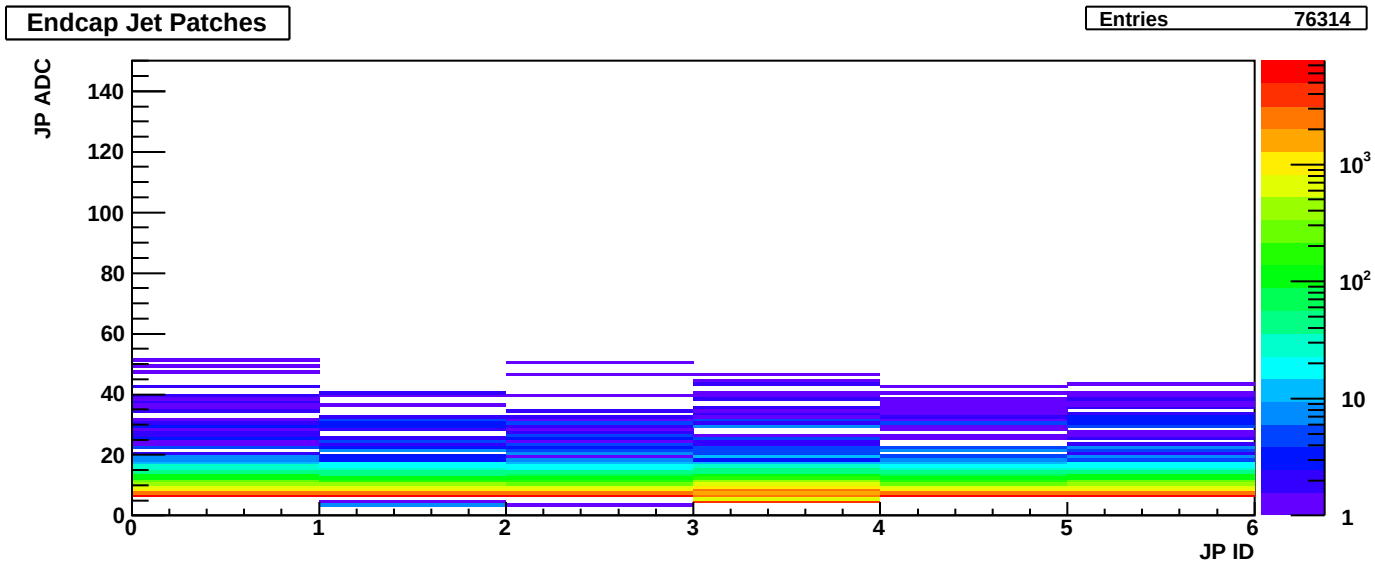

Barrel EMC L0 Input - High Tower
Entries 3815700
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

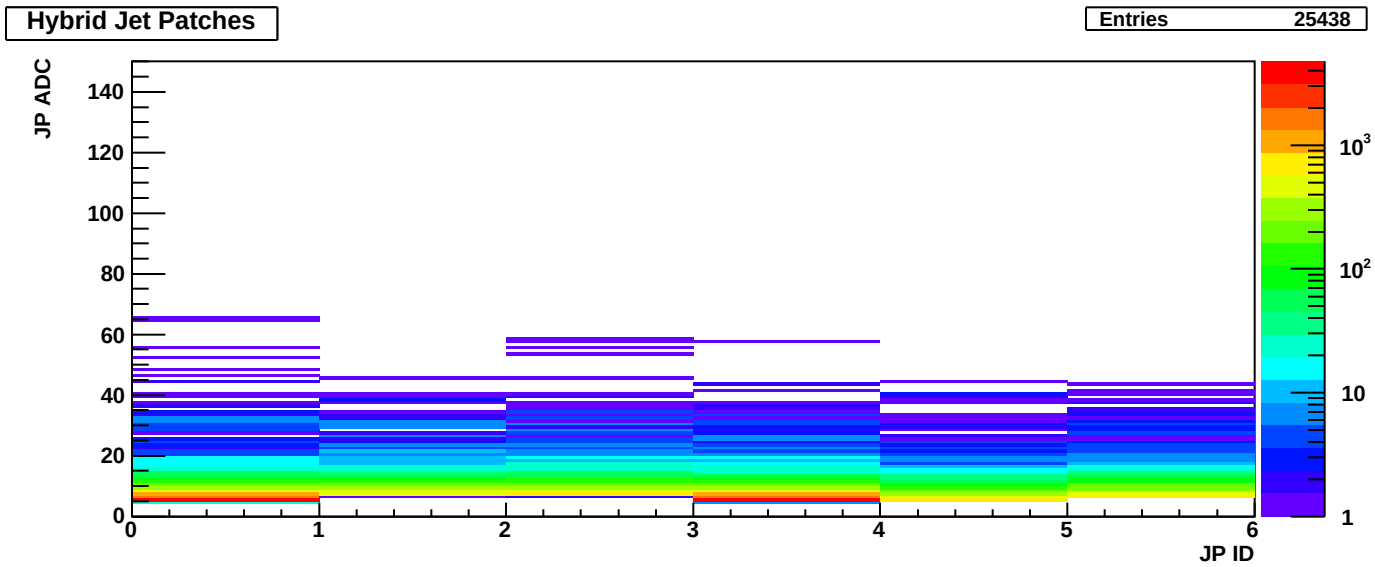

Barrel EMC L0 Input - Patch Sum
Entries 3815700
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

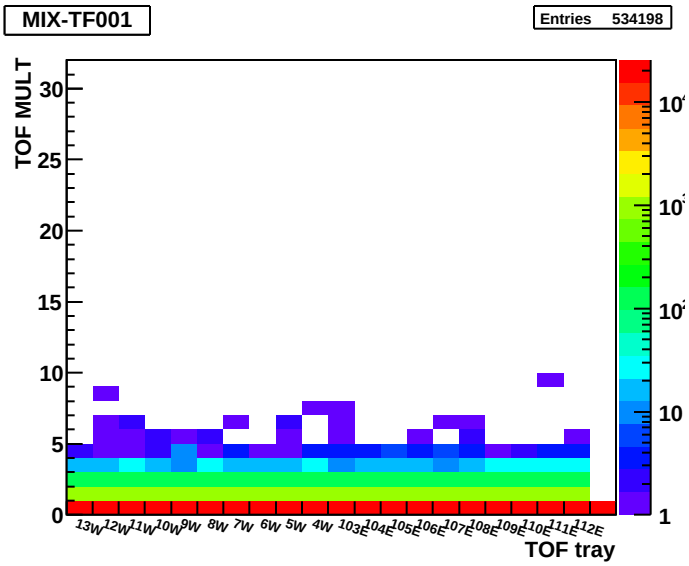

Endcap EMC L0 Input - High Tower
Entries 1144710
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

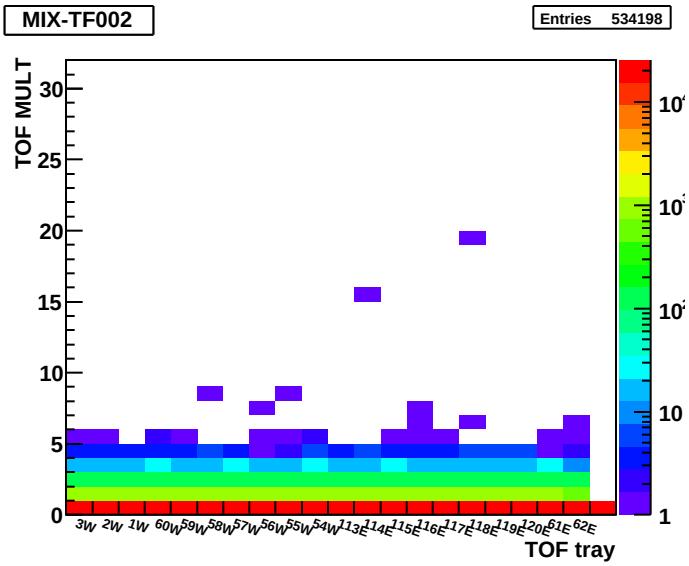

Endcap EMC L0 Input - Patch Sum
Entries 1144710
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

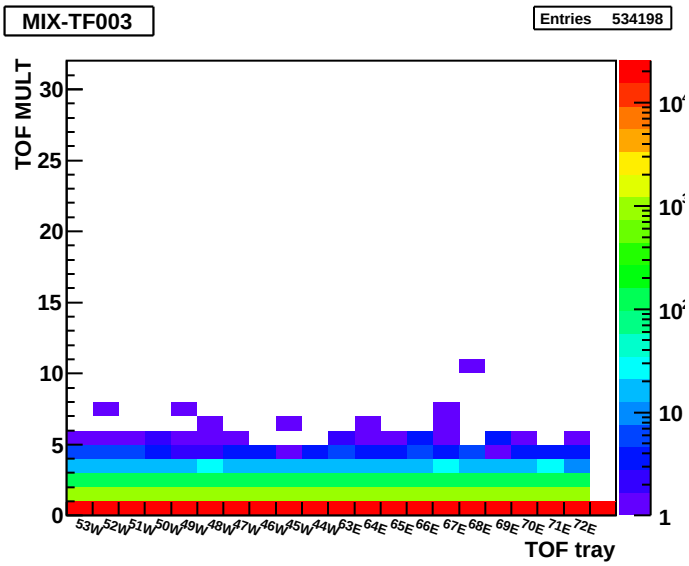

Barrel Jet Patches
Entries 228942
JP ADC
JP ID

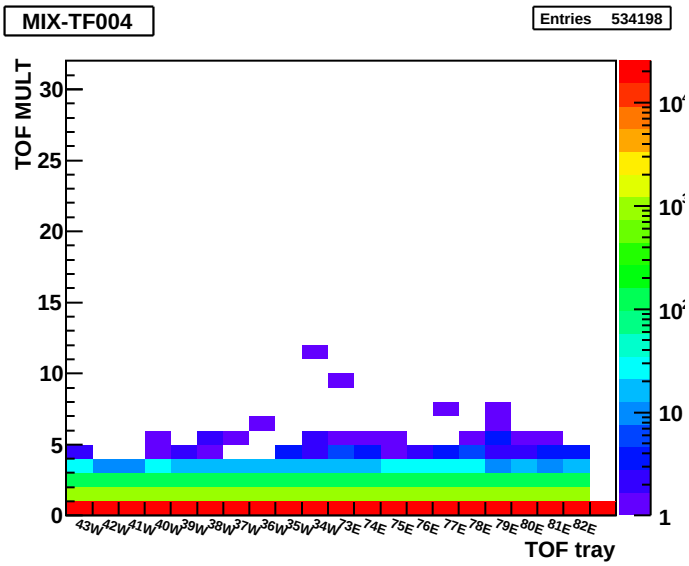

Endcap Jet Patches
Entries 76314
JP ADC
JP ID

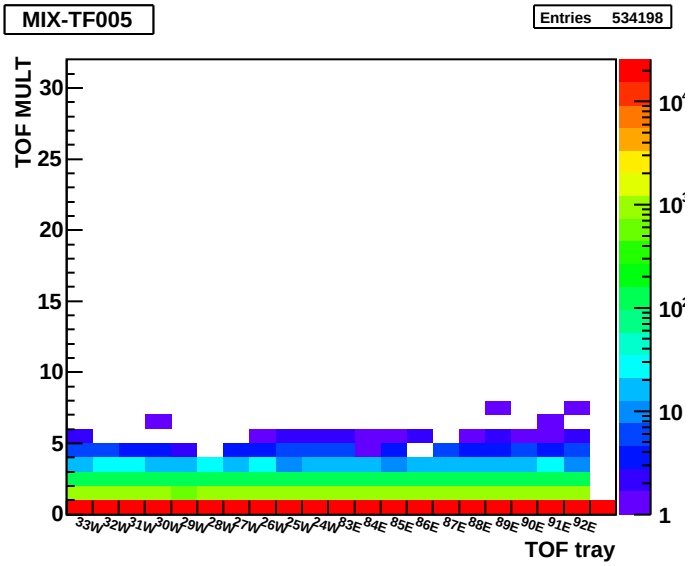

Hybrid Jet Patches
Entries 25438
JP ADC
JP ID

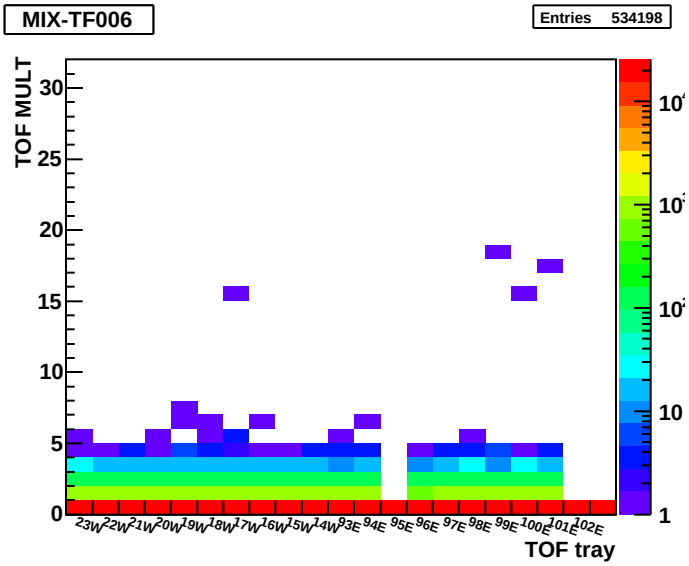

MIX-TF001
Entries 534198
TOF MULT
TOF tray

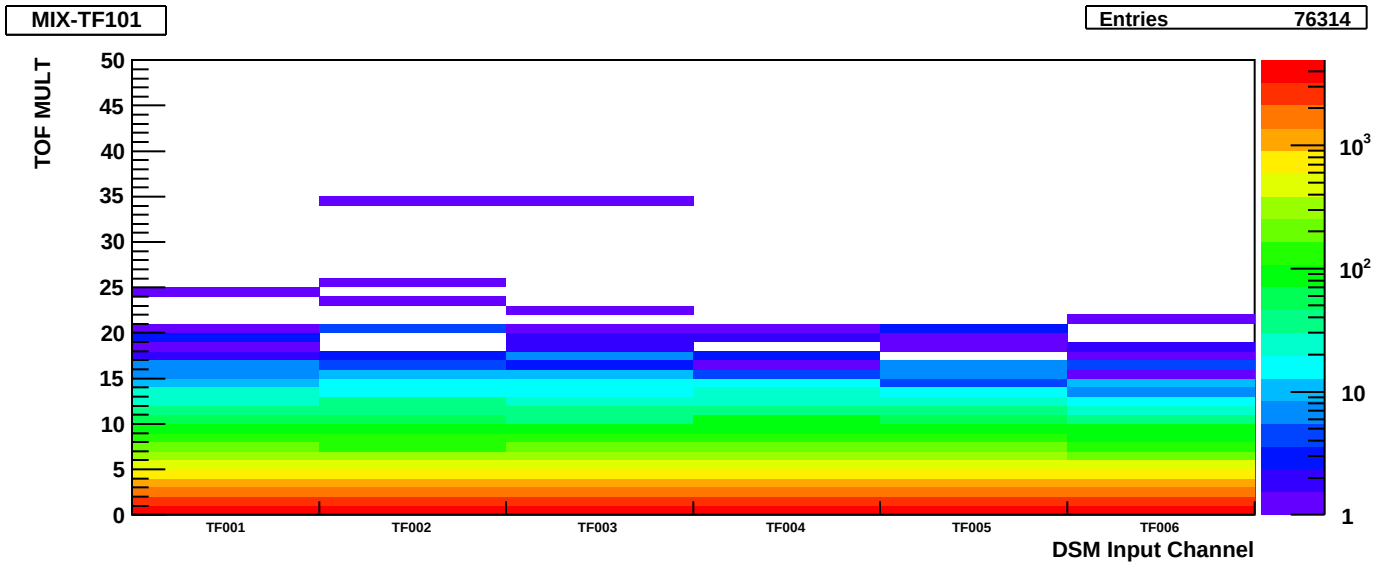

MIX-TF002
Entries 534198
TOF MULT
TOF tray

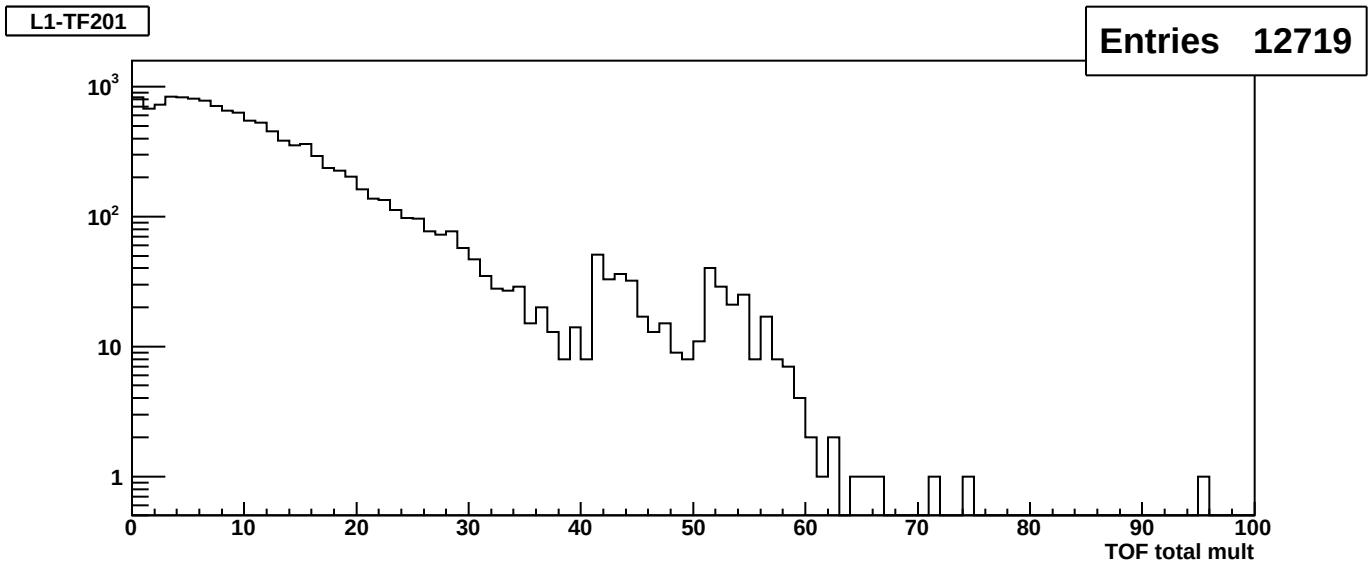

MIX-TF003
Entries 534198
TOF MULT
TOF tray

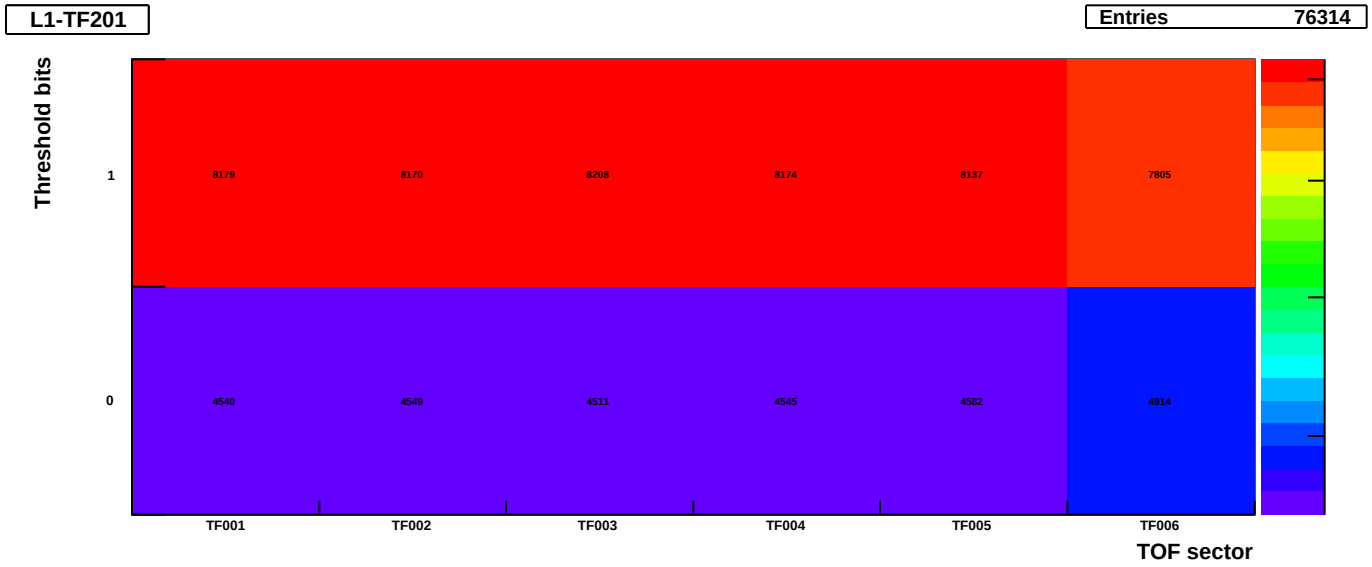

MIX-TF004
Entries 534198
TOF MULT
TOF tray

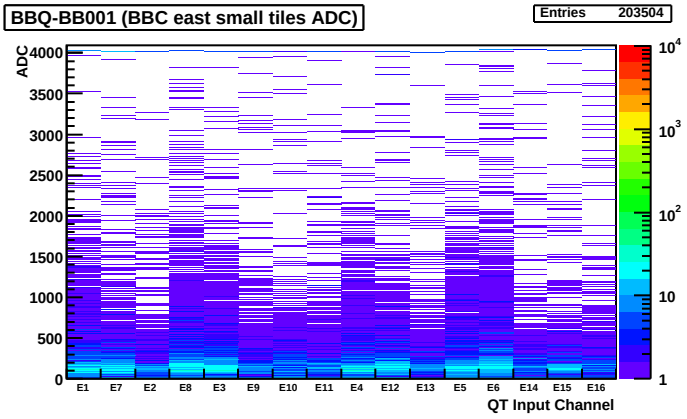

MIX-TF005
Entries 534198
TOF MULT
TOF tray

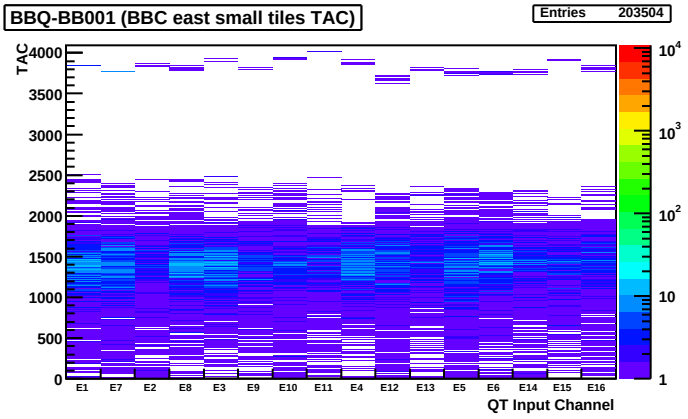

MIX-TF006
Entries 534198
TOF MULT
TOF tray

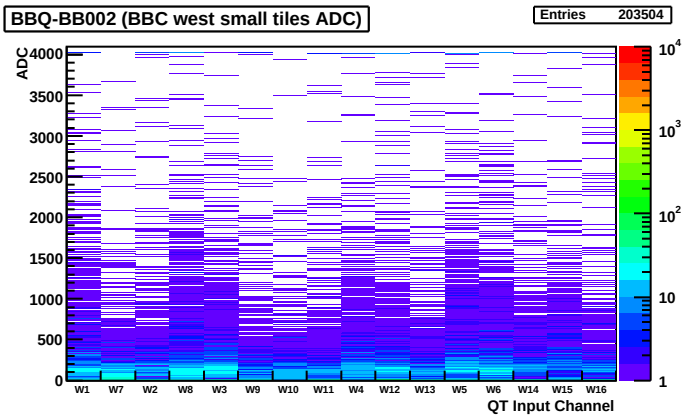

MIX-TF101
Entries 76314
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

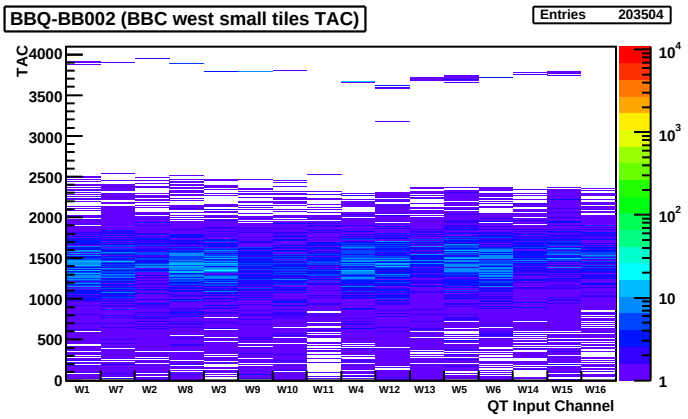

L1-TF201
Entries 12719
TOF total mult

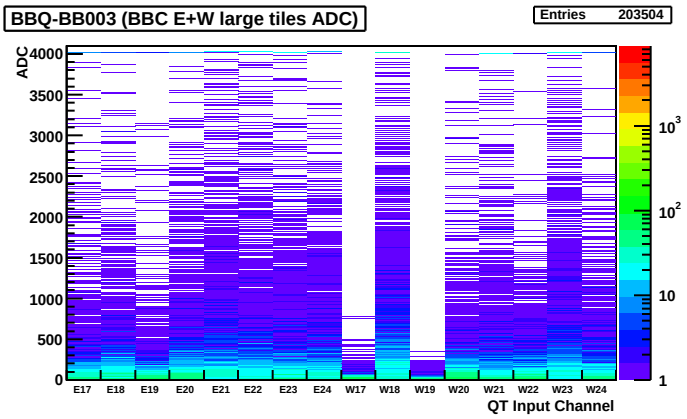

L1-TF201
Entries 76314
Threshold bits
8179
8170
8208
8174
8137
7805
4540
4549
4511
4545
4582
4914
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

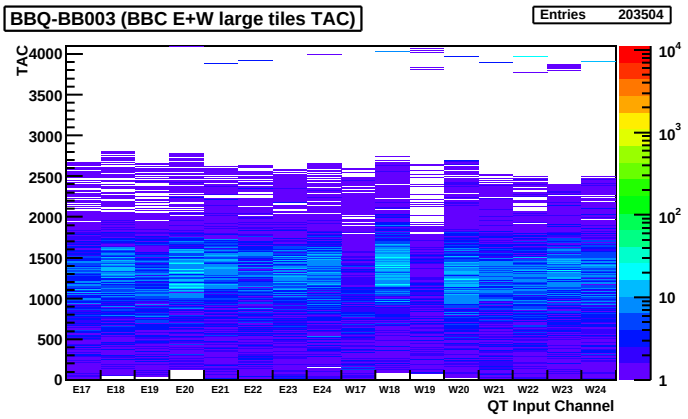

BBQ-BB001 (BBC east small tiles ADC)
Entries 203504
ADC
QT Input Channel

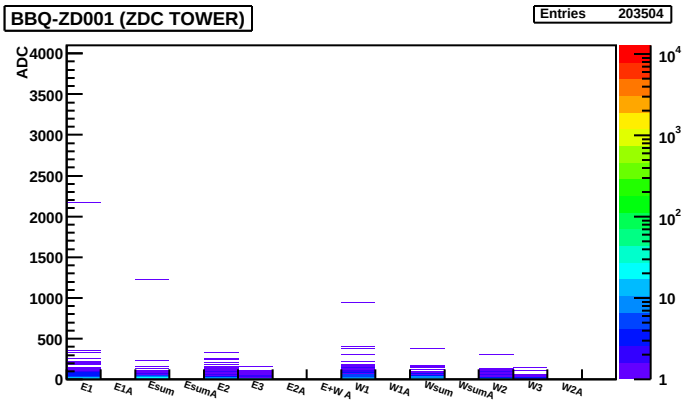

BBQ-BB001 (BBC east small tiles TAC)
Entries 203504
TAC
QT Input Channel

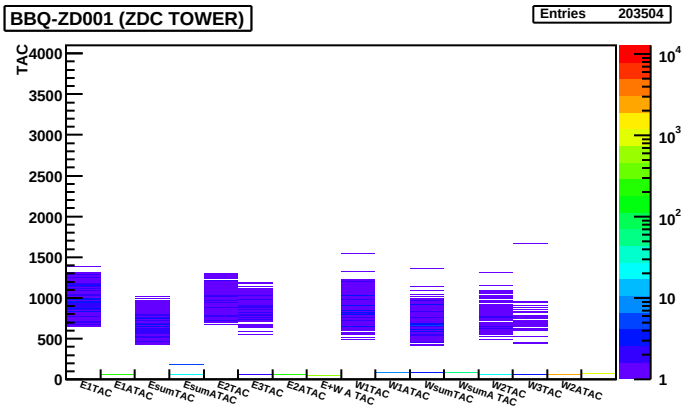

BBQ-BB002 (BBC west small tiles ADC)
Entries 203504
ADC
QT Input Channel

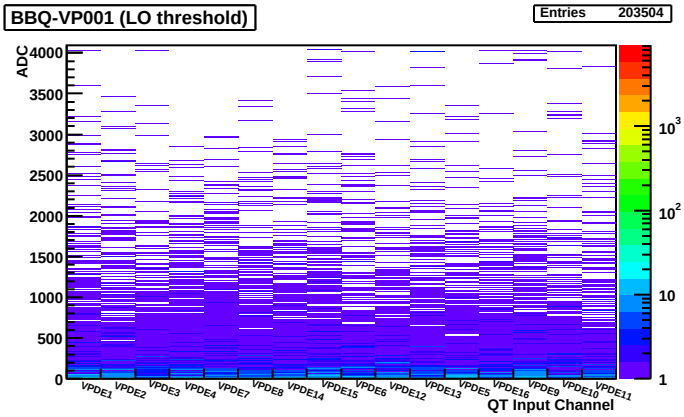

BBQ-BB002 (BBC west small tiles TAC)
Entries 203504
TAC
QT Input Channel

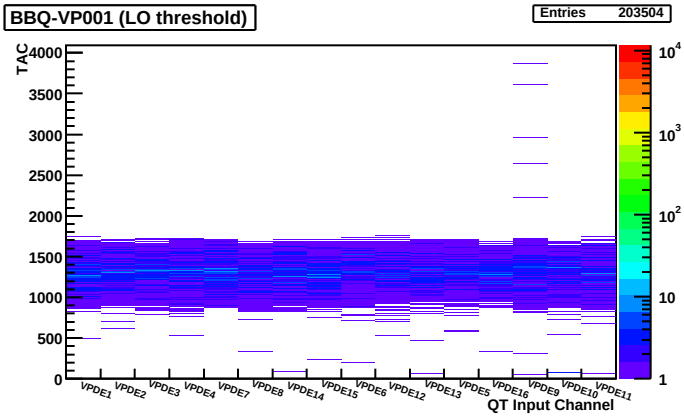

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 203504
ADC
QT Input Channel

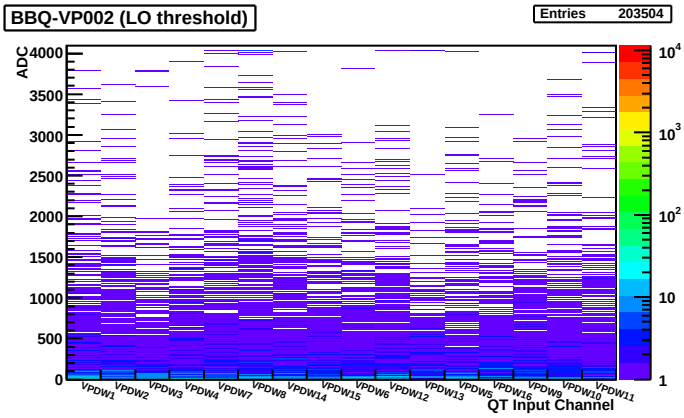

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 203504
TAC
QT Input Channel

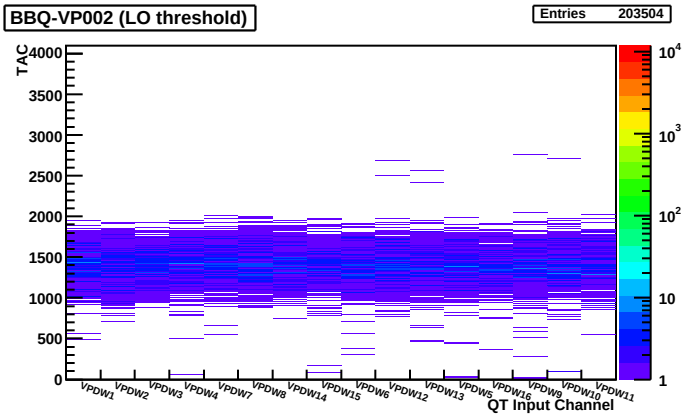

BBQ-ZD001 (ZDC TOWER)
Entries 203504
ADC

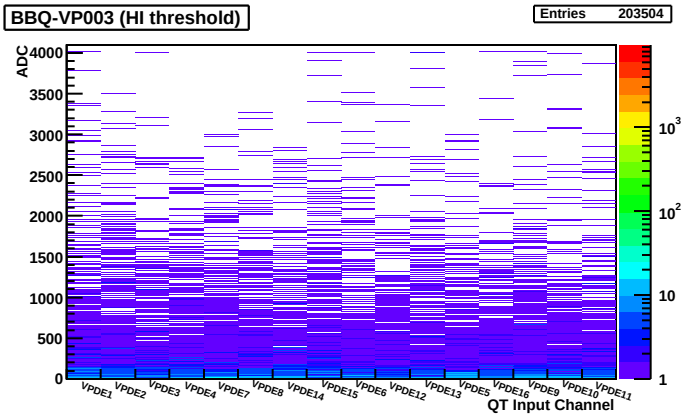

BBQ-ZD001 (ZDC TOWER)
Entries 203504
TAC

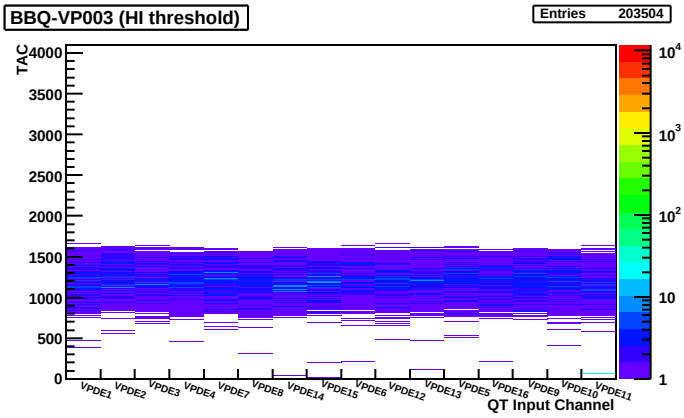

BBQ-VP001 (LO threshold)
Entries 203504
ADC
QT Input Channel

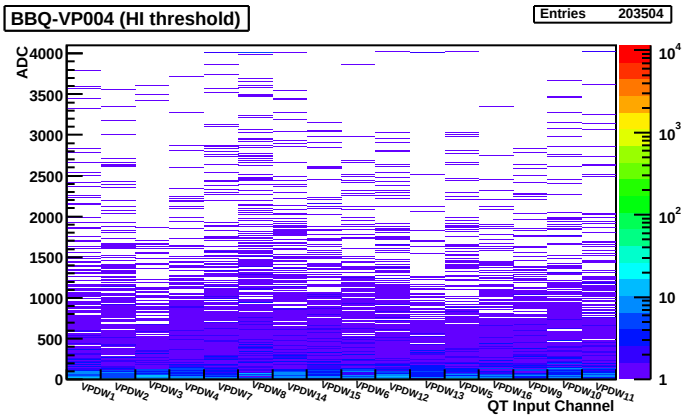

BBQ-VP001 (LO threshold)
Entries 203504
TAC
QT Input Channel

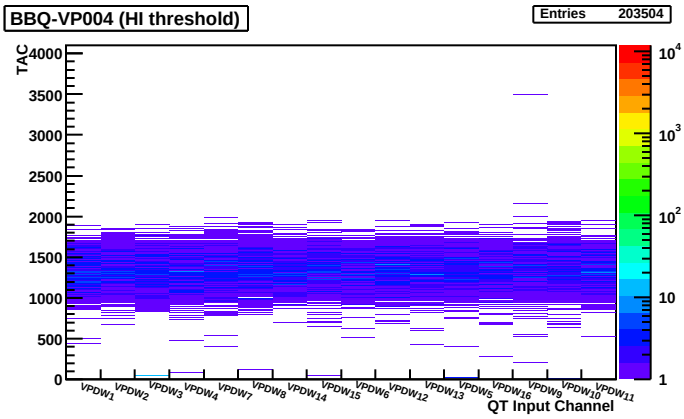

BBQ-VP002 (LO threshold)
Entries 203504
ADC
QT Input Channel

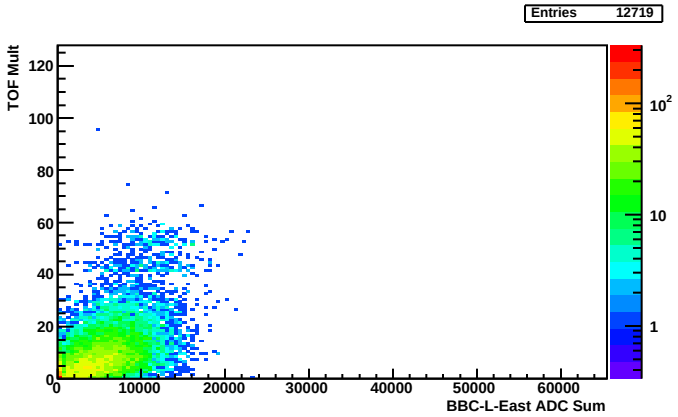

BBQ-VP002 (LO threshold)
Entries 203504
TAC
QT Input Channel

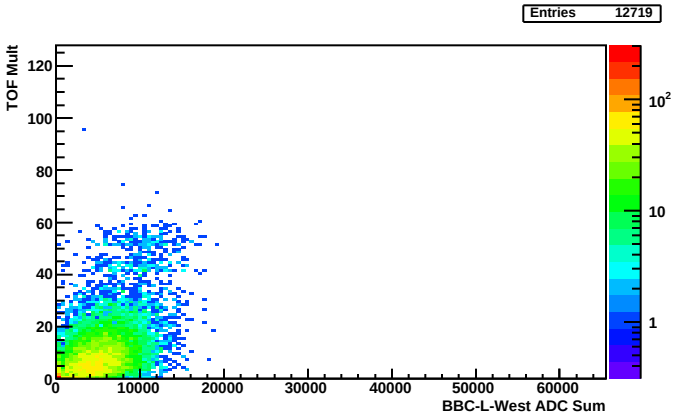

BBQ-VP003 (HI threshold)
Entries 203504
ADC
QT Input Channel

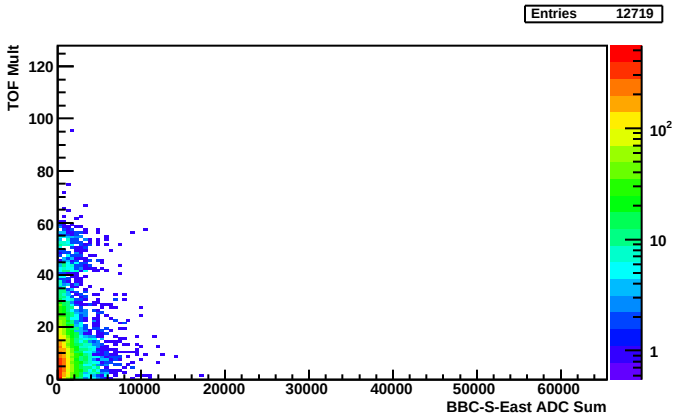

BBQ-VP003 (HI threshold)
Entries 203504
TAC
QT Input Channel

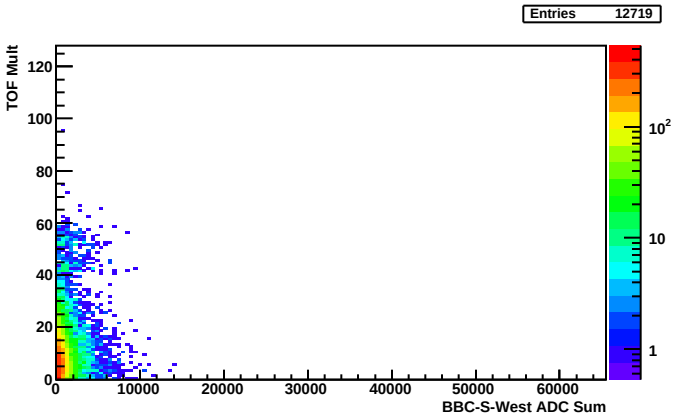

BBQ-VP004 (HI threshold)
Entries 203504
ADC
QT Input Channel

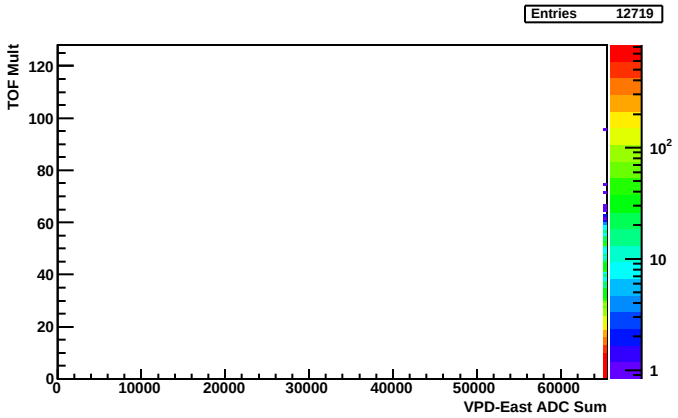

BBQ-VP004 (HI threshold)
Entries 203504
TAC
QT Input Channel

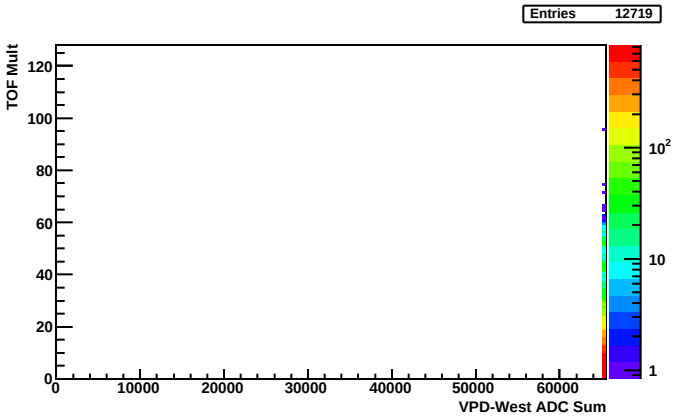

Entries 12719
TOF Mult
BBC-L-East ADC Sum

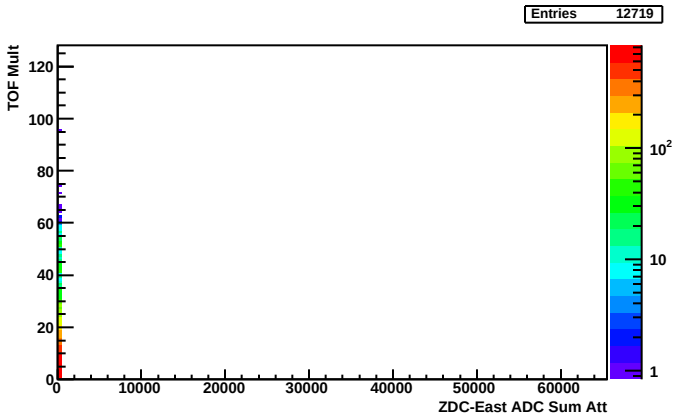

Entries 12719
TOF Mult
BBC-L-West ADC Sum

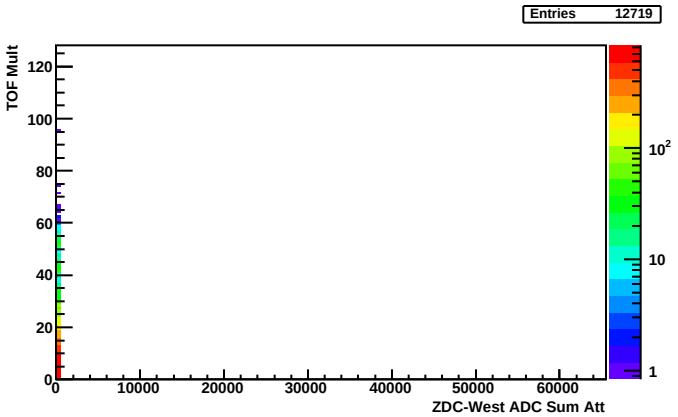

Entries 12719
TOF Mult
BBC-S-East ADC Sum

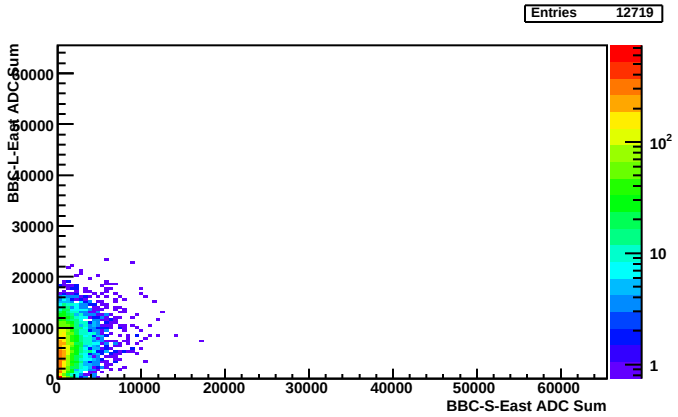

Entries 12719
TOF Mult
BBC-S-West ADC Sum

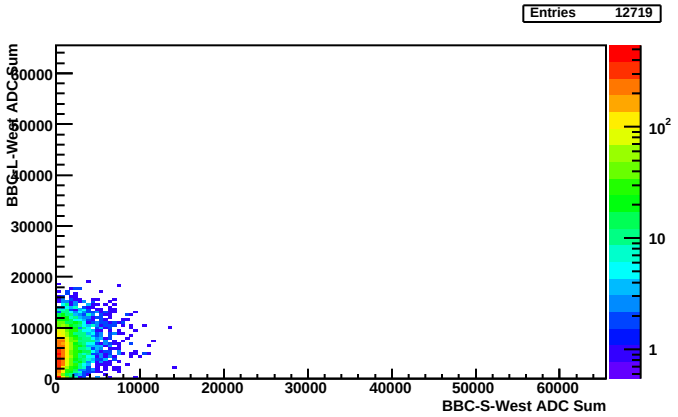

Entries 12719
TOF Mult
VPD-East ADC Sum

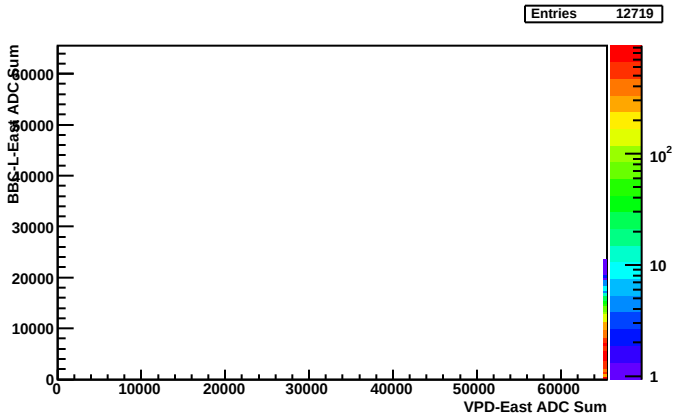

Entries 12719
TOF Mult
VPD-West ADC Sum

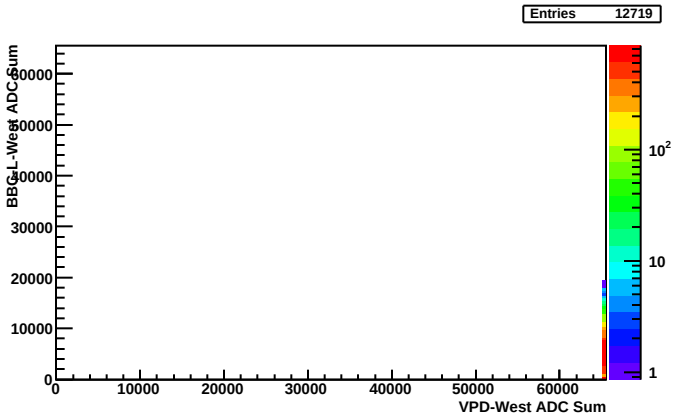

Entries 12719
TOF Mult
ZDC-East ADC Sum Att

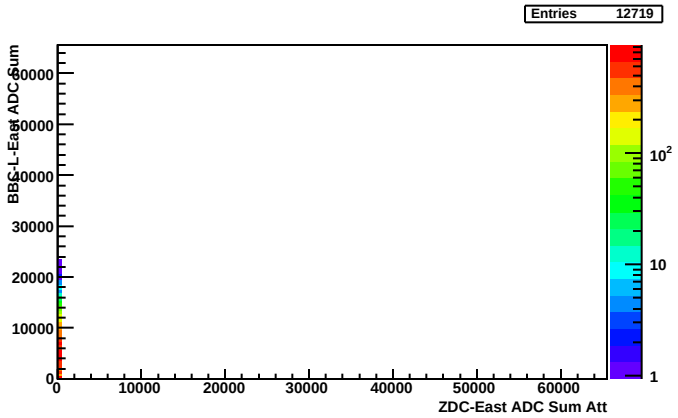

Entries 12719
TOF Mult
ZDC-West ADC Sum Att

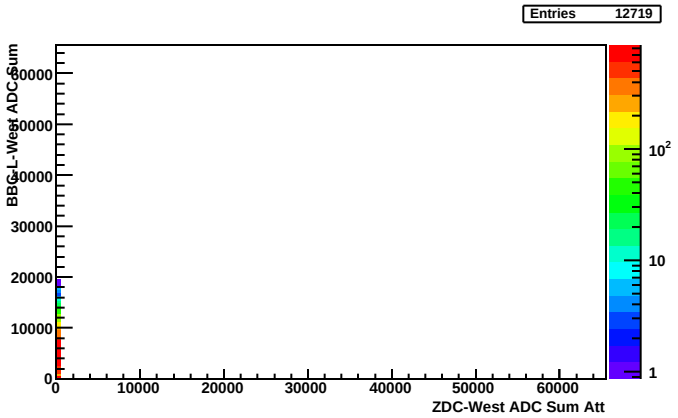

Entries 12719
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 12719
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 12719
BBC-L-East ADC Sum
VPD-East ADC Sum


Entries 12719
BBC-L-West ADC Sum
VPD-West ADC Sum


Entries 12719
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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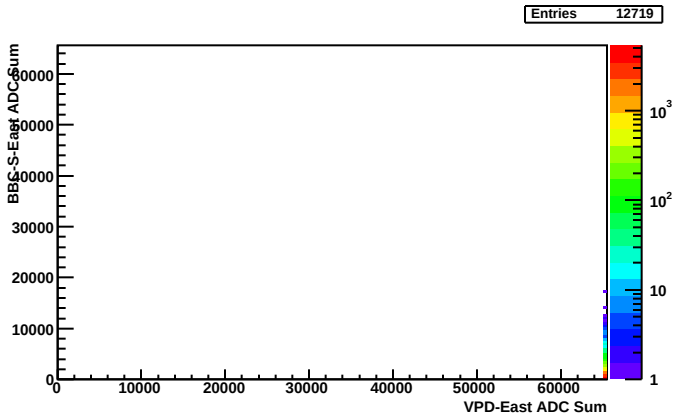
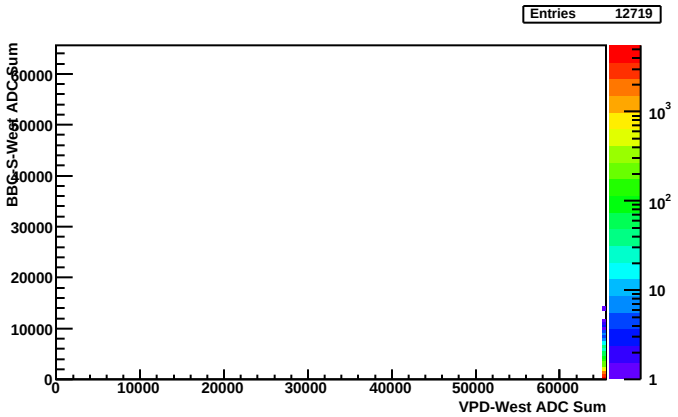
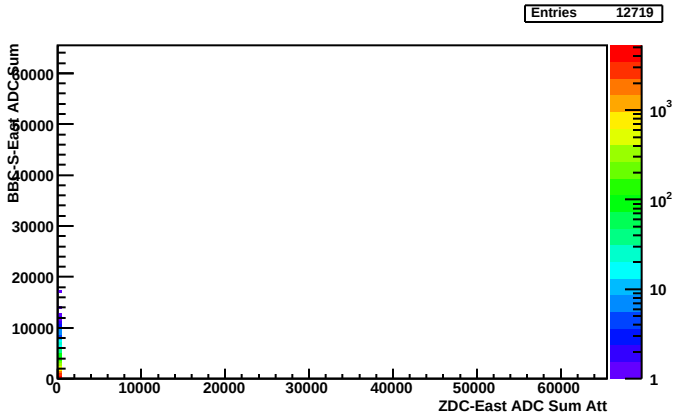
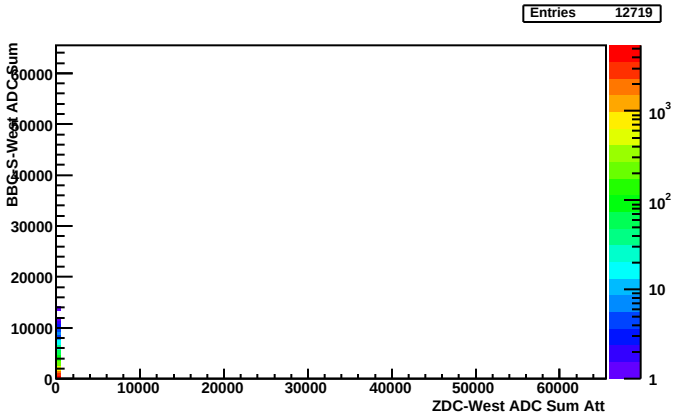
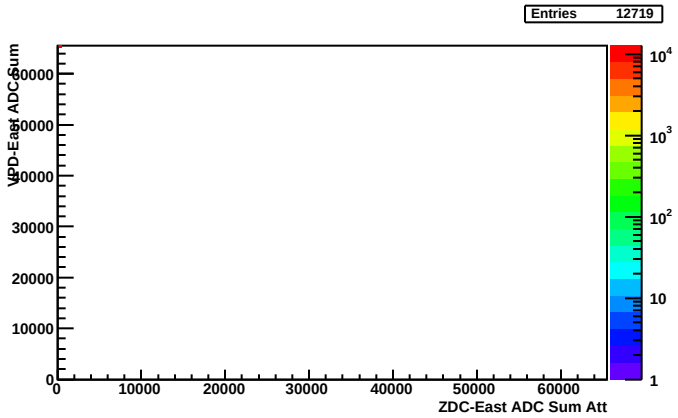
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

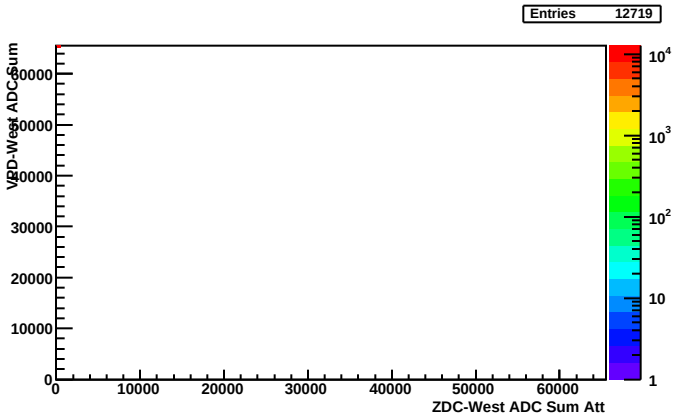

Entries 12719
BBC-S-East ADC Sum
VPD-East ADC Sum

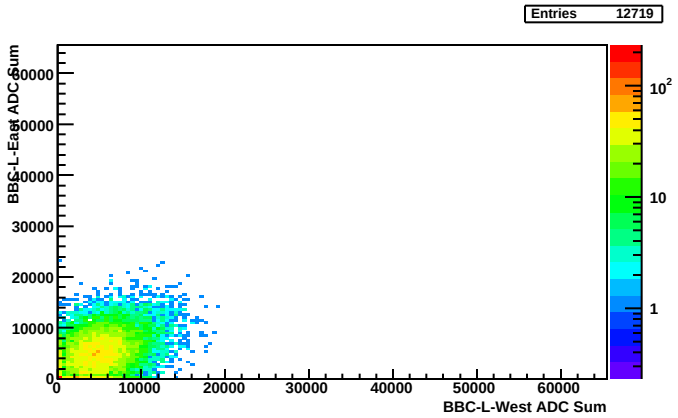

Entries 12719
BBC-S-West ADC Sum
VPD-West ADC Sum

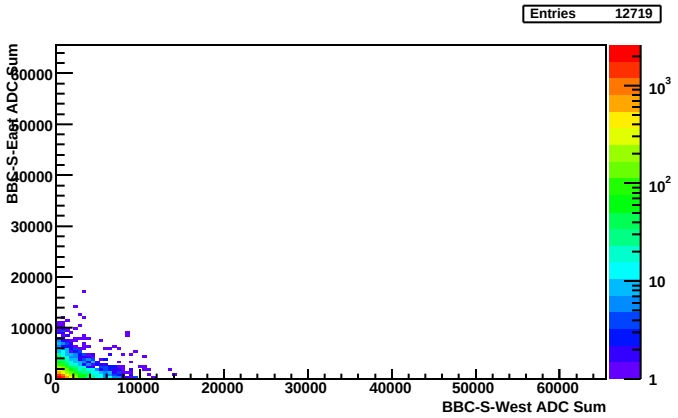

Entries 12719
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

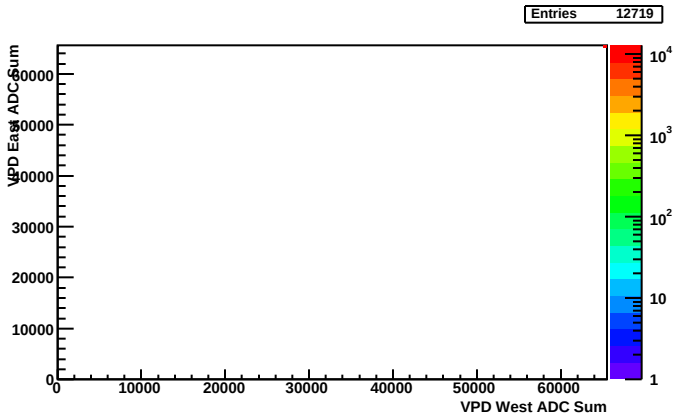

Entries 12719
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

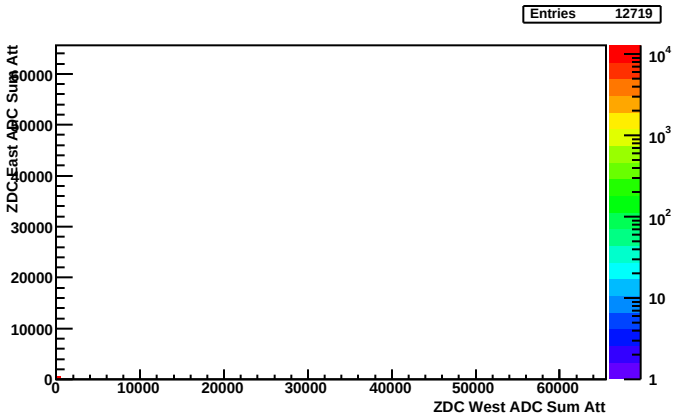

Entries 12719
VPD-East ADC Sum
ZDC-East ADC Sum Att

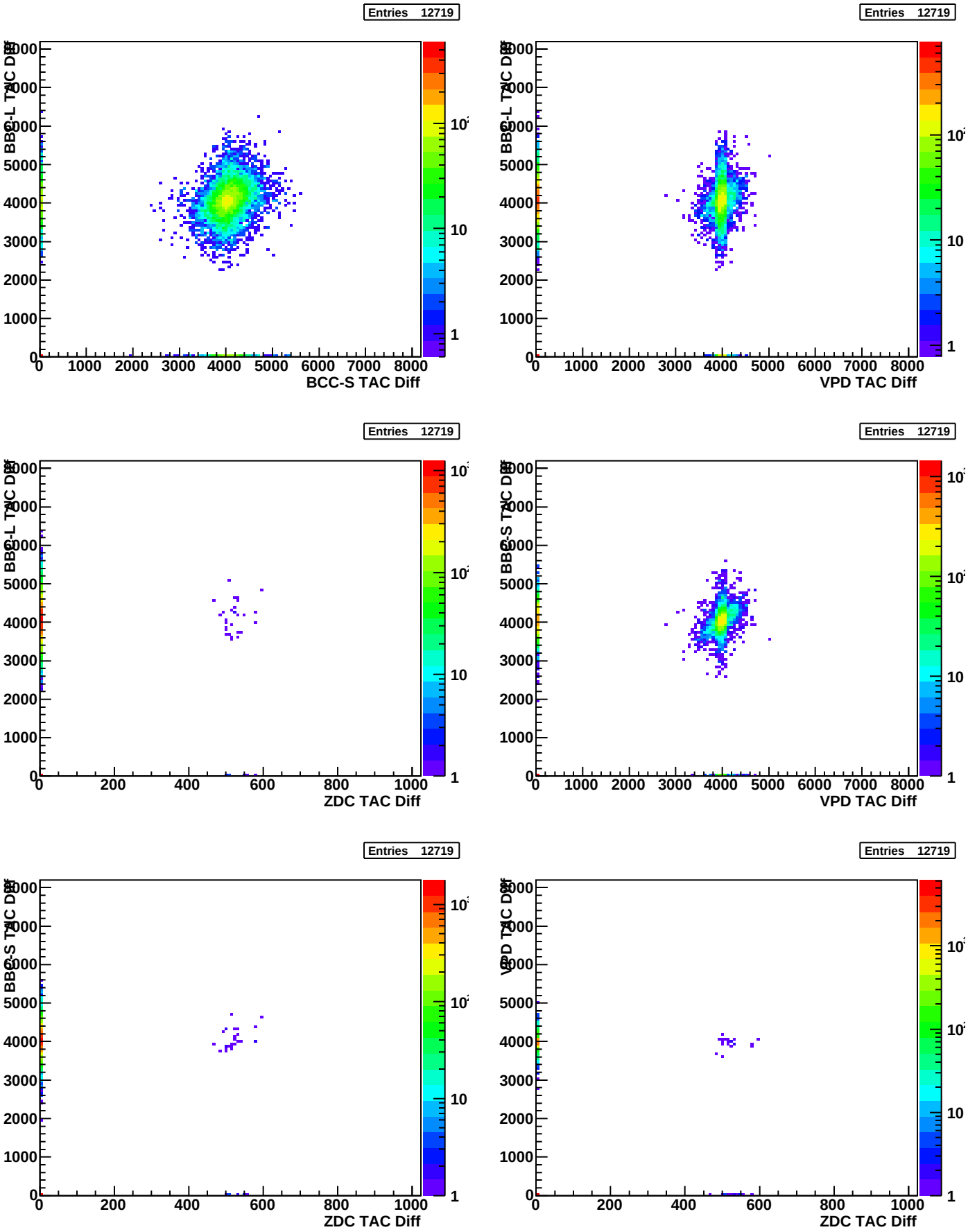

Entries 12719
VPD-West ADC Sum
ZDC-West ADC Sum Att

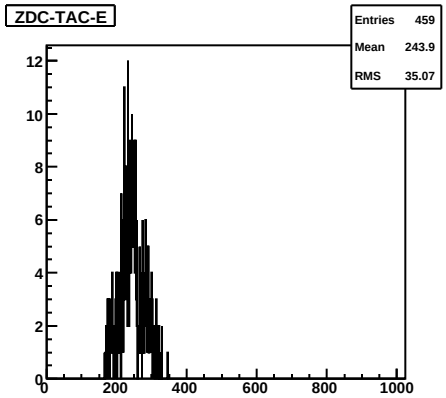

Entries 12719
BBC-L-East ADC Sum
BBC-L-West ADC Sum

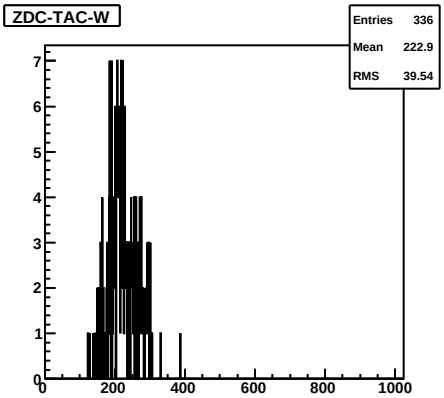

Entries 12719
BBC-S-East ADC Sum
BBC-S-West ADC Sum

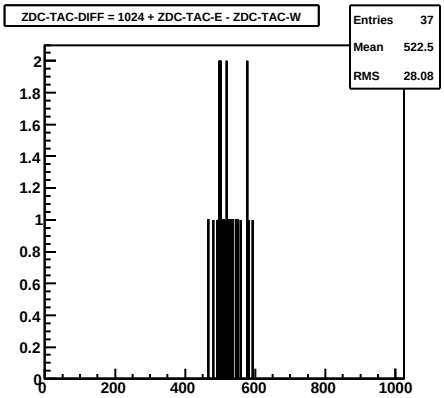

Entries 12719
VPD East ADC Sum
VPD West ADC Sum

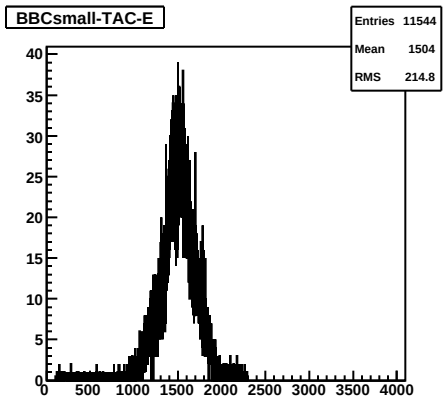

Entries 12719
ZDC East ADC Sum Att
ZDC West ADC Sum Att

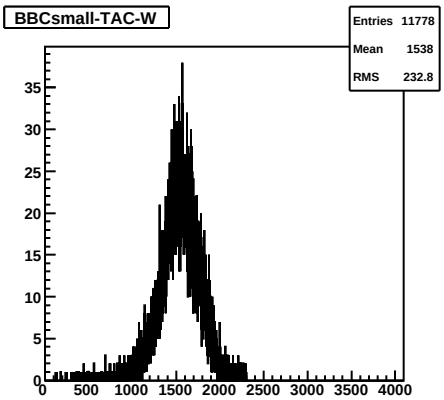

Entries 12719
BBC-L TAC Diff
BCC-S TAC Diff
Entries 12719
BBC-L TAC Diff
VPD TAC Diff
Entries 12719
BBC-L TAC Diff
ZDC TAC Diff
Entries 12719
BBC-S TAC Diff
VPD TAC Diff
Entries 12719
BBC-S TAC Diff
ZDC TAC Diff
Entries 12719
VPD TAC Diff
ZDC TAC Diff

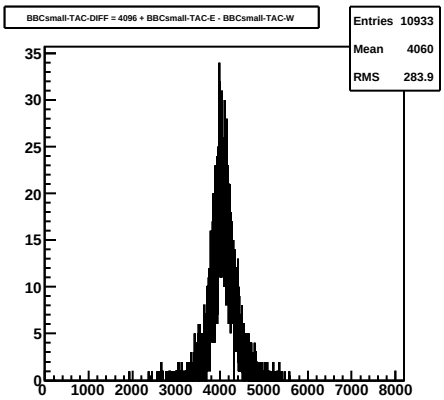

ZDC-TAC-E
Entries 459
Mean 243.9
RMS 35.07

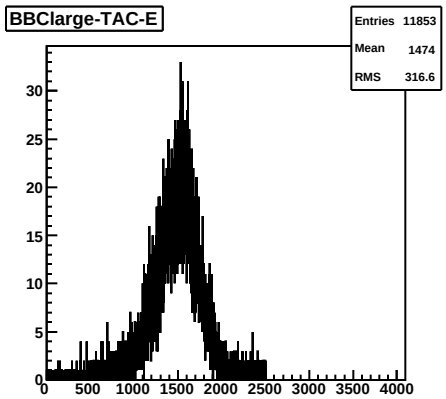

ZDC-TAC-W
Entries 336
Mean 222.9
RMS 39.54

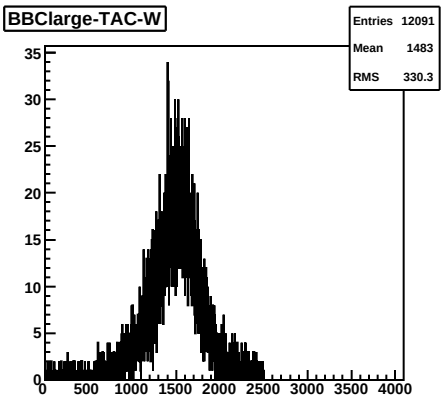

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 37
Mean 522.5
RMS 28.08

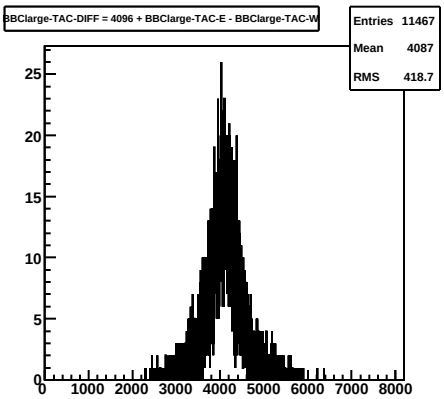

BBCsmall-TAC-E
Entries 11544
Mean 1504
RMS 214.8

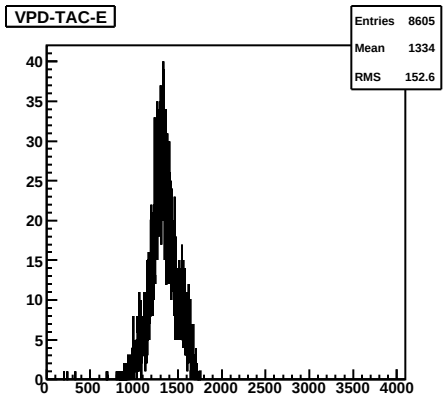

BBCsmall-TAC-W
Entries 11778
Mean 1538
RMS 232.8

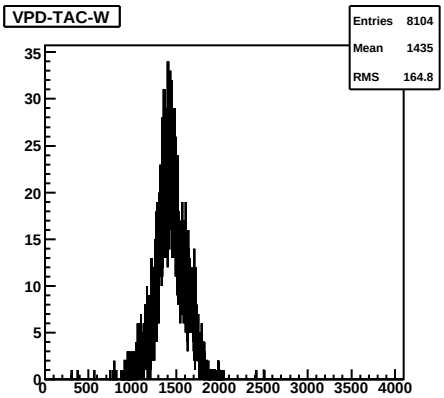

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 10933
Mean 4060
RMS 283.9

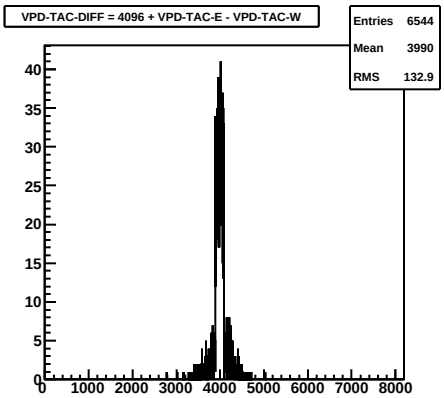

BBClarge-TAC-E
Entries 11853
Mean 1474
RMS 316.6

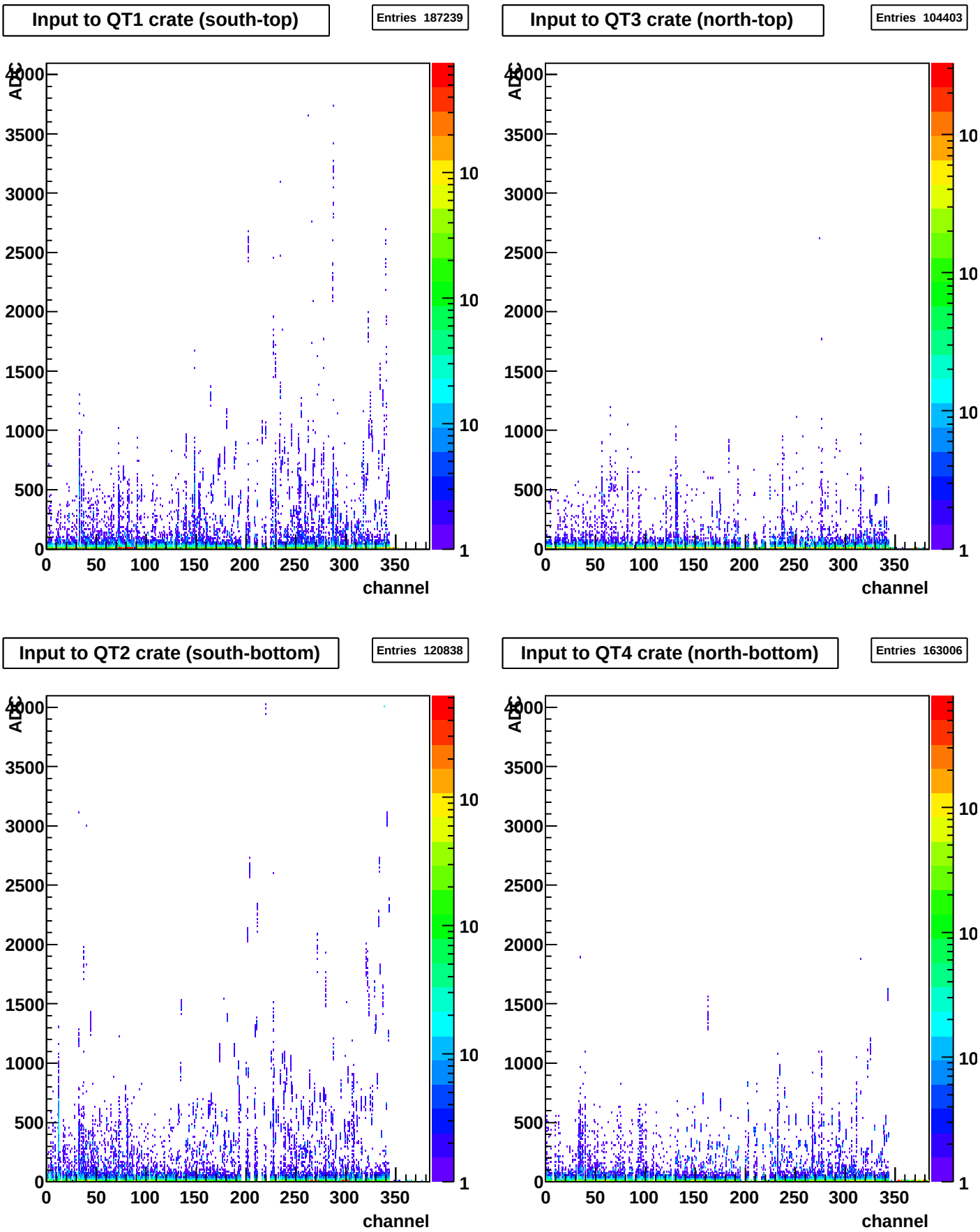

BBClarge-TAC-W
Entries 12091
Mean 1483
RMS 330.3

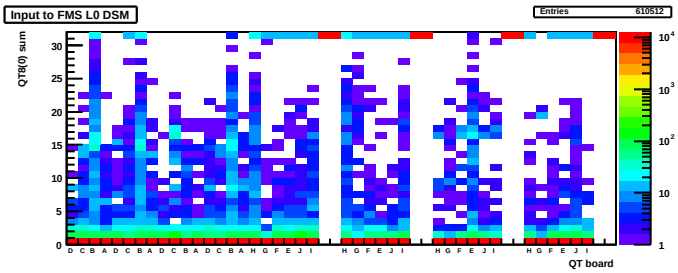

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 11467
Mean 4087
RMS 418.7

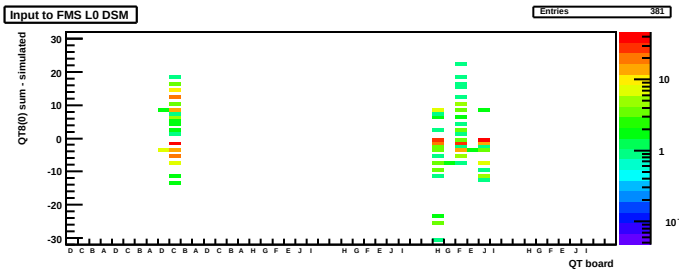

VPD-TAC-E
Entries 8605
Mean 1334
RMS 152.6

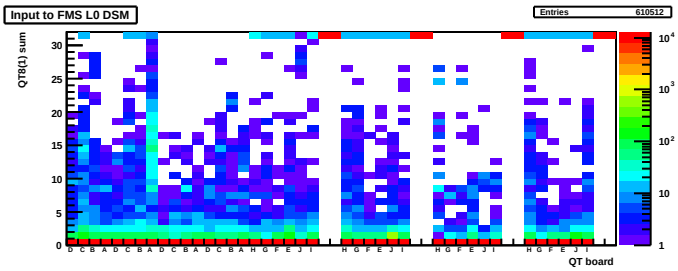

VPD-TAC-W
Entries 8104
Mean 1435
RMS 164.8

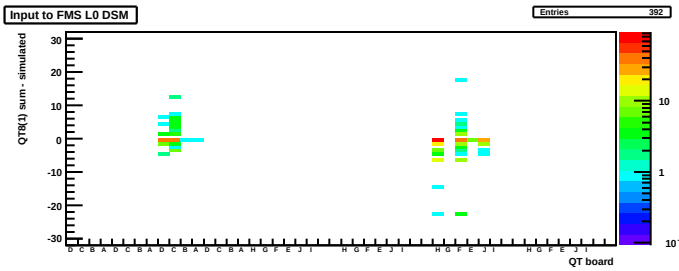

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 6544
Mean 3990
RMS 132.9

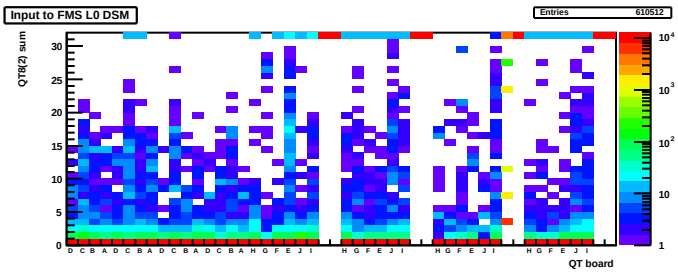

Input to QT1 crate (south-top)
Entries 187239
Input to QT3 crate (north-top)
Entries 104403
Input to QT2 crate (south-bottom)
Entries 120838
Input to QT4 crate (north-bottom)
Entries 163006
ADC
channel
0
500
1000
1500
2000
2500
3000
3500
4000
0
50
100
150
200
250
300
350
1
10

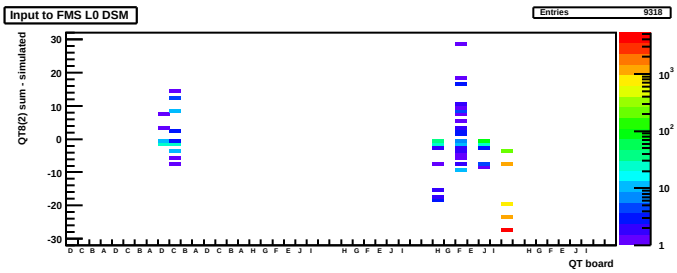

Input to FMS L0 DSM
Entries 610512
QT8(0) sum
QT board

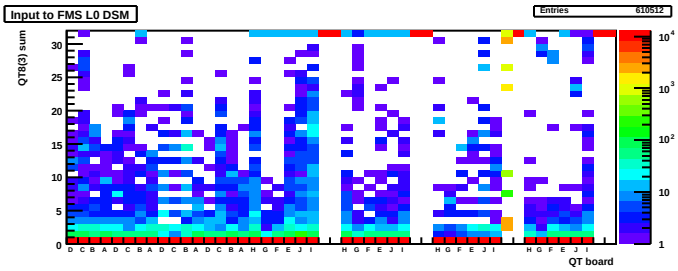

Input to FMS L0 DSM
Entries 381
QT8(0) sum - simulated
QT board

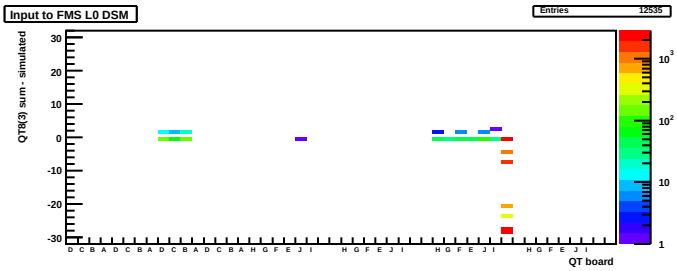

Input to FMS L0 DSM
Entries 610512
QT8(1) sum
QT board

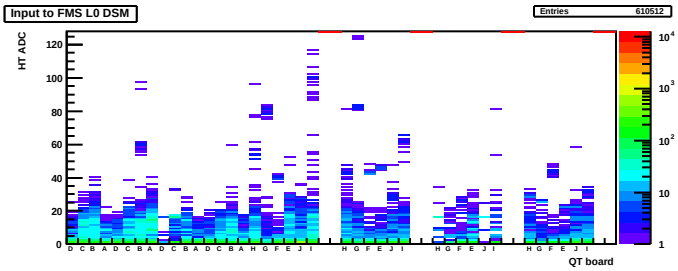

Input to FMS L0 DSM
Entries 392
QT8(1) sum - simulated
QT board

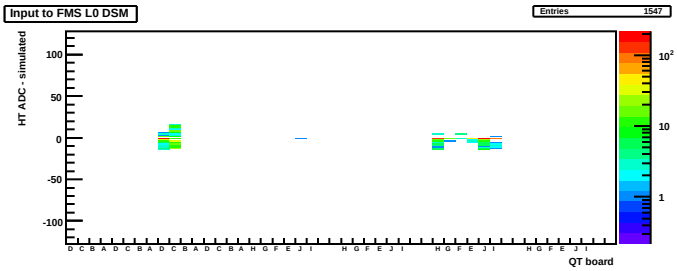

Input to FMS L0 DSM
Entries 610512
QT8(2) sum
QT board

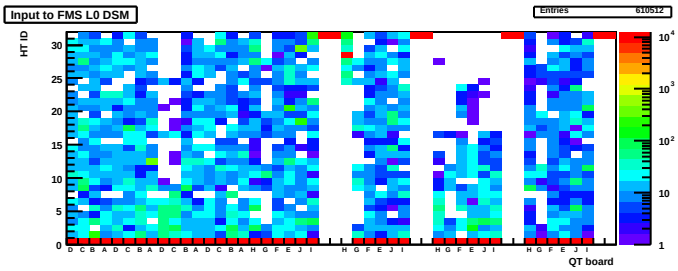

Input to FMS L0 DSM
Entries 9318
QT8(2) sum - simulated
QT board

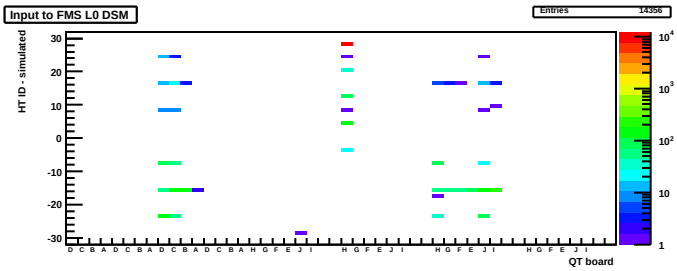

Input to FMS L0 DSM
Entries 610512
QT8(3) sum
QT board

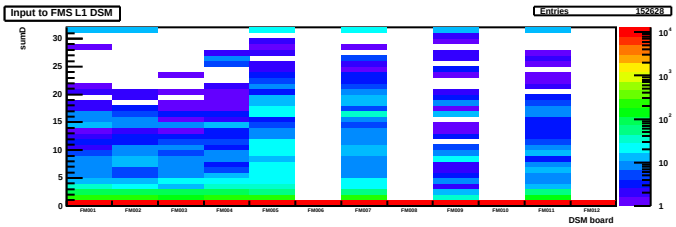

Input to FMS L0 DSM
Entries 12535
QT8(3) sum - simulated
QT board

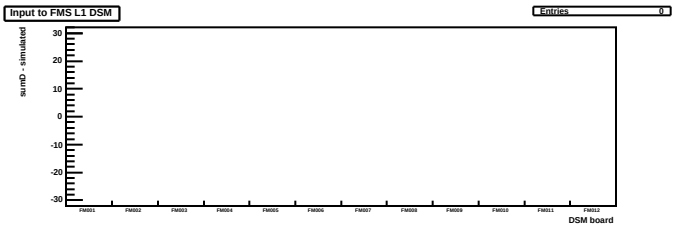

Input to FMS L0 DSM
Entries 610512
HT ADC
QT board

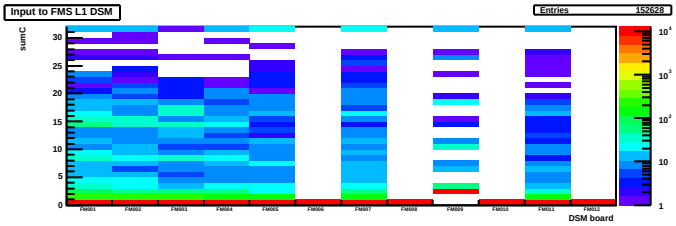

Input to FMS L0 DSM
Entries 1547
HT ADC - simulated
QT board

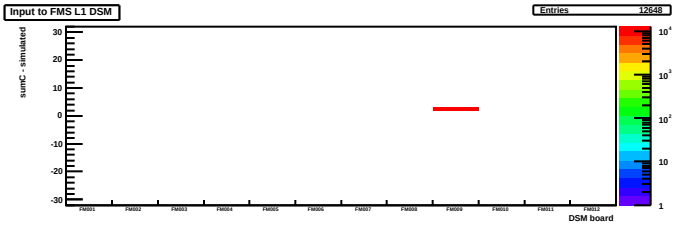

Input to FMS L0 DSM
Entries 610512
HT ID
QT board

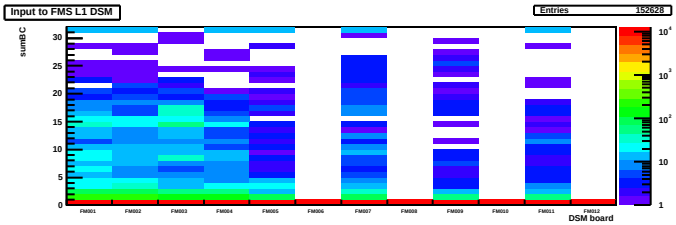

Input to FMS L0 DSM
Entries 14356
HT ID - simulated
QT board

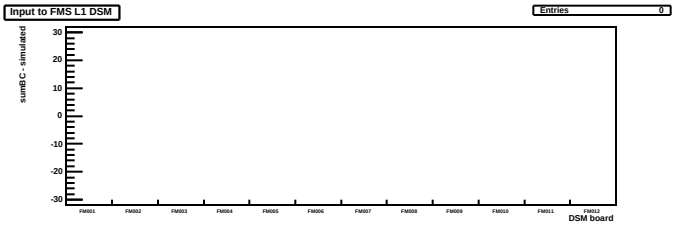

Input to FMS L1 DSM
Entries 152628
sumD
DSM board

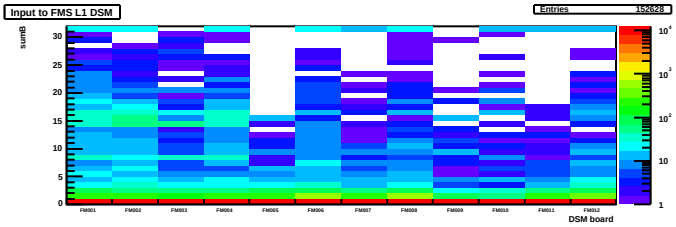

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

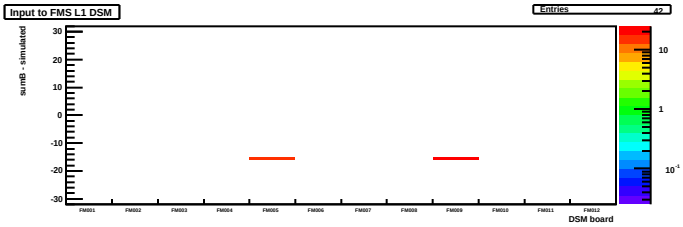

Input to FMS L1 DSM
Entries 152628
sumC
DSM board

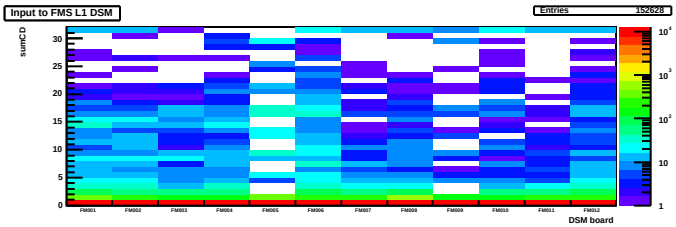

Input to FMS L1 DSM
Entries 12648
sumC - simulated
DSM board

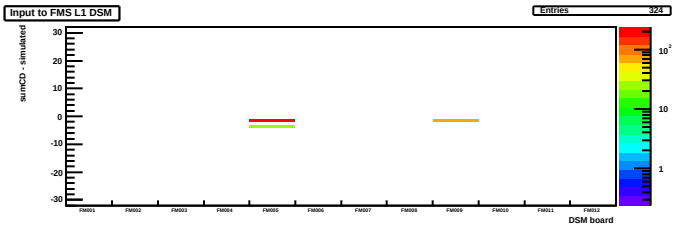

Input to FMS L1 DSM
Entries 152628
sumBC
DSM board

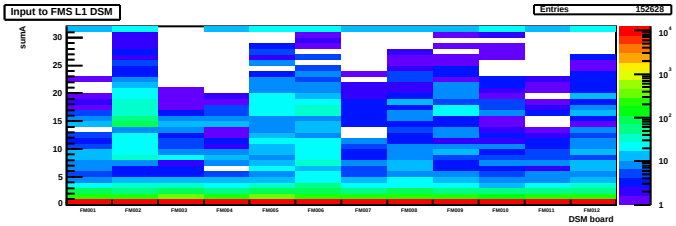

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

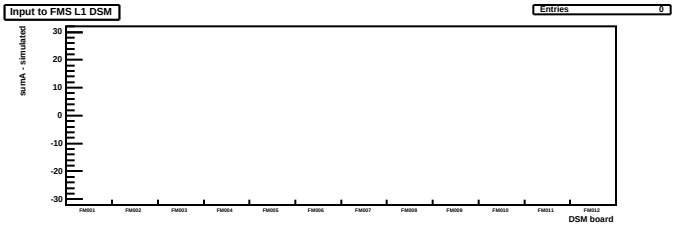

Input to FMS L1 DSM
Entries 152628
sumB
DSM board

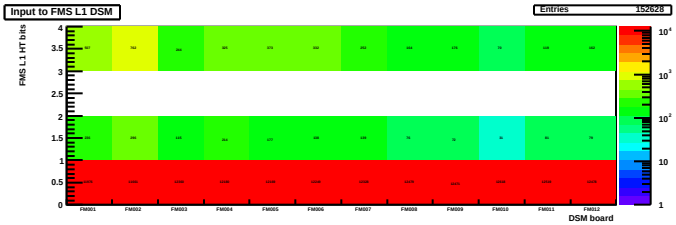

Input to FMS L1 DSM
Entries 42
sumB - simulated
DSM board

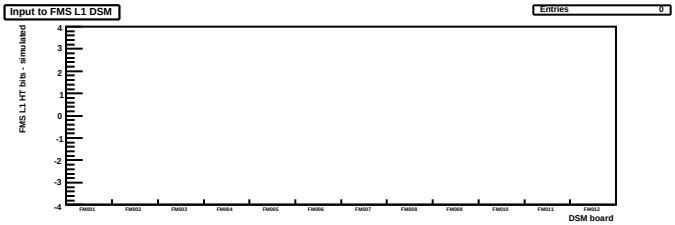

Input to FMS L1 DSM
Entries 152628
sumCD
DSM board

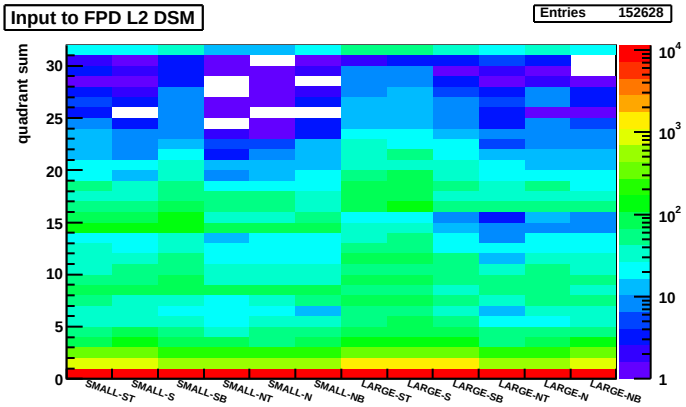

Input to FMS L1 DSM
Entries 324
sumCD - simulated
DSM board

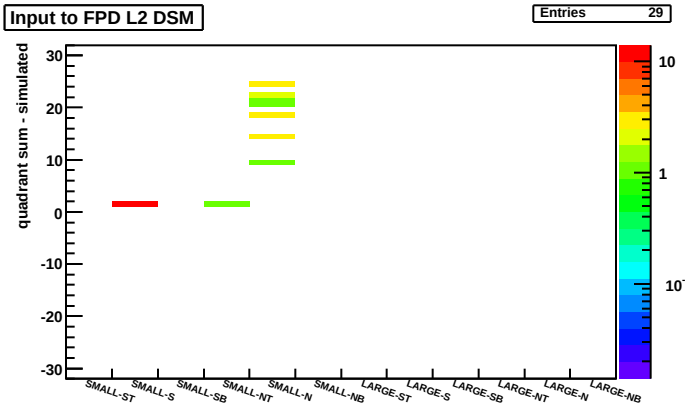

Input to FMS L1 DSM
Entries 152628
sumA
DSM board

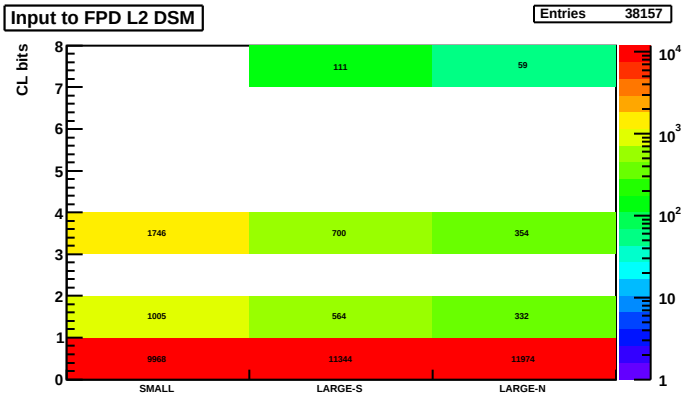

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

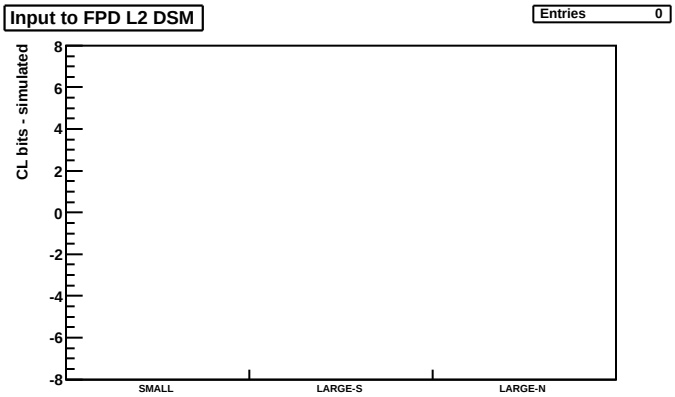

Input to FMS L1 DSM
Entries 152628
FMS L1 HT bits
DSM board

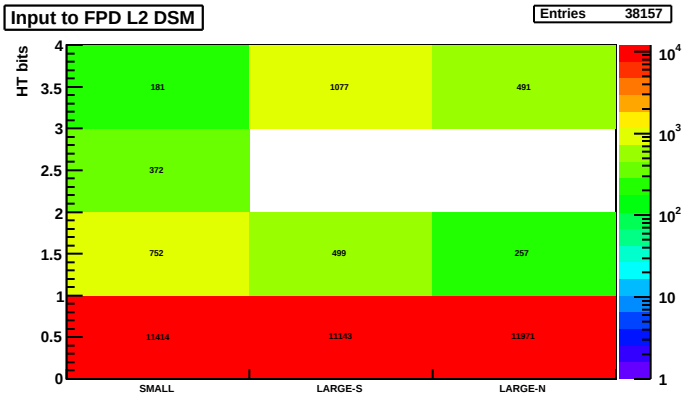

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

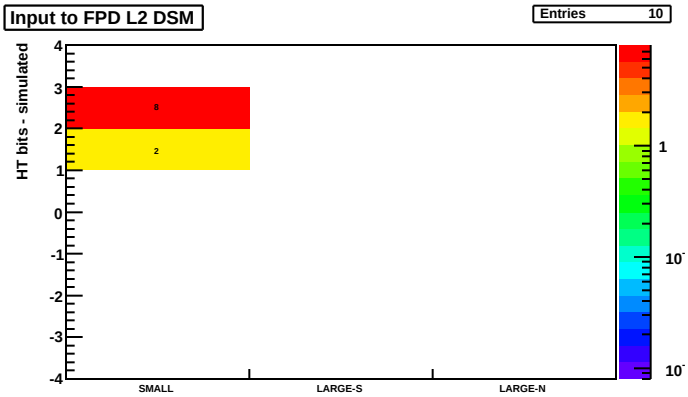

Input to FPD L2 DSM
Entries 152628
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

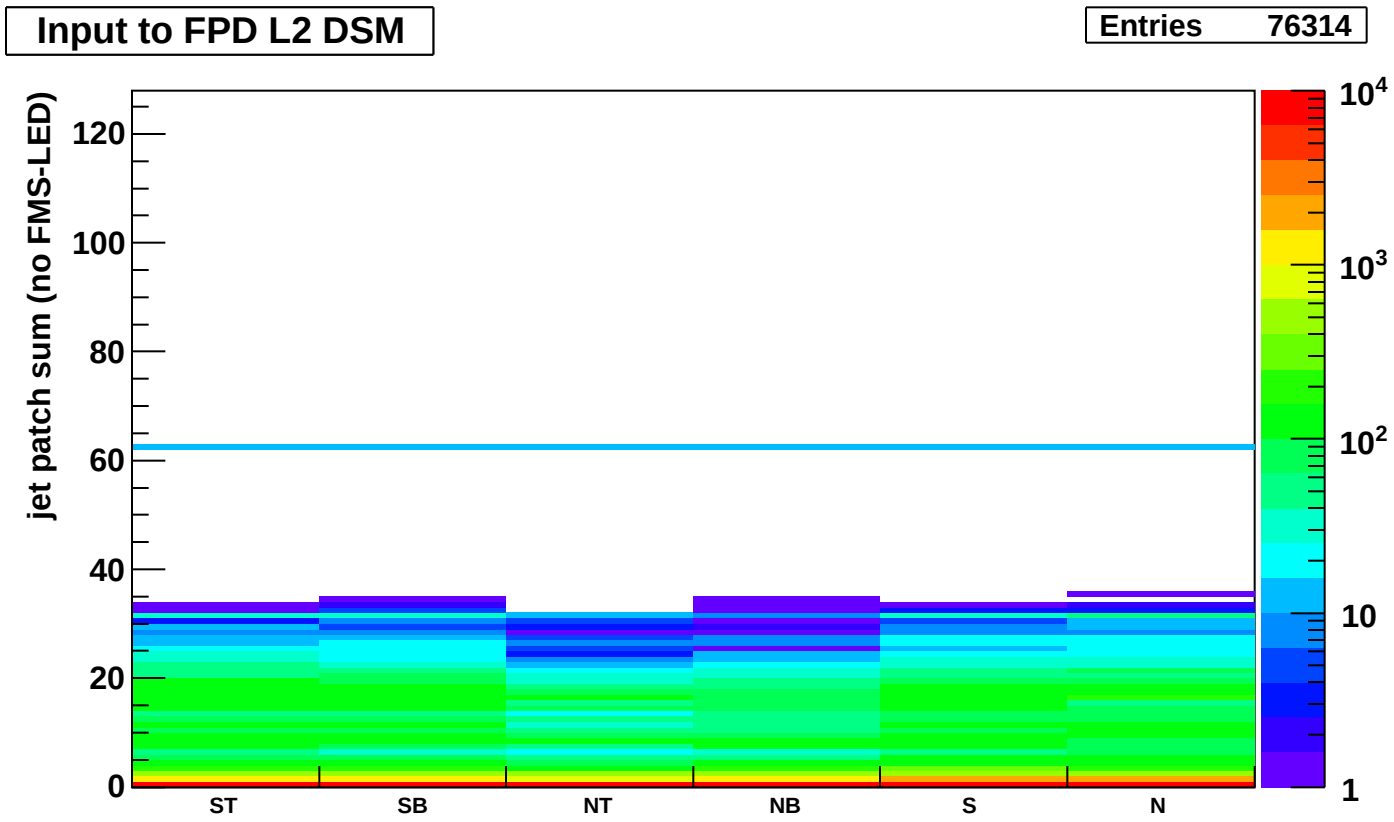

Input to FPD L2 DSM
Entries 29
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

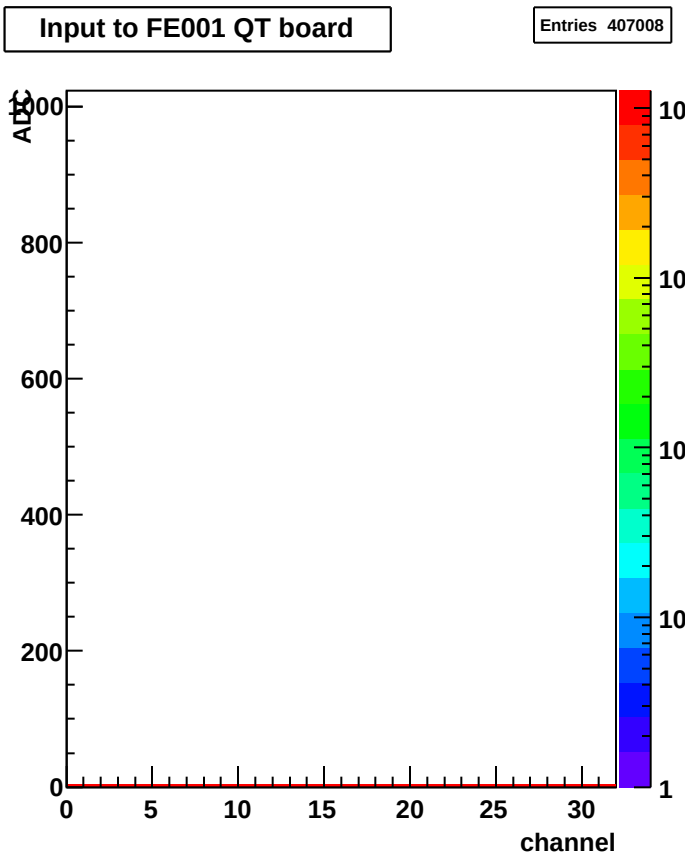

Input to FPD L2 DSM
Entries 38157
CL bits
111
59
1746
700
354
1005
564
332
9968
11344
11974
SMALL
LARGE-S
LARGE-N

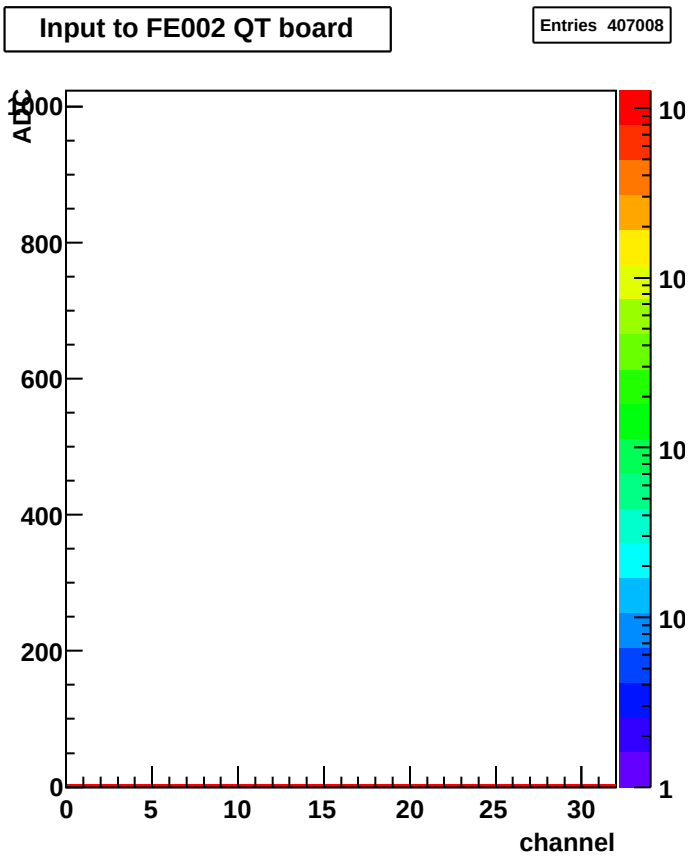

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

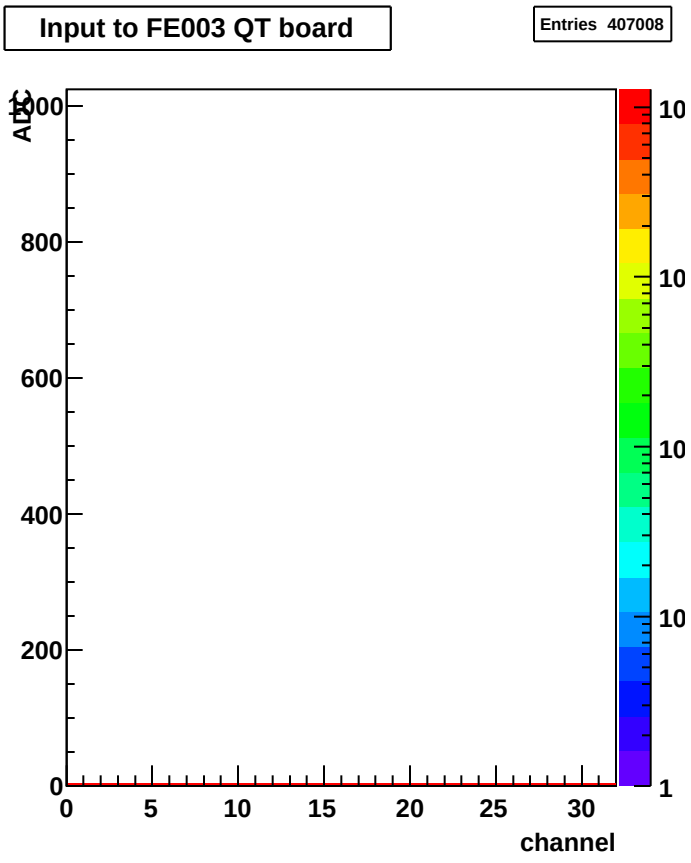

Input to FPD L2 DSM
Entries 38157
HT bits
181
1077
491
372
752
499
257
11414
11143
11971
SMALL
LARGE-S
LARGE-N

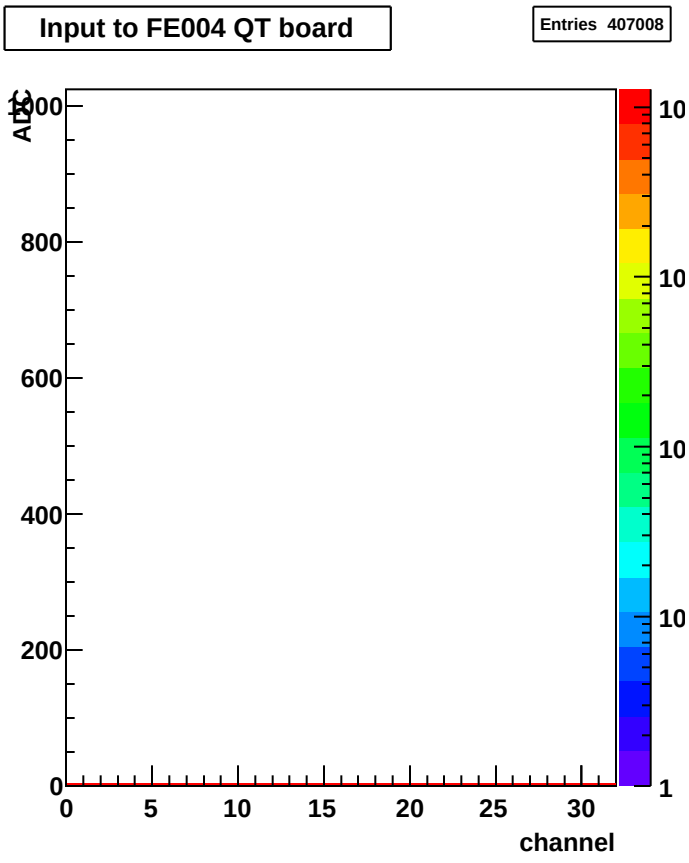

Input to FPD L2 DSM
Entries 10
HT bits - simulated
8
2
SMALL
LARGE-S
LARGE-N

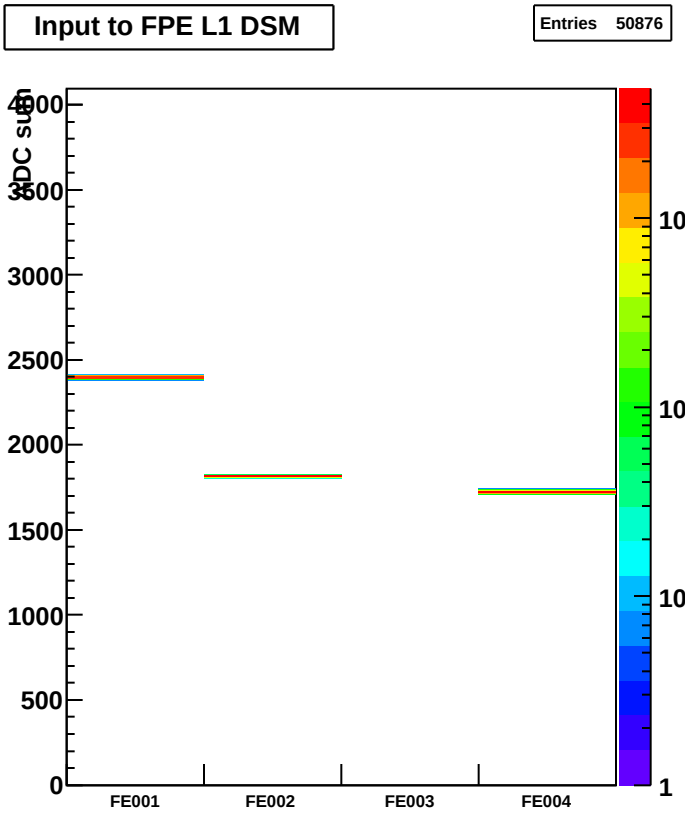

Input to FPD L2 DSM
Entries 76314
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4

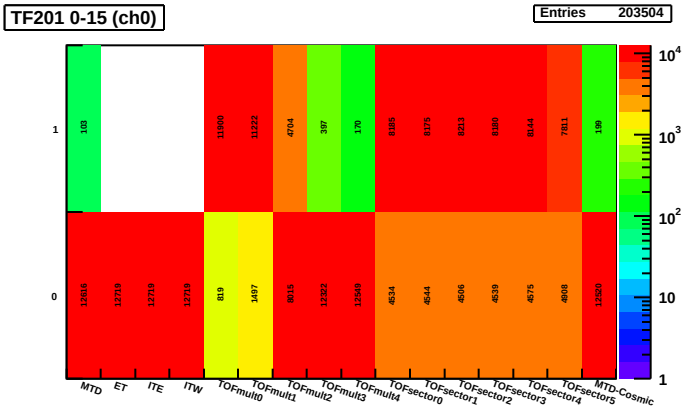

Input to FE001 QT board
Entries 407008
ADC
channel

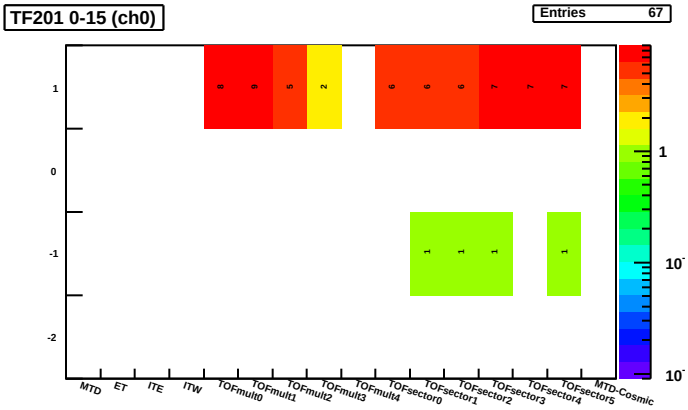

Input to FE002 QT board
Entries 407008
ADC
channel

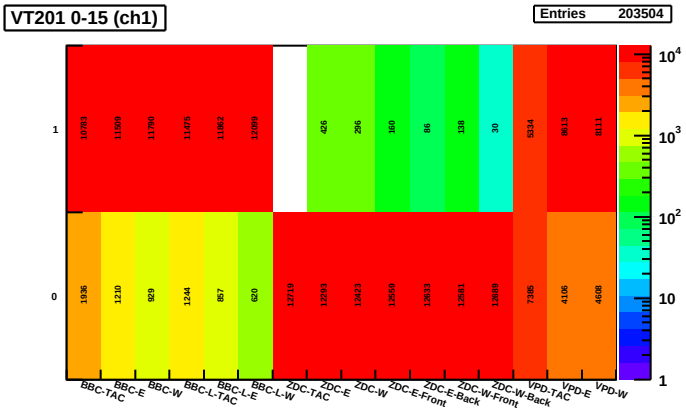

Input to FE003 QT board
Entries 407008
ADC
channel

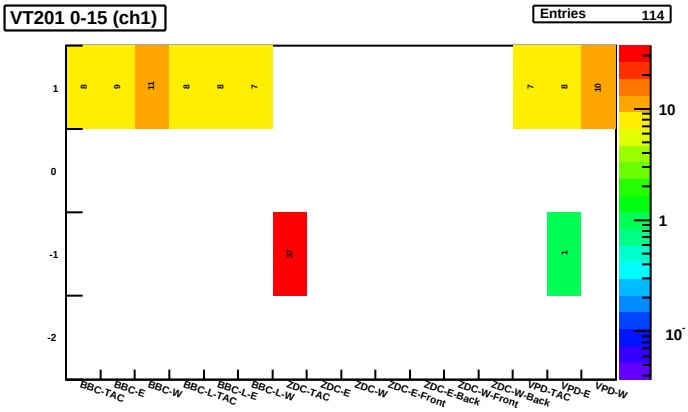

Input to FE004 QT board
Entries 407008
ADC
channel

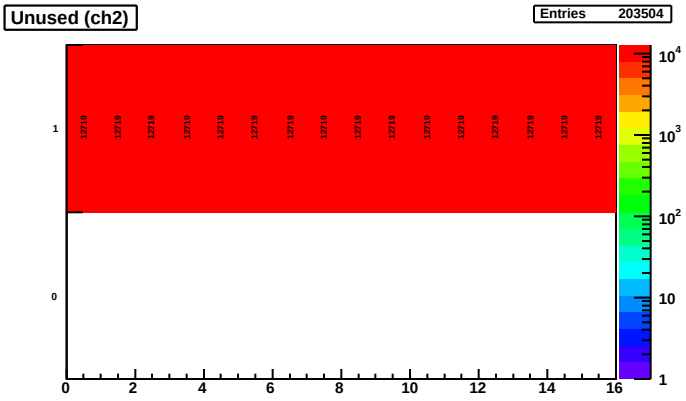

Input to FPE L1 DSM
Entries 50876
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
1

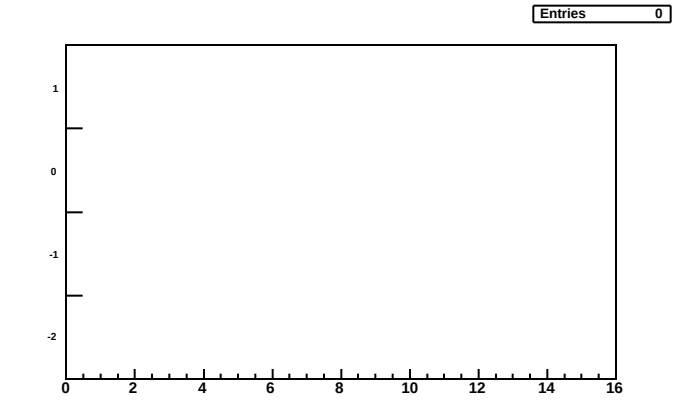

TF201 0-15 (ch0)
Entries 203504
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

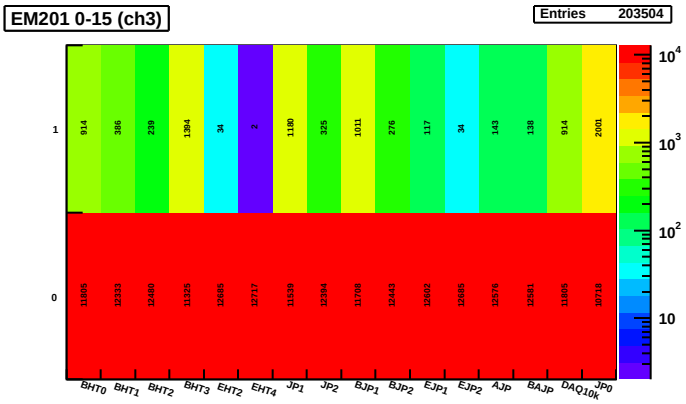

TF201 0-15 (ch0)
Entries 67
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

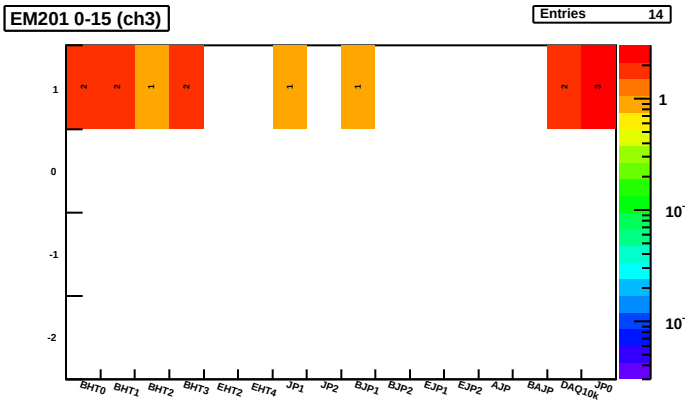

VT201 0-15 (ch1)
Entries 203504
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 114
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

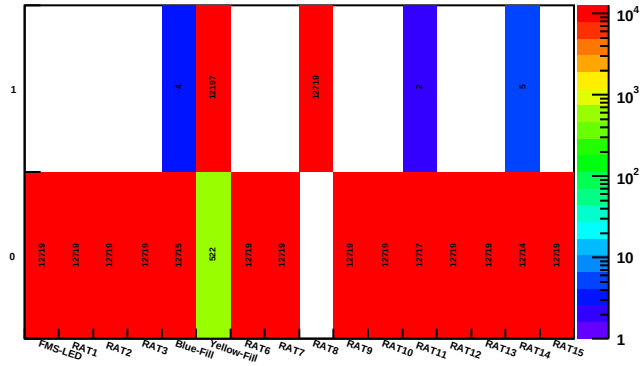

Unused (ch2)
Entries 203504

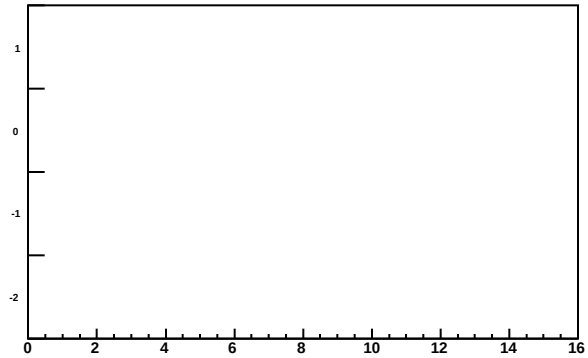

Entries 0


EM201 0-15 (ch3)
Entries 203504
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

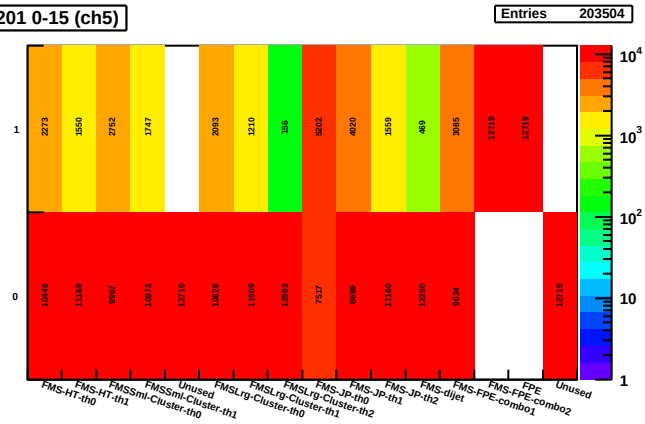

EM201 0-15 (ch3)
Entries 14
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

RAT board (ch4)

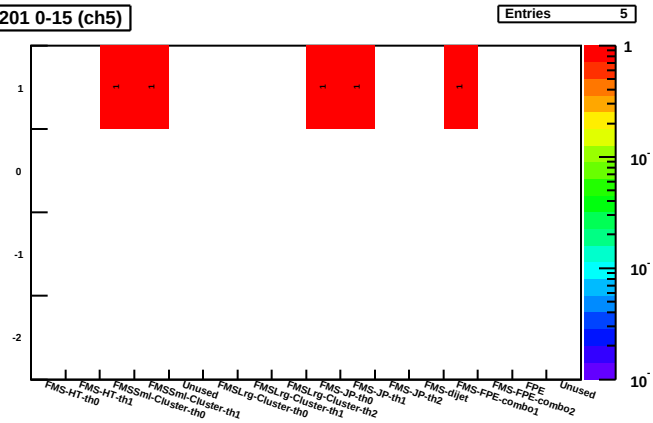




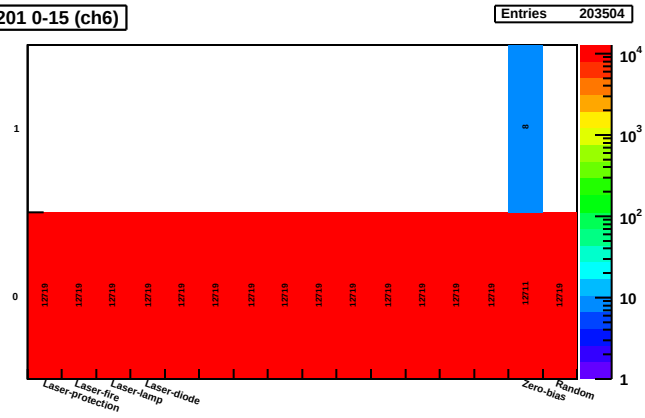
FP201 0-15 (ch5)

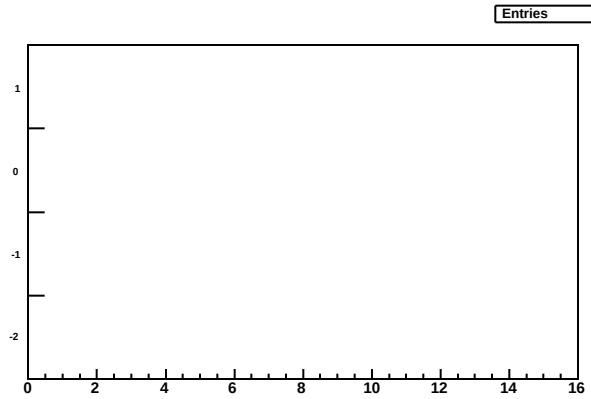
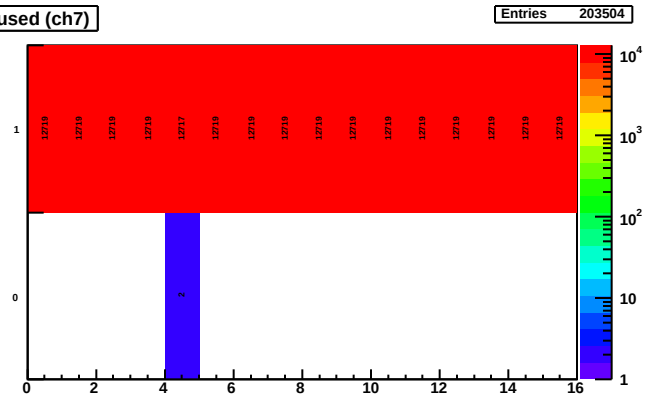
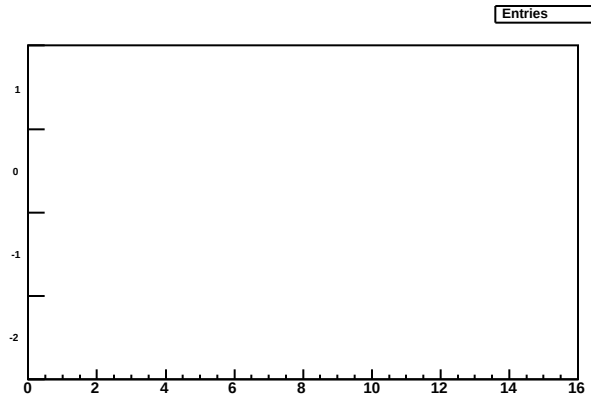


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)