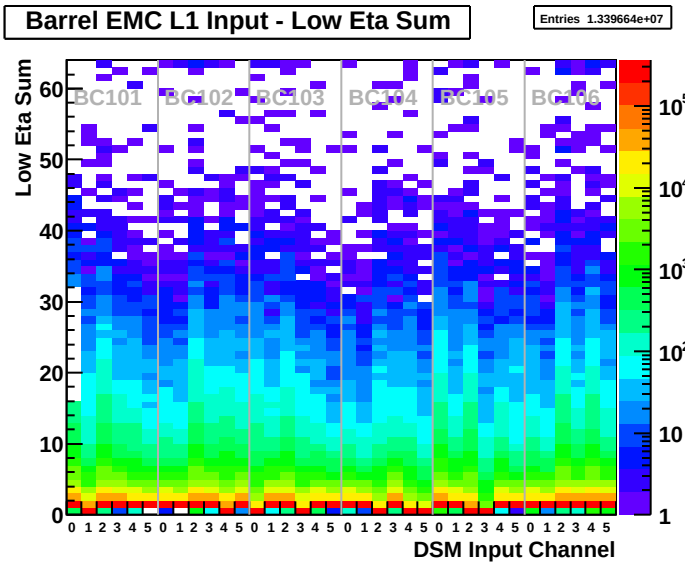
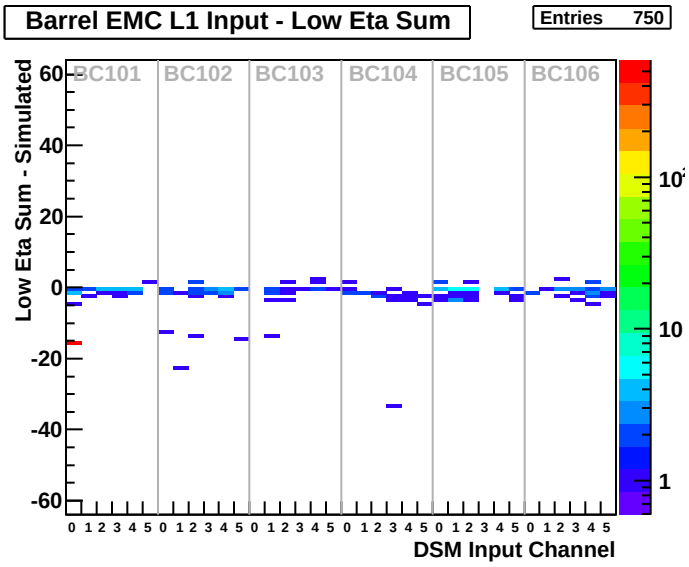
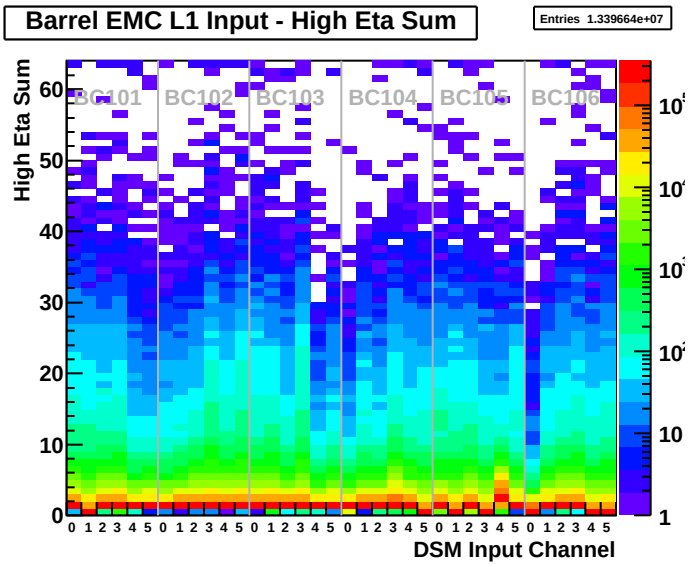
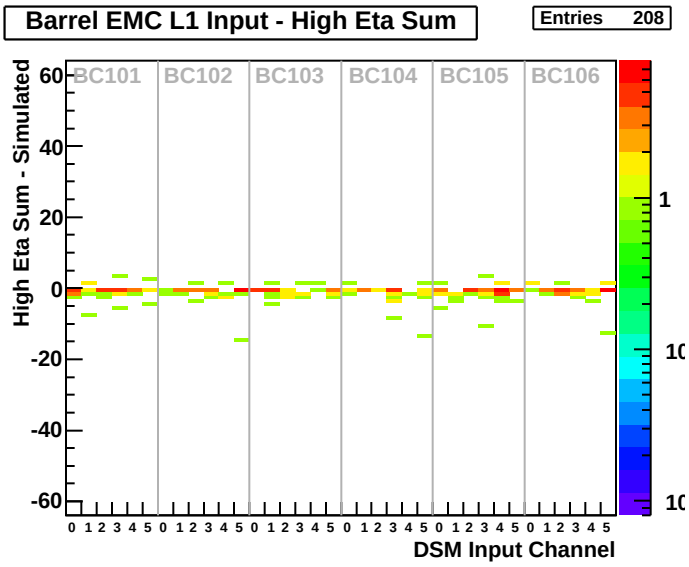
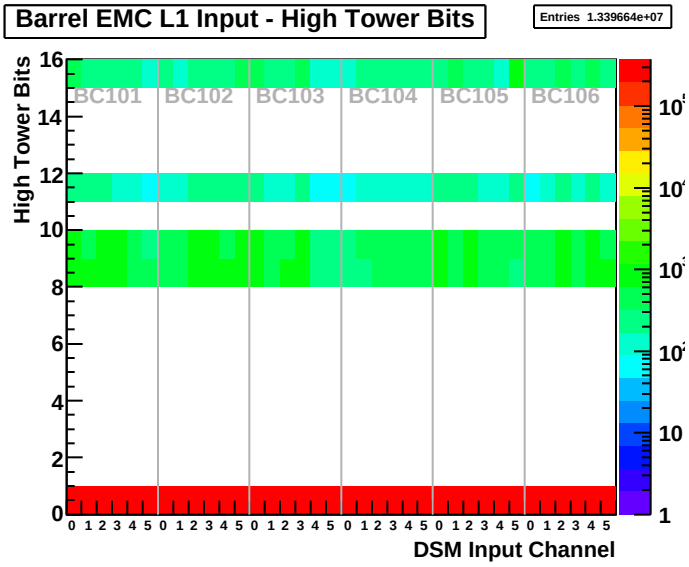
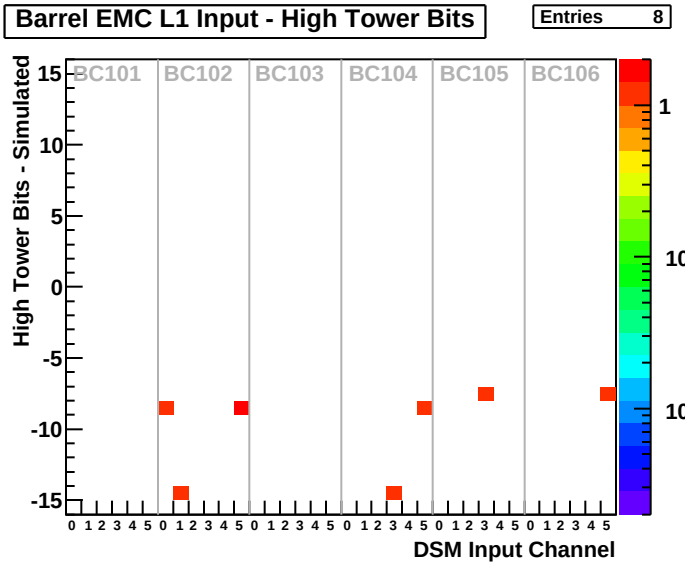

Barrel EMC L1 Input - Low Eta Sum
Entries 1.339664e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

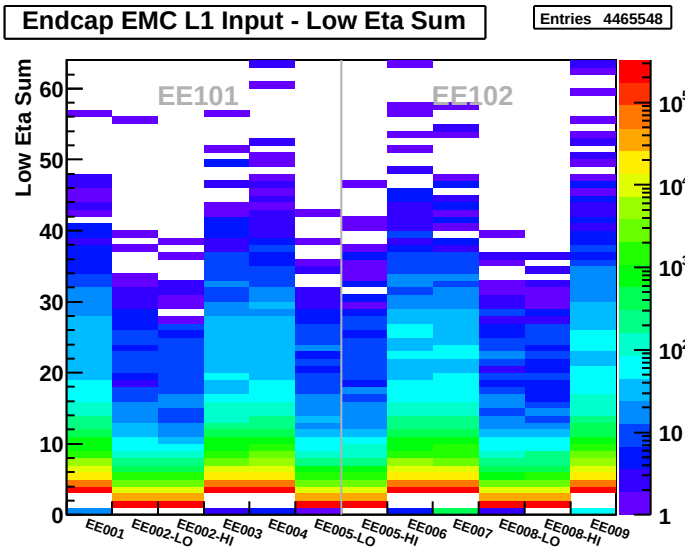

Barrel EMC L1 Input - Low Eta Sum
Entries 750
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

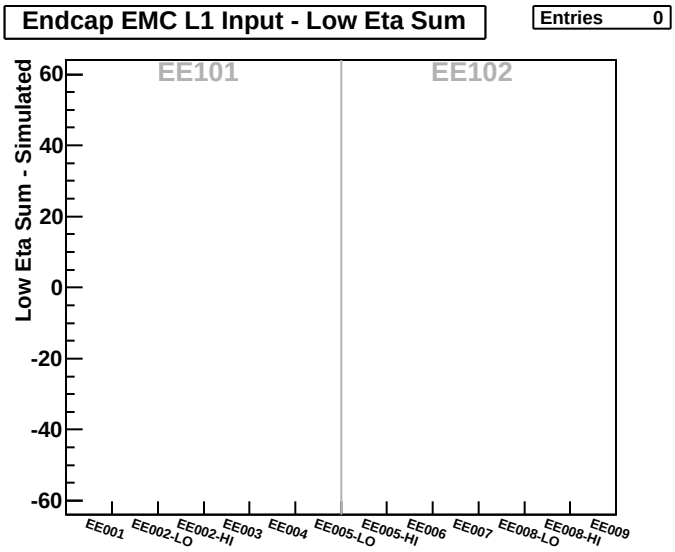

Barrel EMC L1 Input - High Eta Sum
Entries 1.339664e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

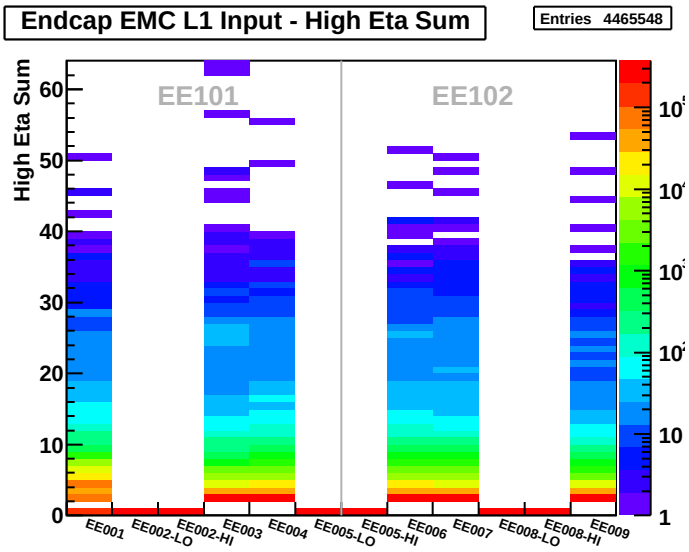

Barrel EMC L1 Input - High Eta Sum
Entries 208
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

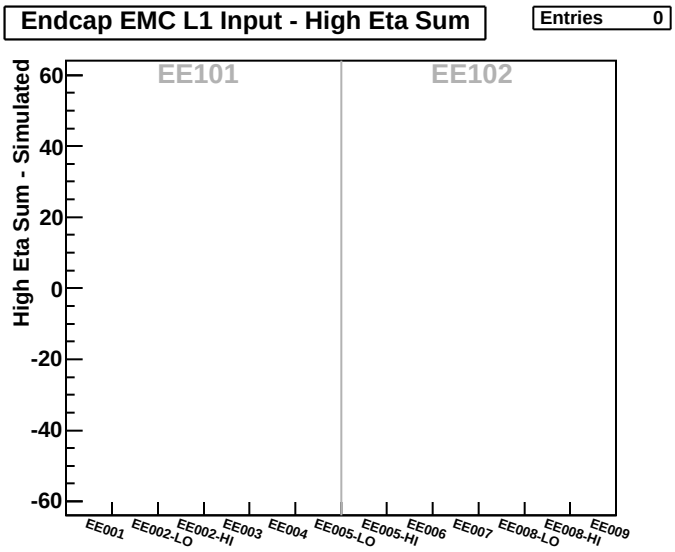

Barrel EMC L1 Input - High Tower Bits
Entries 1.339664e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

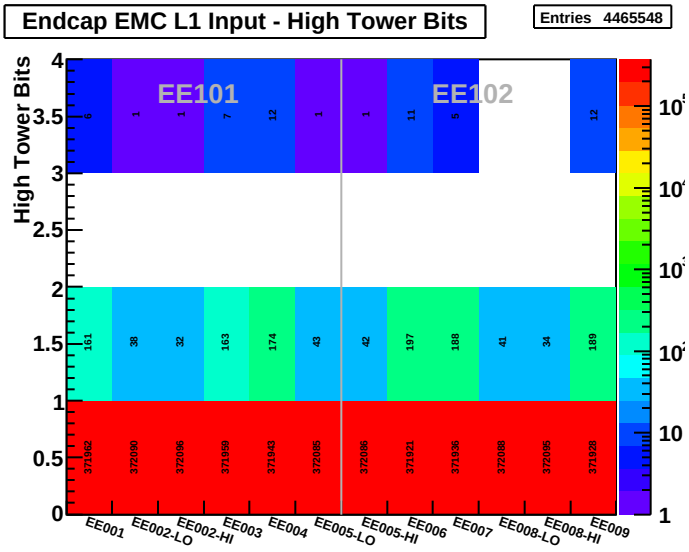

Barrel EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

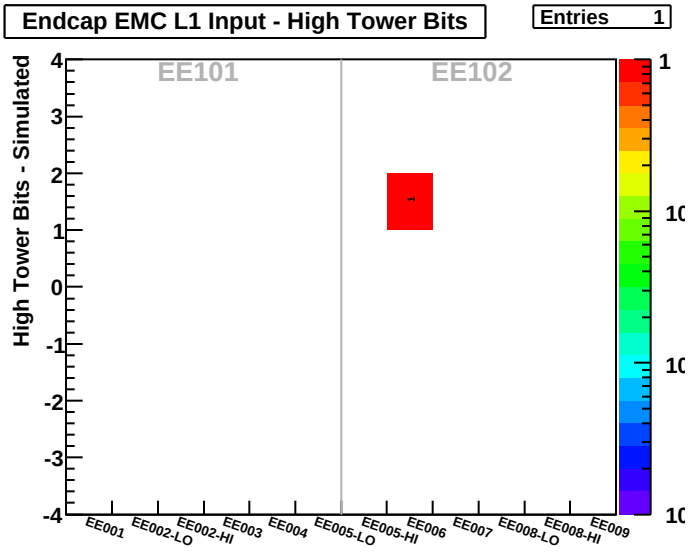

Endcap EMC L1 Input - Low Eta Sum
Entries 4465548
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

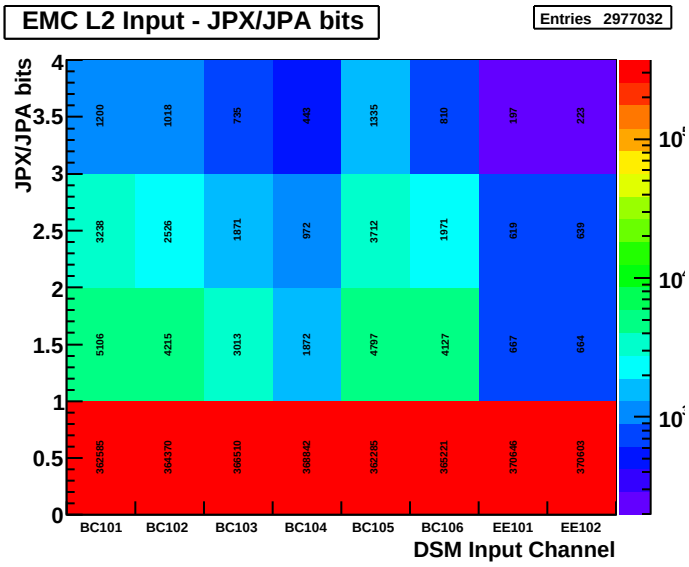

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

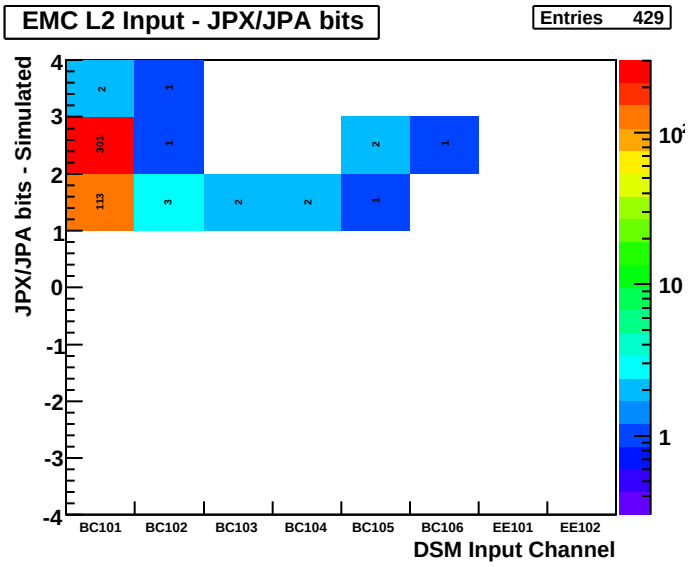

Endcap EMC L1 Input - High Eta Sum
Entries 4465548
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

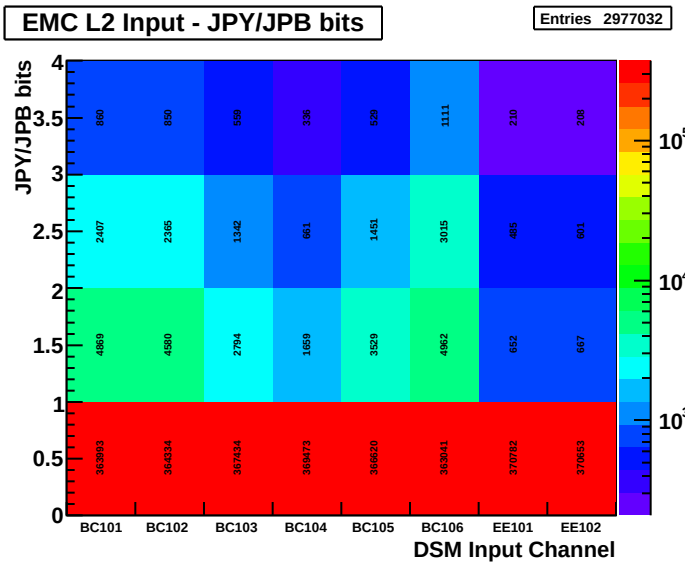

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

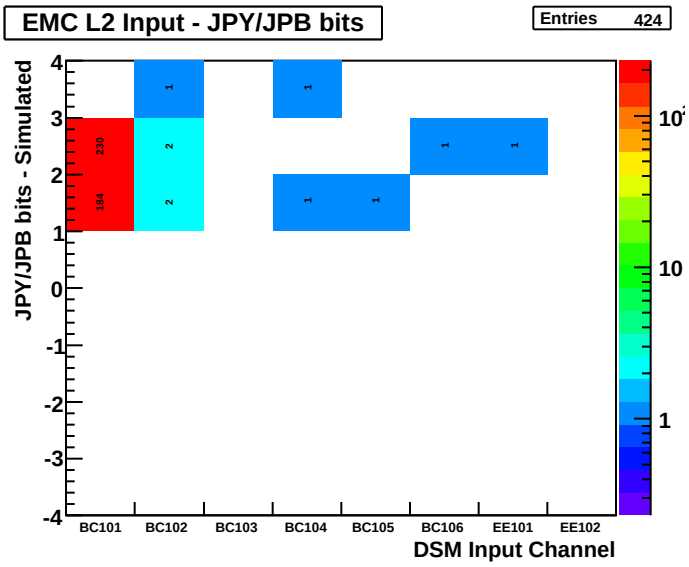

Endcap EMC L1 Input - High Tower Bits
Entries 4465548
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

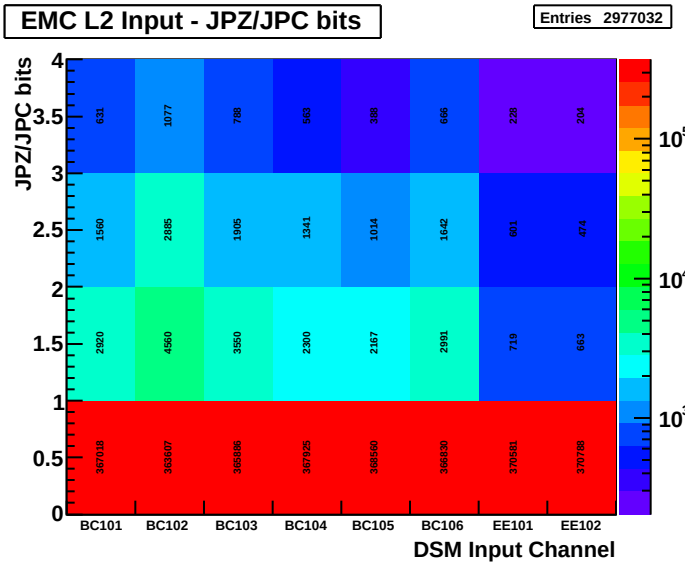

Endcap EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

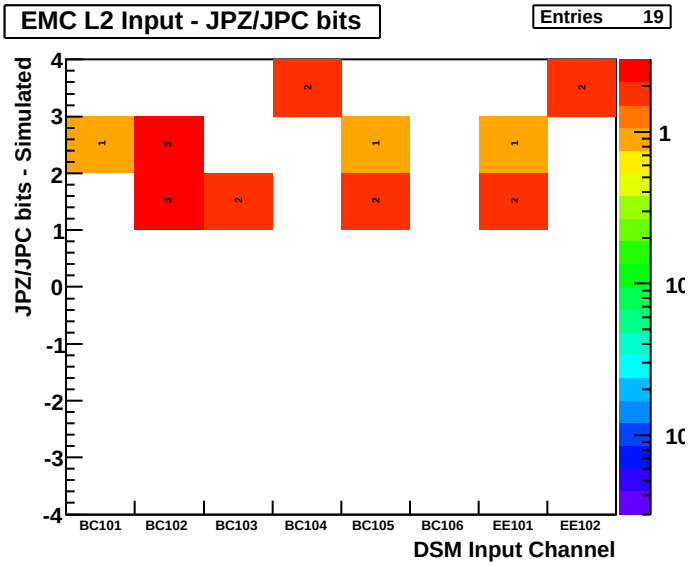

EMC L2 Input - JPX/JPA bits
Entries 2977032
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

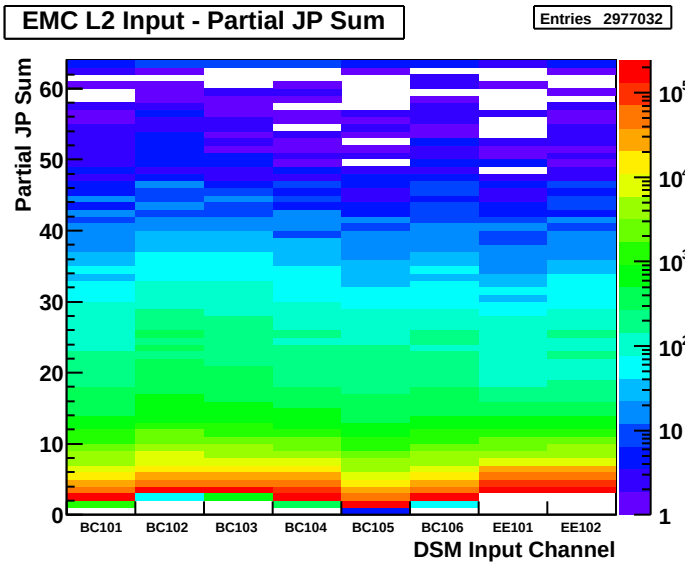

EMC L2 Input - JPX/JPA bits
Entries 429
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

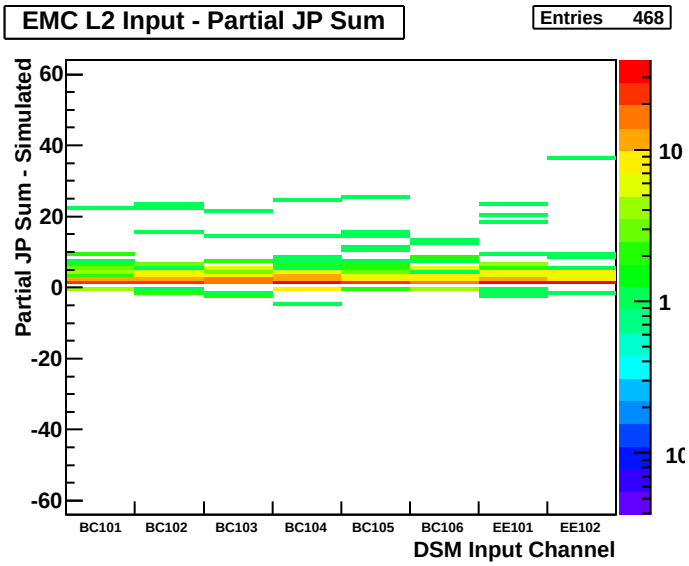

EMC L2 Input - JPY/JPB bits
Entries 2977032
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

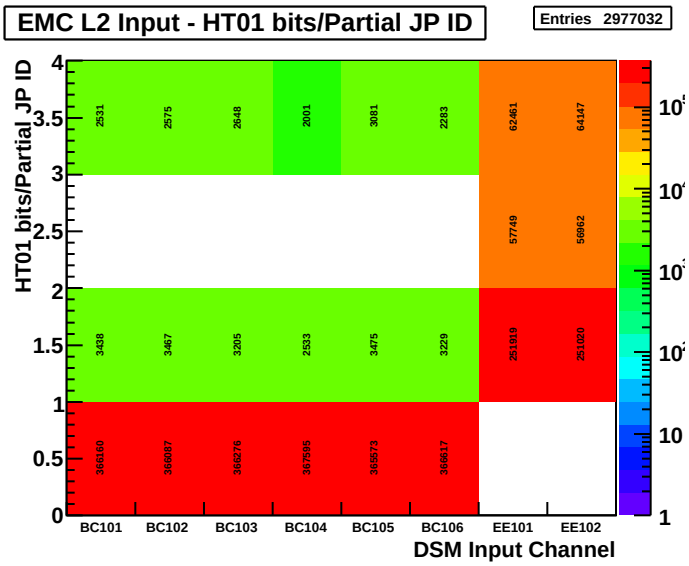

EMC L2 Input - JPY/JPB bits
Entries 424
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

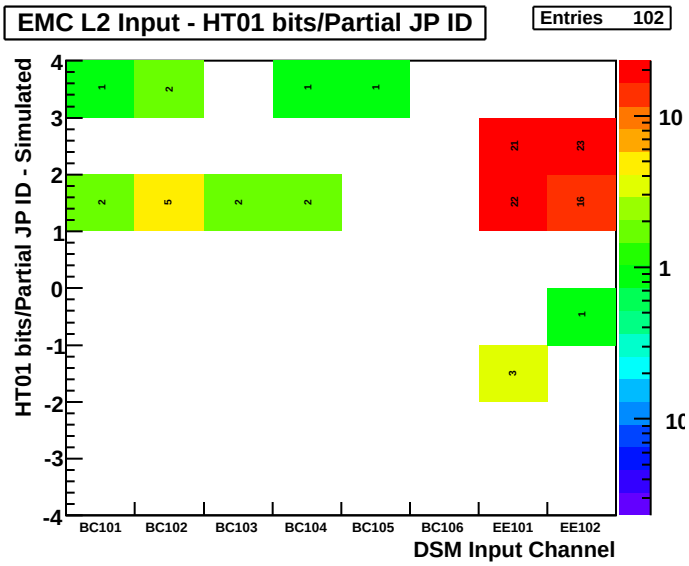

EMC L2 Input - JPZ/JPC bits
Entries 2977032
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

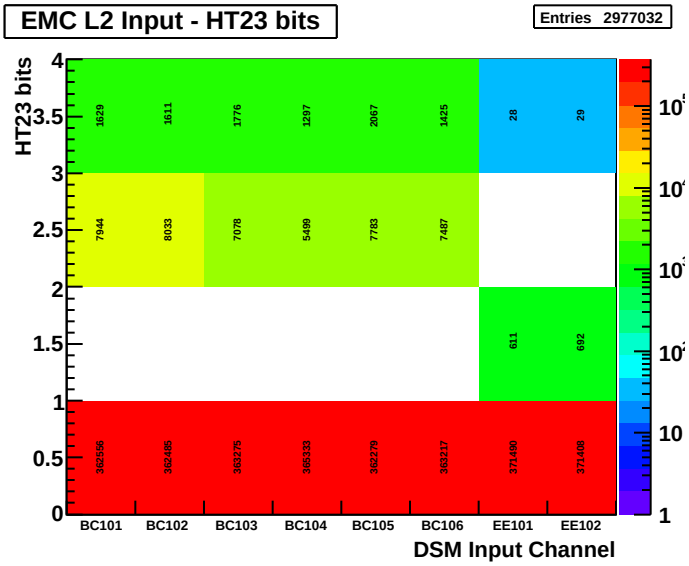

EMC L2 Input - JPZ/JPC bits
Entries 19
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

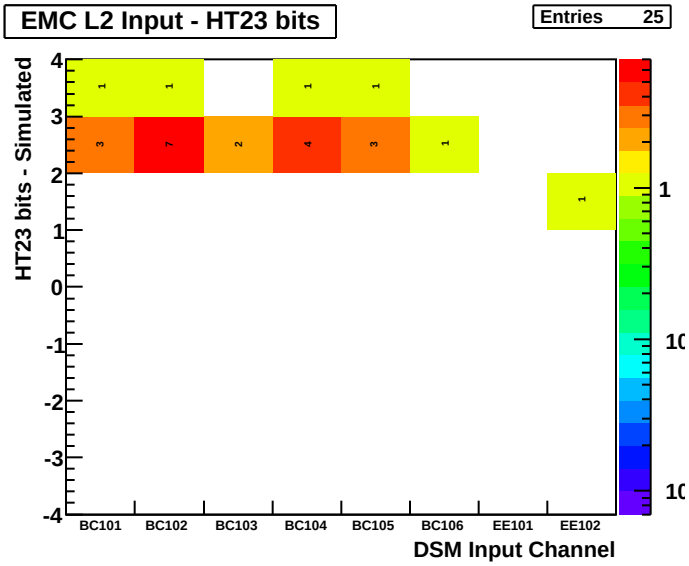

EMC L2 Input - Partial JP Sum
Entries 2977032
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

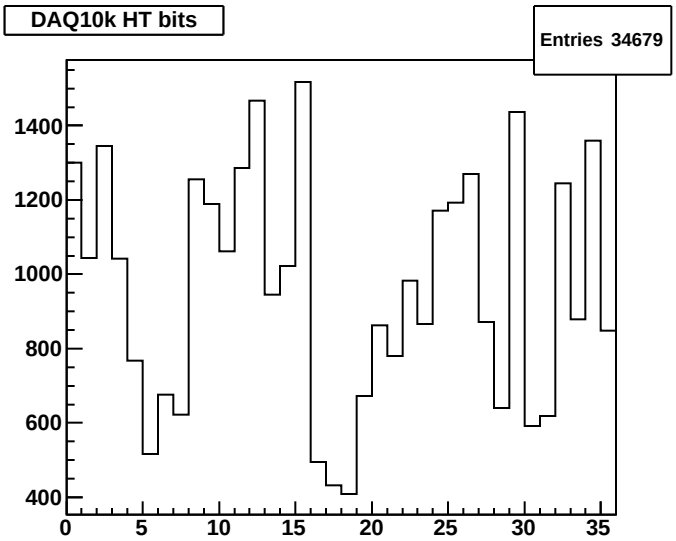

EMC L2 Input - Partial JP Sum
Entries 468
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

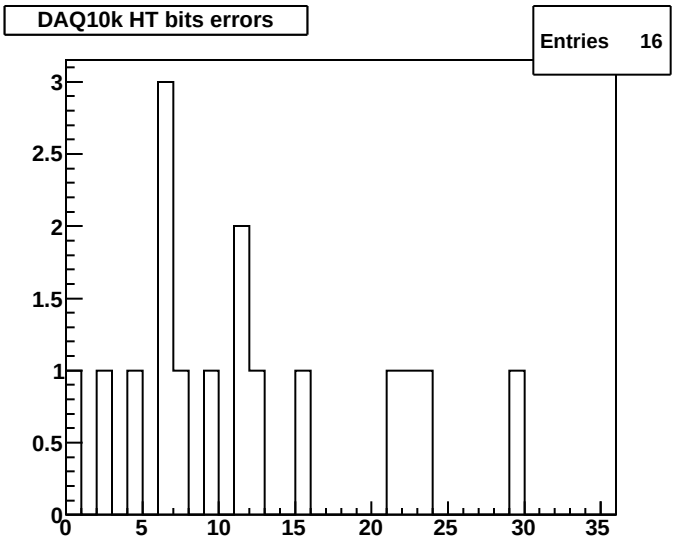

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2977032
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

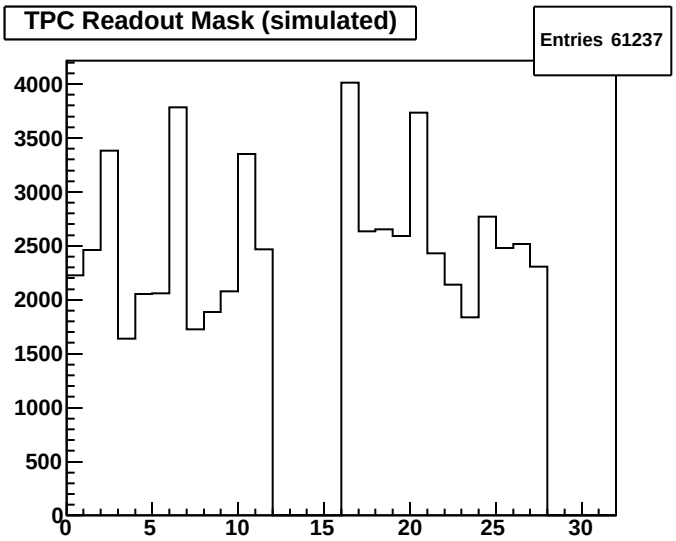

EMC L2 Input - HT01 bits/Partial JP ID
Entries 102
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

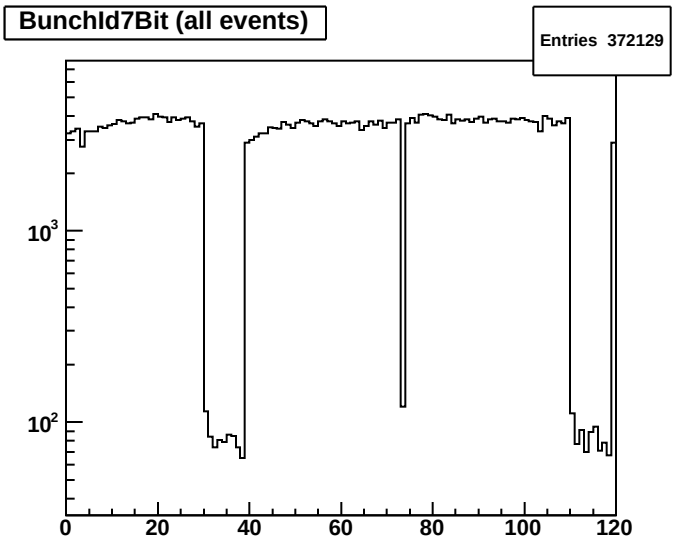

EMC L2 Input - HT23 bits
Entries 2977032
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

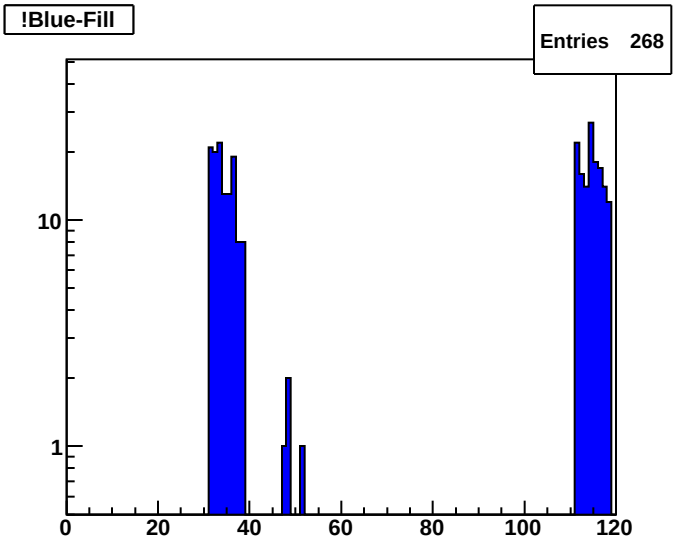

EMC L2 Input - HT23 bits
Entries 25
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

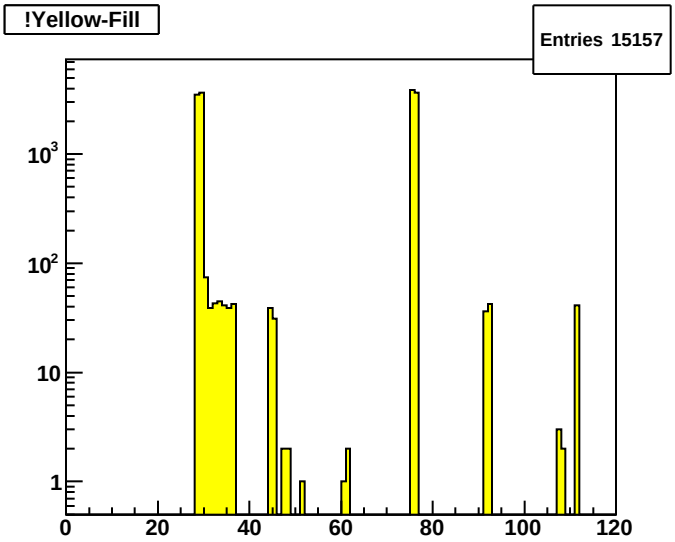

DAQ10k HT bits
Entries 34679

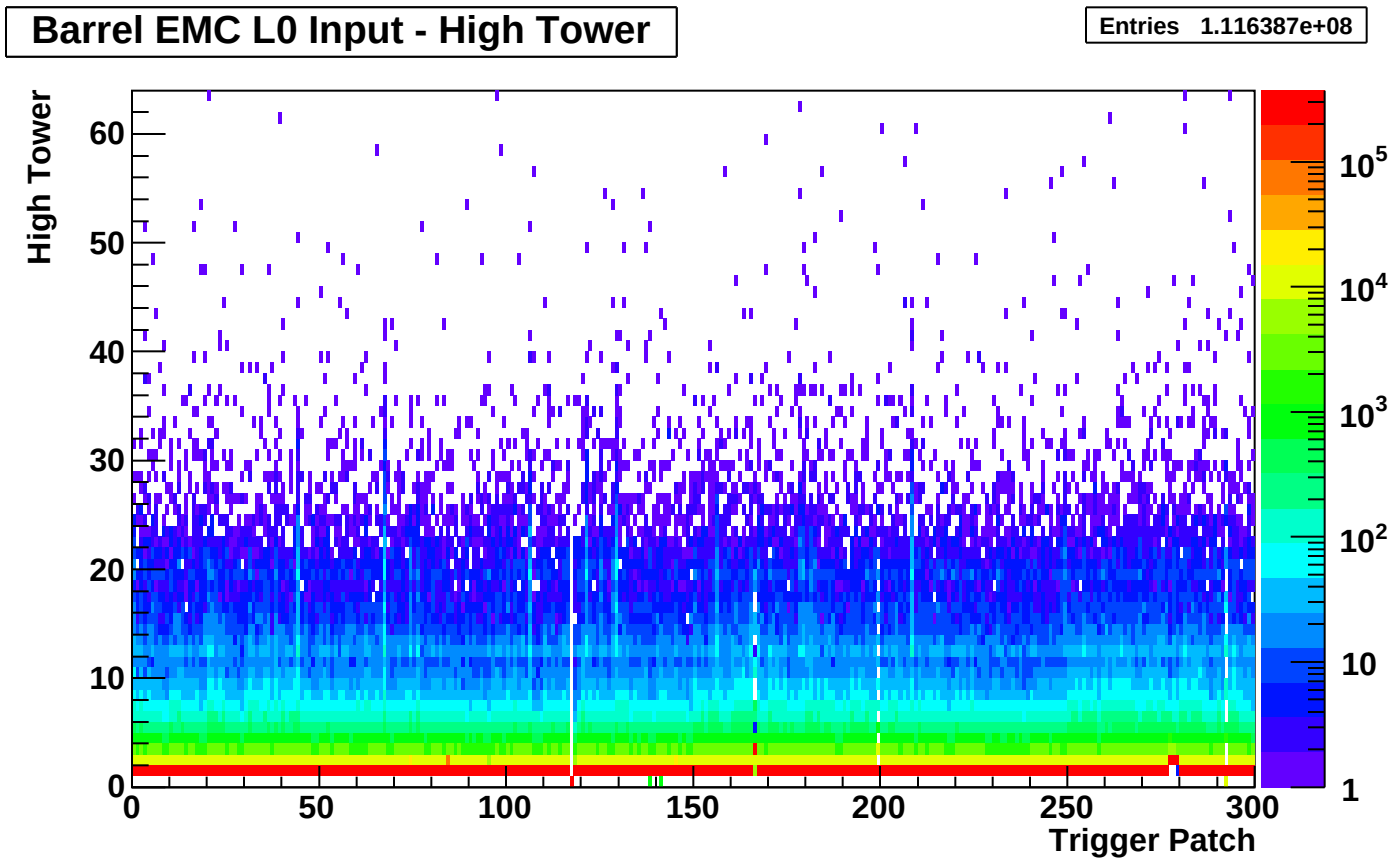

DAQ10k HT bits errors
Entries 16

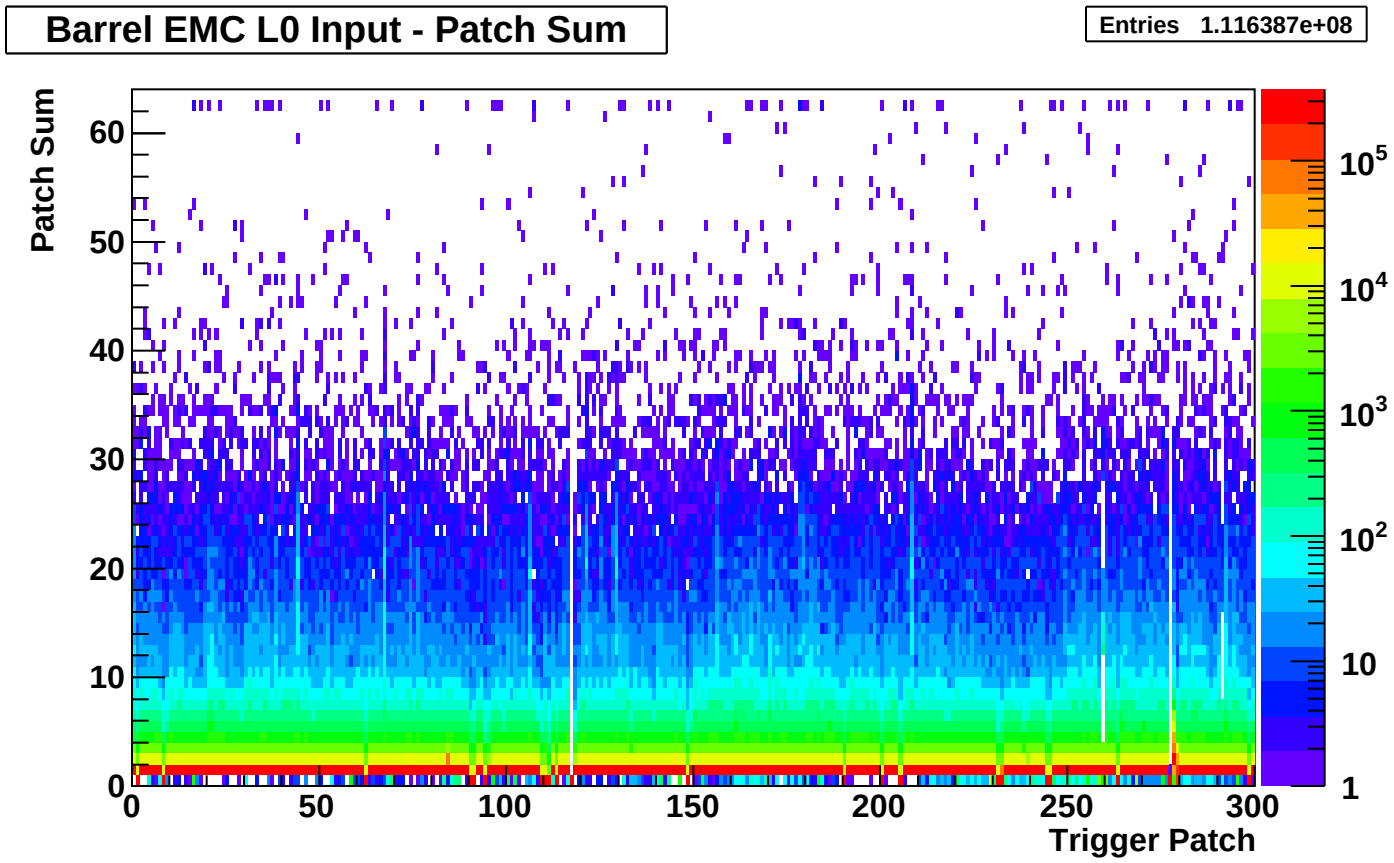

TPC Readout Mask (simulated)
Entries 61237

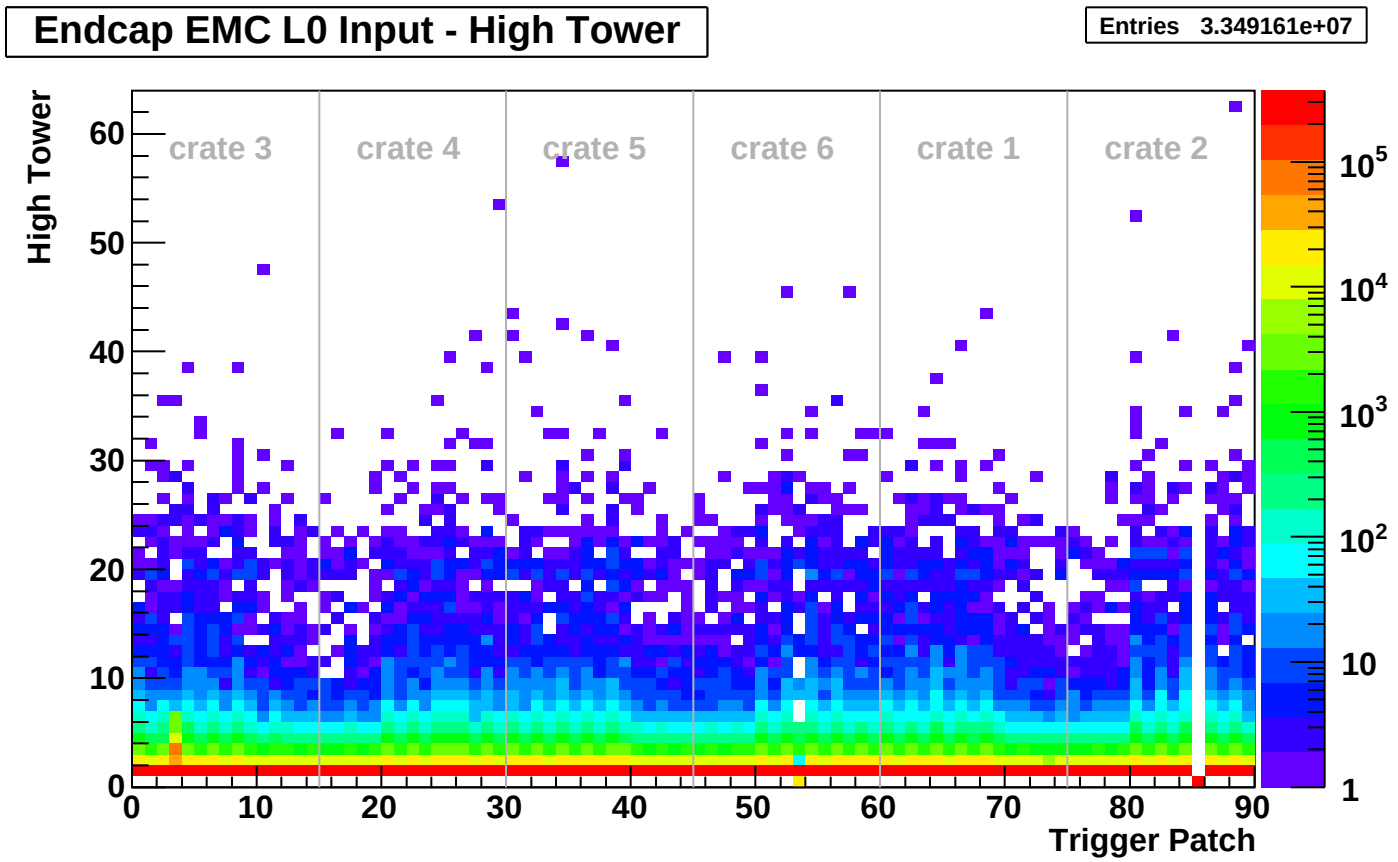

BunchId7Bit (all events)
Entries 372129

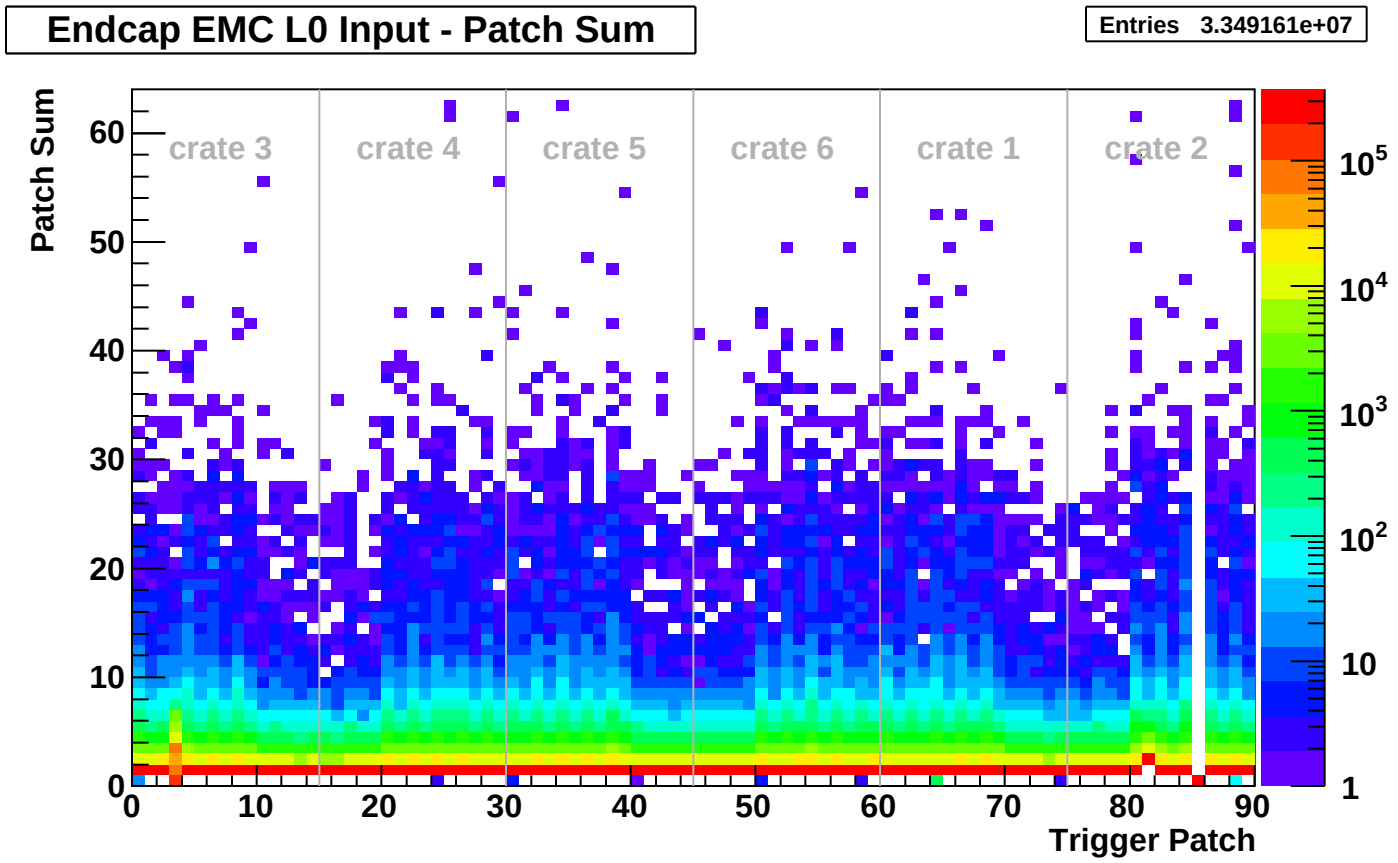

!Blue-Fill
Entries 268

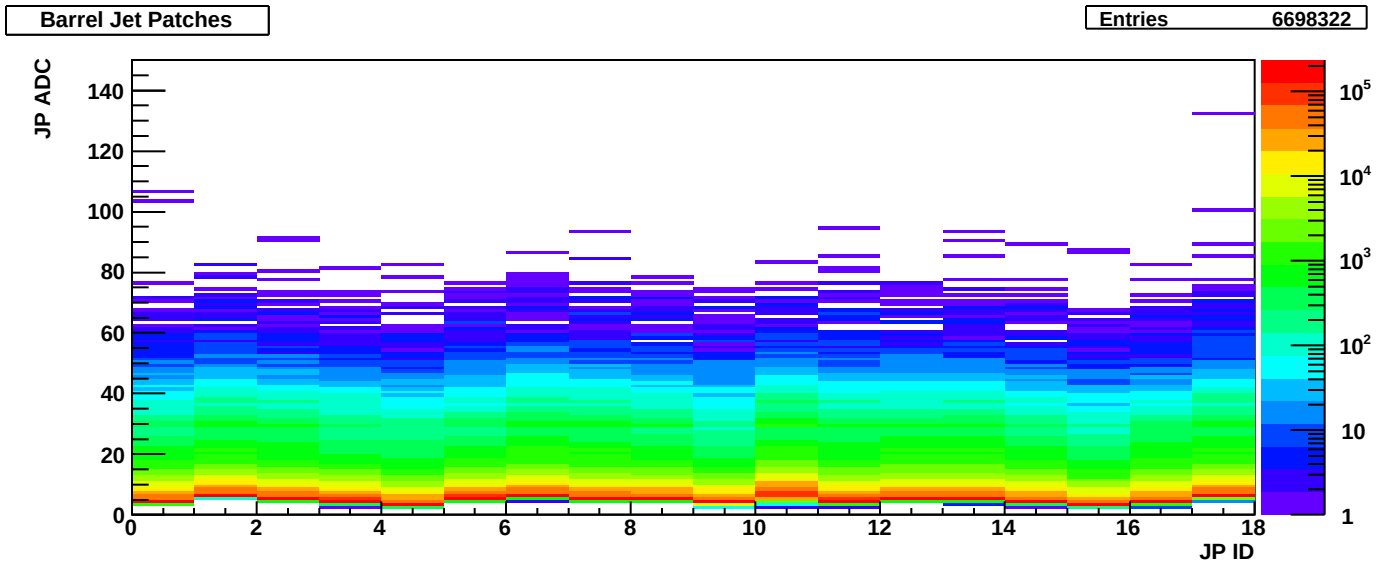

!Yellow-Fill
Entries 15157

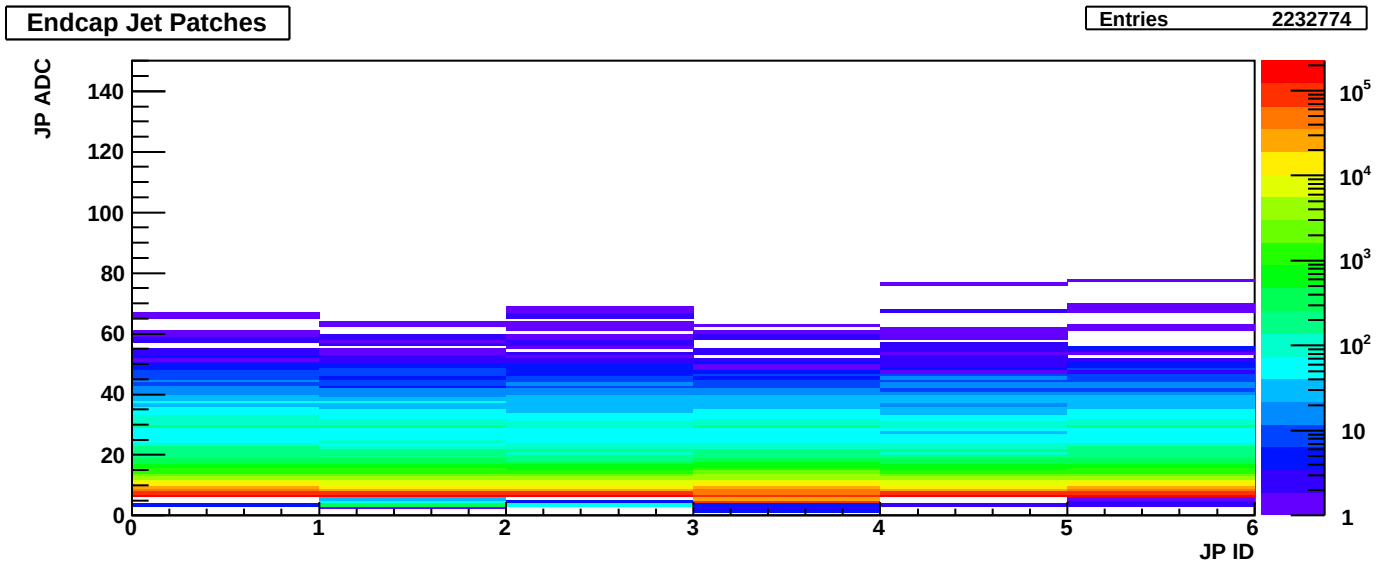

Barrel EMC L0 Input - High Tower
Entries 1.116387e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

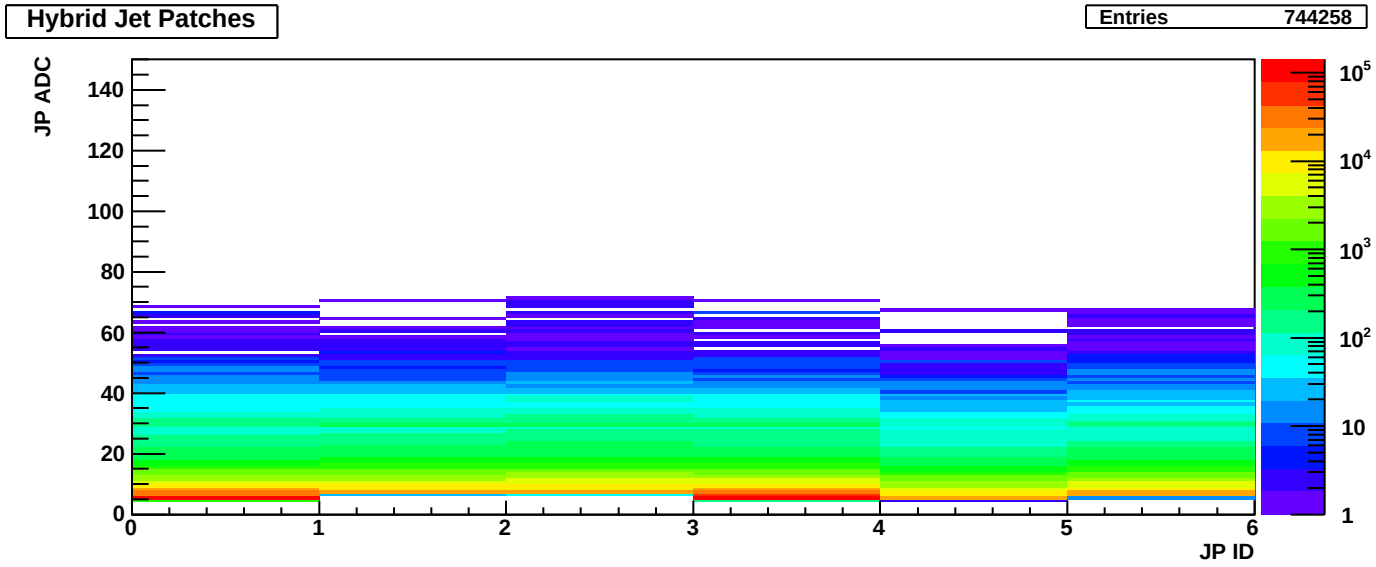

Barrel EMC L0 Input - Patch Sum
Entries 1.116387e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

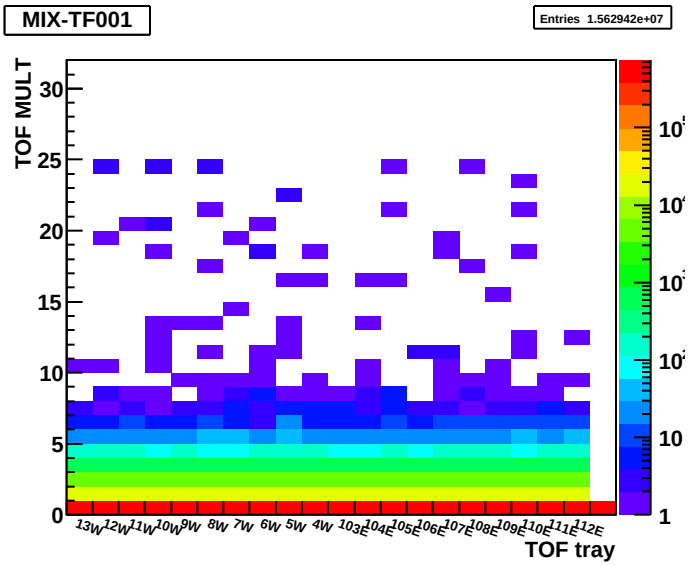

Endcap EMC L0 Input - High Tower
Entries 3.349161e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

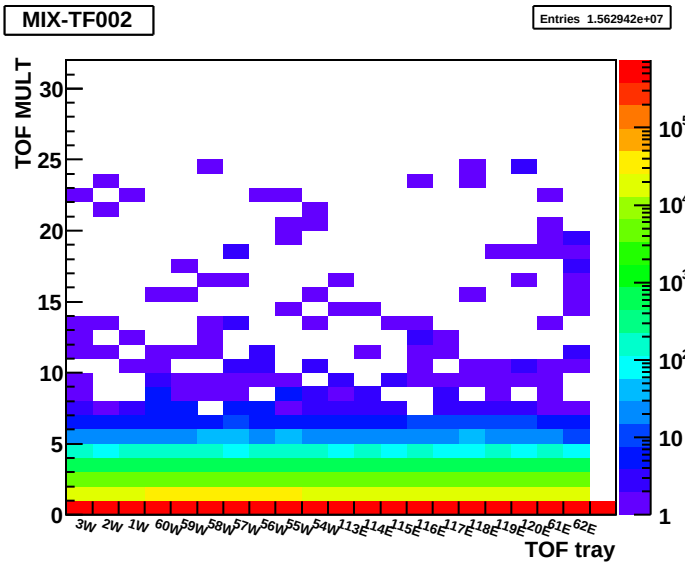

Endcap EMC L0 Input - Patch Sum
Entries 3.349161e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

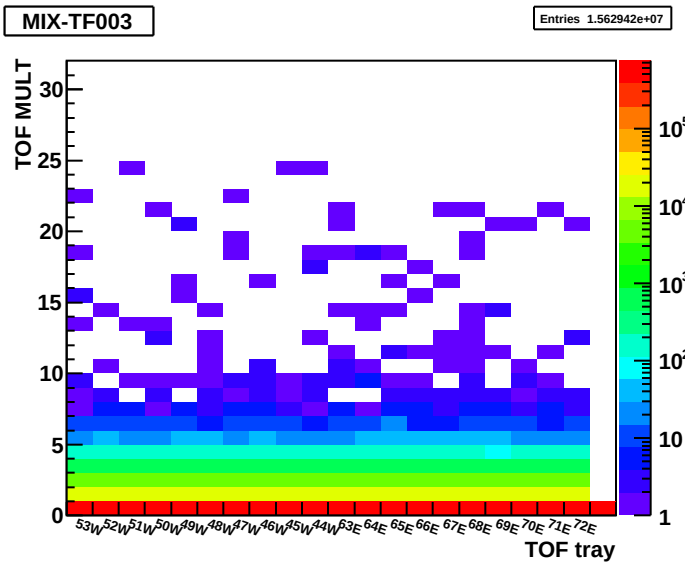

Barrel Jet Patches
Entries 6698322
JP ADC
JP ID

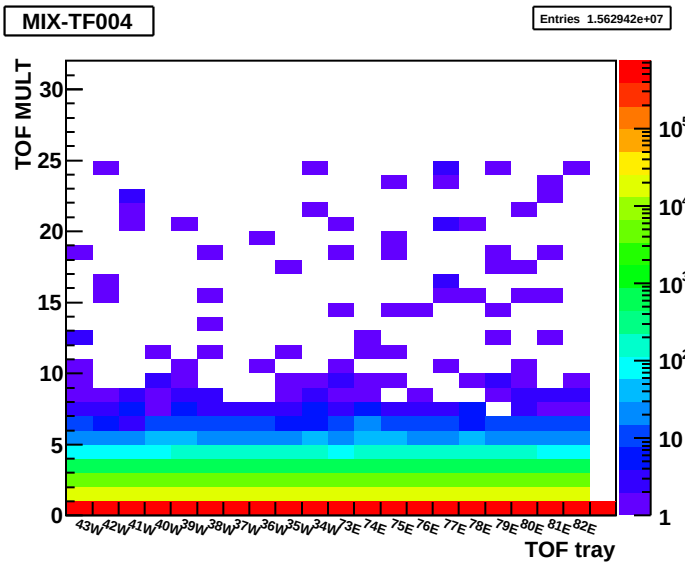

Endcap Jet Patches
Entries 2232774
JP ADC
JP ID

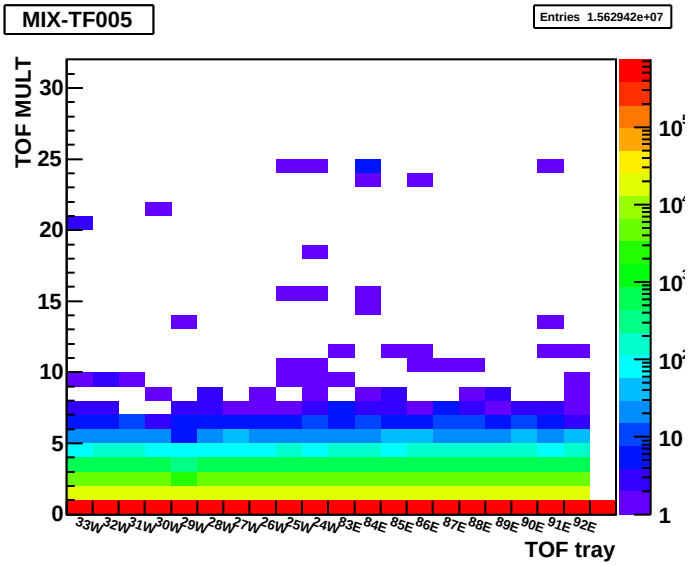

Hybrid Jet Patches
Entries 744258
JP ADC
JP ID

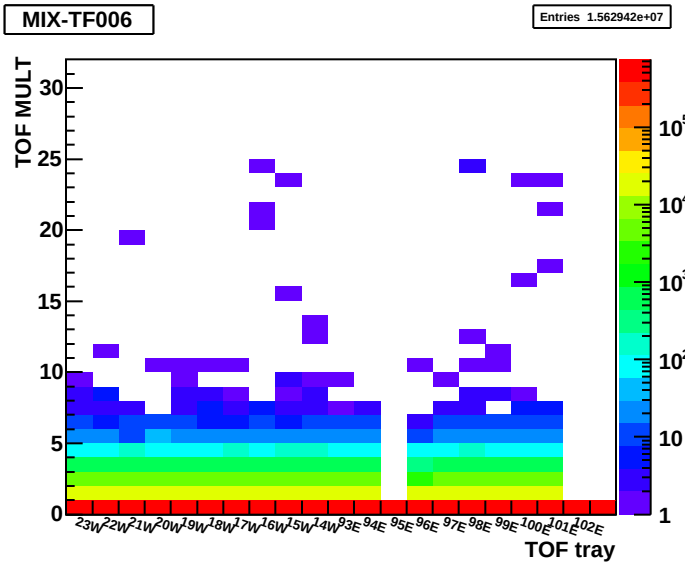

MIX-TF001
Entries 1.562942e+07
TOF MULT
TOF tray

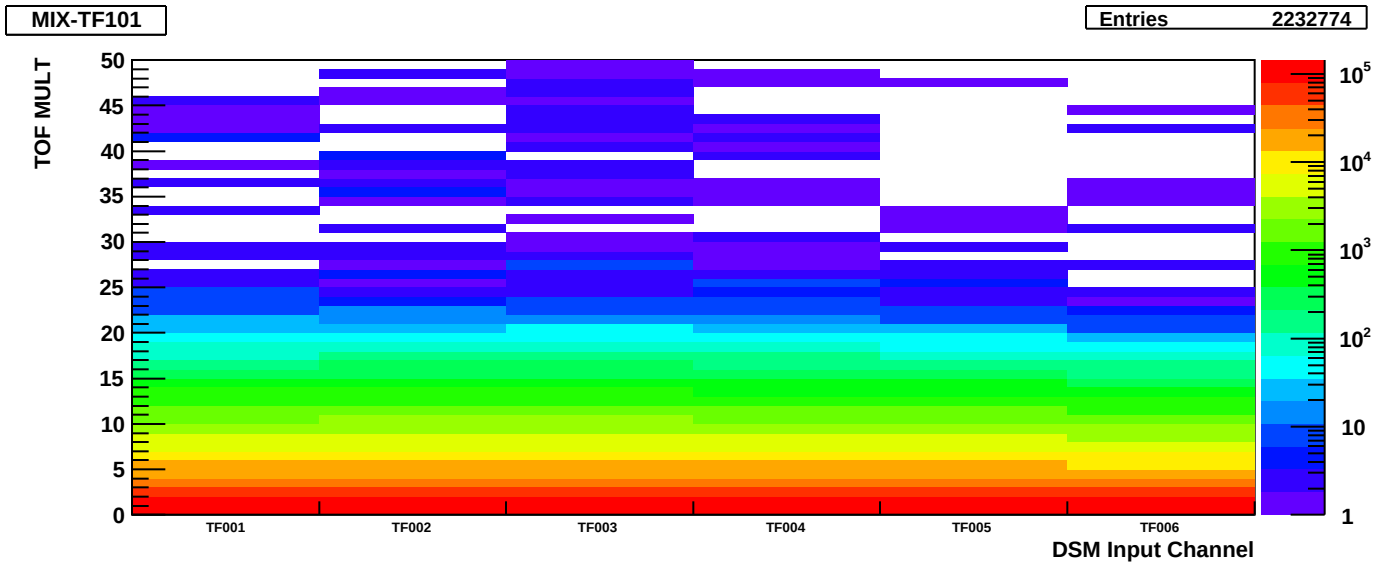

MIX-TF002
Entries 1.562942e+07
TOF MULT
TOF tray

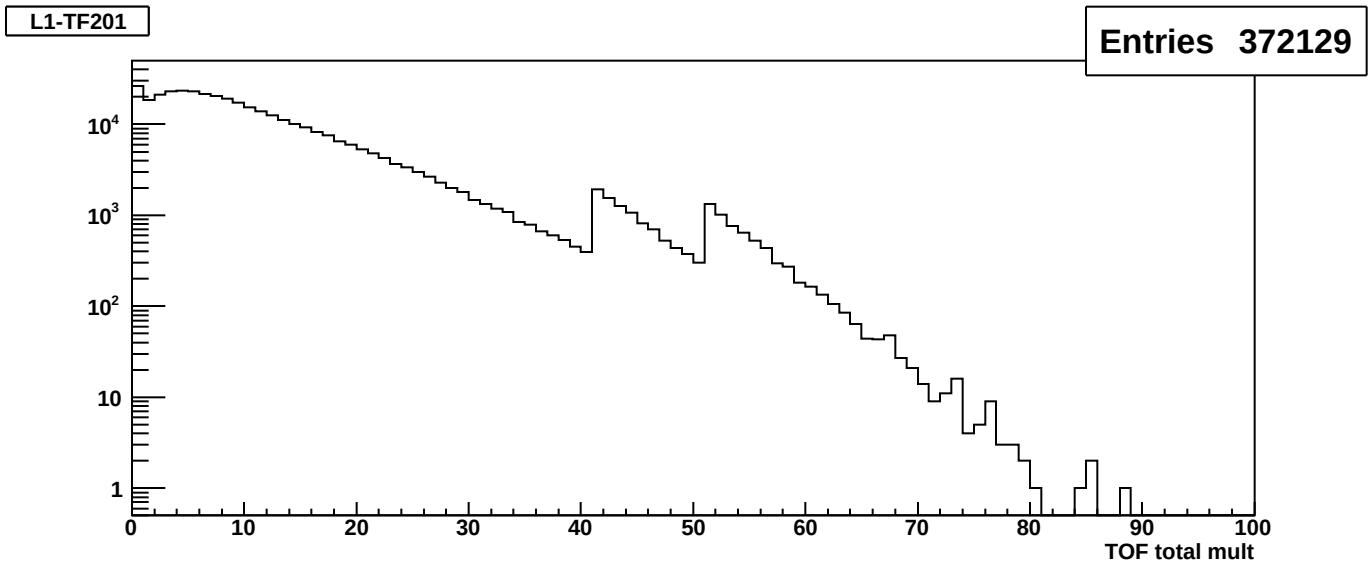

MIX-TF003
Entries 1.562942e+07
TOF MULT
TOF tray

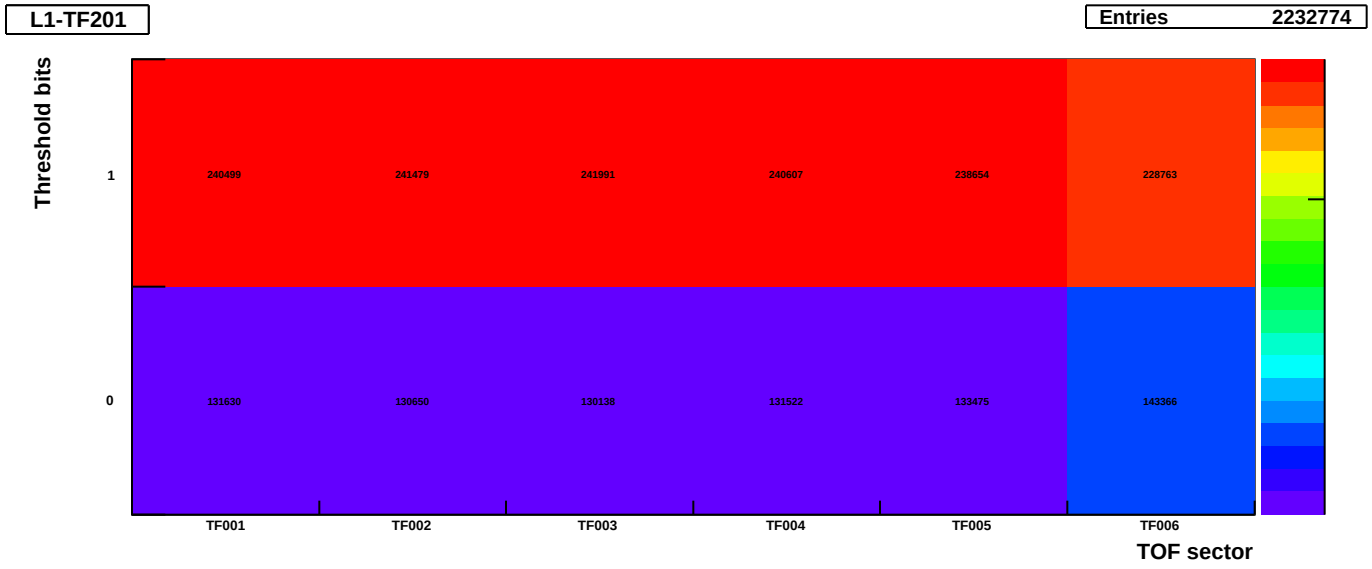

MIX-TF004
Entries 1.562942e+07
TOF MULT
TOF tray

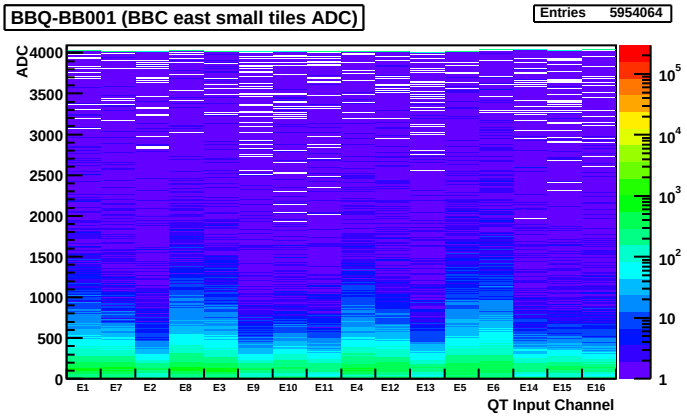

MIX-TF005
Entries 1.562942e+07
TOF MULT
TOF tray

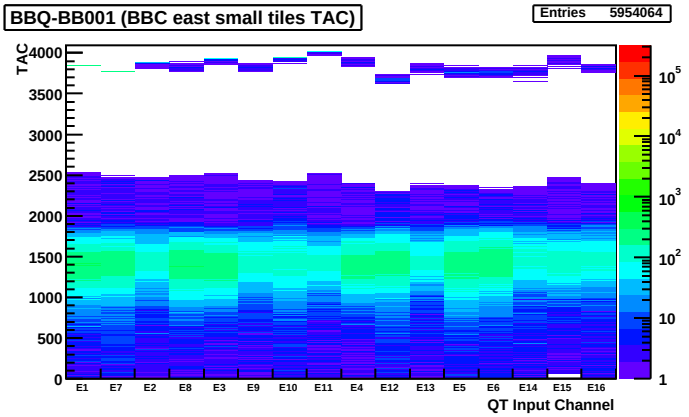

MIX-TF006
Entries 1.562942e+07
TOF MULT
TOF tray

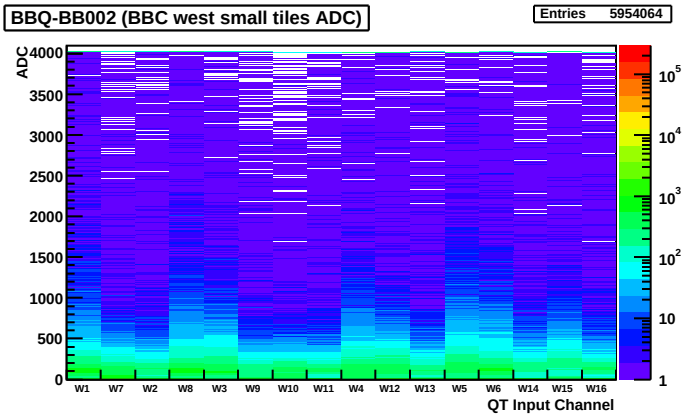

MIX-TF101
Entries 2232774
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

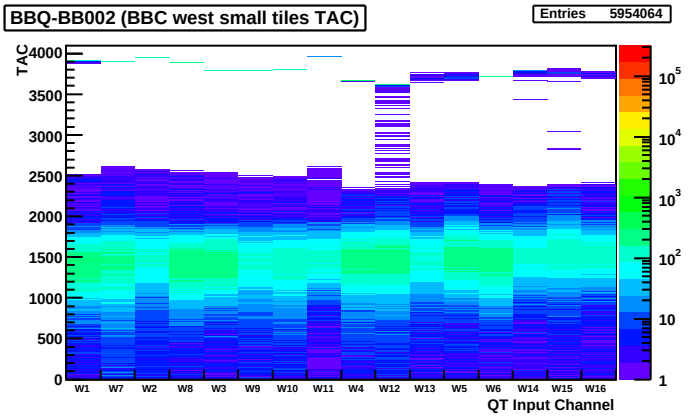

L1-TF201
Entries 372129
TOF total mult

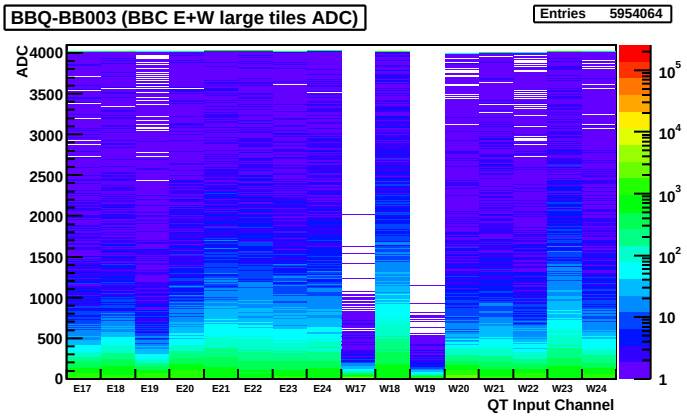

L1-TF201
Entries 2232774
Threshold bits
240499
241479
241991
240607
238654
228763
131630
130650
130138
131522
133475
143366
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

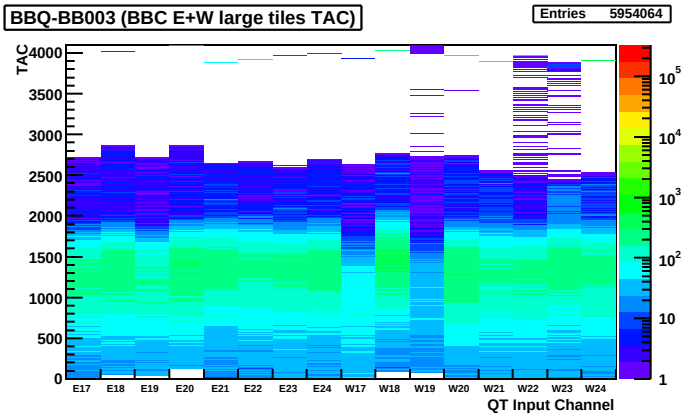

BBQ-BB001 (BBC east small tiles ADC)
Entries 5954064
ADC
QT Input Channel

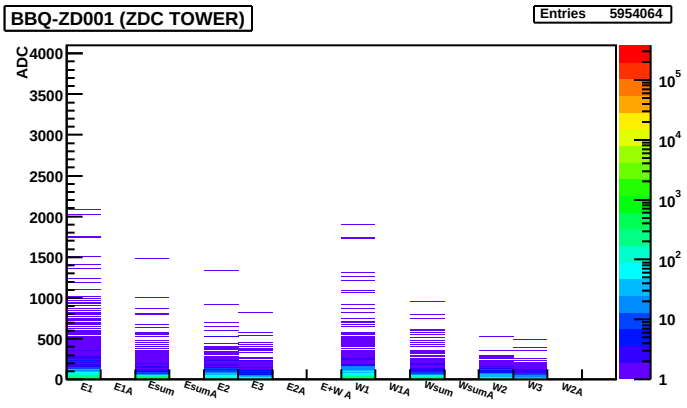

BBQ-BB001 (BBC east small tiles TAC)
Entries 5954064
TAC
QT Input Channel

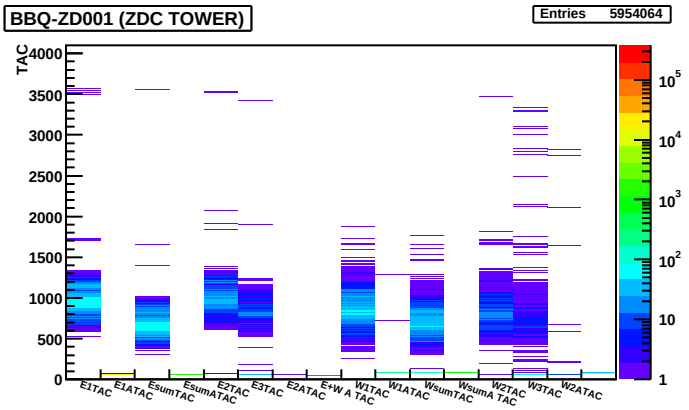

BBQ-BB002 (BBC west small tiles ADC)
Entries 5954064
ADC
QT Input Channel

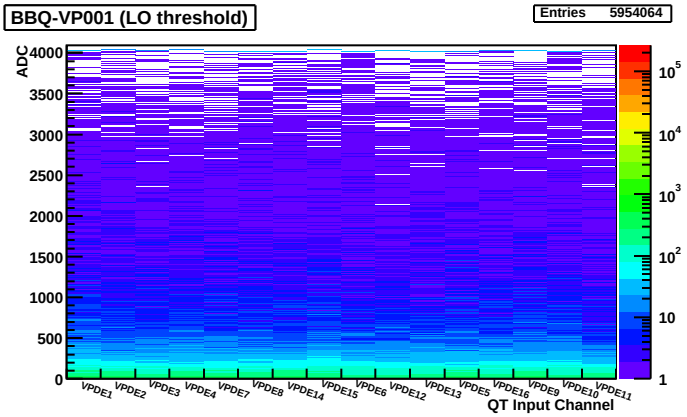

BBQ-BB002 (BBC west small tiles TAC)
Entries 5954064
TAC
QT Input Channel

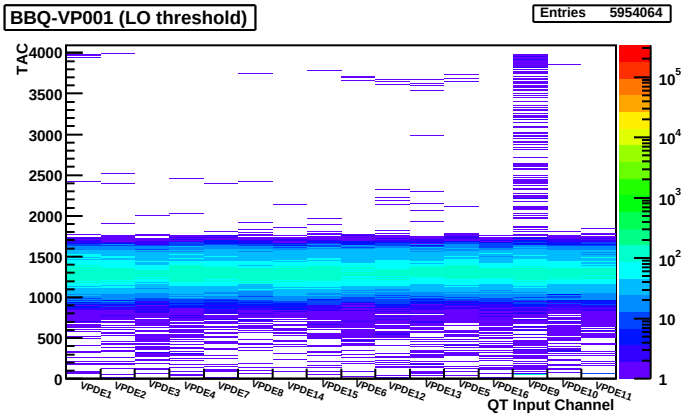

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5954064
ADC
QT Input Channel

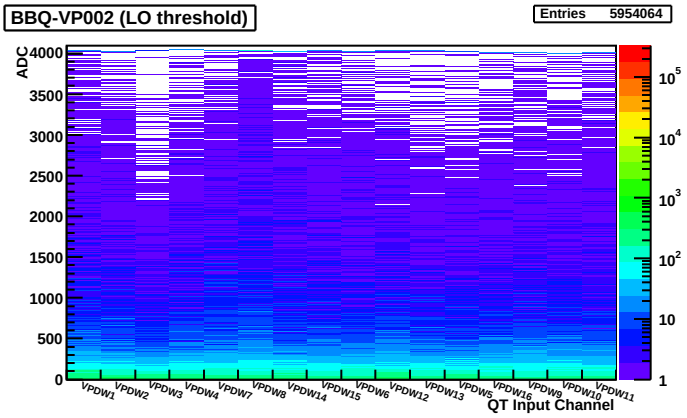

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5954064
TAC
QT Input Channel

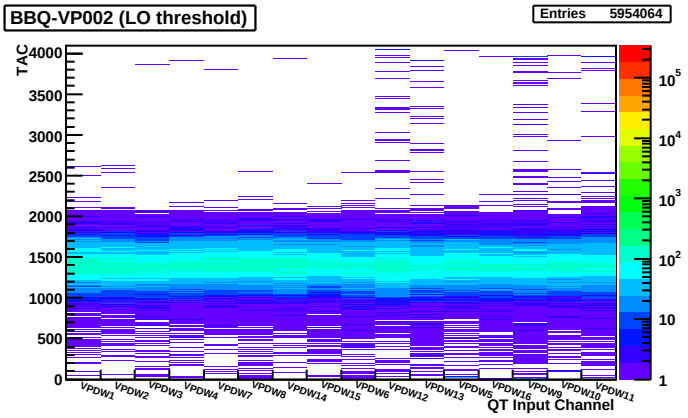

BBQ-ZD001 (ZDC TOWER)
Entries 5954064
ADC

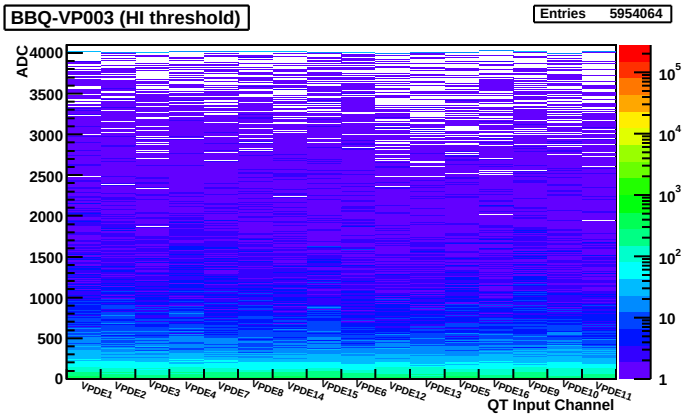

BBQ-ZD001 (ZDC TOWER)
Entries 5954064
TAC

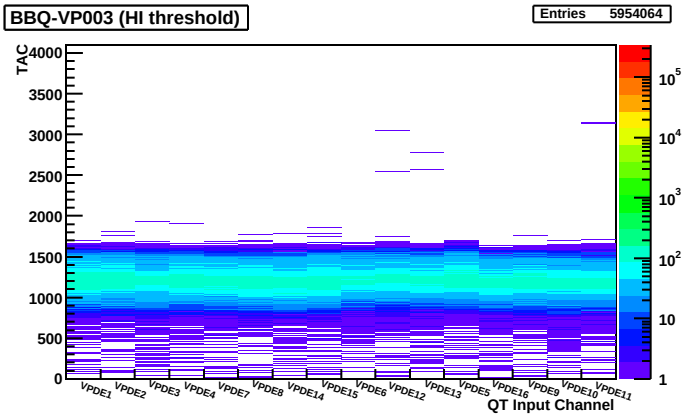

BBQ-VP001 (LO threshold)
Entries 5954064
ADC
QT Input Channel

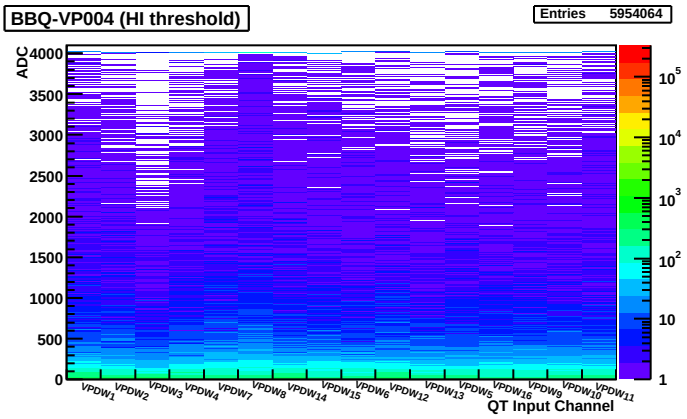

BBQ-VP001 (LO threshold)
Entries 5954064
TAC
QT Input Channel

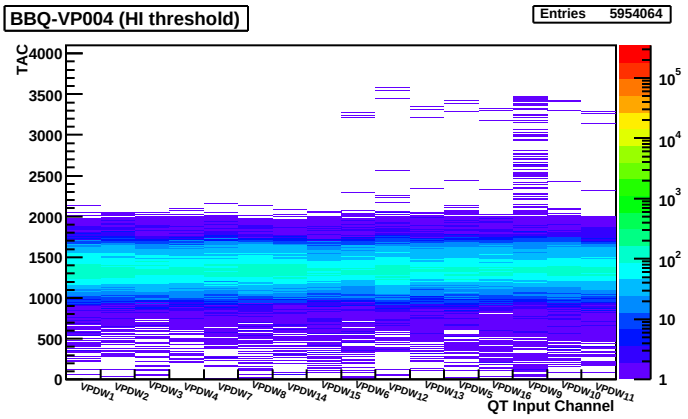

BBQ-VP002 (LO threshold)
Entries 5954064
ADC
QT Input Channel

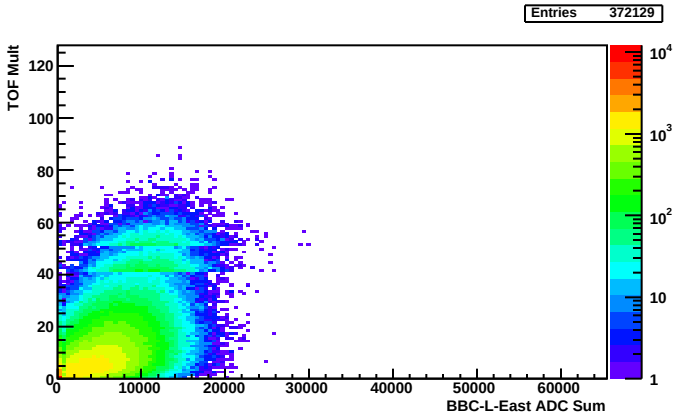

BBQ-VP002 (LO threshold)
Entries 5954064
TAC
QT Input Channel

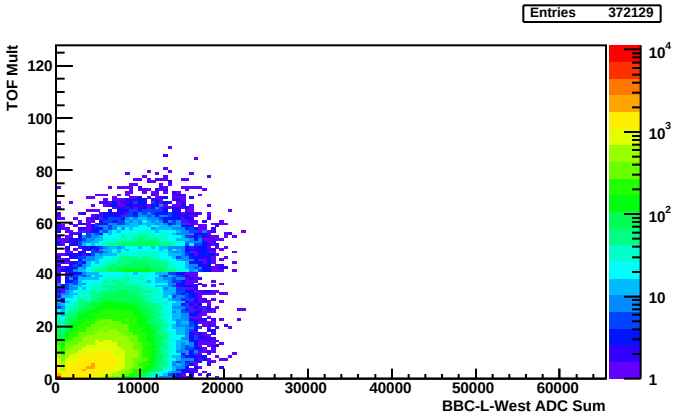

BBQ-VP003 (HI threshold)
Entries 5954064
ADC
QT Input Channel

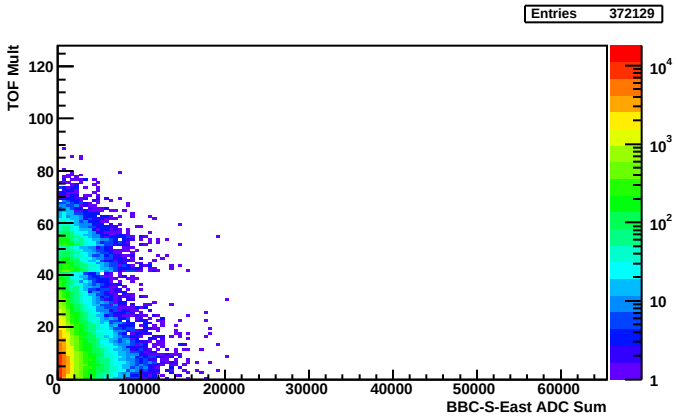

BBQ-VP003 (HI threshold)
Entries 5954064
TAC
QT Input Channel

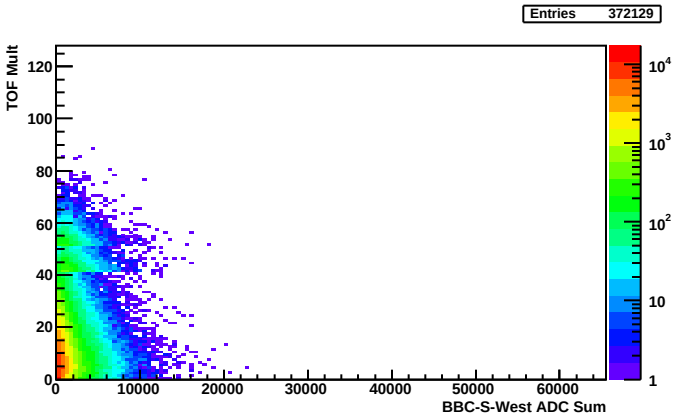

BBQ-VP004 (HI threshold)
Entries 5954064
ADC
QT Input Channel

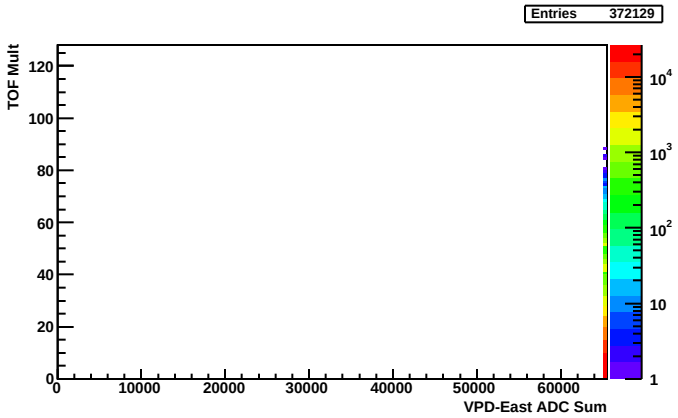

BBQ-VP004 (HI threshold)
Entries 5954064
TAC
QT Input Channel

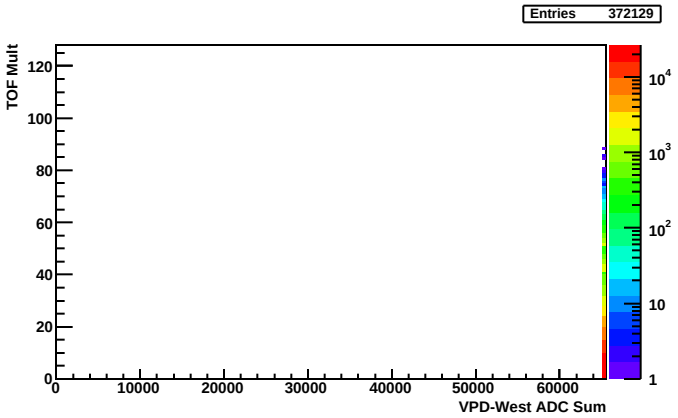

Entries 372129
TOF Mult
BBC-L-East ADC Sum

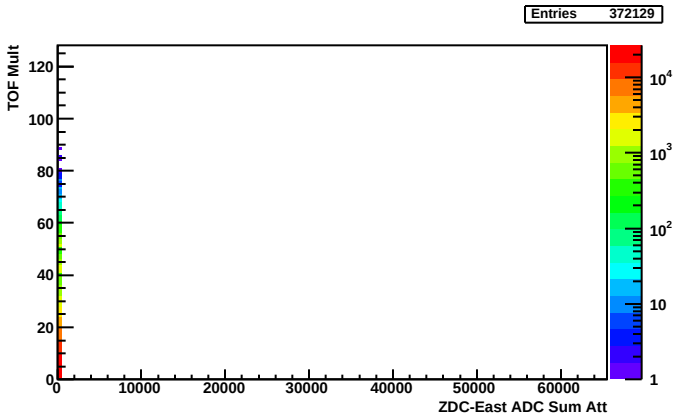

Entries 372129
TOF Mult
BBC-L-West ADC Sum

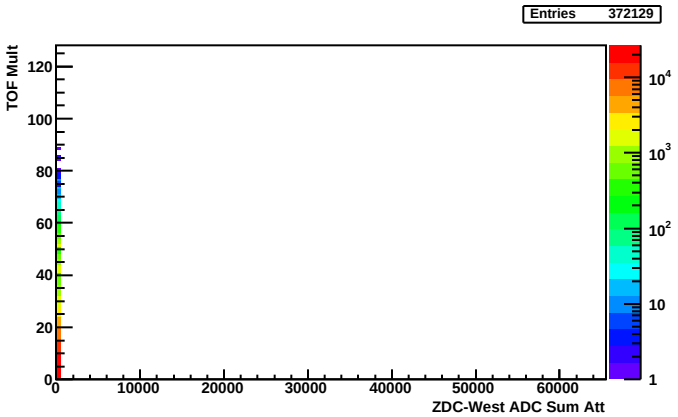

Entries 372129
TOF Mult
BBC-S-East ADC Sum

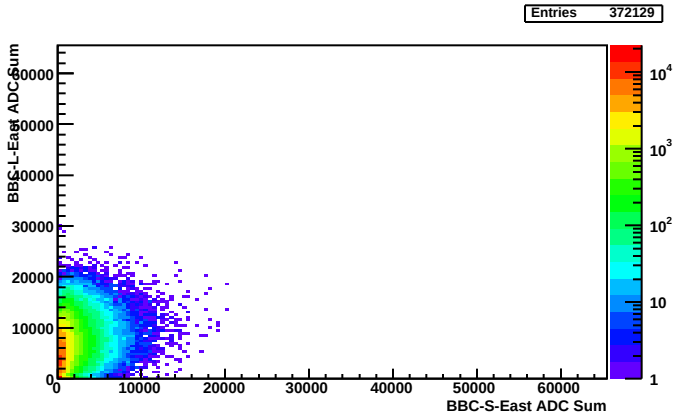

Entries 372129
TOF Mult
BBC-S-West ADC Sum

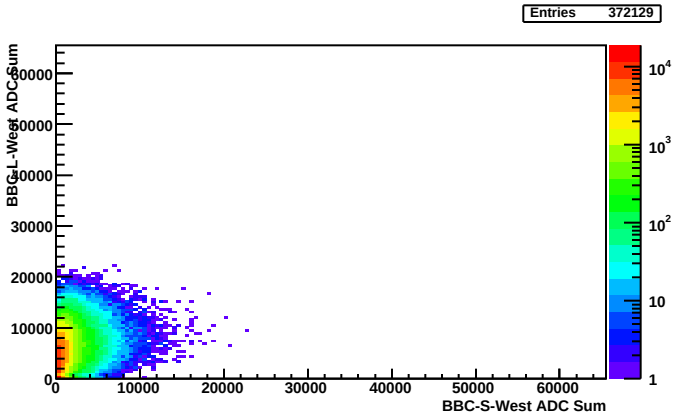

Entries 372129
TOF Mult
VPD-East ADC Sum

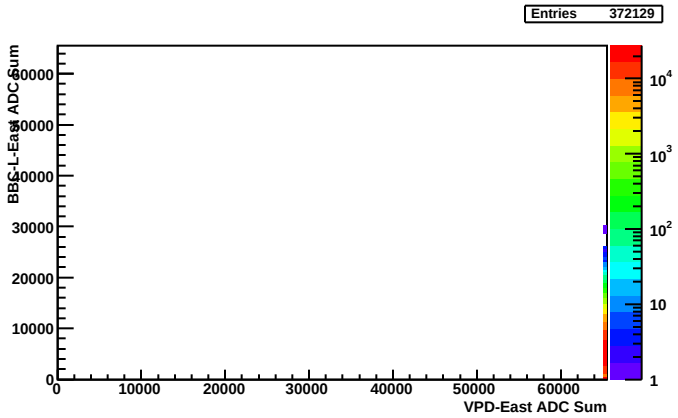

Entries 372129
TOF Mult
VPD-West ADC Sum

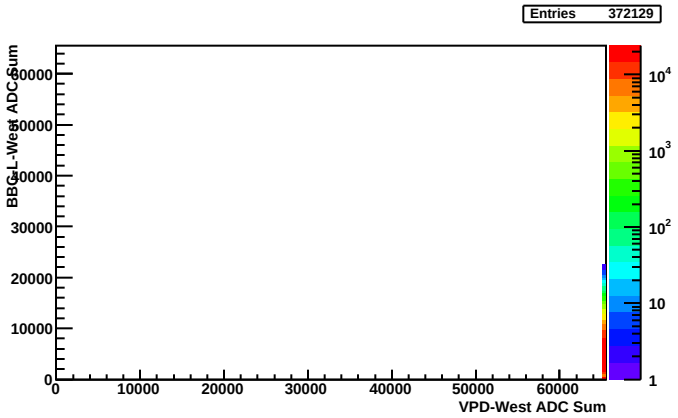

Entries 372129
TOF Mult
ZDC-East ADC Sum Att


Entries 372129
TOF Mult
ZDC-West ADC Sum Att


Entries 372129
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 372129
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 372129
BBC-L-East ADC Sum
VPD-East ADC Sum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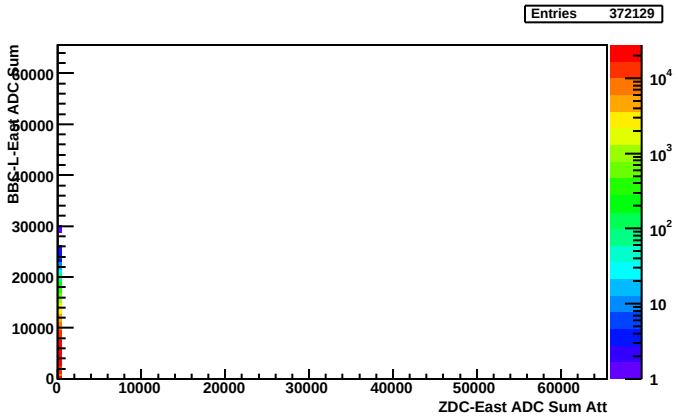
BBC-L-West ADC Sum
VPD-West ADC Sum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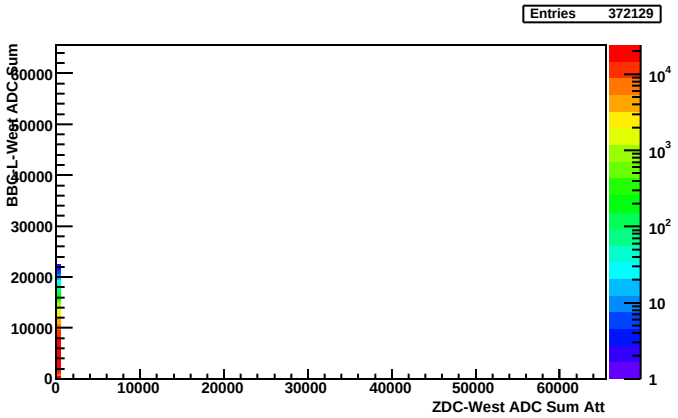
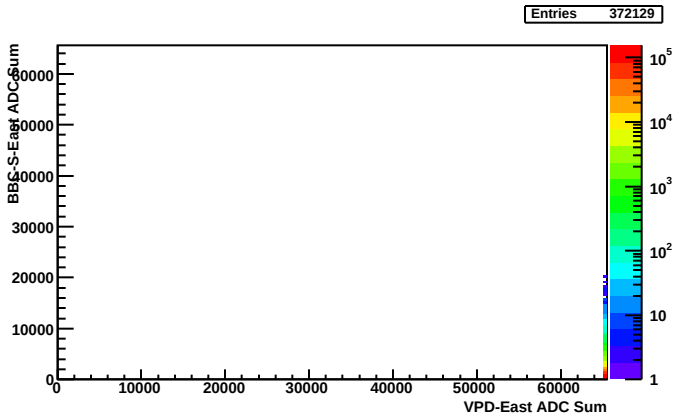
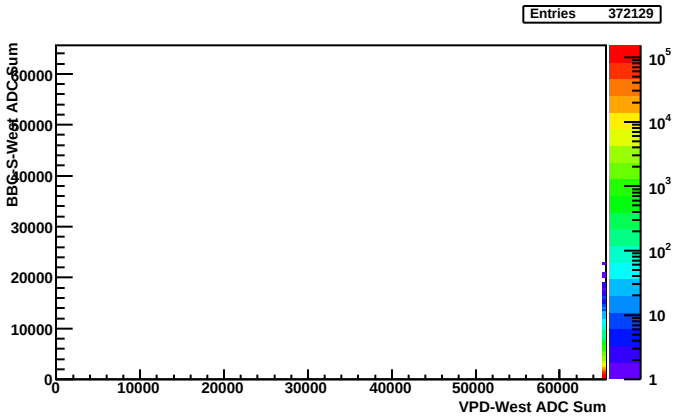
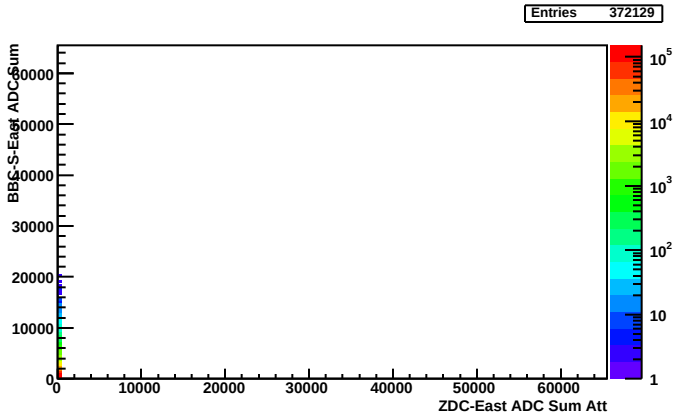
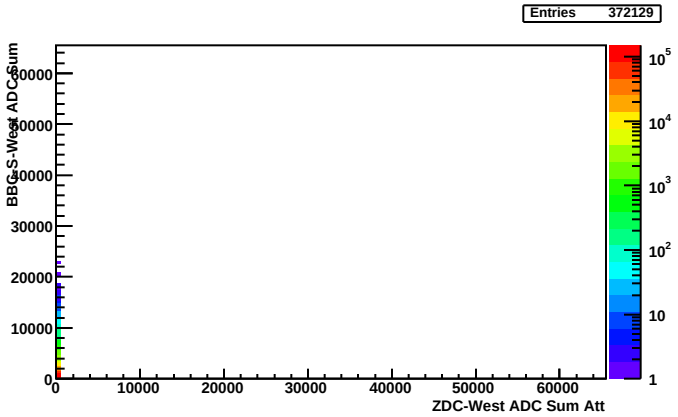
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

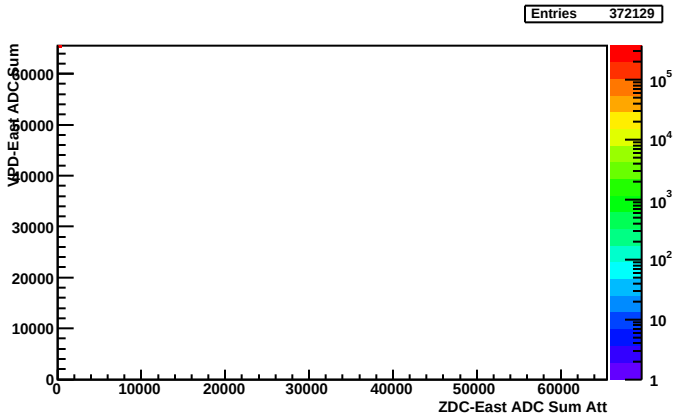

Entries 372129
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

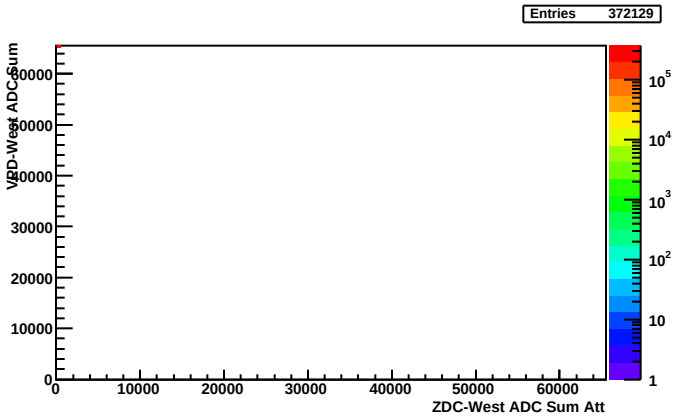

Entries 372129
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 372129
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 372129
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 372129
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 372129
VPD-East ADC Sum
ZDC-East ADC Sum Att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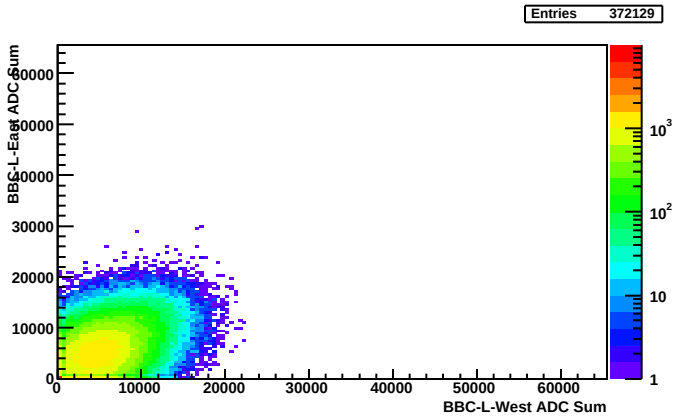
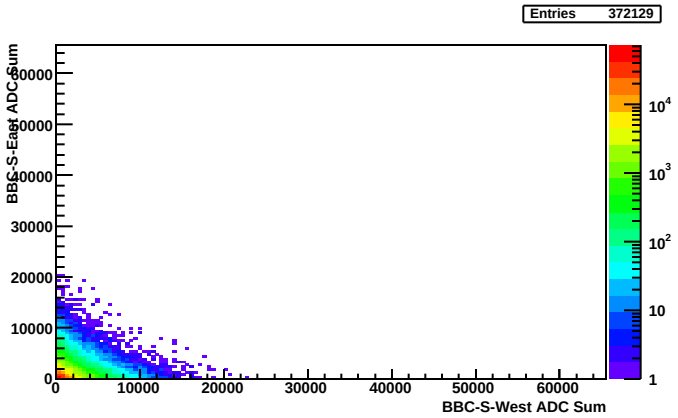
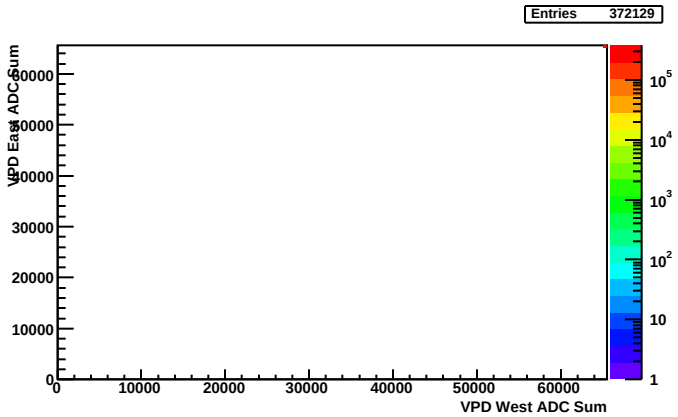
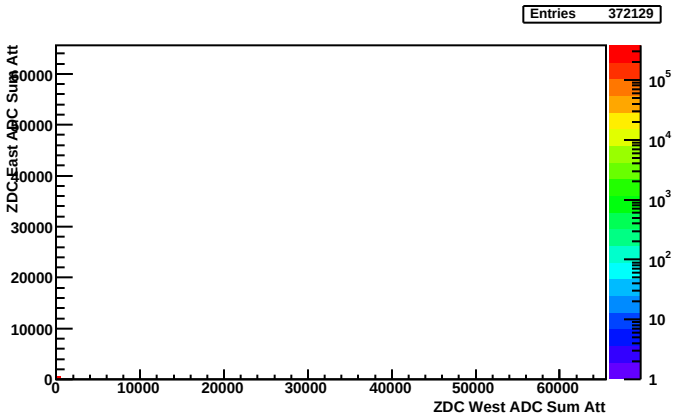
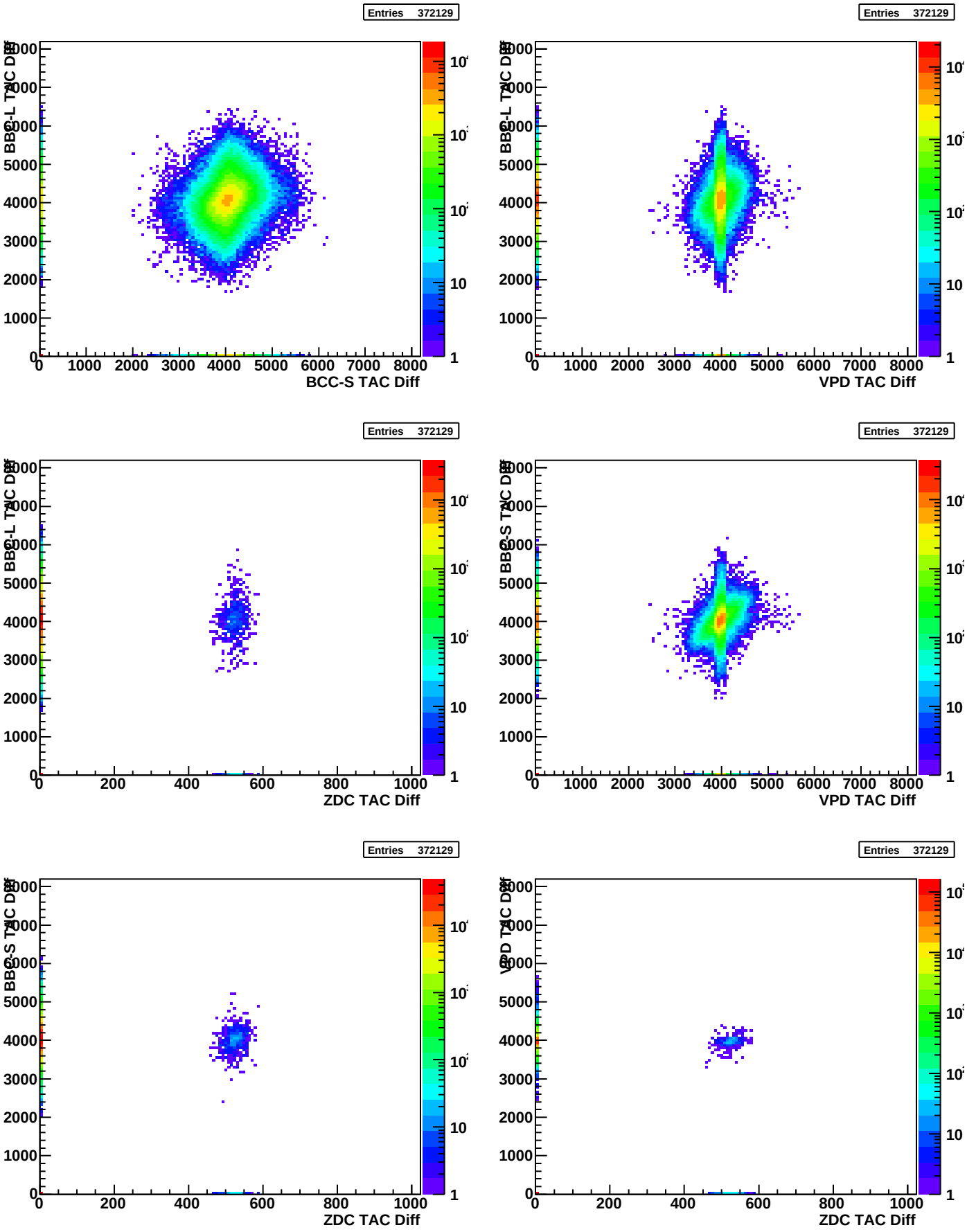
VPD-West ADC Sum
ZDC-West ADC Sum Att

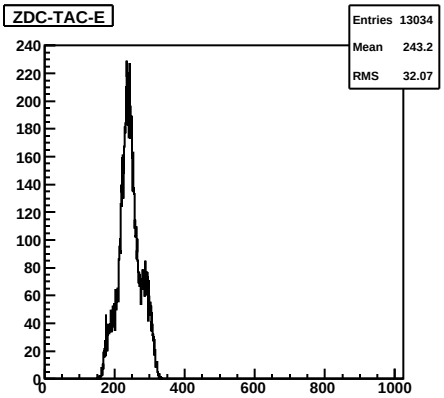

Entries 372129
BBC-L-East ADC Sum
BBC-L-West ADC Sum

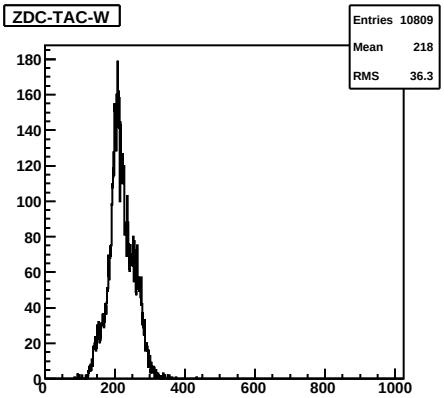

Entries 372129
BBC-S-East ADC Sum
BBC-S-West ADC Sum

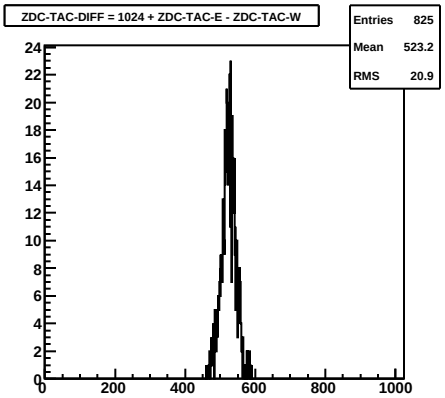

Entries 372129
VPD East ADC Sum
VPD West ADC Sum

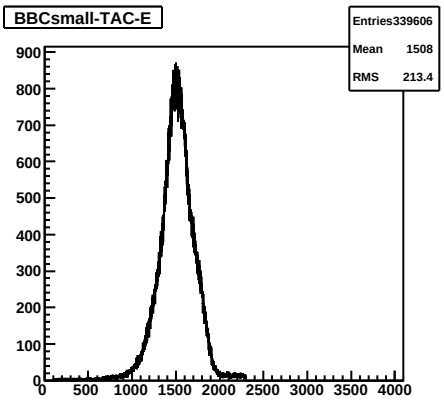

Entries 372129
ZDC East ADC Sum Att
ZDC West ADC Sum Att

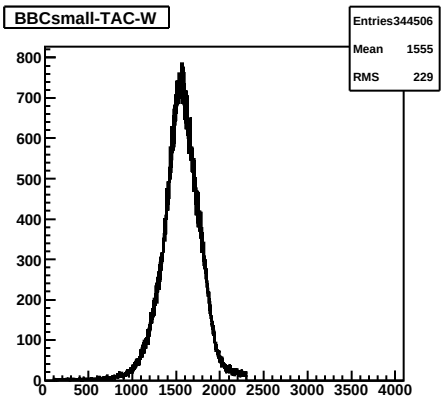

Entries 372129
BBC-L TAC Diff
BCC-S TAC Diff
Entries 372129
BBC-L TAC Diff
VPD TAC Diff
Entries 372129
BBC-L TAC Diff
ZDC TAC Diff
Entries 372129
BBC-S TAC Diff
VPD TAC Diff
Entries 372129
BBC-S TAC Diff
ZDC TAC Diff
Entries 372129
VPD TAC Diff
ZDC TAC Diff

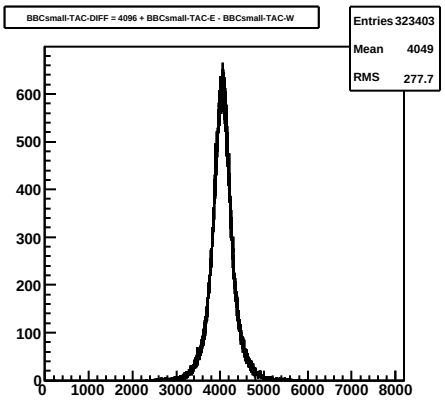

ZDC-TAC-E
Entries 13034
Mean 243.2
RMS 32.07

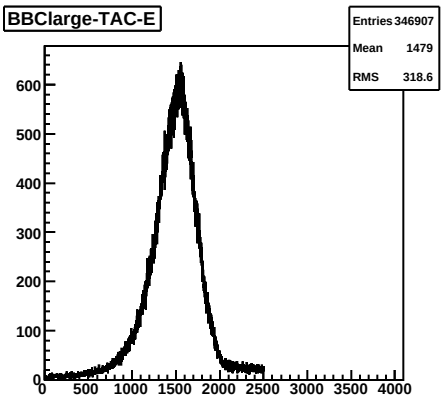

ZDC-TAC-W
Entries 10809
Mean 218
RMS 36.3

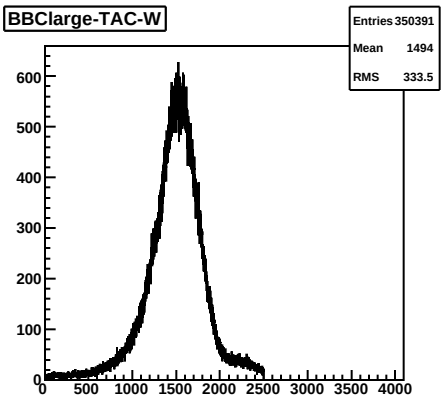

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 825
Mean 523.2
RMS 20.9

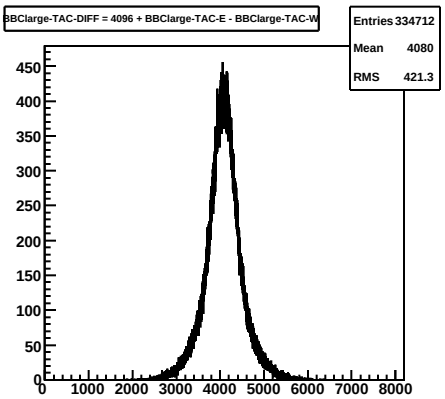

BBCsmall-TAC-E
Entries 339606
Mean 1508
RMS 213.4

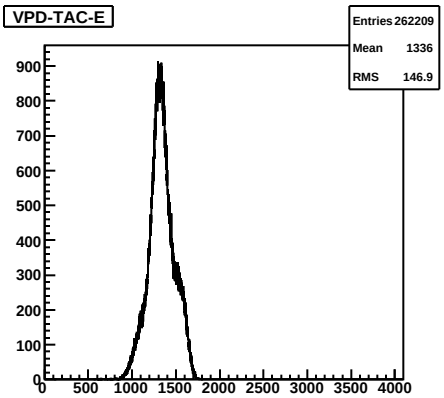

BBCsmall-TAC-W
Entries 344506
Mean 1555
RMS 229

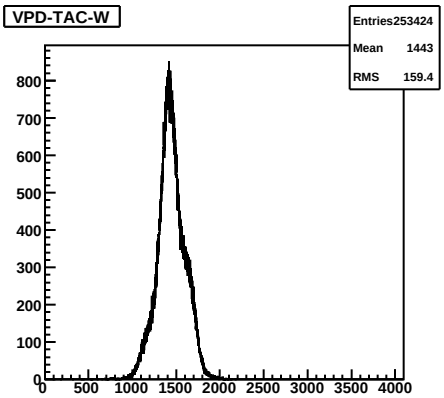

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 323403
Mean 4049
RMS 277.7

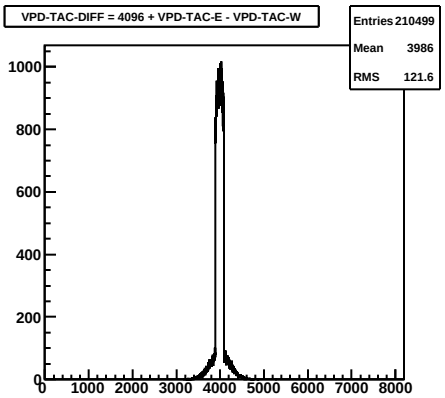

BBClarge-TAC-E
Entries 346907
Mean 1479
RMS 318.6

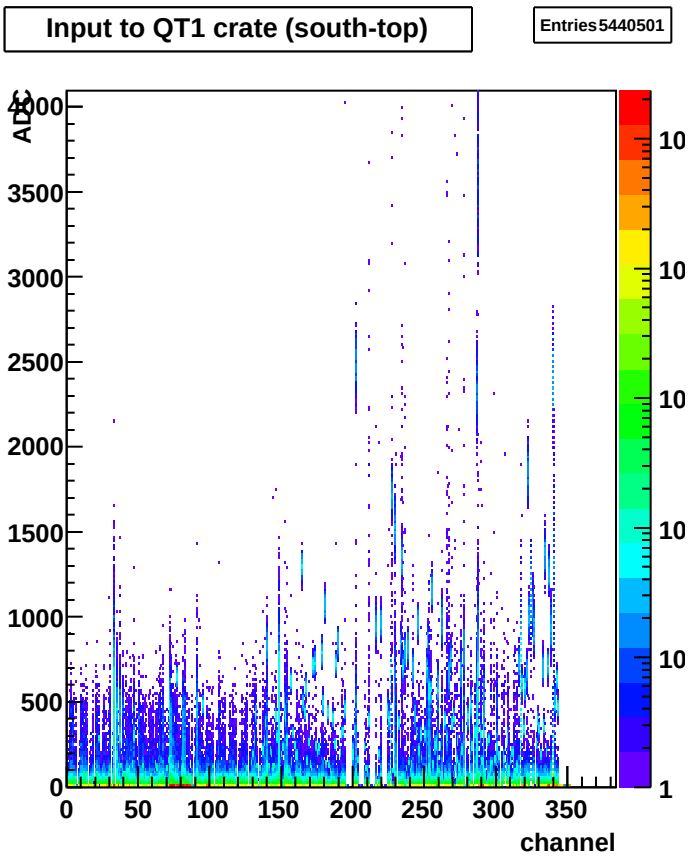

BBClarge-TAC-W
Entries 350391
Mean 1494
RMS 333.5

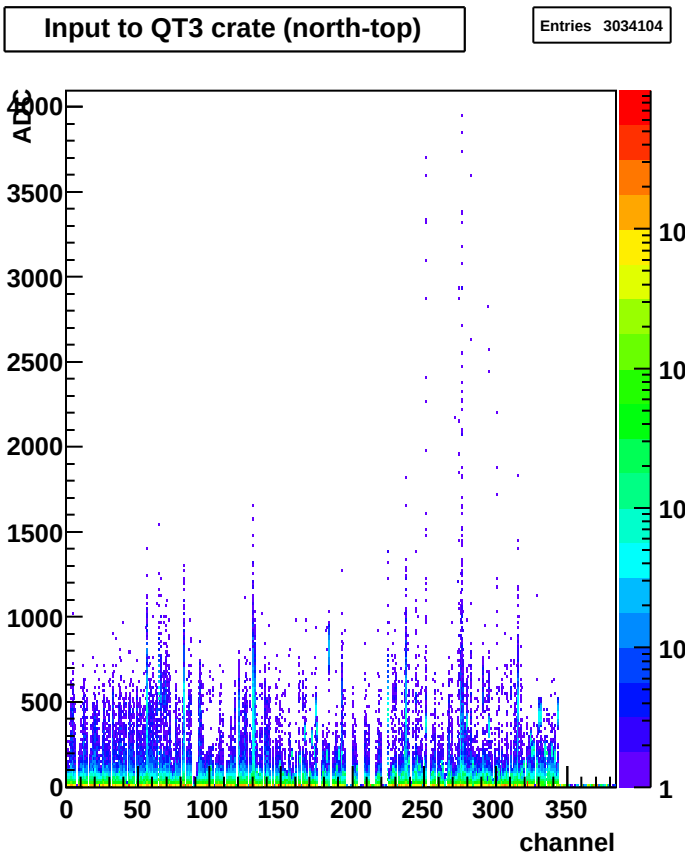

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 334712
Mean 4080
RMS 421.3

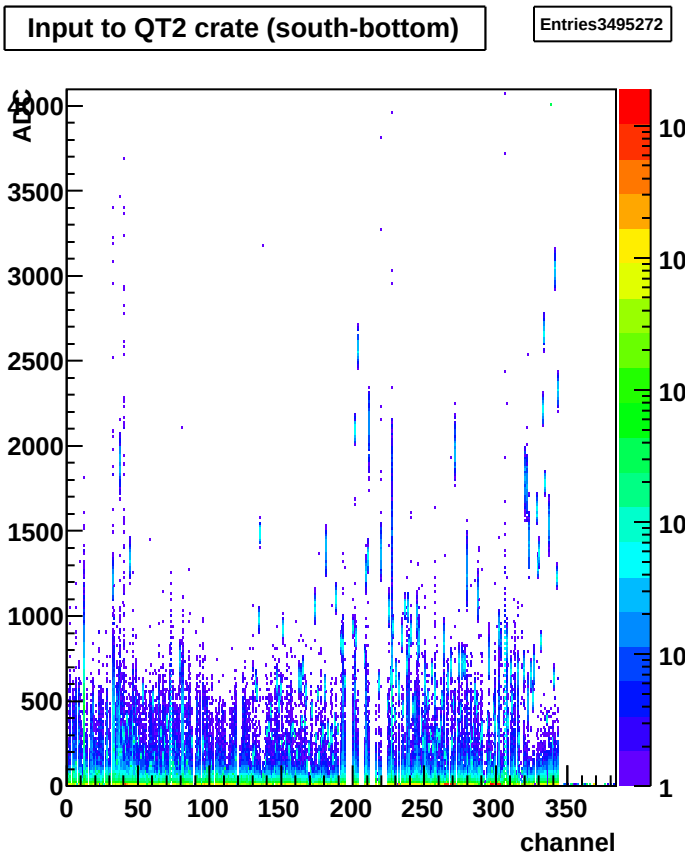

VPD-TAC-E
Entries 262209
Mean 1336
RMS 146.9

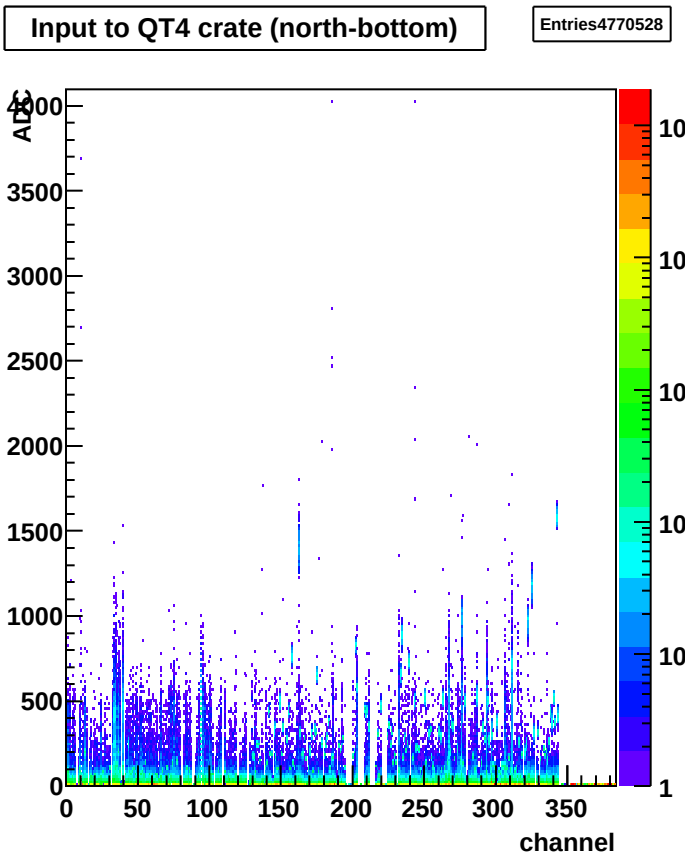

VPD-TAC-W
Entries 253424
Mean 1443
RMS 159.4

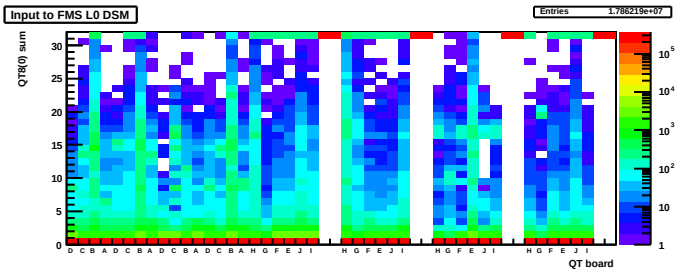

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 210499
Mean 3986
RMS 121.6

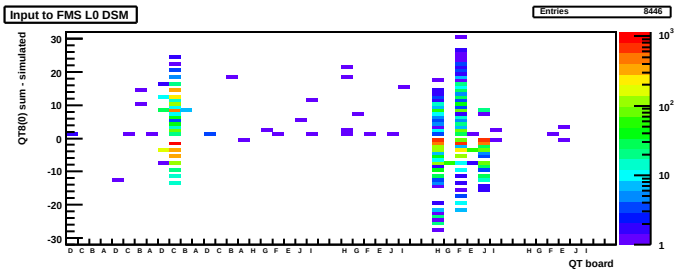

Input to QT1 crate (south-top)
Entries 5440501
ADC
channel

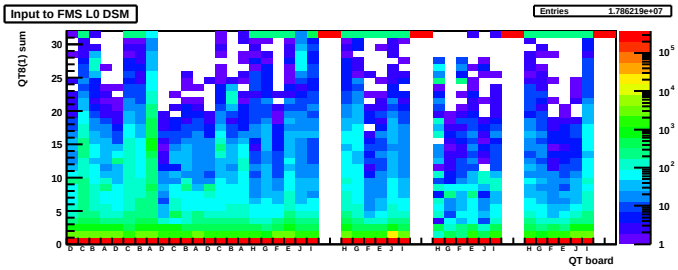

Input to QT3 crate (north-top)
Entries 3034104
ADC
channel

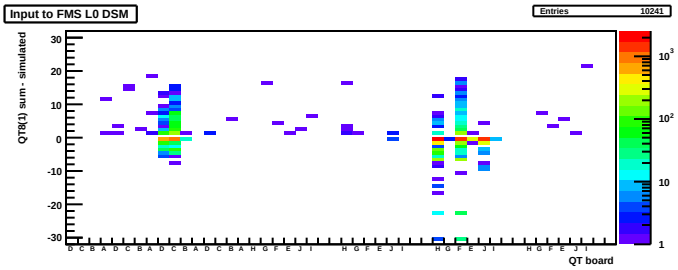

Input to QT2 crate (south-bottom)
Entries3495272
ADC
channel

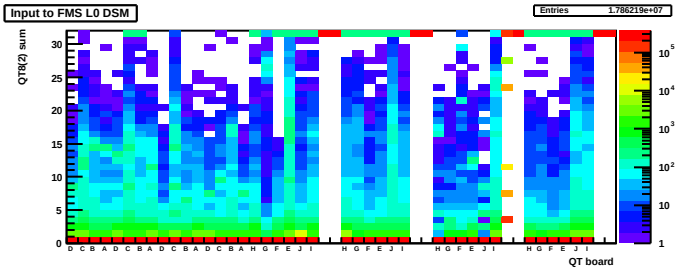

Input to QT4 crate (north-bottom)
Entries4770528
ADC
channel

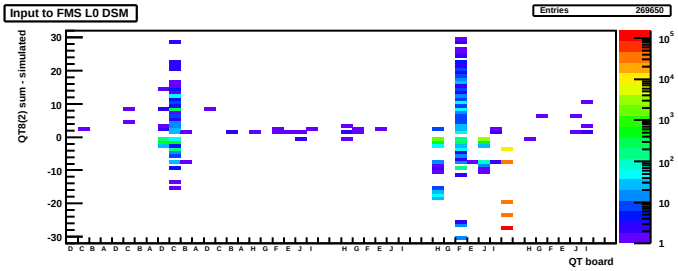

Input to FMS L0 DSM
Entries 1.786219e+07
QT8(0) sum
QT board

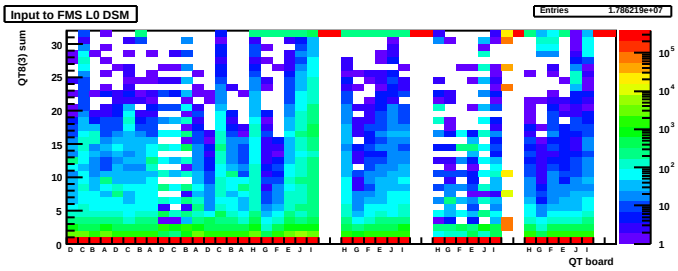

Input to FMS L0 DSM
Entries 8446
QT8(0) sum - simulated
QT board

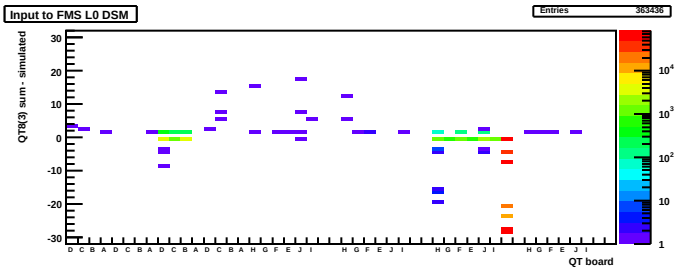

Input to FMS L0 DSM
Entries 1.786219e+07
QT8(1) sum
QT board

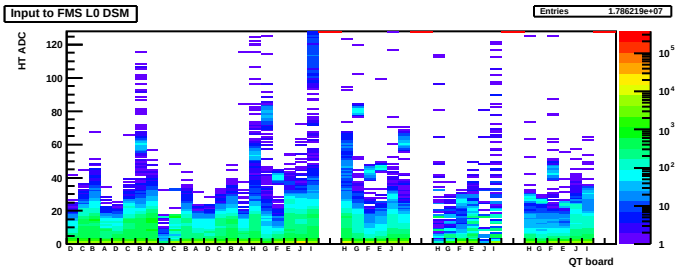

Input to FMS L0 DSM
Entries 10241
QT8(1) sum - simulated
QT board

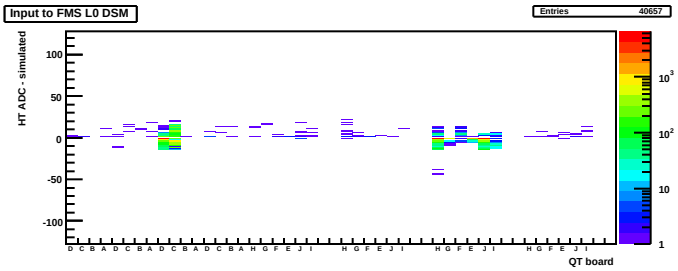

Input to FMS L0 DSM
Entries 1.786219e+07
QT8(2) sum
QT board

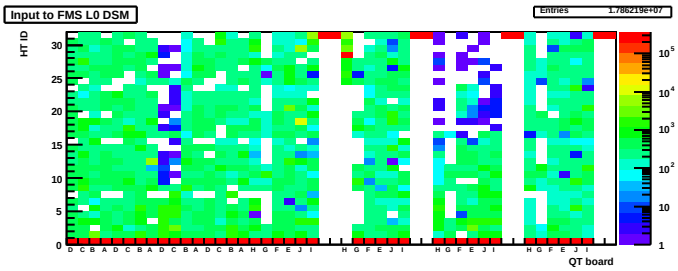

Input to FMS L0 DSM
Entries 269650
QT8(2) sum - simulated
QT board

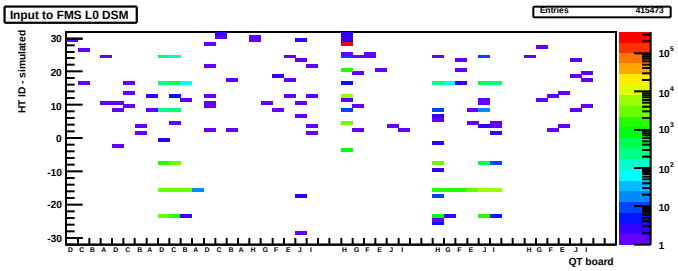

Input to FMS L0 DSM
Entries 1.786219e+07
QT8(3) sum
QT board

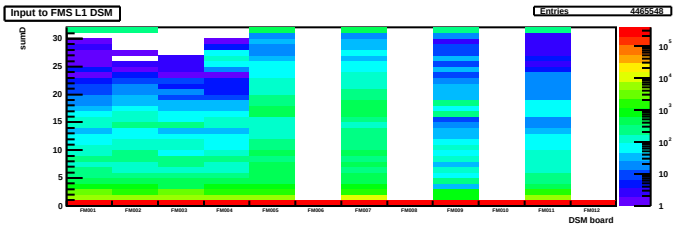

Input to FMS L0 DSM
Entries 363436
QT8(3) sum - simulated
QT board

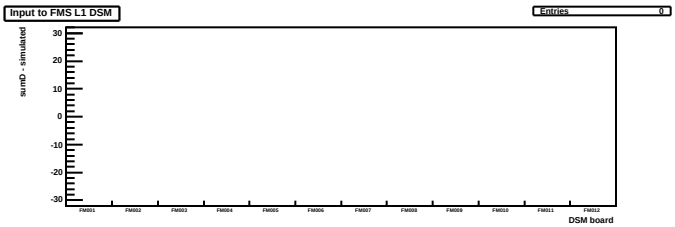

Input to FMS L0 DSM
Entries 1.786219e+07
HT ADC
QT board

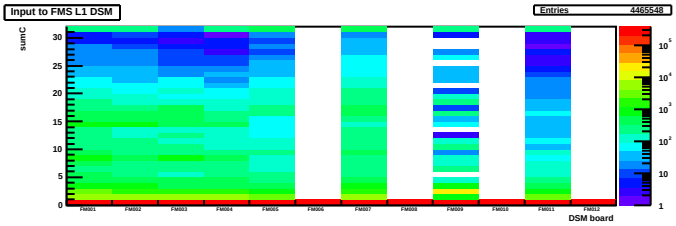

Input to FMS L0 DSM
Entries 40657
HT ADC - simulated
QT board

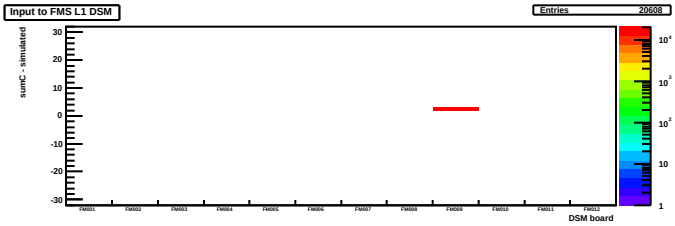

Input to FMS L0 DSM
Entries 1.786219e+07
HT ID
QT board

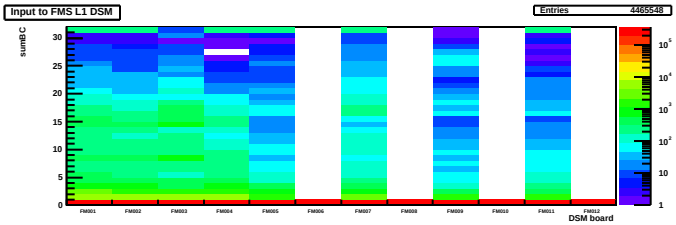

Input to FMS L0 DSM
Entries 415473
HT ID - simulated
QT board

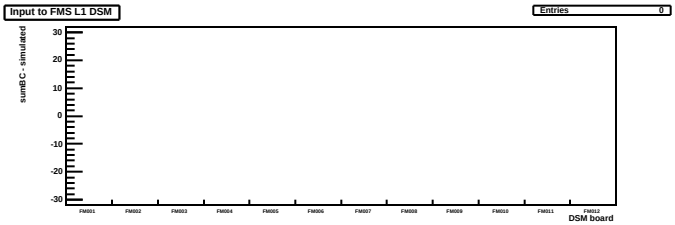

Input to FMS L1 DSM
Entries 4465548
sumD
DSM board

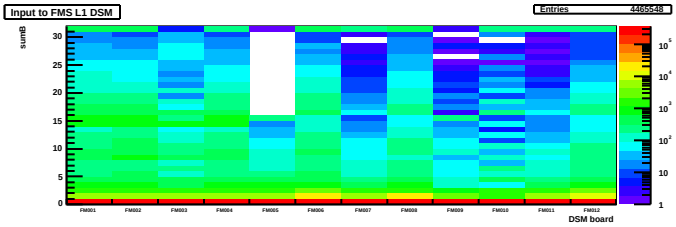

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

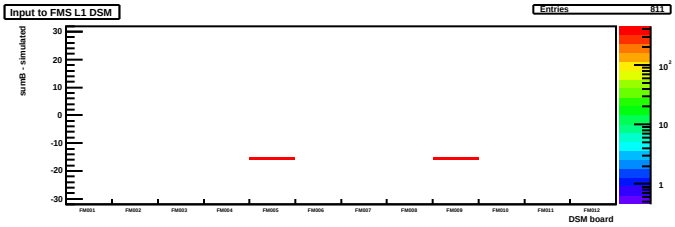

Input to FMS L1 DSM
Entries 4465548
sumC
DSM board

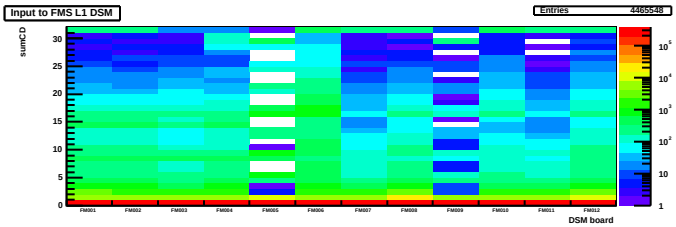

Input to FMS L1 DSM
Entries 20608
sumC - simulated
DSM board

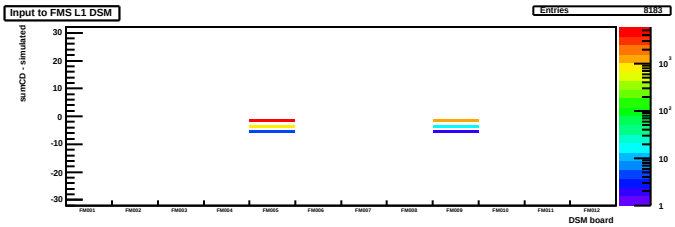

Input to FMS L1 DSM
Entries 4465548
sumBC
DSM board

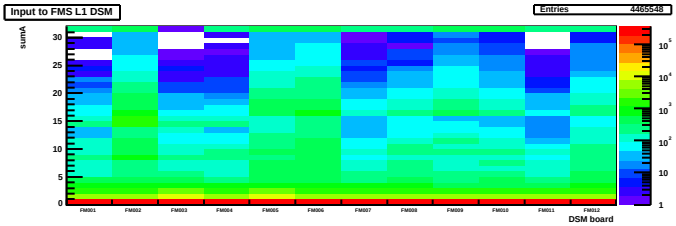

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

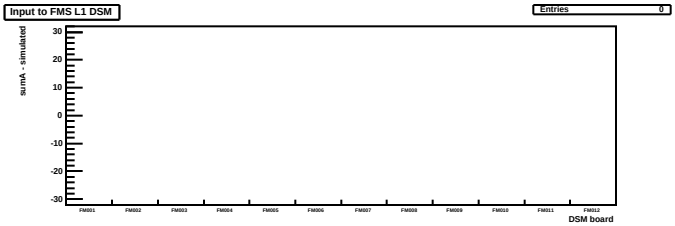

Input to FMS L1 DSM
Entries 4465548
sumB
DSM board

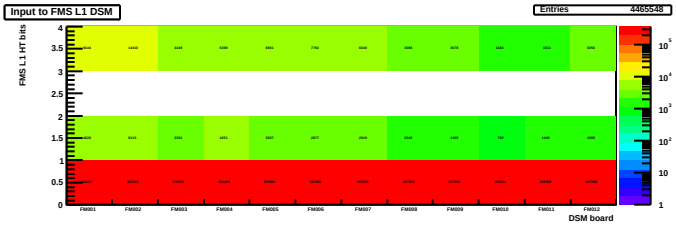

Input to FMS L1 DSM
Entries 811
sumB - simulated
DSM board

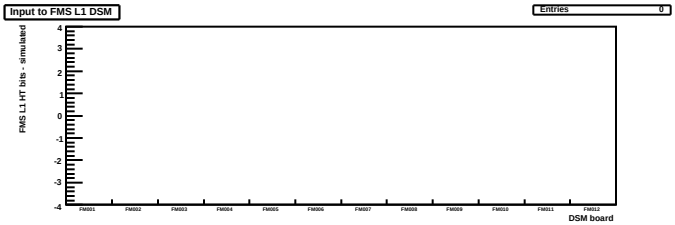

Input to FMS L1 DSM
Entries 4465548
sumCD
DSM board

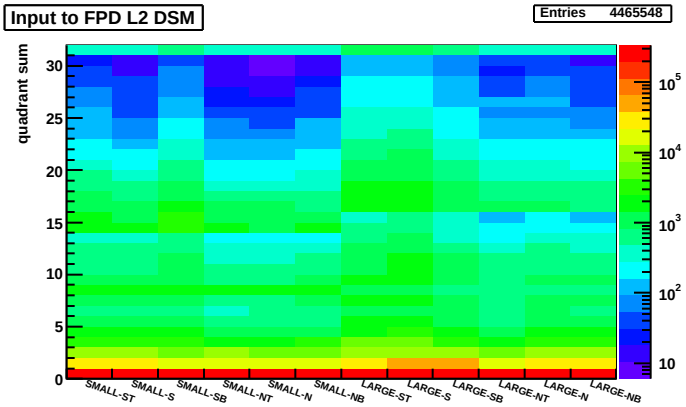

Input to FMS L1 DSM
Entries 8183
sumCD - simulated
DSM board

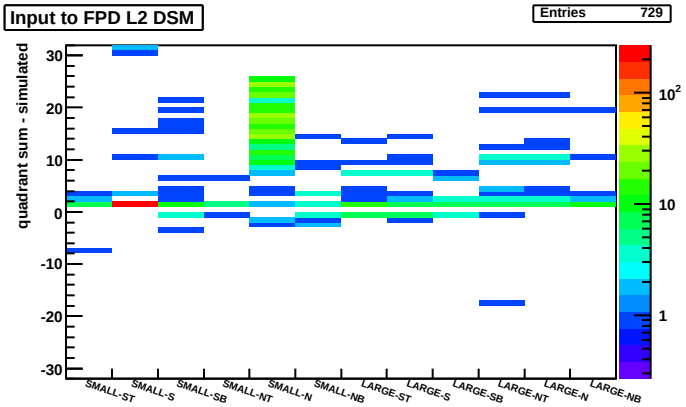

Input to FMS L1 DSM
Entries 4465548
sumA
DSM board

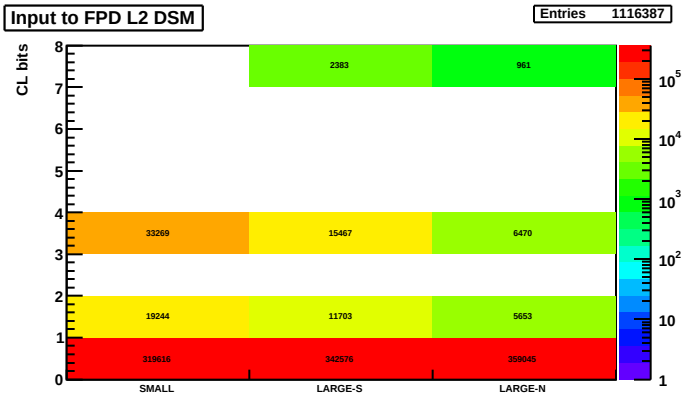

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

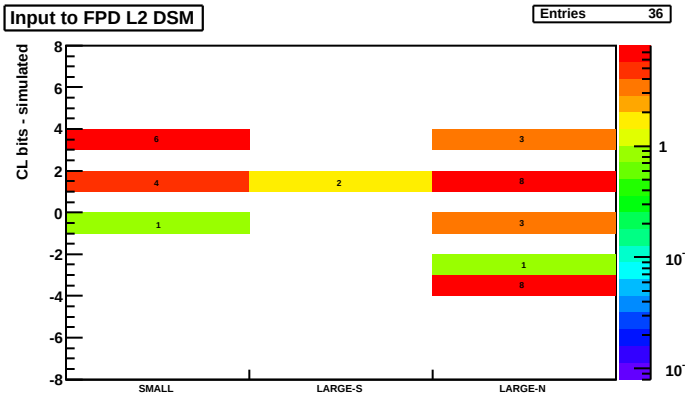

Input to FMS L1 DSM
Entries 4465548
FMS L1 HT bits
DSM board

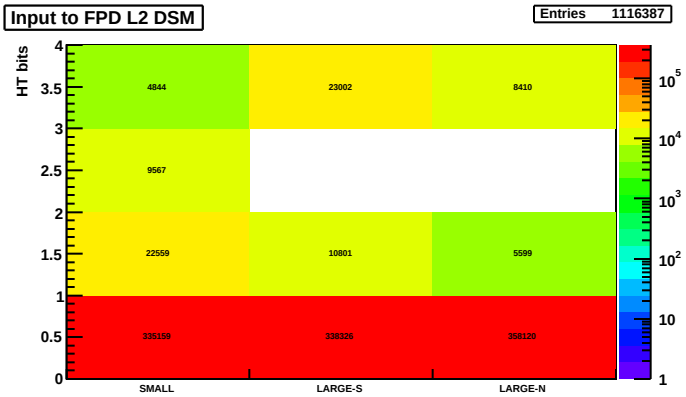

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

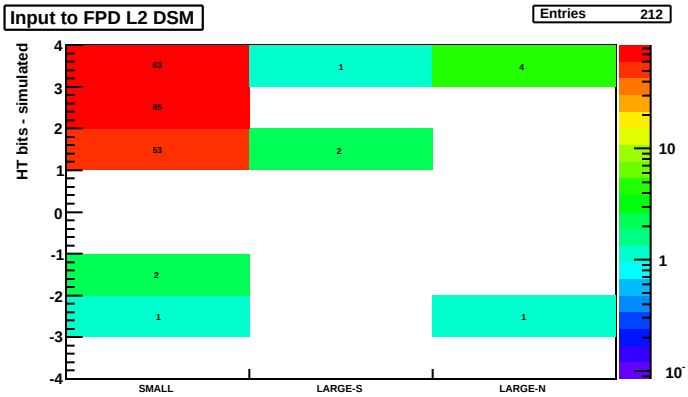

Input to FPD L2 DSM
Entries 4465548
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

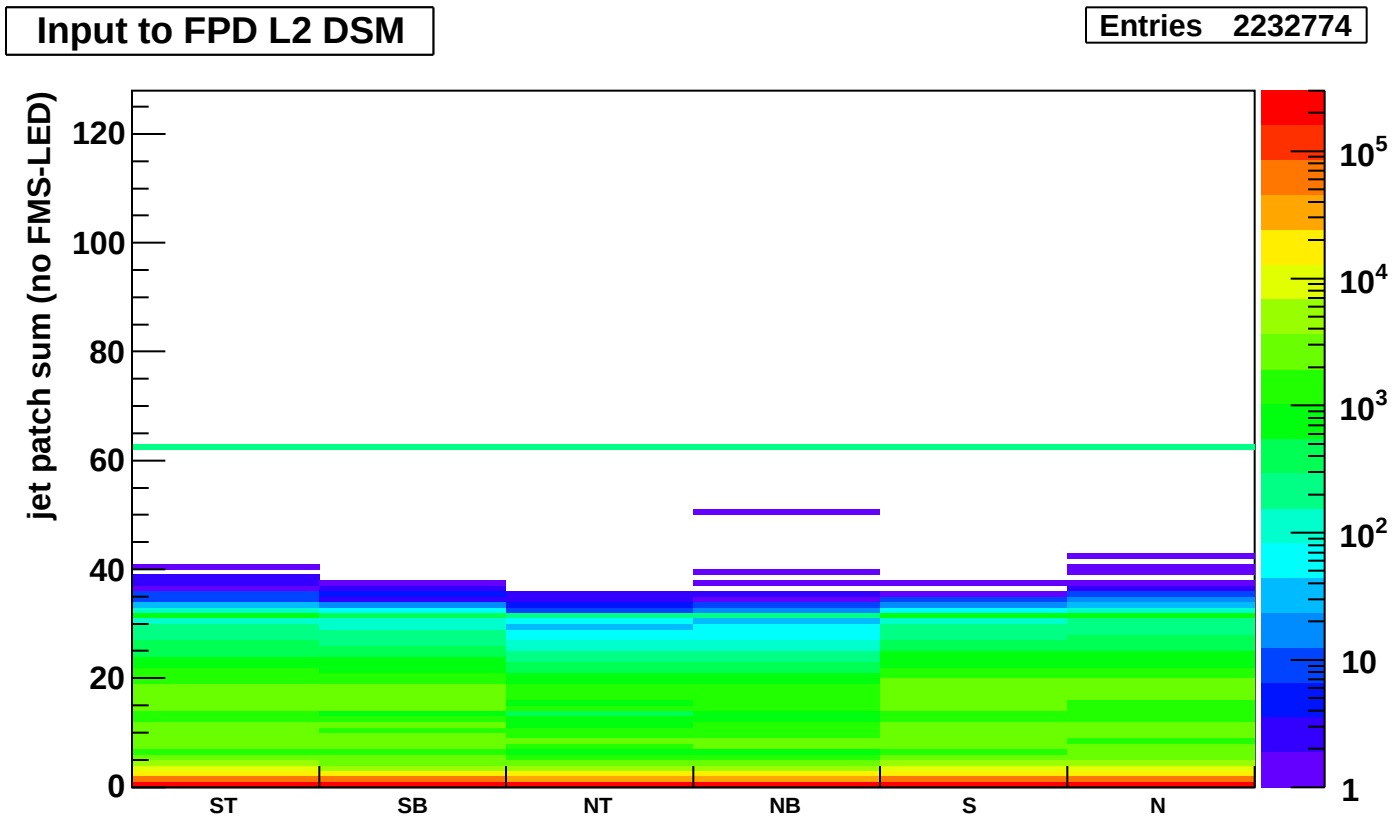

Input to FPD L2 DSM
Entries 729
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

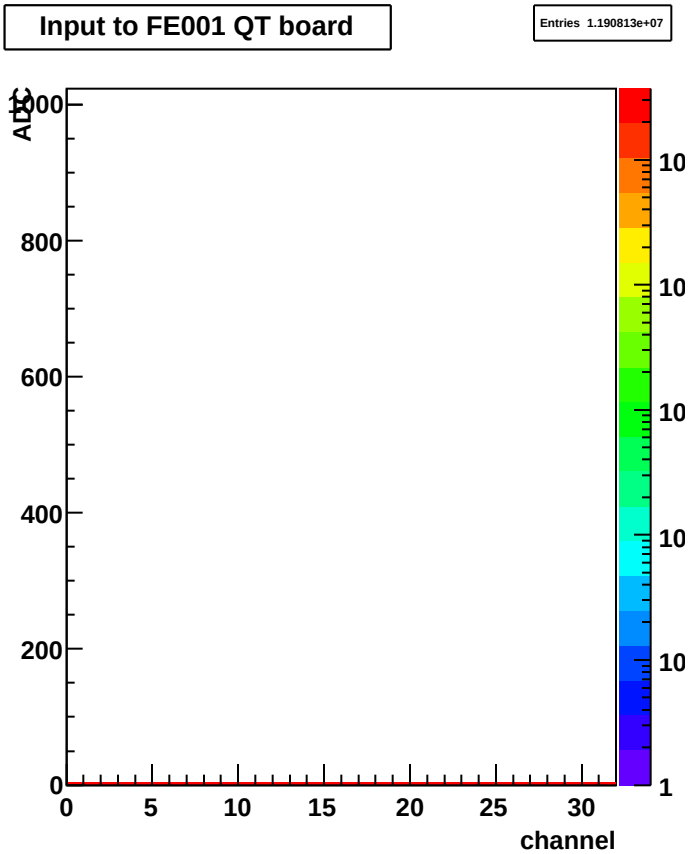

Input to FPD L2 DSM
Entries 1116387
CL bits
2383
961
33269
15467
6470
19244
11703
5653
319616
342576
359045
SMALL
LARGE-S
LARGE-N

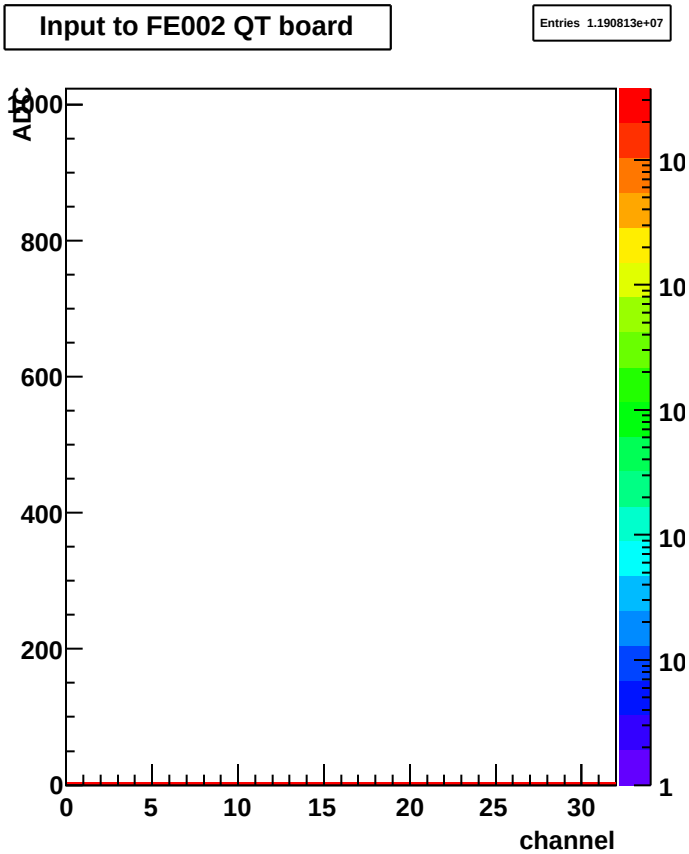

Input to FPD L2 DSM
Entries 36
CL bits - simulated
6
3
4
2
8
1
3
1
8
SMALL
LARGE-S
LARGE-N

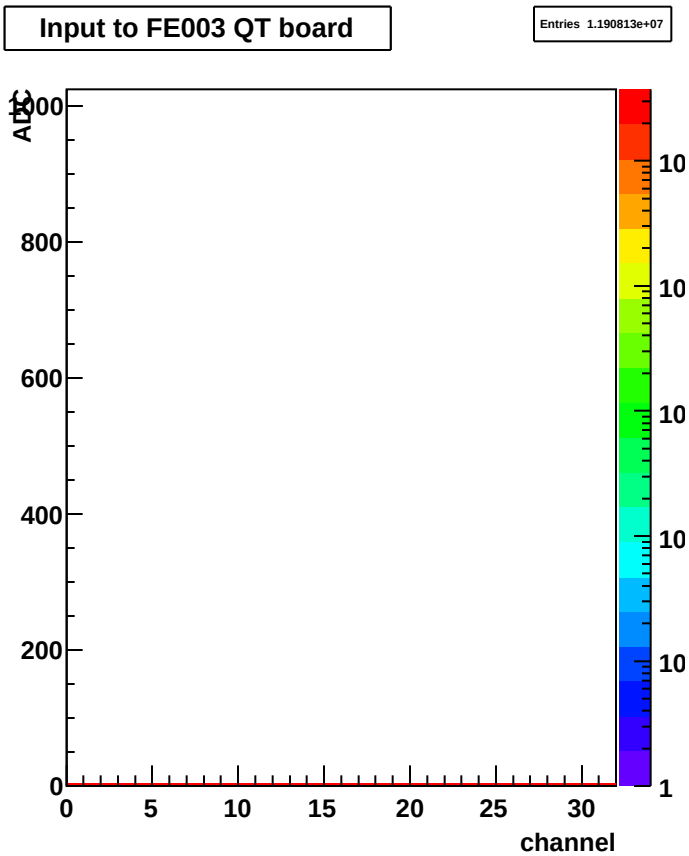

Input to FPD L2 DSM
Entries 1116387
HT bits
4844
23002
8410
9567
22559
10801
5599
335159
338326
358120
SMALL
LARGE-S
LARGE-N

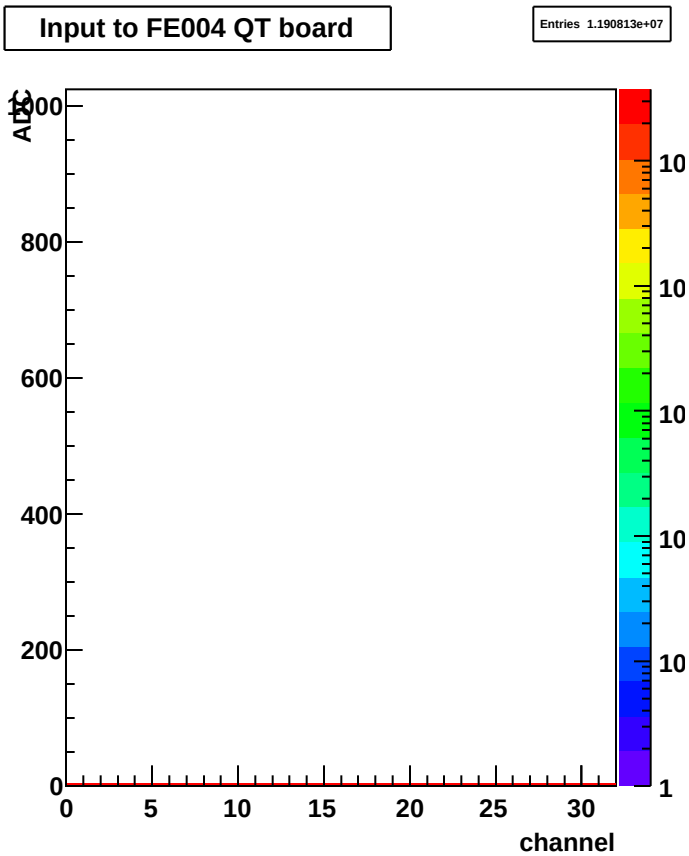

Input to FPD L2 DSM
Entries 212
HT bits - simulated
63
1
4
85
53
2
2
1
1
SMALL
LARGE-S
LARGE-N

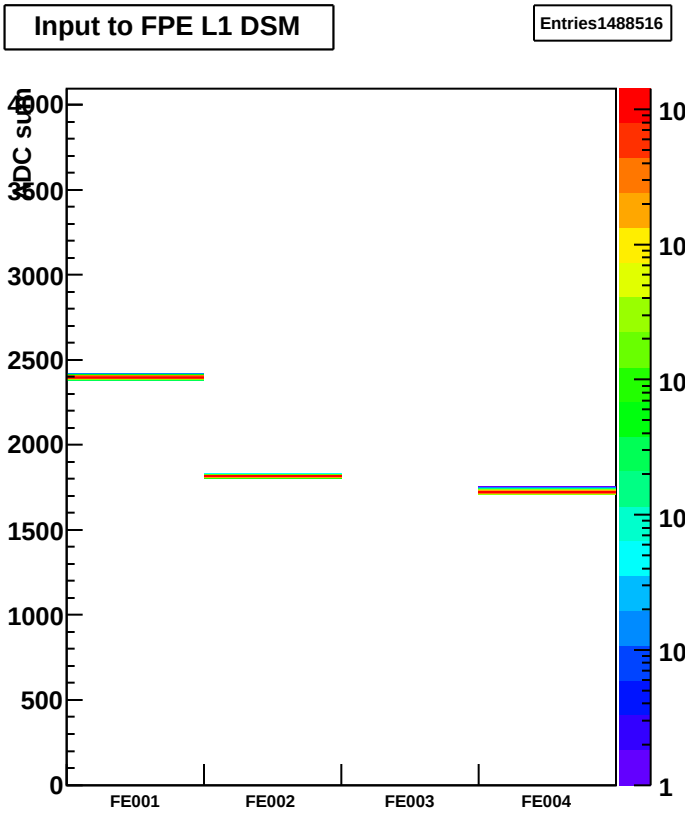

Input to FPD L2 DSM
Entries 2232774
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

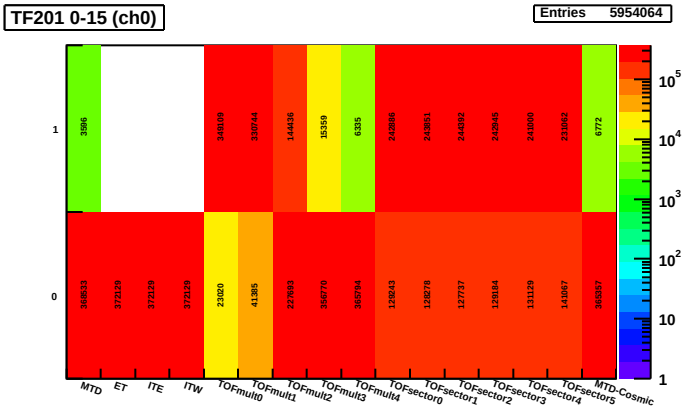

Input to FE001 QT board
Entries 1.190813e+07
ADC
channel

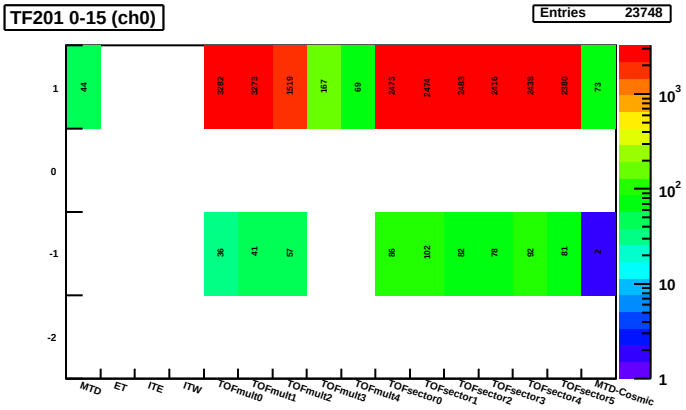

Input to FE002 QT board
Entries 1.190813e+07
ADC
channel

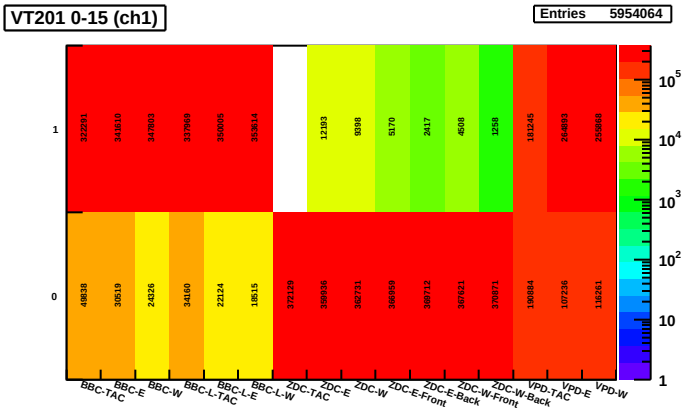

Input to FE003 QT board
Entries 1.190813e+07
ADC
channel

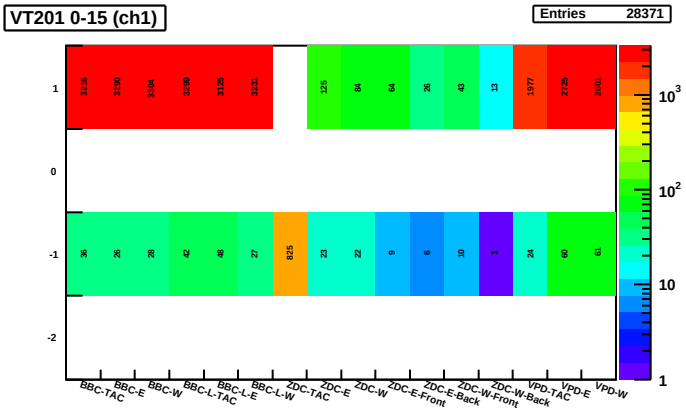

Input to FE004 QT board
Entries 1.190813e+07
ADC
channel

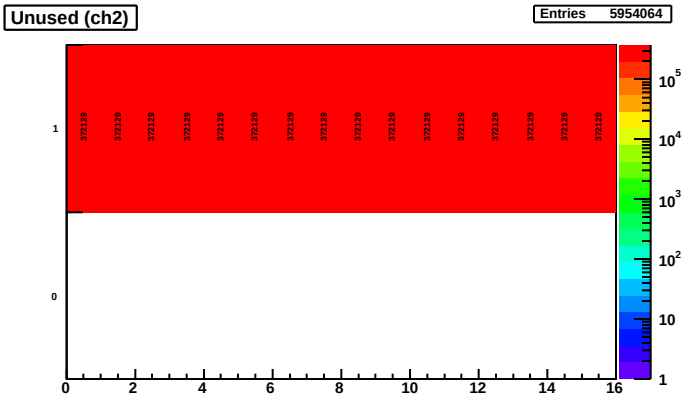

Input to FPE L1 DSM
Entries1488516
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

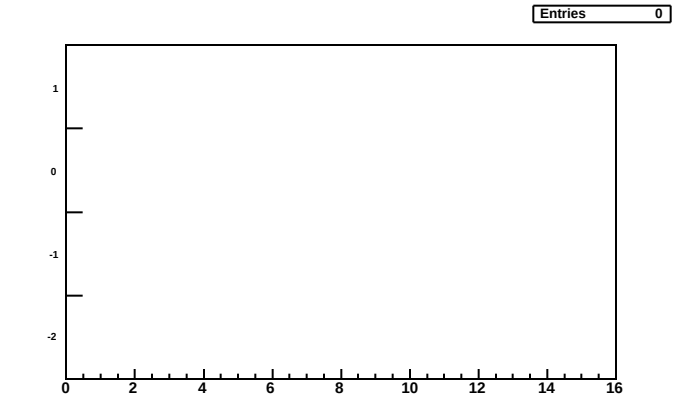

TF201 0-15 (ch0)
Entries 5954064
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 23748
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 5954064
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 28371
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 5954064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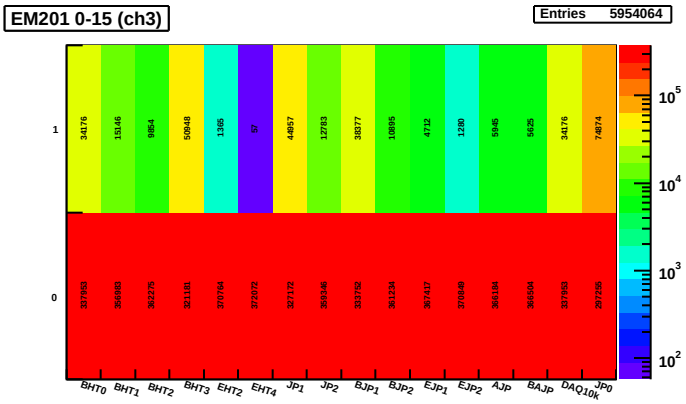
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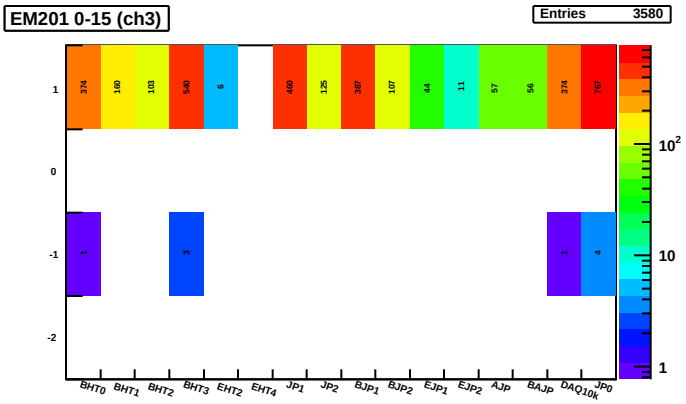
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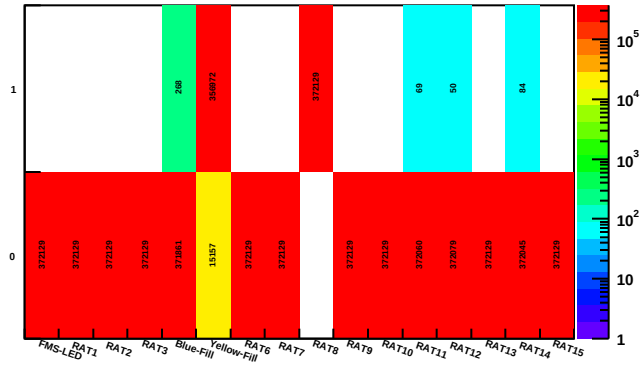
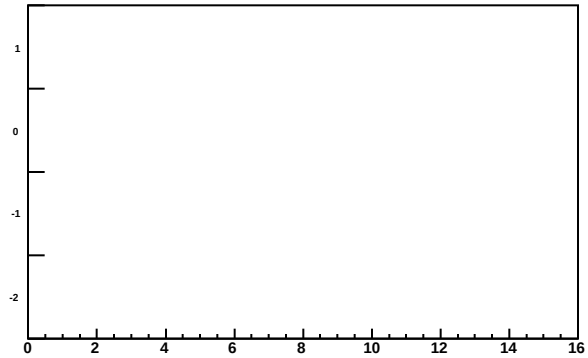
RAT board (ch4)





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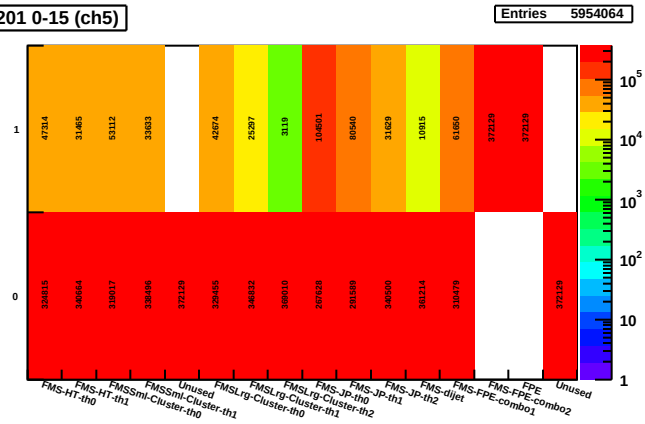

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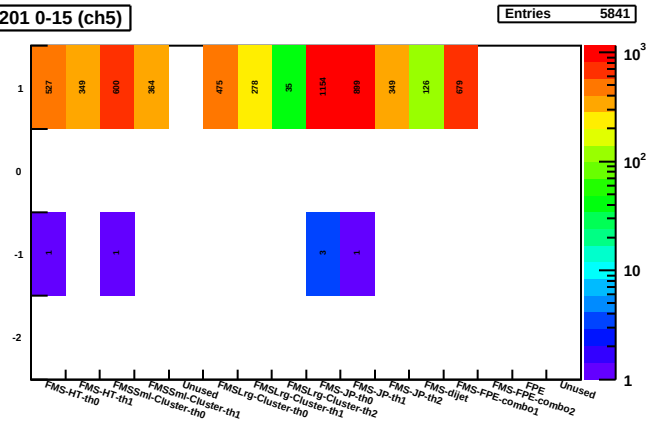

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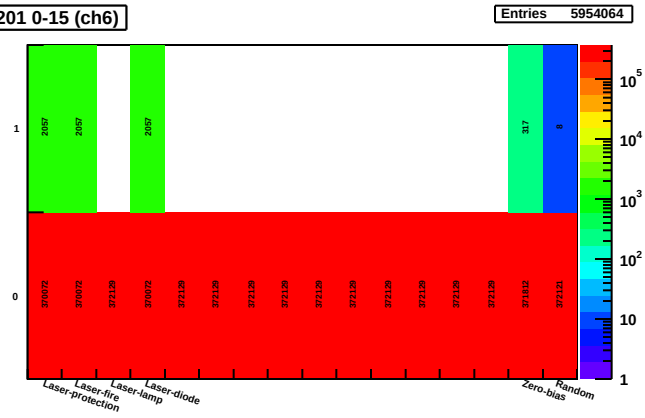

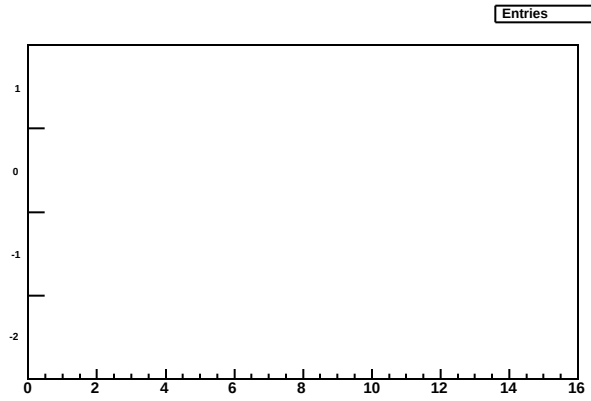
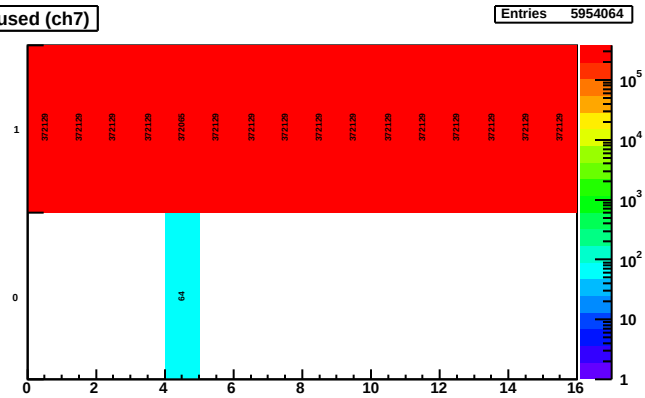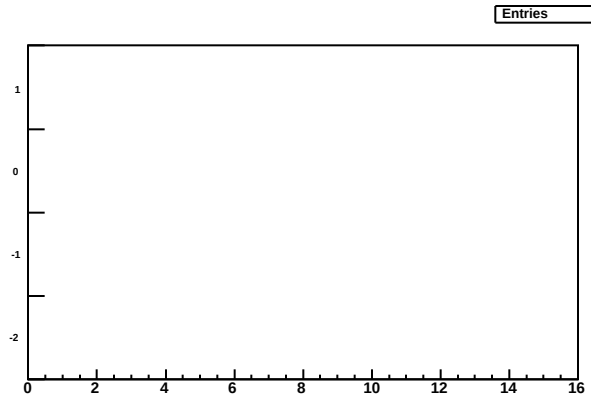
Unused (ch7)