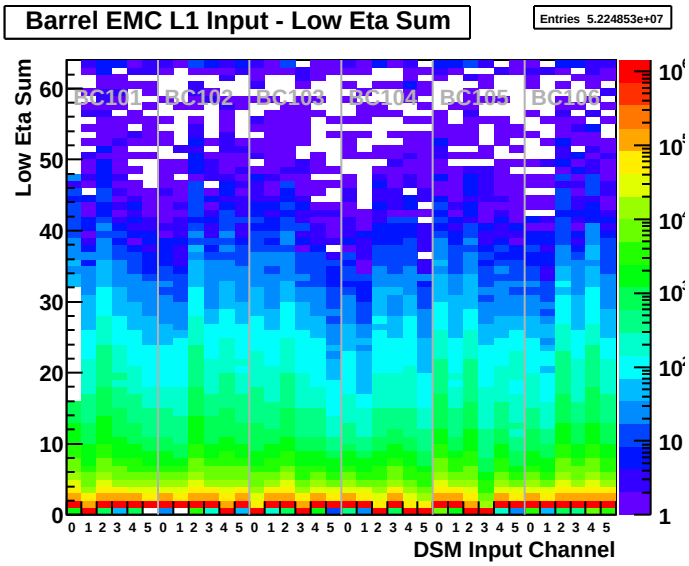
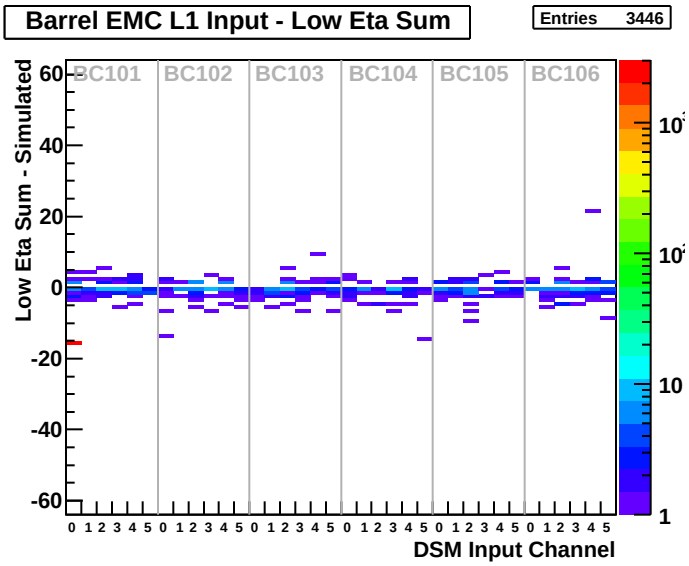
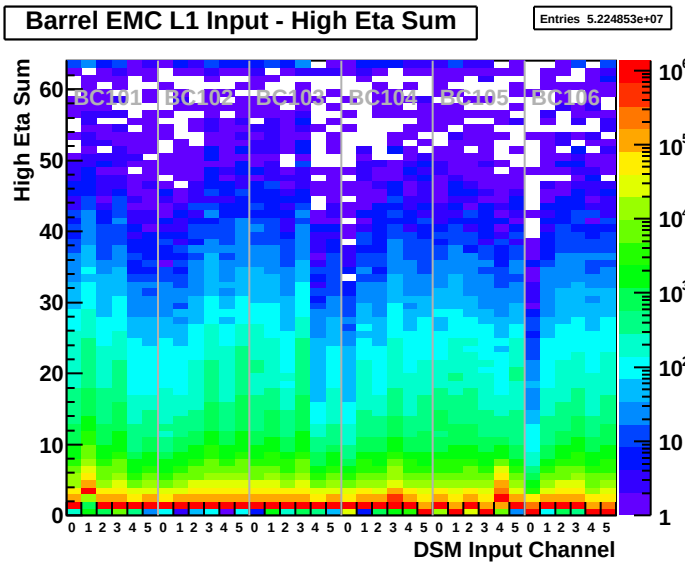
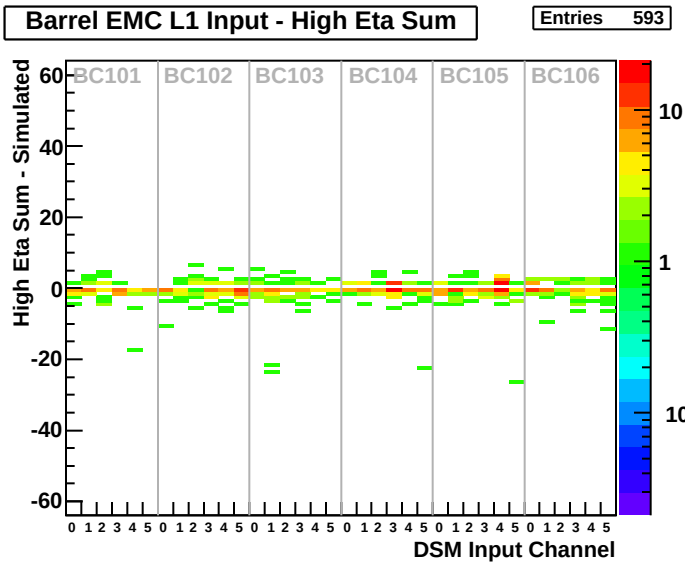
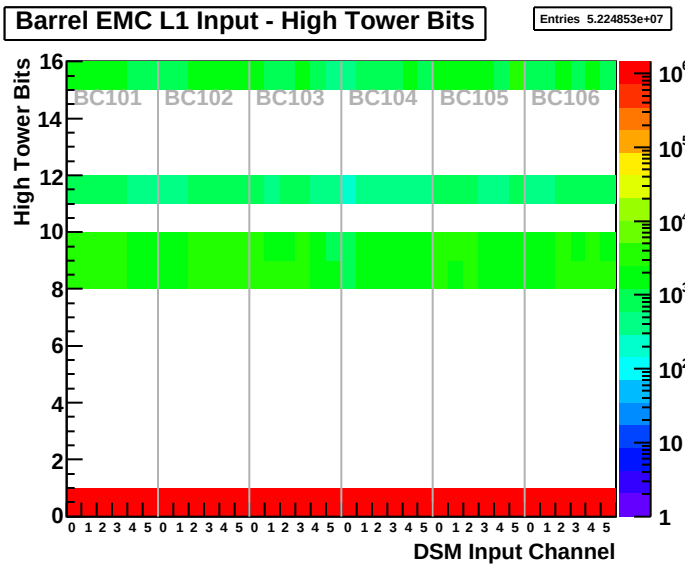
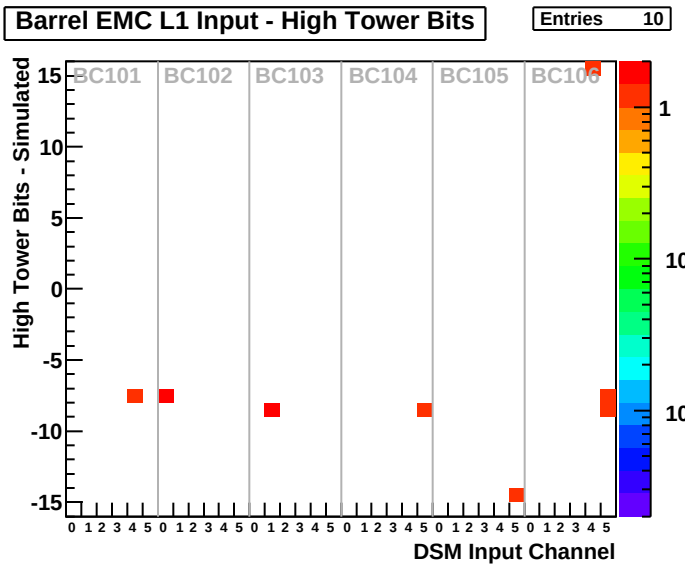

Barrel EMC L1 Input - Low Eta Sum
Entries 5.224853e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

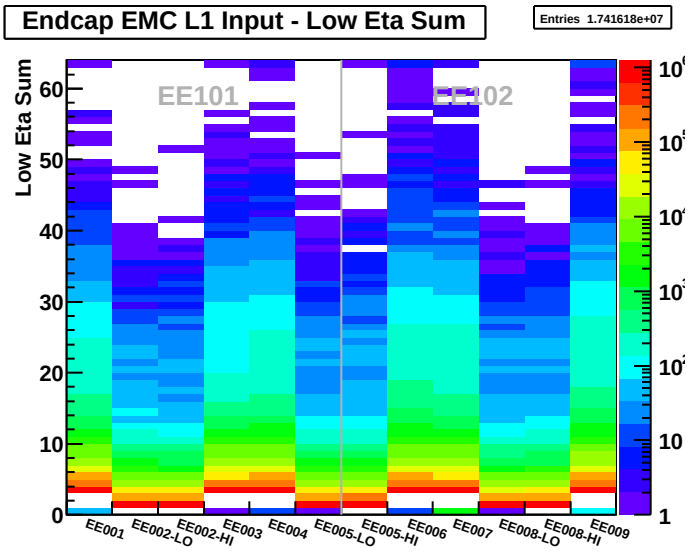

Barrel EMC L1 Input - Low Eta Sum
Entries 3446
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

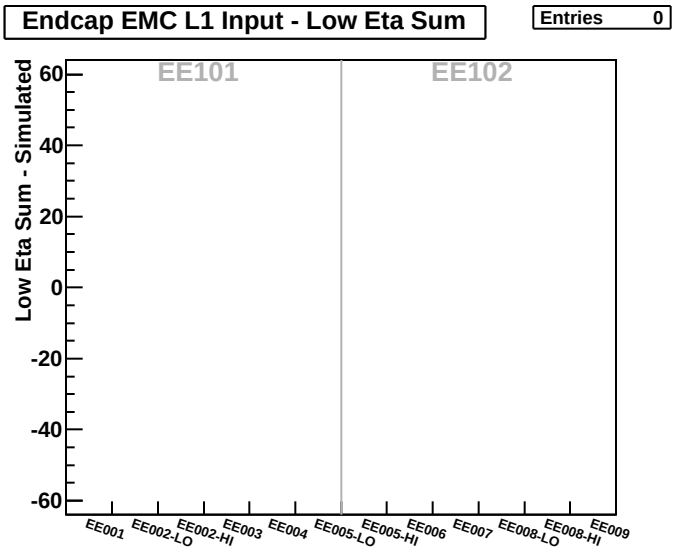

Barrel EMC L1 Input - High Eta Sum
Entries 5.224853e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

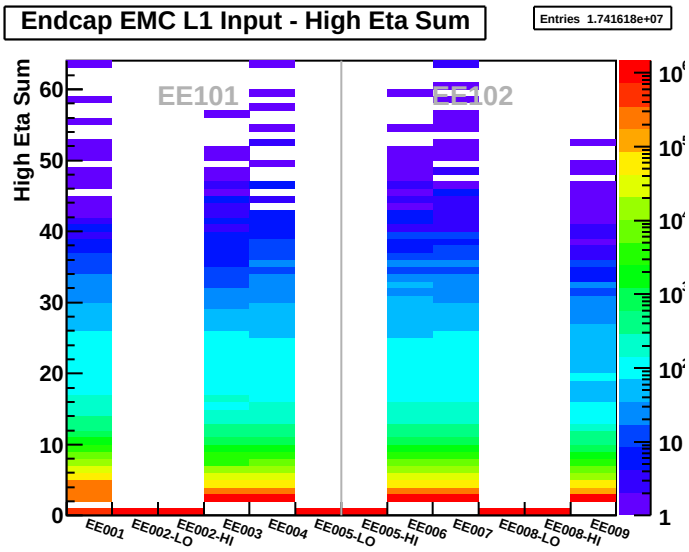

Barrel EMC L1 Input - High Eta Sum
Entries 593
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

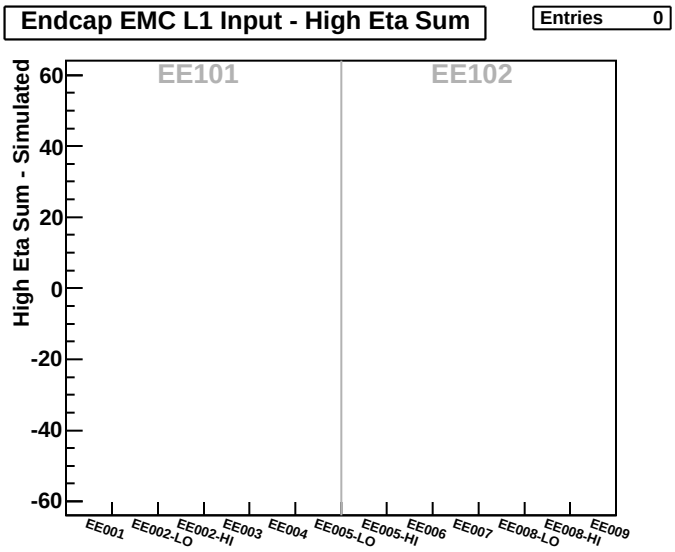

Barrel EMC L1 Input - High Tower Bits
Entries 5.224853e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

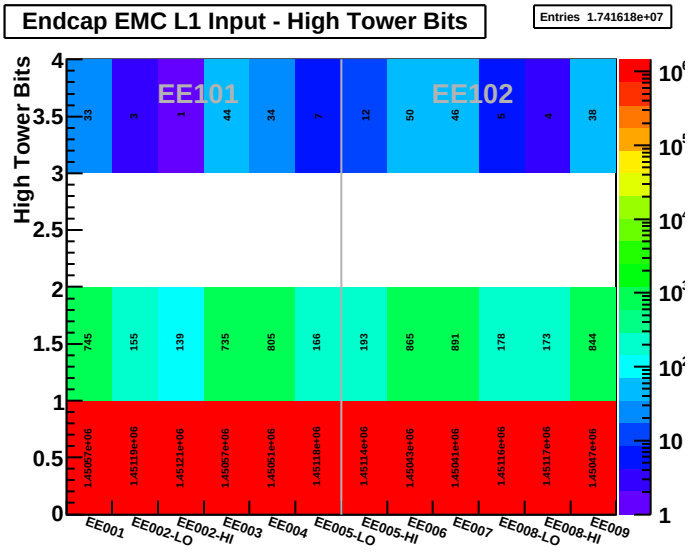

Barrel EMC L1 Input - High Tower Bits
Entries 10
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

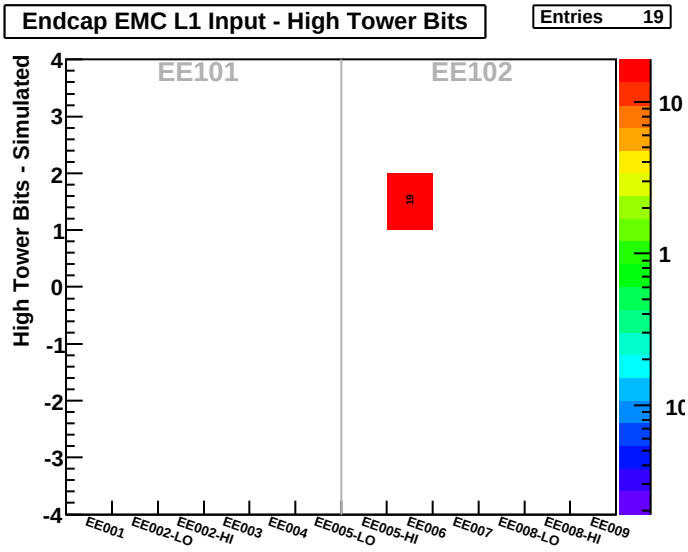

Endcap EMC L1 Input - Low Eta Sum
Entries 1.741618e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

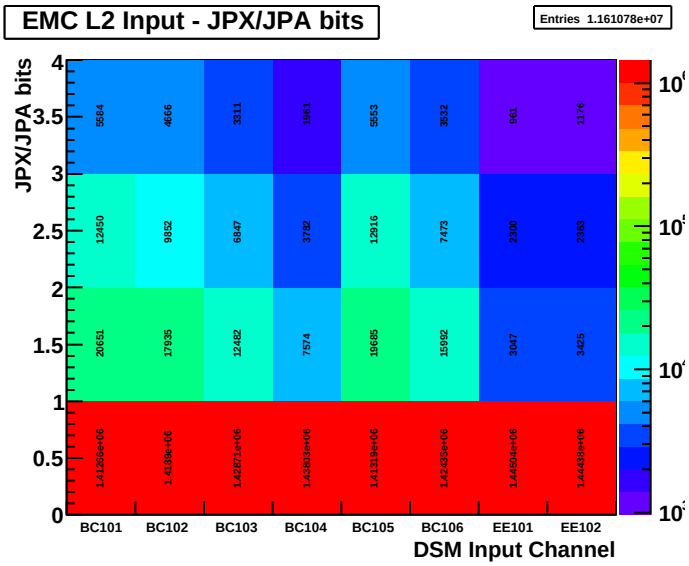

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

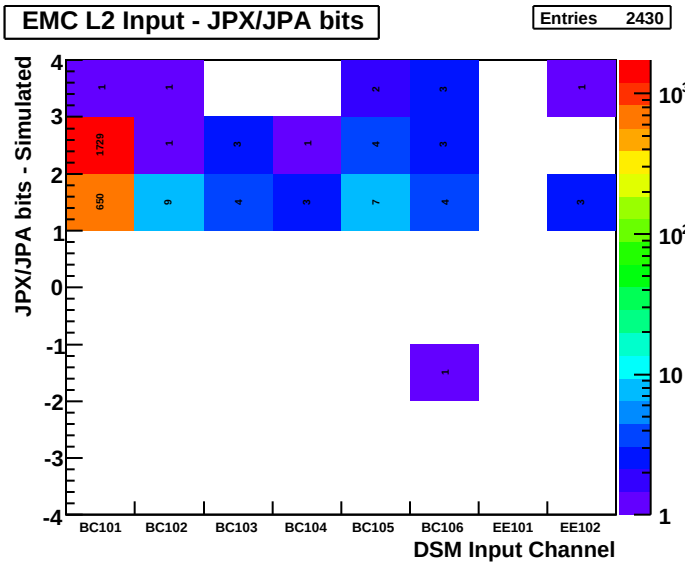

Endcap EMC L1 Input - High Eta Sum
Entries 1.741618e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

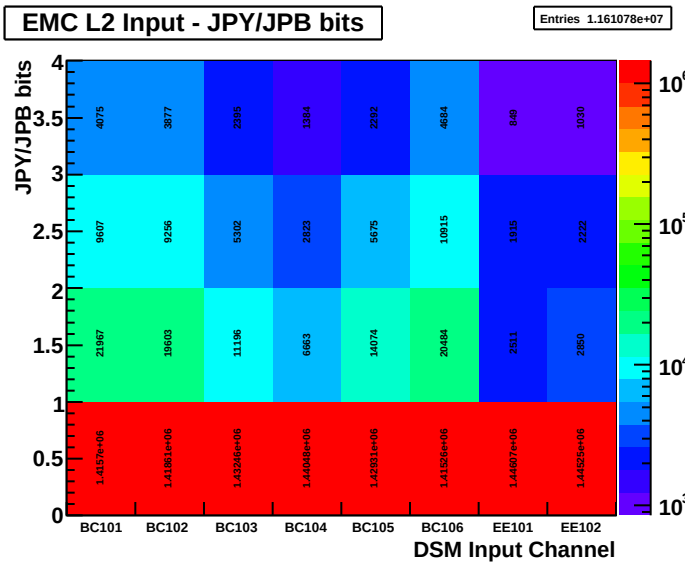

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

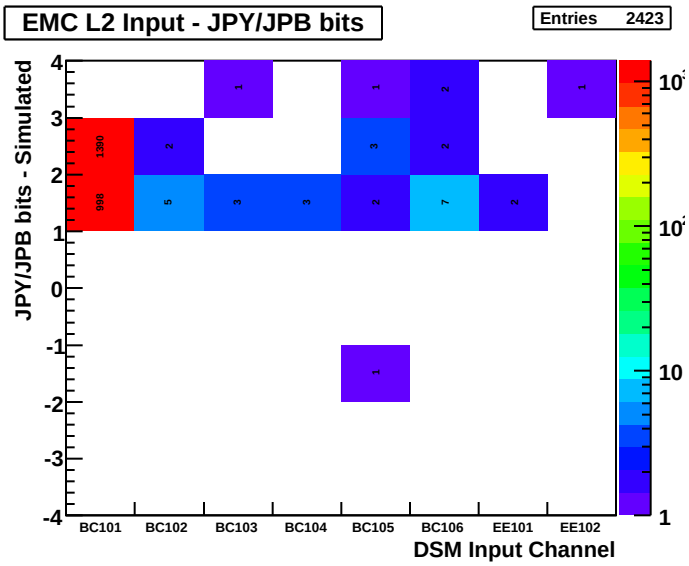

Endcap EMC L1 Input - High Tower Bits
Entries 1.741618e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

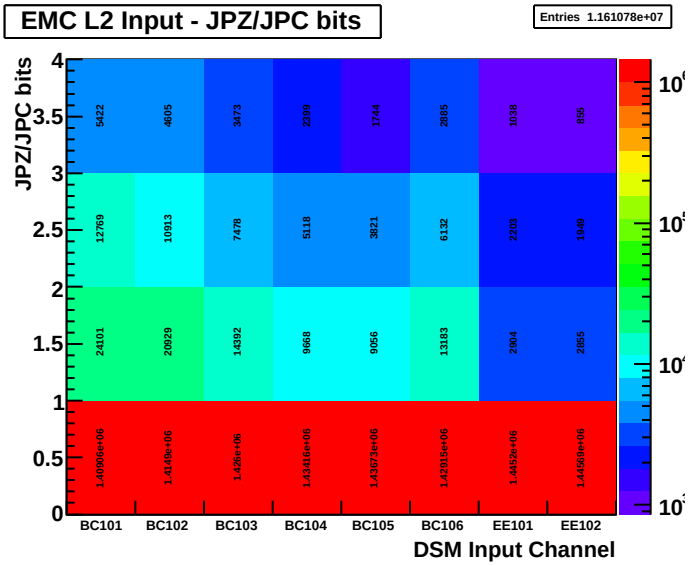

Endcap EMC L1 Input - High Tower Bits
Entries 19
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

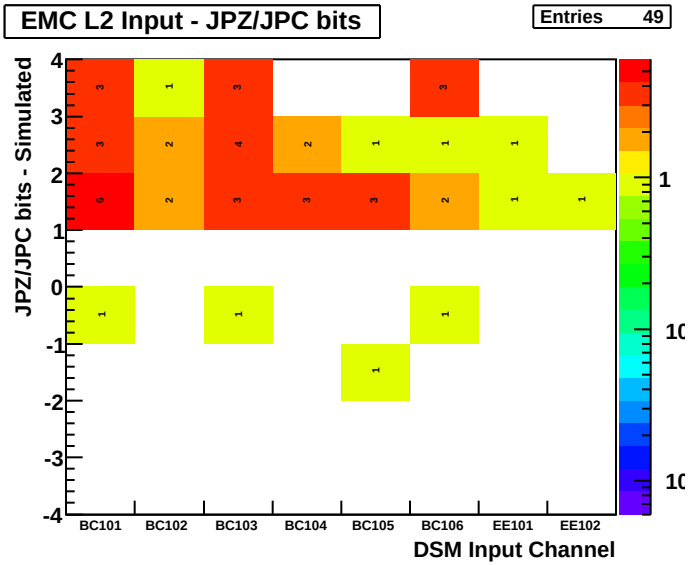

EMC L2 Input - JPX/JPA bits
Entries 1.161078e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5584 4666 3311 1961 5553 3532 961 1176
12450 9852 6847 3782 12916 7473 2300 2363
20651 17935 12482 7574 19685 15992 3047 3425
1.41266e+06 1.4180e+06 1.42871e+06 1.43803e+06 1.41319e+06 1.42435e+06 1.44504e+06 1.44428e+06

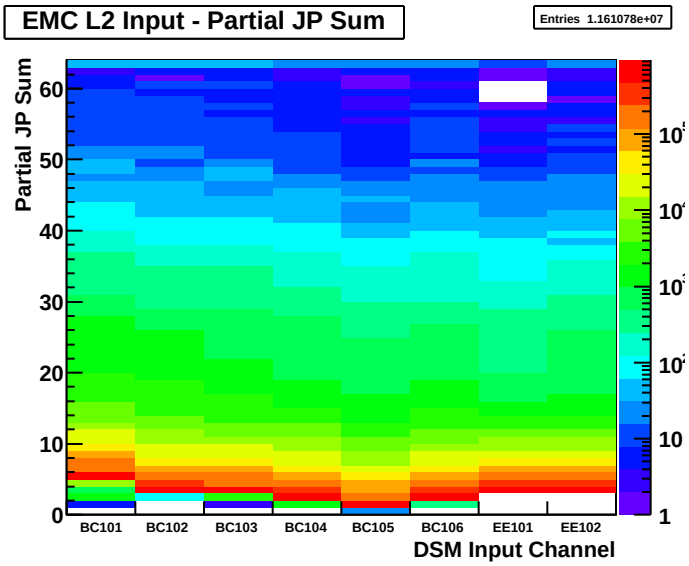

EMC L2 Input - JPX/JPA bits
Entries 2430
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

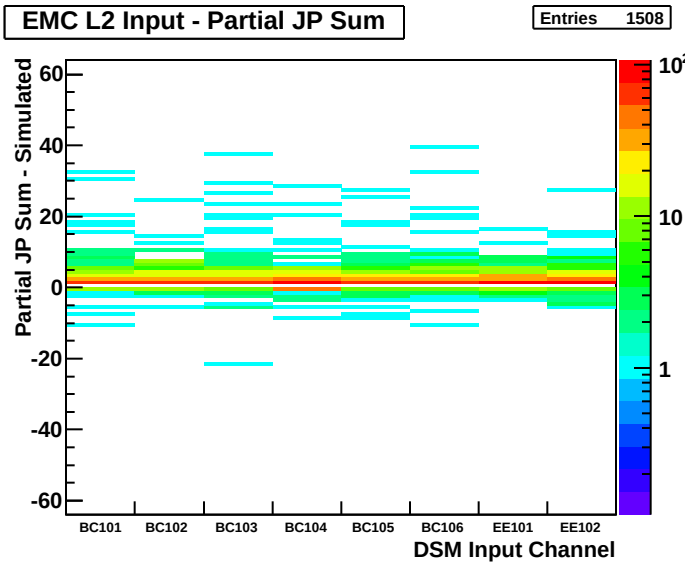

EMC L2 Input - JPY/JPB bits
Entries 1.161078e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
4075 3877 2395 1384 2292 4684 849 1030
9607 9256 5302 2823 5675 10915 1915 2222
21967 19603 11196 6663 14074 20484 2511 2950
1.4157e+06 1.41861e+06 1.43266e+06 1.44048e+06 1.42931e+06 1.41526e+06 1.44607e+06 1.44525e+06

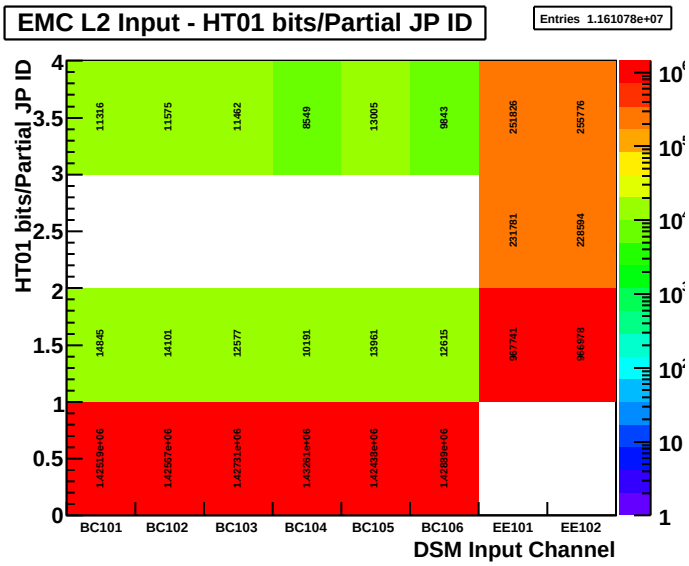

EMC L2 Input - JPY/JPB bits
Entries 2423
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

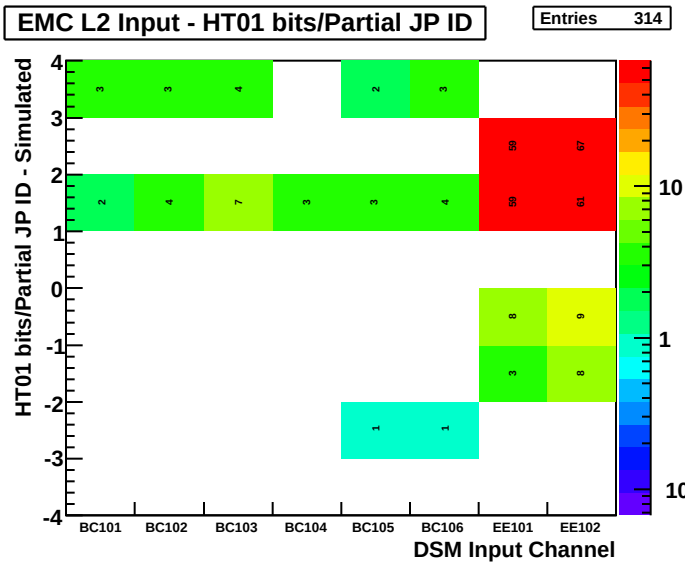

EMC L2 Input - JPZ/JPC bits
Entries 1.161078e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
5422 4605 3473 2309 1744 2885 1038 855
12769 10913 7478 5118 3821 6132 2203 1949
24101 20929 14392 9668 9056 13183 2904 2855
1.40906e+06 1.4149e+06 1.426e+06 1.43416e+06 1.43573e+06 1.42915e+06 1.4452e+06 1.44569e+06

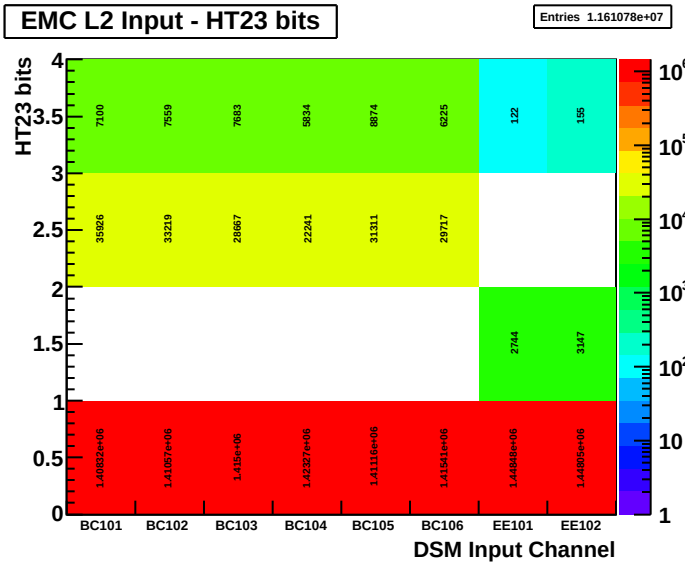

EMC L2 Input - JPZ/JPC bits
Entries 49
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

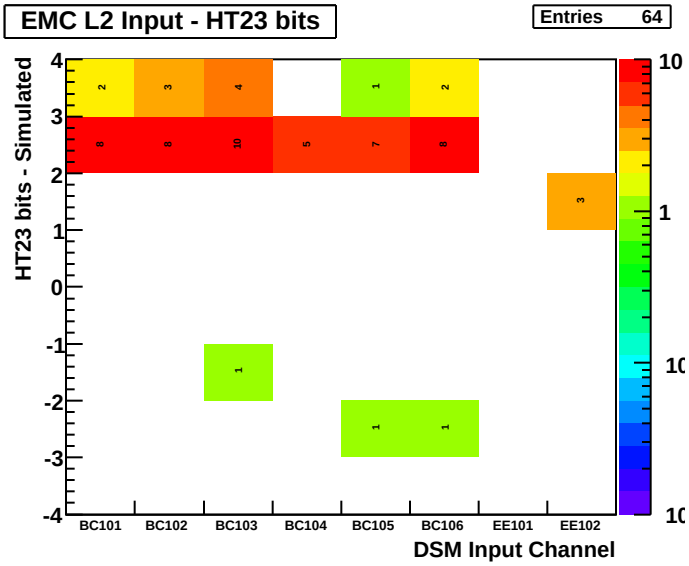

EMC L2 Input - Partial JP Sum
Entries 1.161078e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

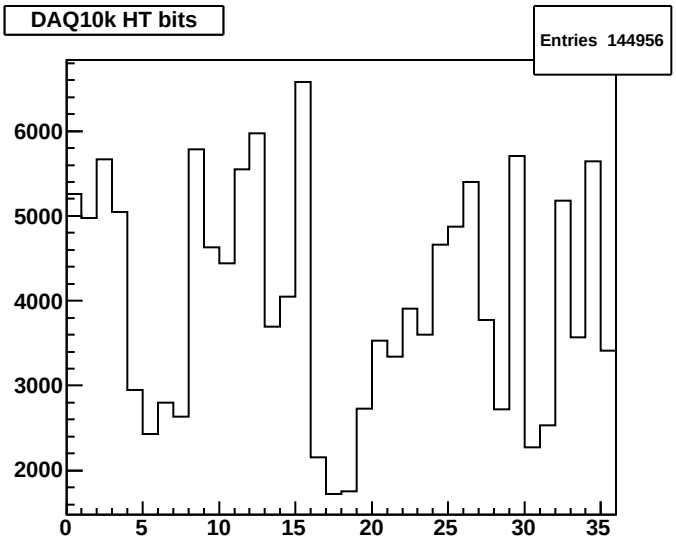

EMC L2 Input - Partial JP Sum
Entries 1508
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

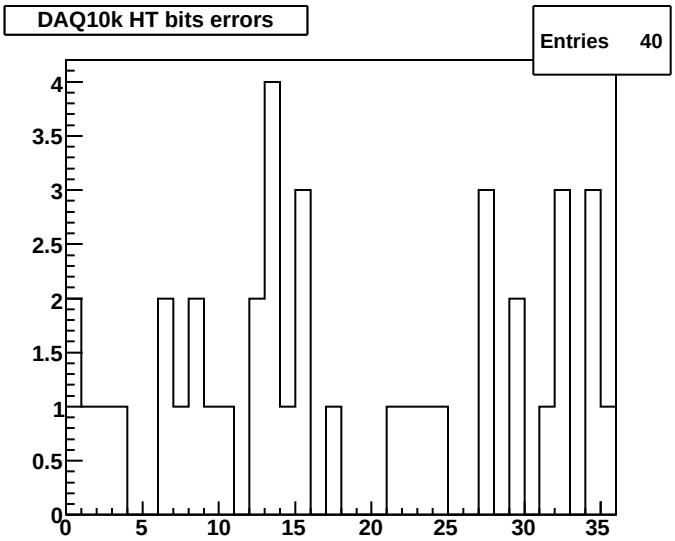

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.161078e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

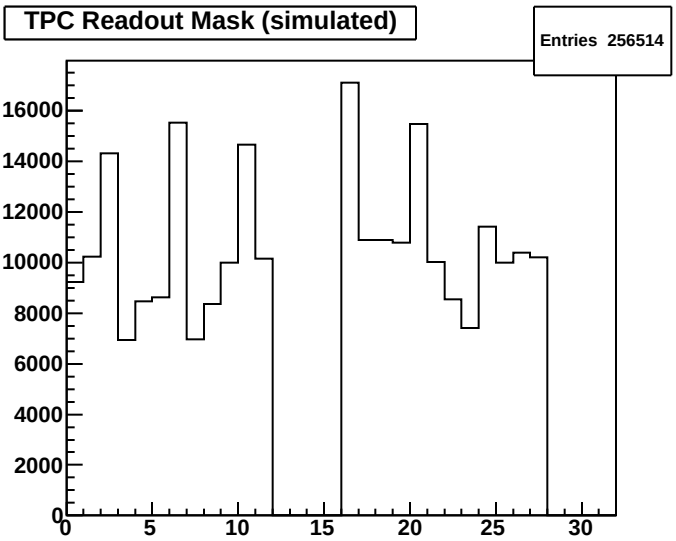

EMC L2 Input - HT01 bits/Partial JP ID
Entries 314
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

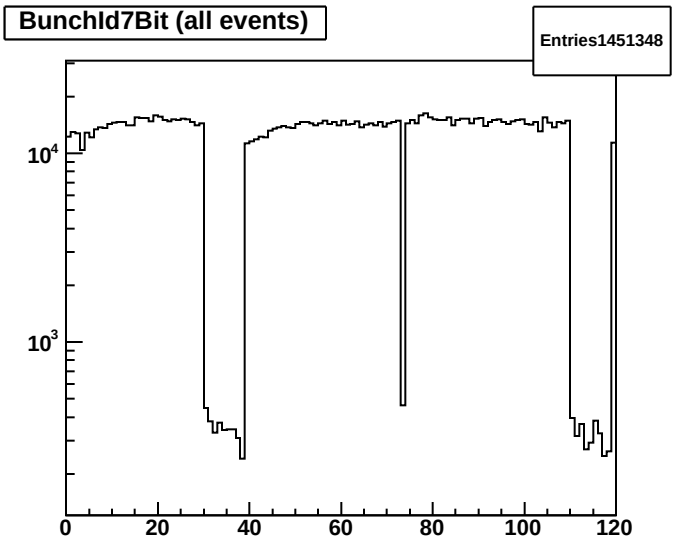

EMC L2 Input - HT23 bits
Entries 1.161078e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

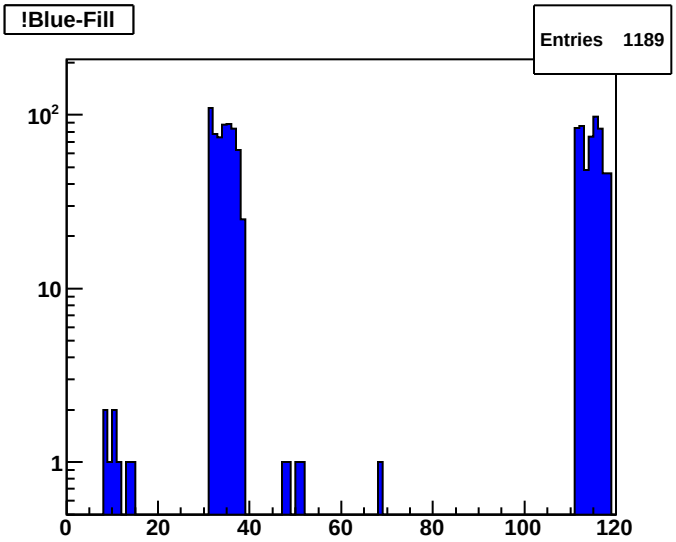

EMC L2 Input - HT23 bits
Entries 64
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

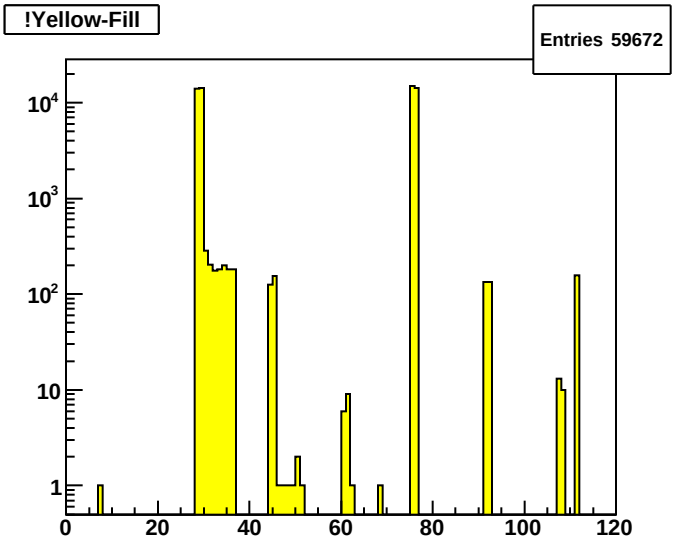

DAQ10k HT bits
Entries 144956

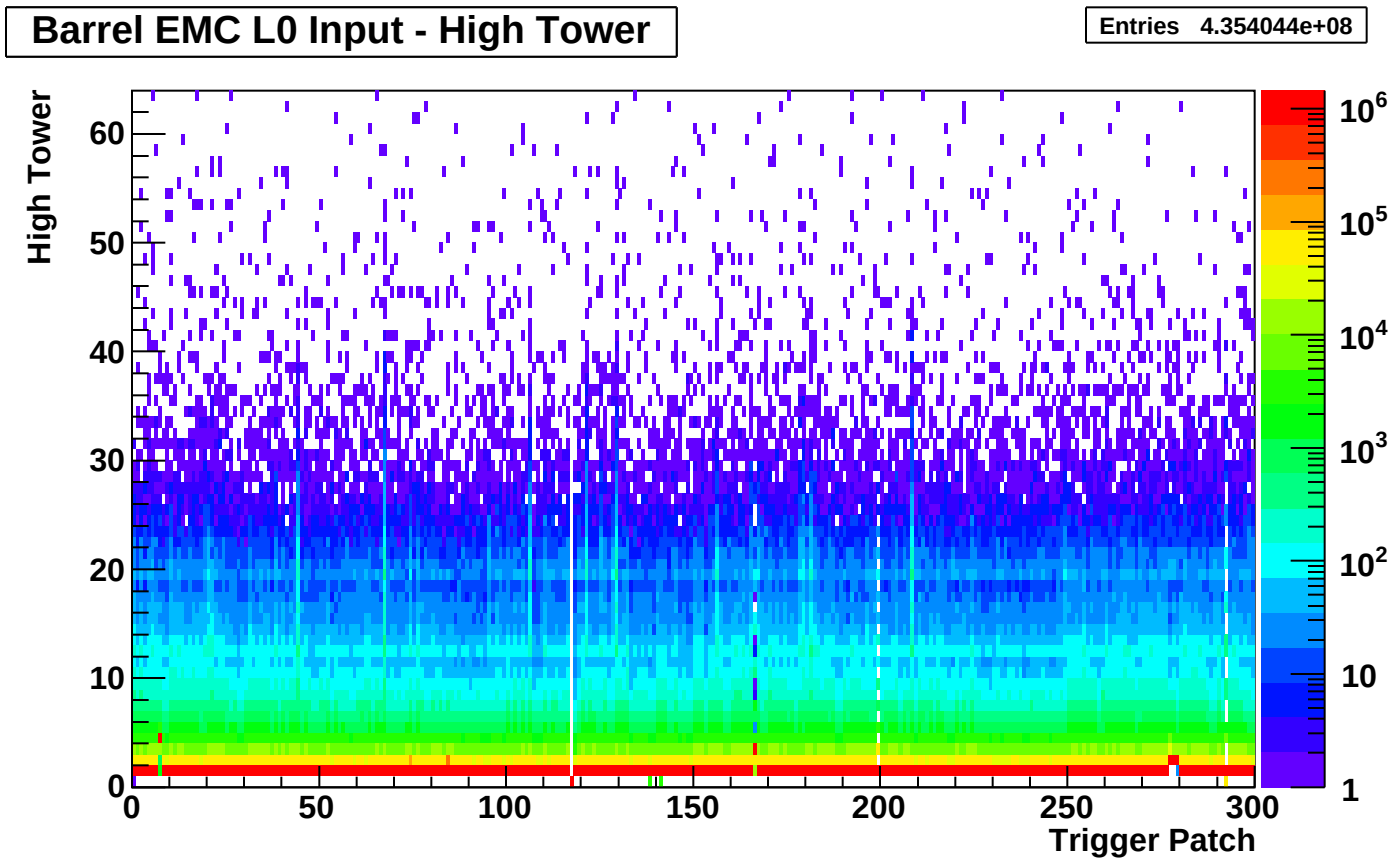

DAQ10k HT bits errors
Entries 40

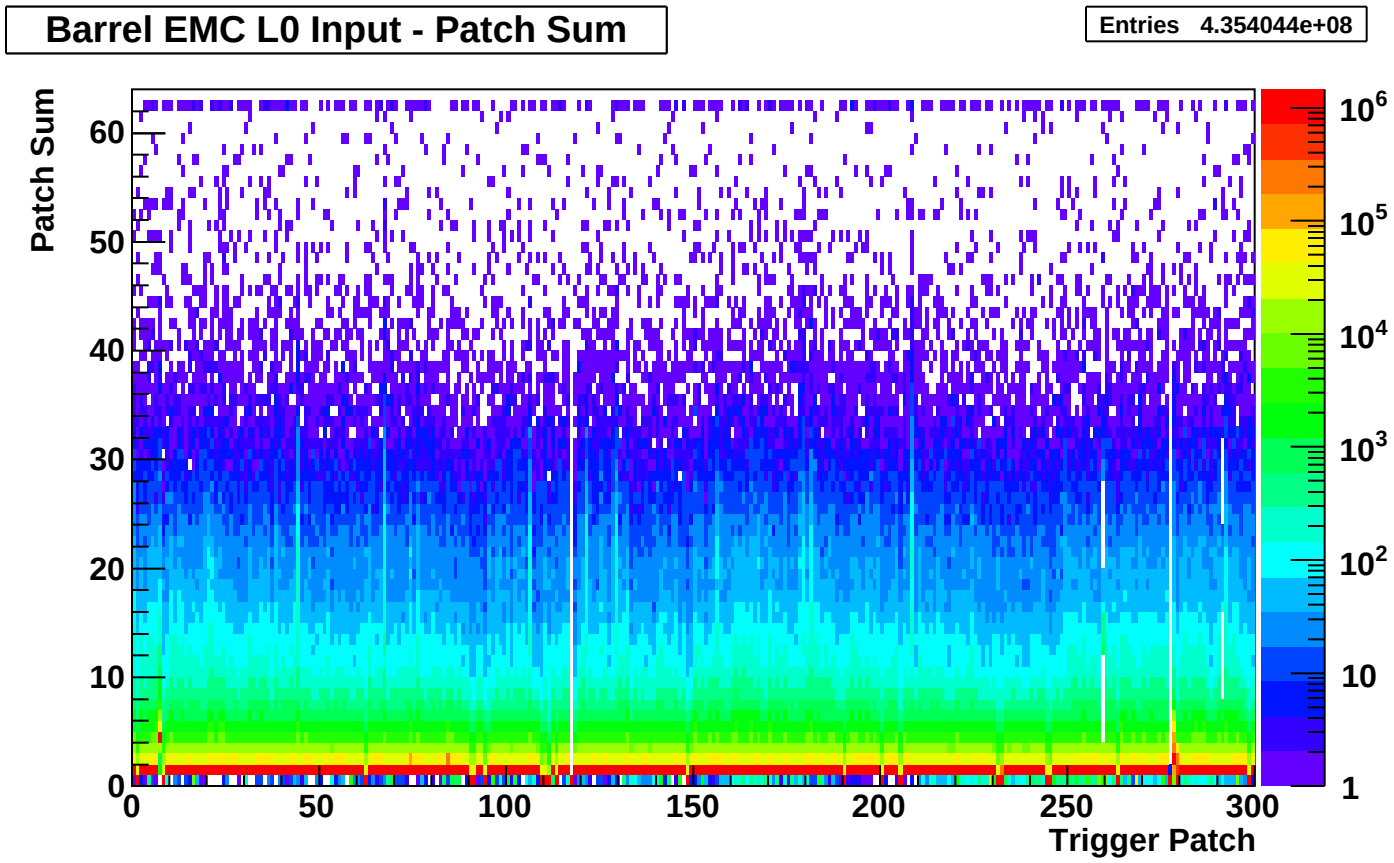

TPC Readout Mask (simulated)
Entries 256514

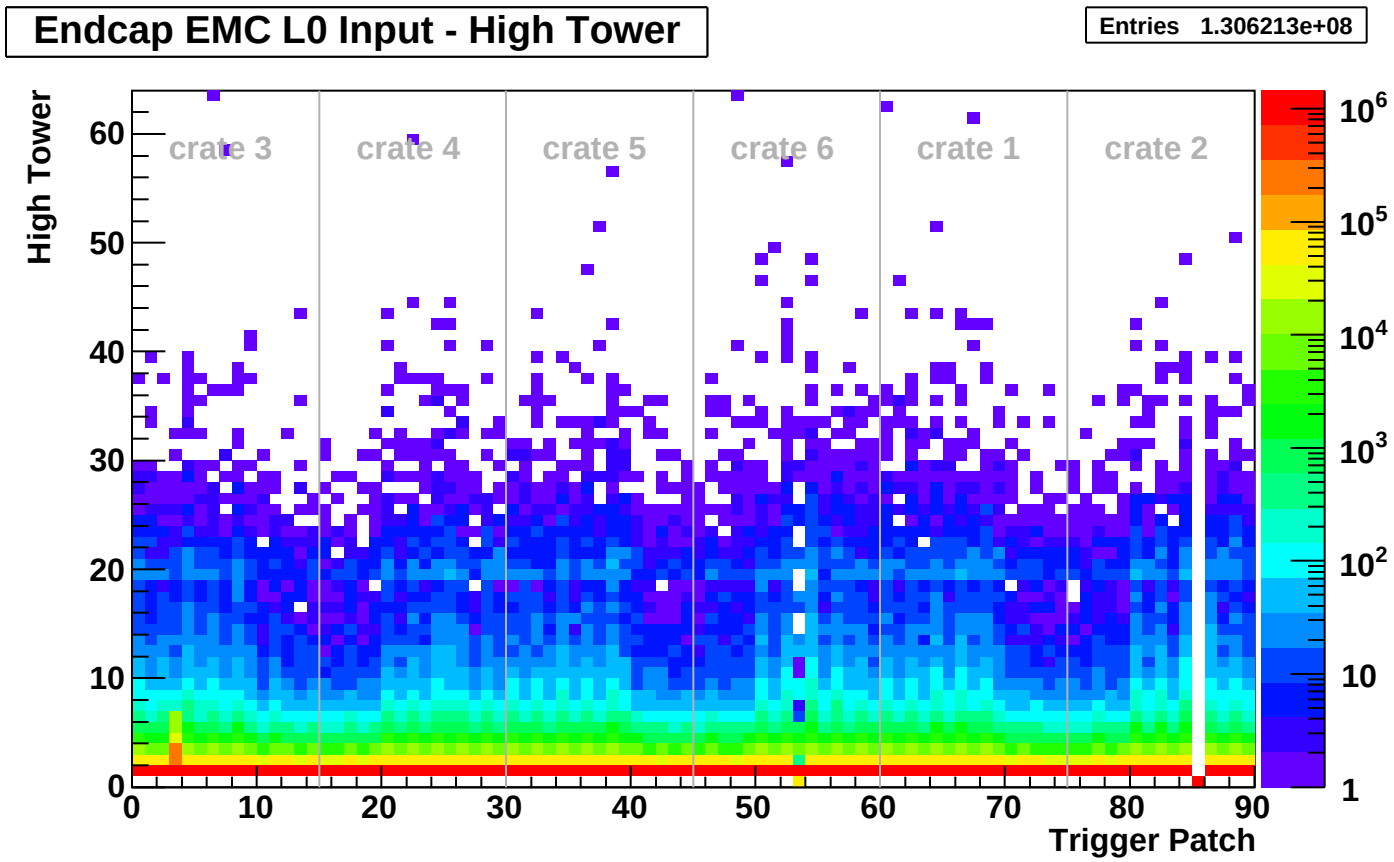

BunchId7Bit (all events)
Entries1451348

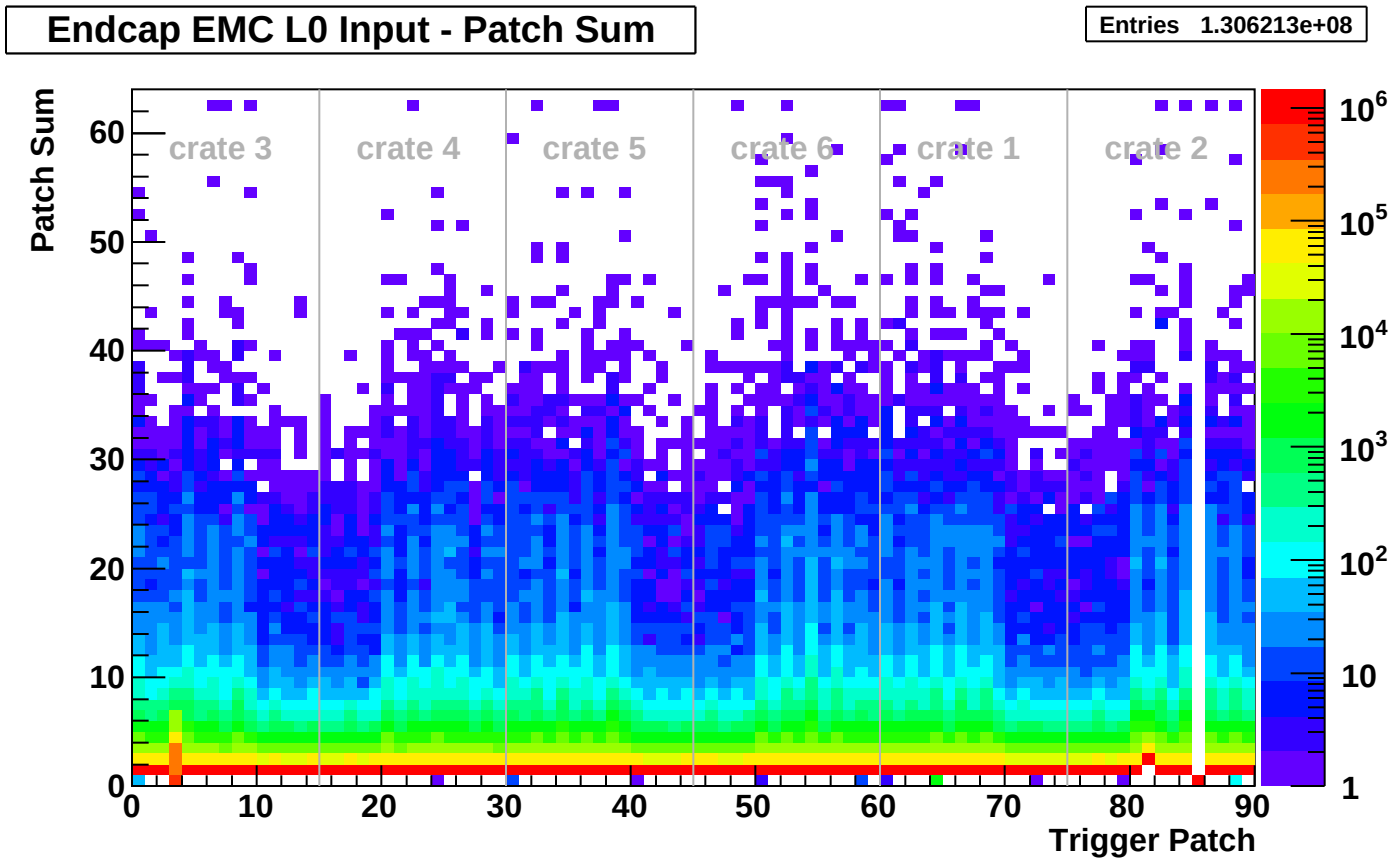

!Blue-Fill
Entries 1189

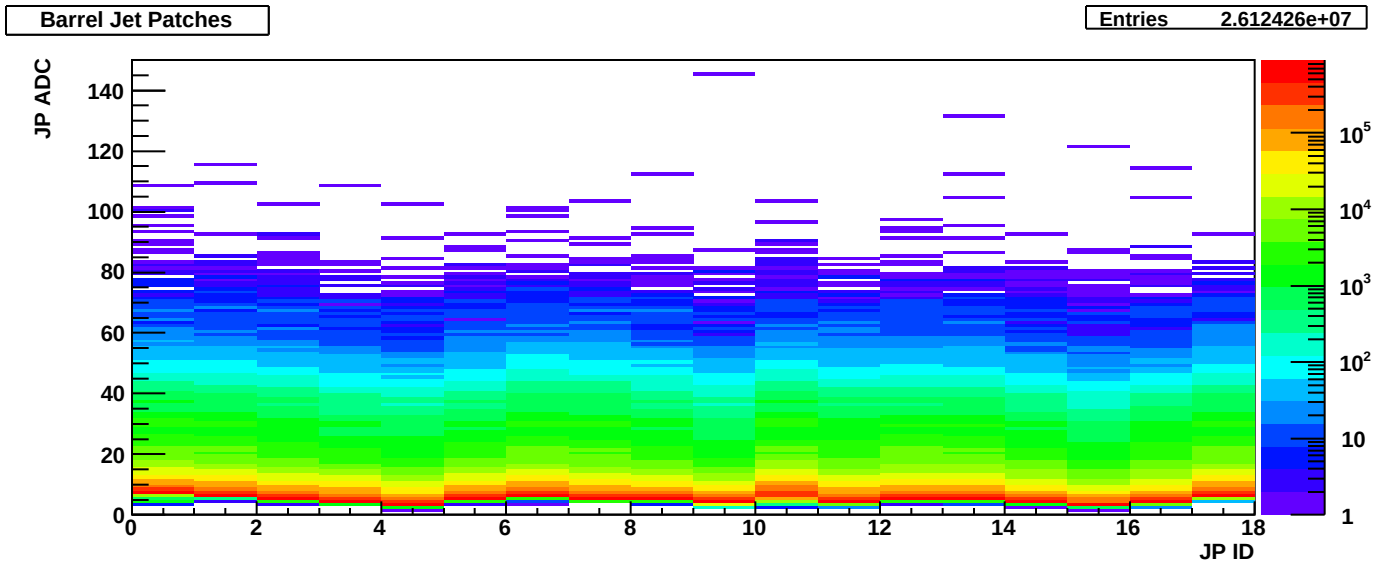

!Yellow-Fill
Entries 59672

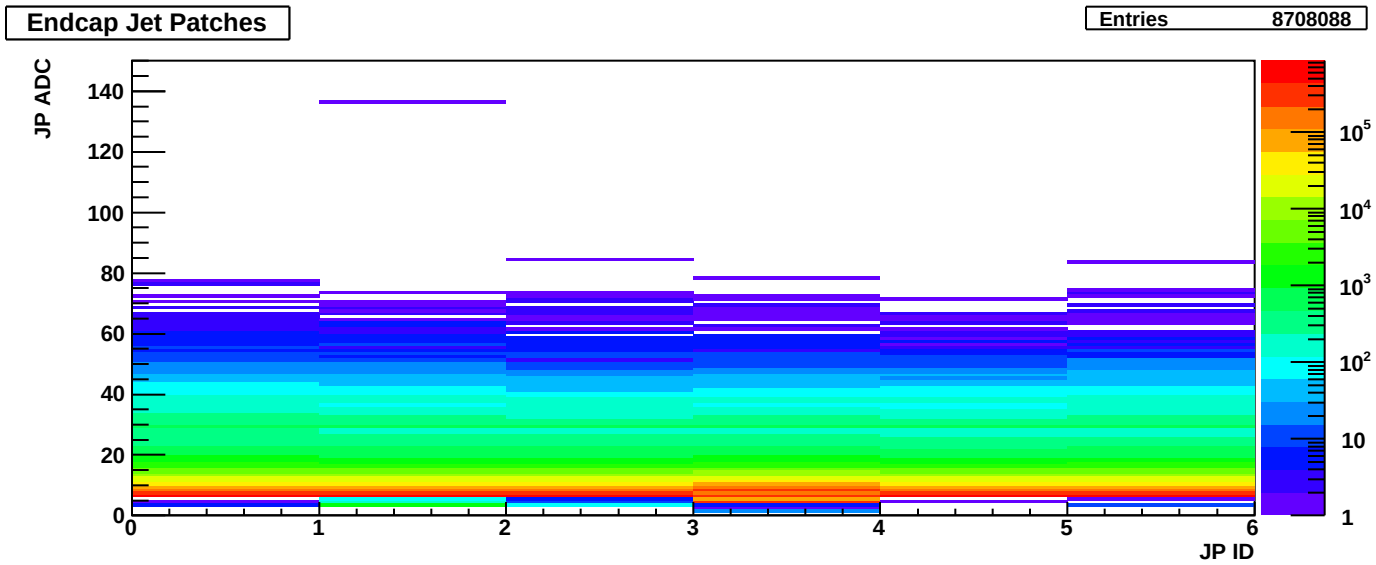

Barrel EMC L0 Input - High Tower
Entries 4.354044e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

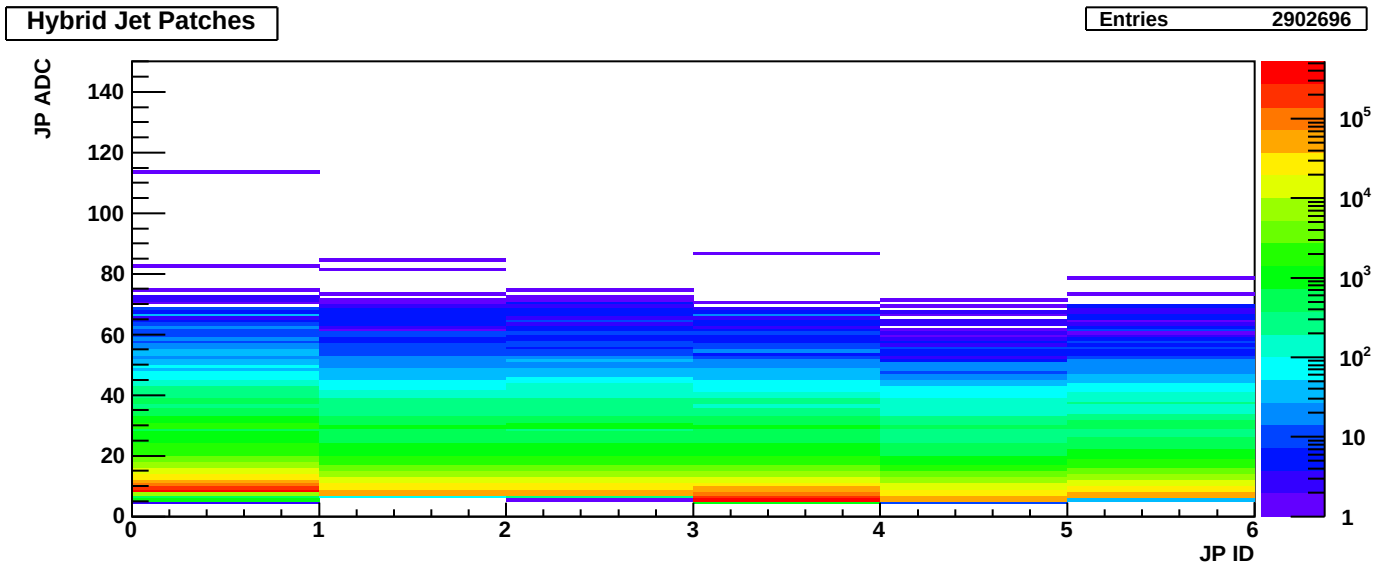

Barrel EMC L0 Input - Patch Sum
Entries 4.354044e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

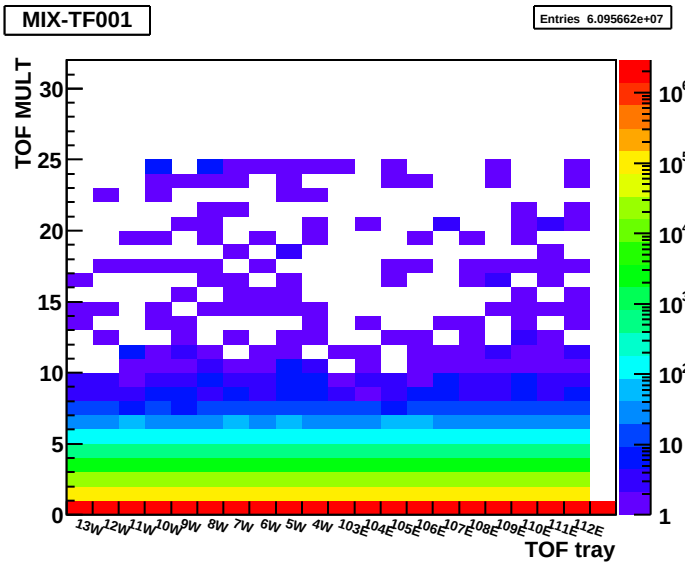

Endcap EMC L0 Input - High Tower
Entries 1.306213e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

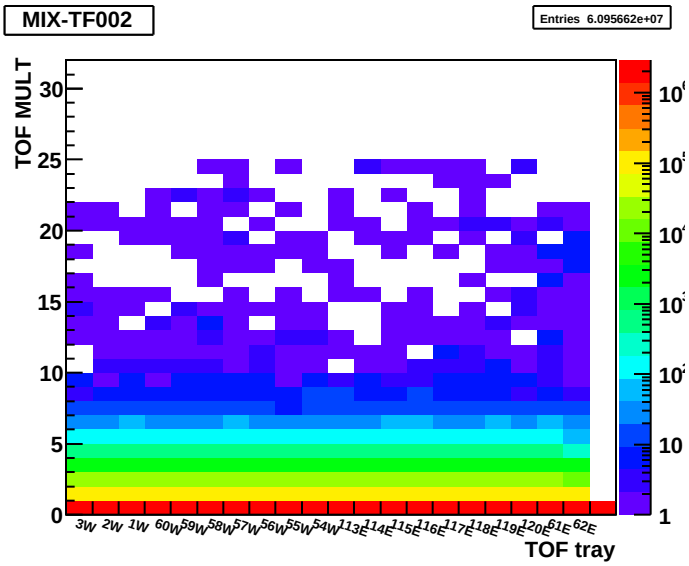

Endcap EMC L0 Input - Patch Sum
Entries 1.306213e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

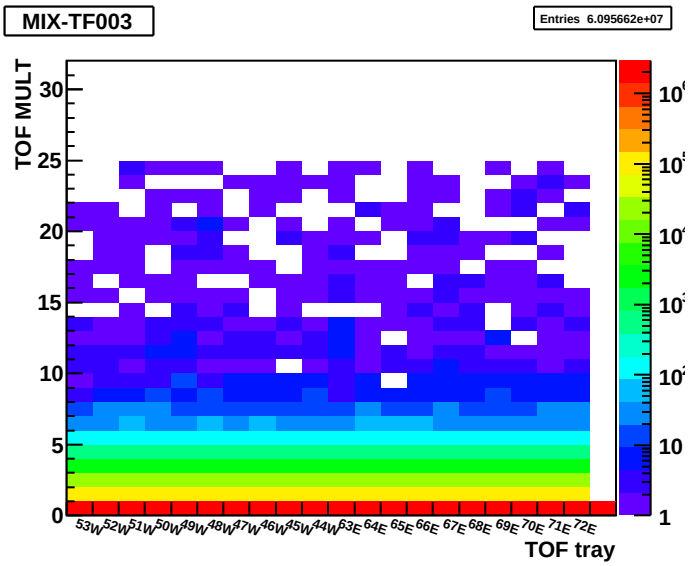

Barrel Jet Patches
Entries 2.612426e+07
JP ADC
JP ID

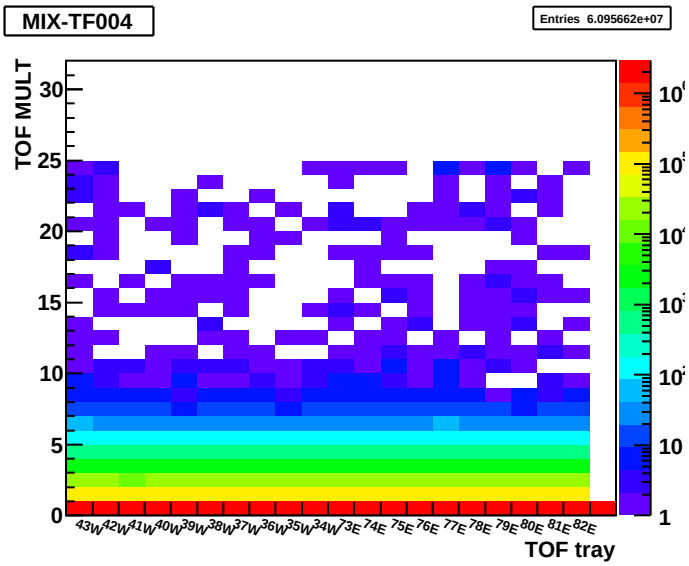

Endcap Jet Patches
Entries 8708088
JP ADC
JP ID

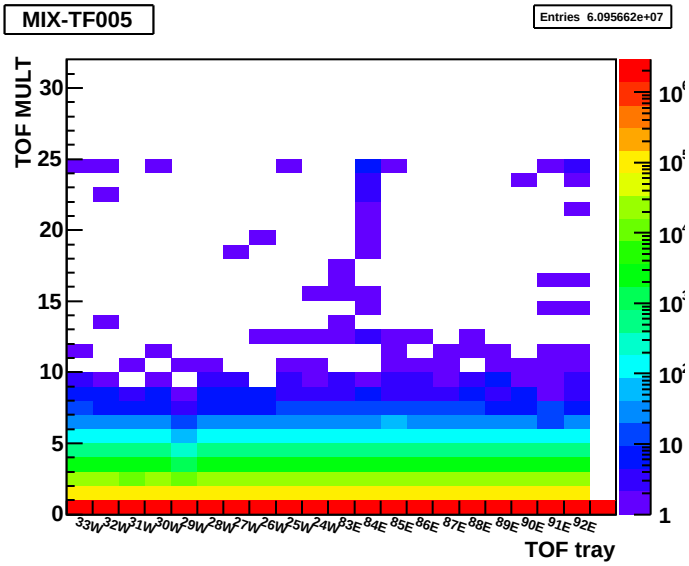

Hybrid Jet Patches
Entries 2902696
JP ADC
JP ID

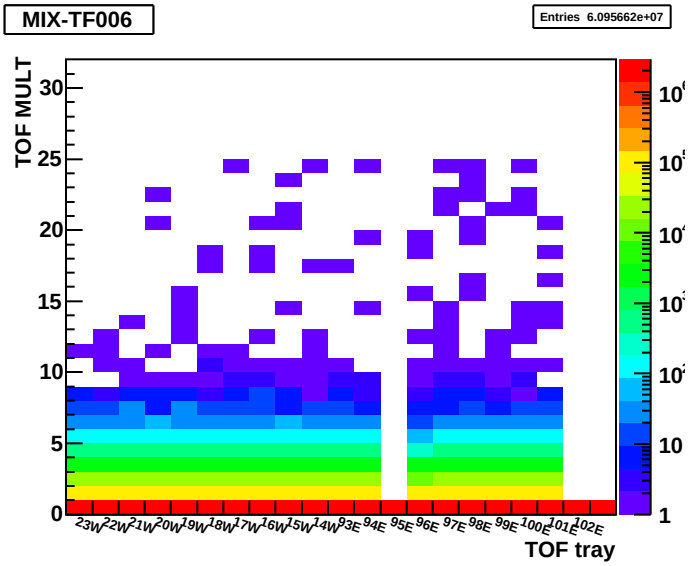

MIX-TF001
Entries 6.095662e+07
TOF MULT
TOF tray

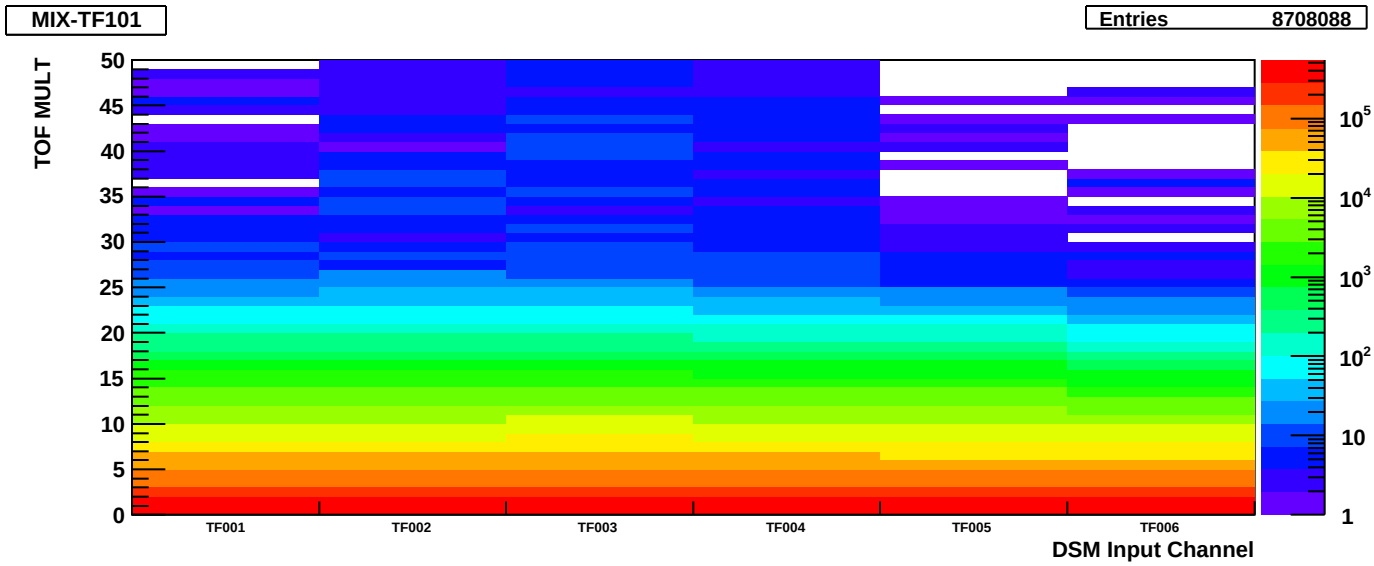

MIX-TF002
Entries 6.095662e+07
TOF MULT
TOF tray

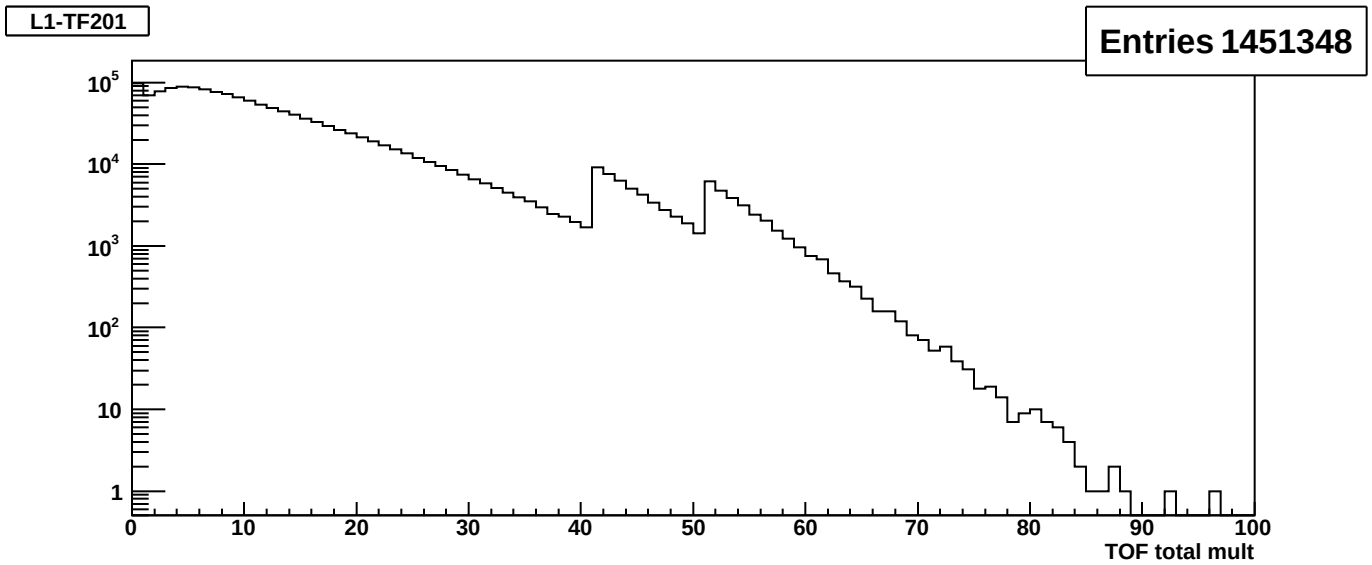

MIX-TF003
Entries 6.095662e+07
TOF MULT
TOF tray

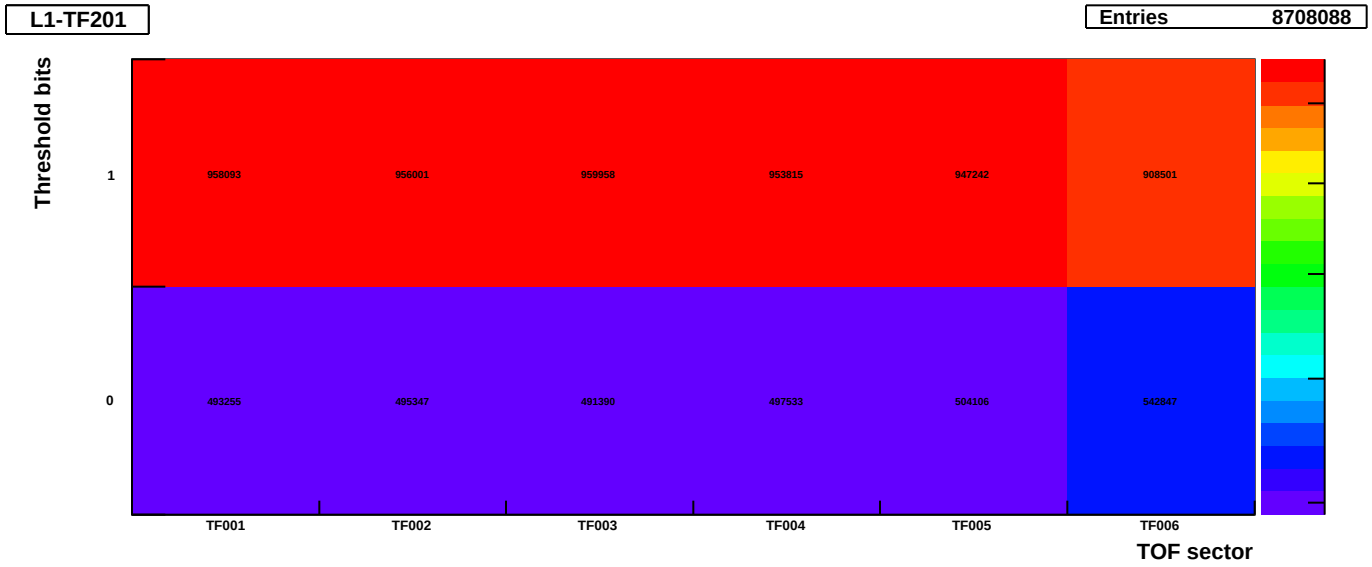

MIX-TF004
Entries 6.095662e+07
TOF MULT
TOF tray

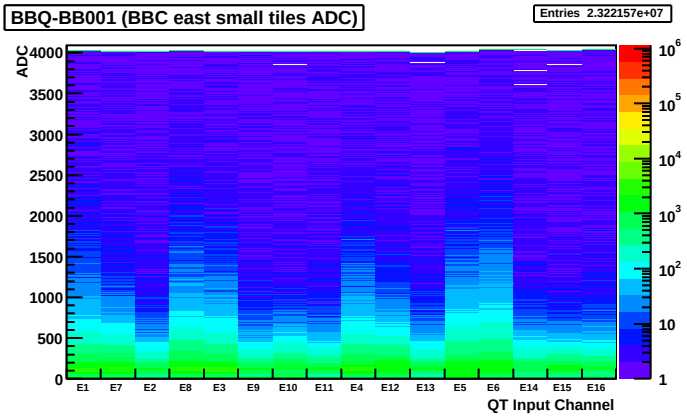

MIX-TF005
Entries 6.095662e+07
TOF MULT
TOF tray

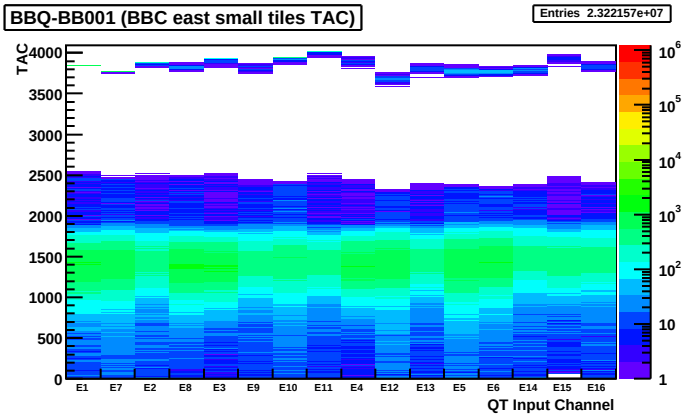

MIX-TF006
Entries 6.095662e+07
TOF MULT
TOF tray

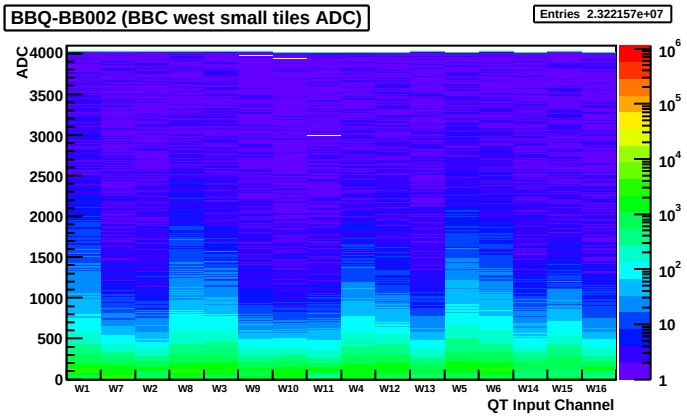

MIX-TF101
Entries 8708088
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

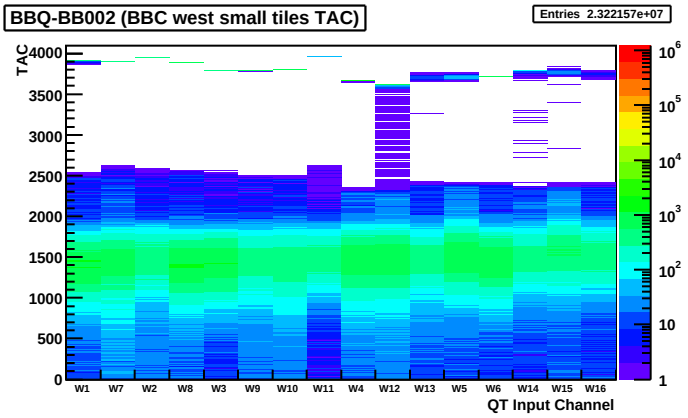

L1-TF201
Entries 1451348
10^5
10^4
10^3
10^2
10
1
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

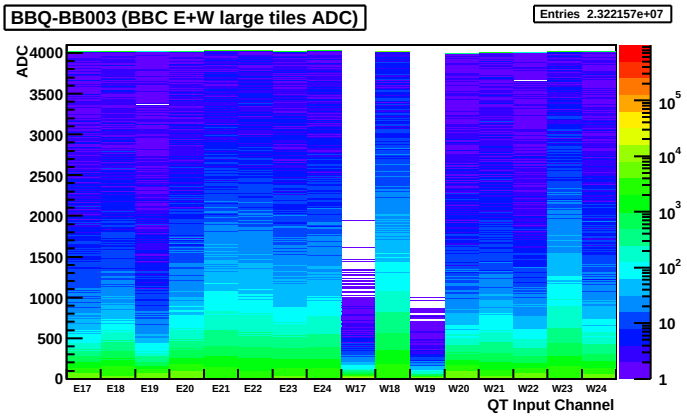

L1-TF201
Entries 8708088
Threshold bits
1
0
958093
956001
959958
953815
947242
908501
493255
495347
491390
497533
504106
542847
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

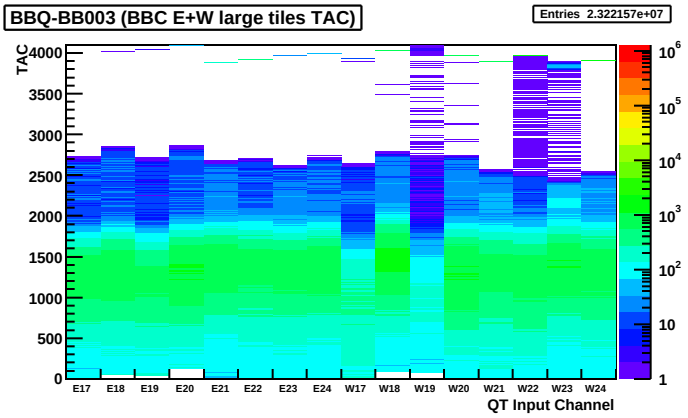

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.322157e+07
ADC
QT Input Channel

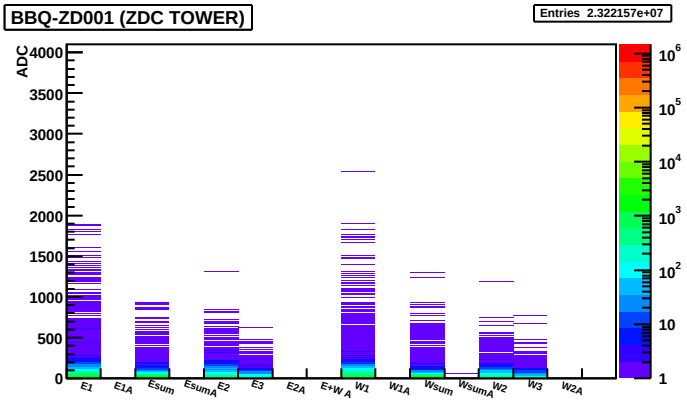

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.322157e+07
TAC
QT Input Channel

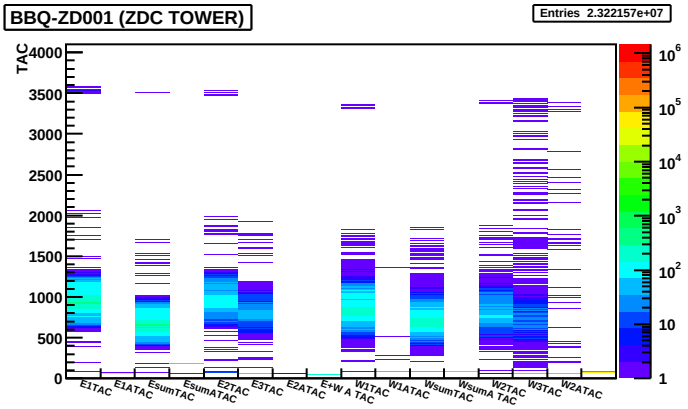

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.322157e+07
ADC
QT Input Channel

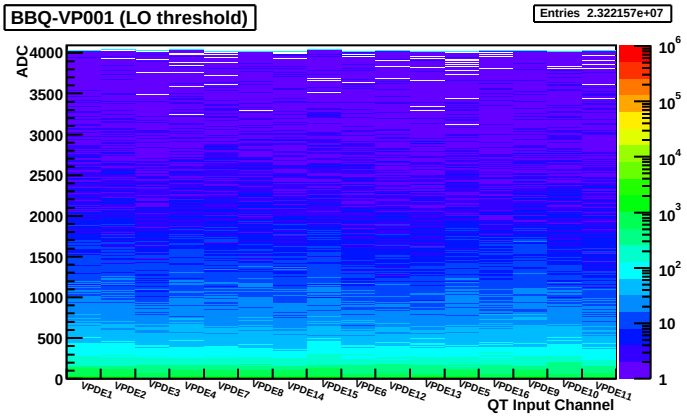

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.322157e+07
TAC
QT Input Channel

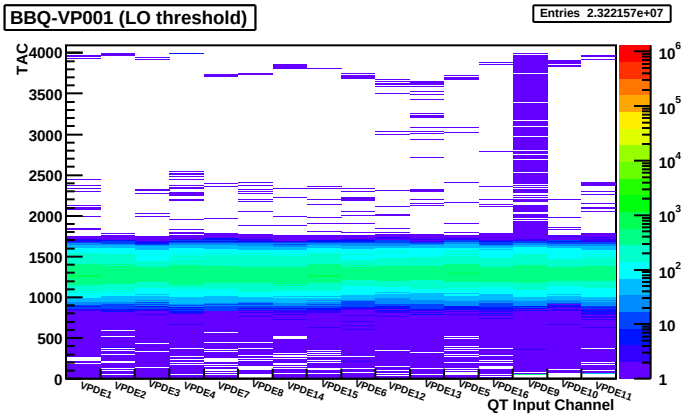

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.322157e+07
ADC
QT Input Channel

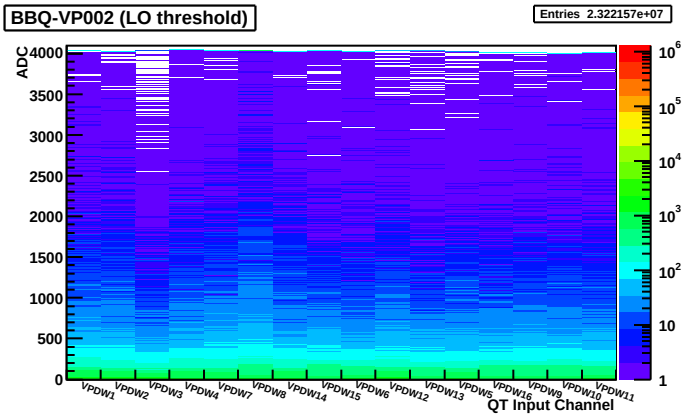

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.322157e+07
TAC
QT Input Channel

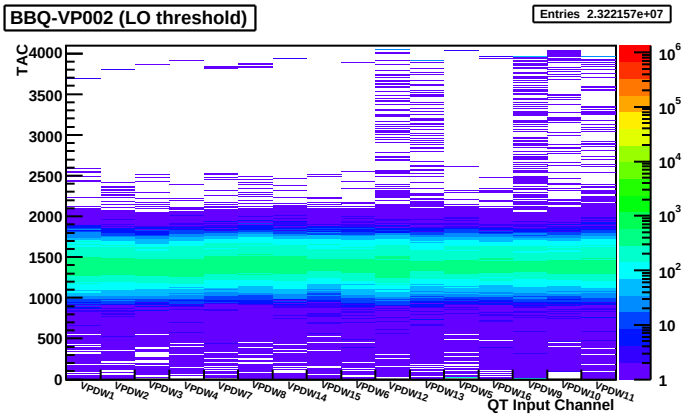

BBQ-ZD001 (ZDC TOWER)
Entries 2.322157e+07
ADC

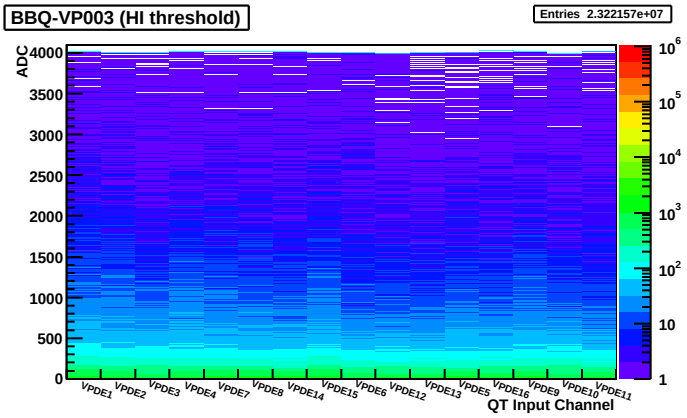

BBQ-ZD001 (ZDC TOWER)
Entries 2.322157e+07
TAC

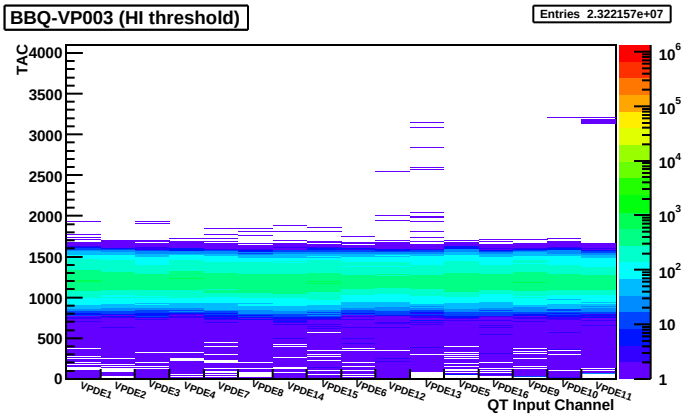

BBQ-VP001 (LO threshold)
Entries 2.322157e+07
ADC
QT Input Channel

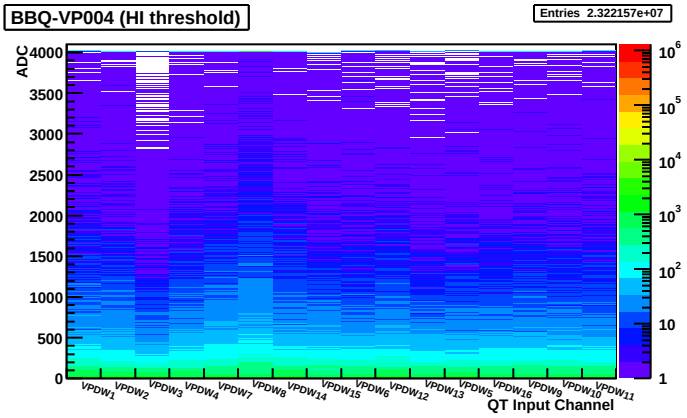

BBQ-VP001 (LO threshold)
Entries 2.322157e+07
TAC
QT Input Channel

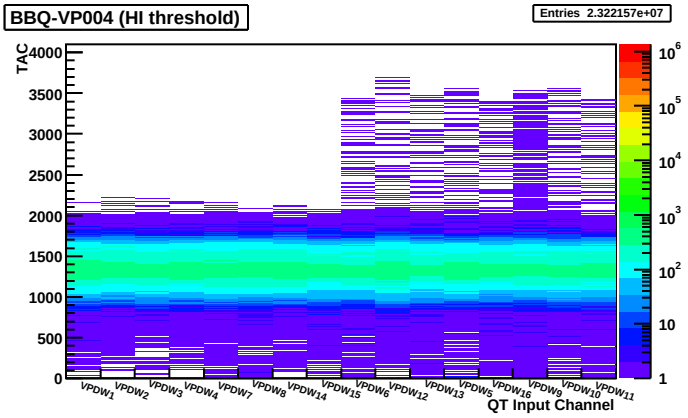

BBQ-VP002 (LO threshold)
Entries 2.322157e+07
ADC
QT Input Channel

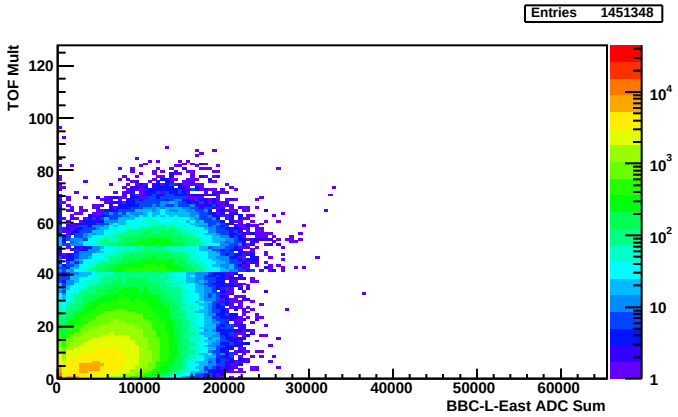

BBQ-VP002 (LO threshold)
Entries 2.322157e+07
TAC
QT Input Channel

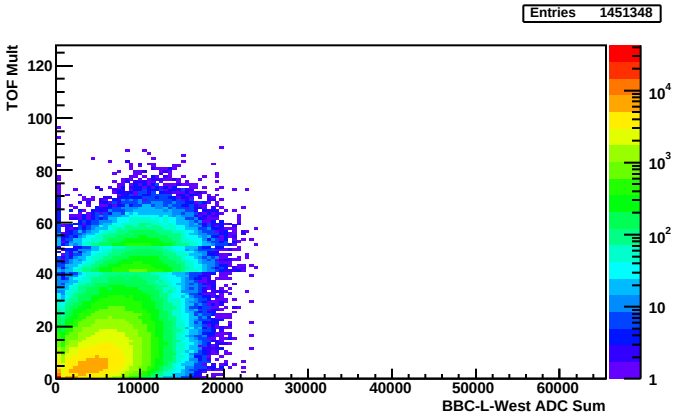

BBQ-VP003 (HI threshold)
Entries 2.322157e+07
ADC
QT Input Channel

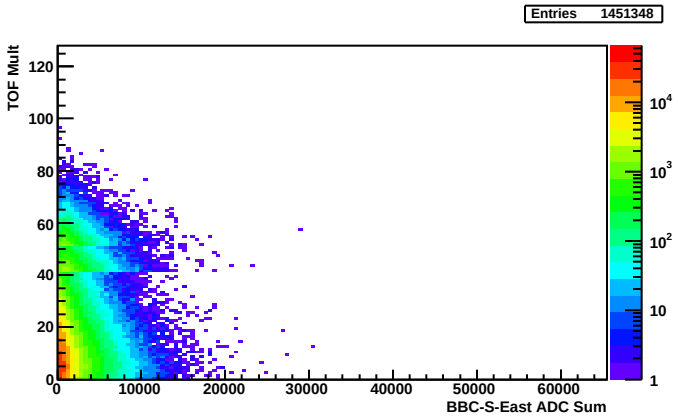

BBQ-VP003 (HI threshold)
Entries 2.322157e+07
TAC
QT Input Channel

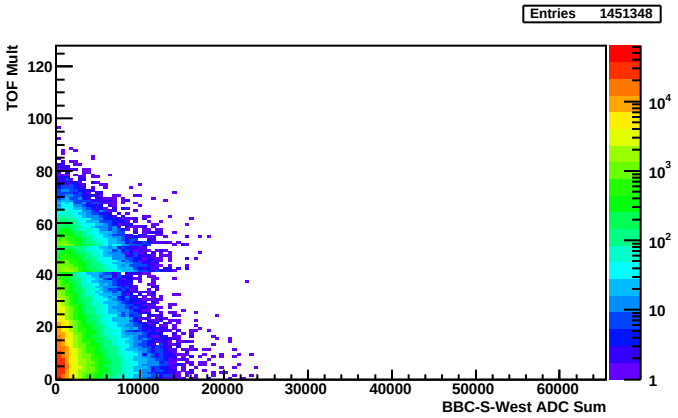

BBQ-VP004 (HI threshold)
Entries 2.322157e+07
ADC
QT Input Channel

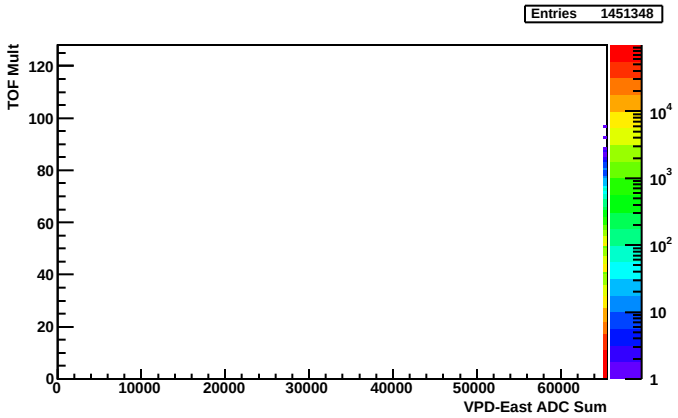

BBQ-VP004 (HI threshold)
Entries 2.322157e+07
TAC
QT Input Channel

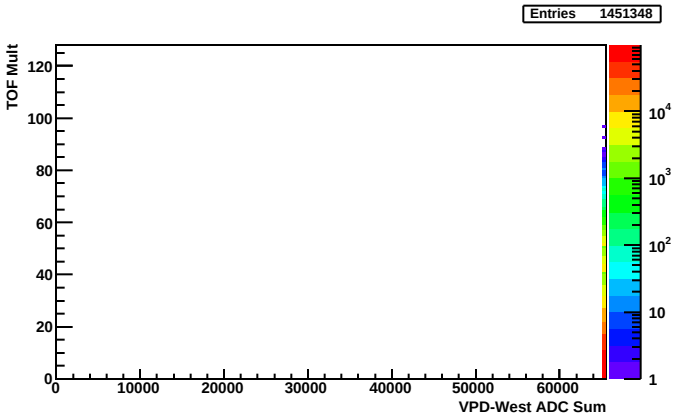

Entries 1451348
TOF Mult
BBC-L-East ADC Sum

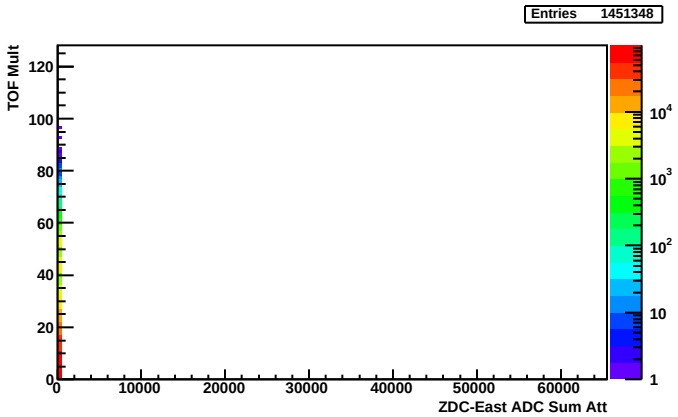

Entries 1451348
TOF Mult
BBC-L-West ADC Sum

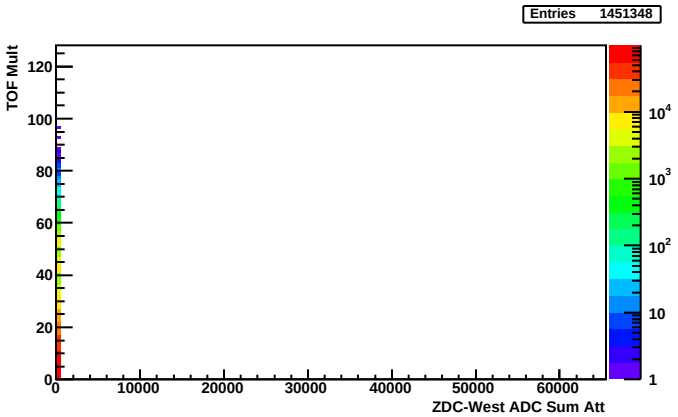

Entries 1451348
TOF Mult
BBC-S-East ADC Sum

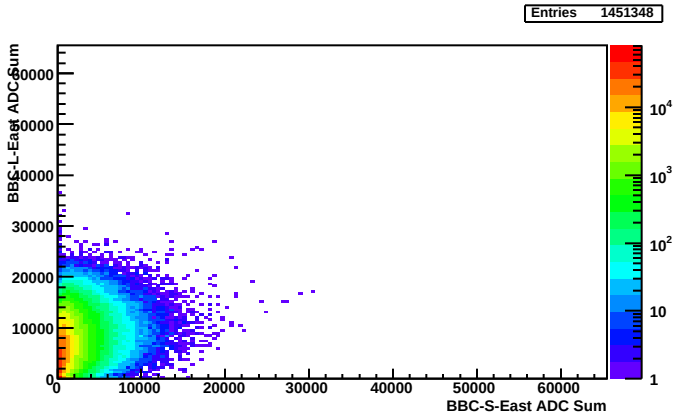

Entries 1451348
TOF Mult
BBC-S-West ADC Sum


Entries 1451348
TOF Mult
VPD-East ADC Sum


Entries 1451348
TOF Mult
VPD-West ADC Sum


Entries 1451348
TOF Mult
ZDC-East ADC Sum Att


Entries 1451348
TOF Mult
ZDC-West ADC Sum Att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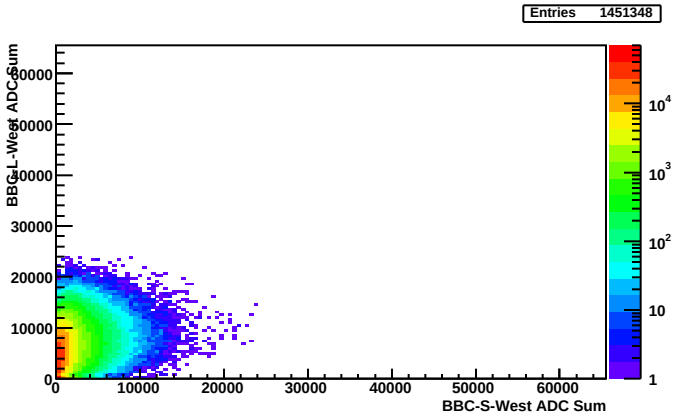
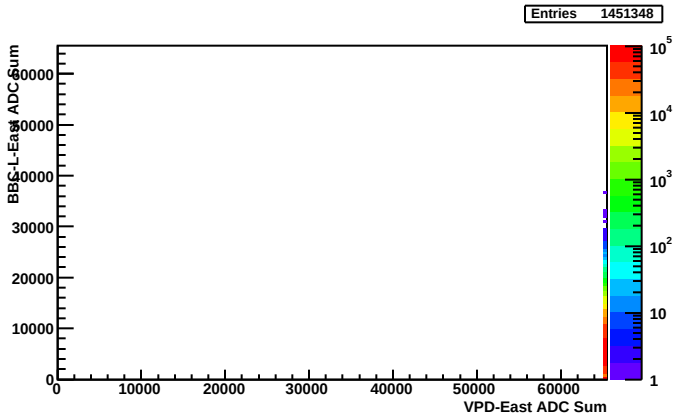
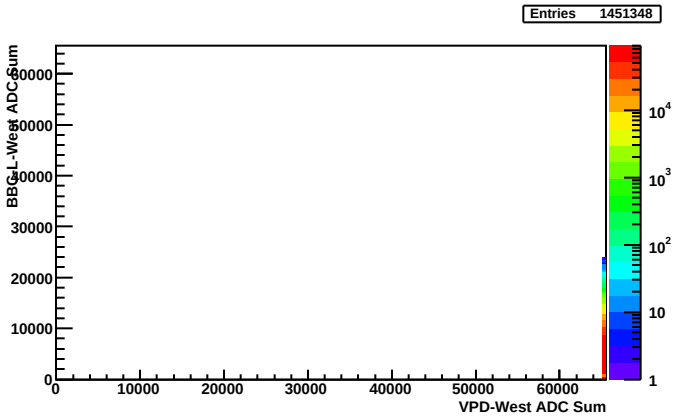
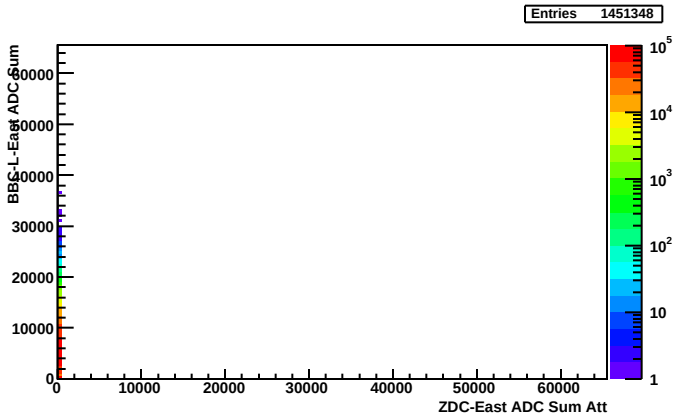
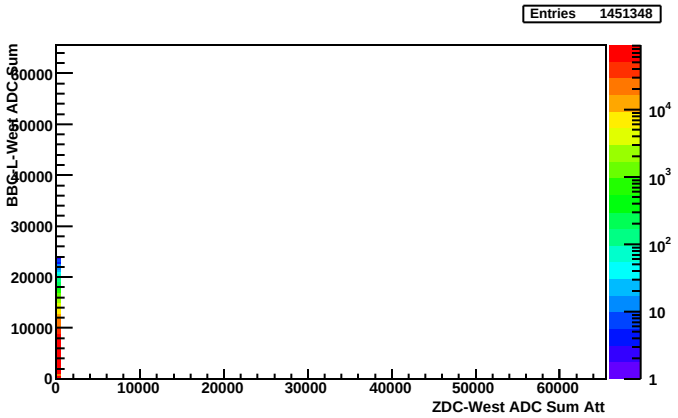
BBC-L-East ADC Sum
BBC-S-East ADC Sum

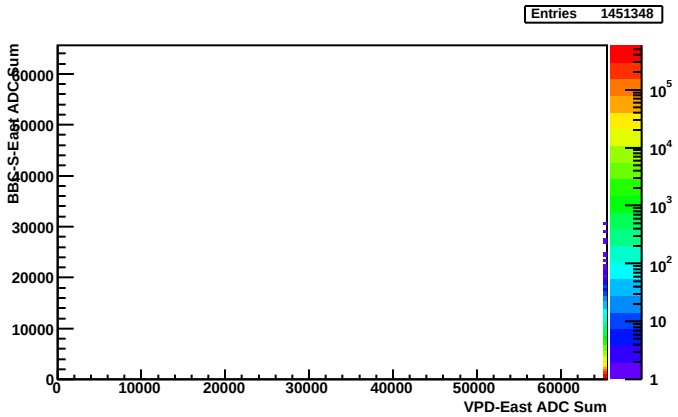

Entries 1451348
BBC-L-West ADC Sum
BBC-S-West ADC Sum

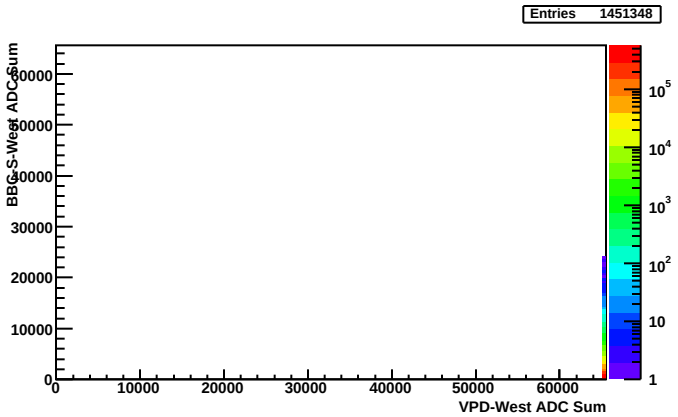

Entries 1451348
BBC-L-East ADC Sum
VPD-East ADC Sum

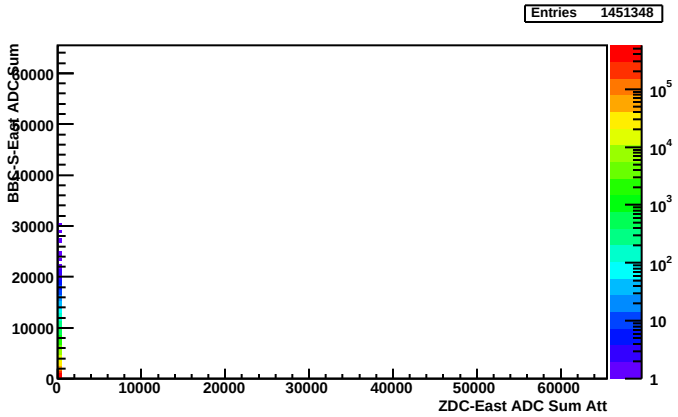

Entries 1451348
BBC-L-West ADC Sum
VPD-West ADC Sum

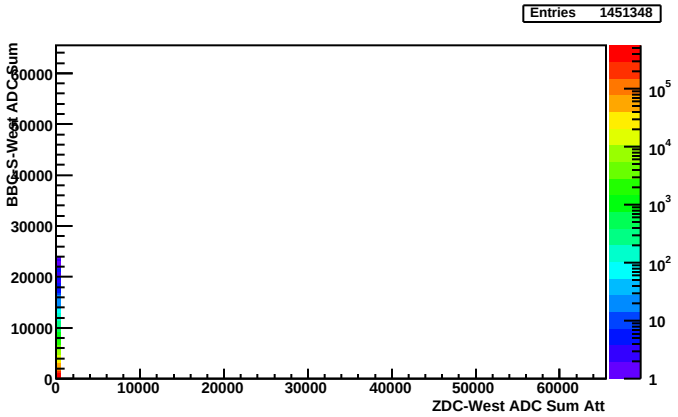

Entries 1451348
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

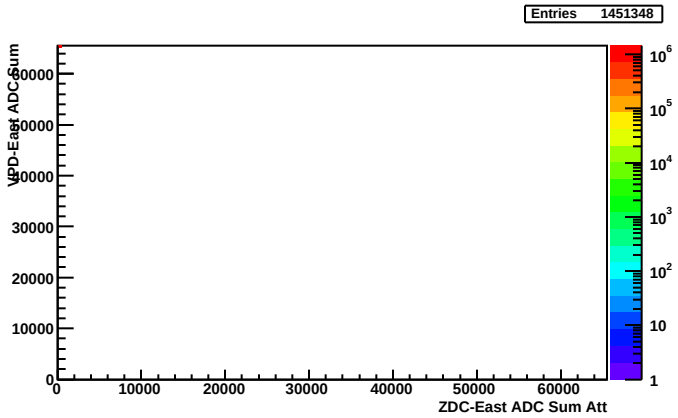

Entries 1451348
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

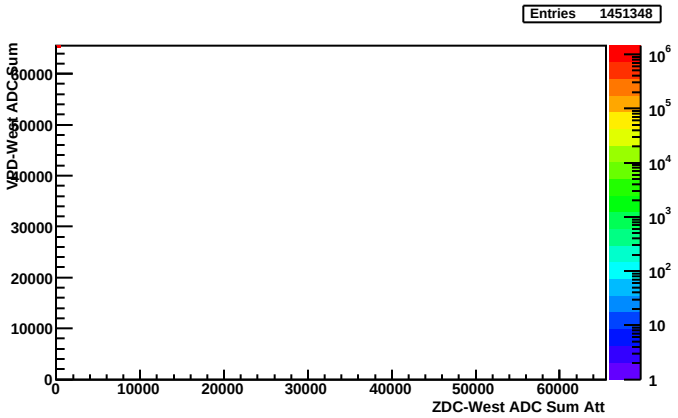

Entries 1451348
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 1451348
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 1451348
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 1451348
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1451348
VPD-East ADC Sum
ZDC-East ADC Sum Att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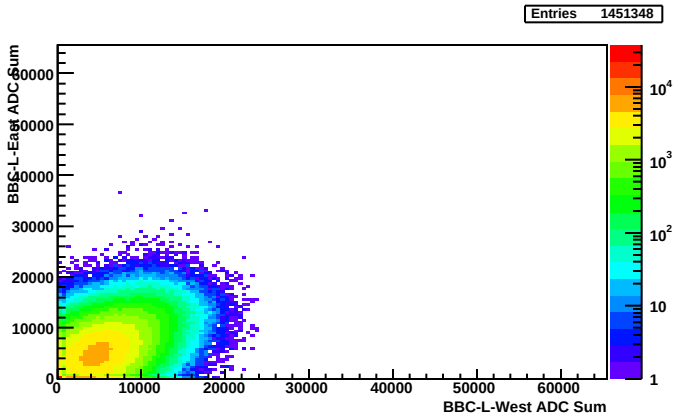
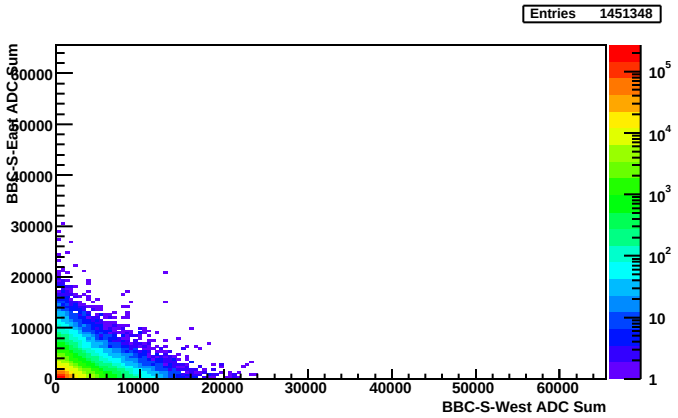
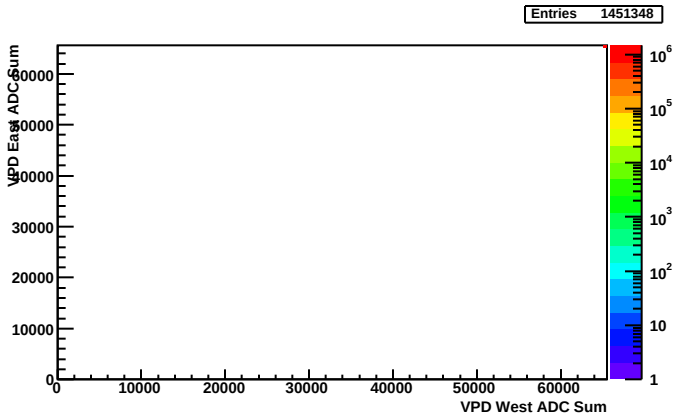
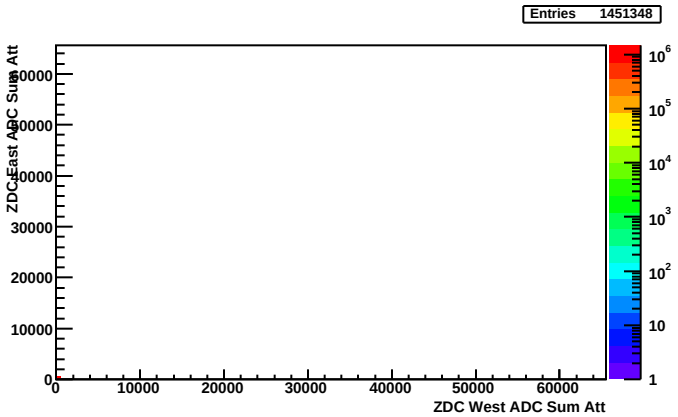
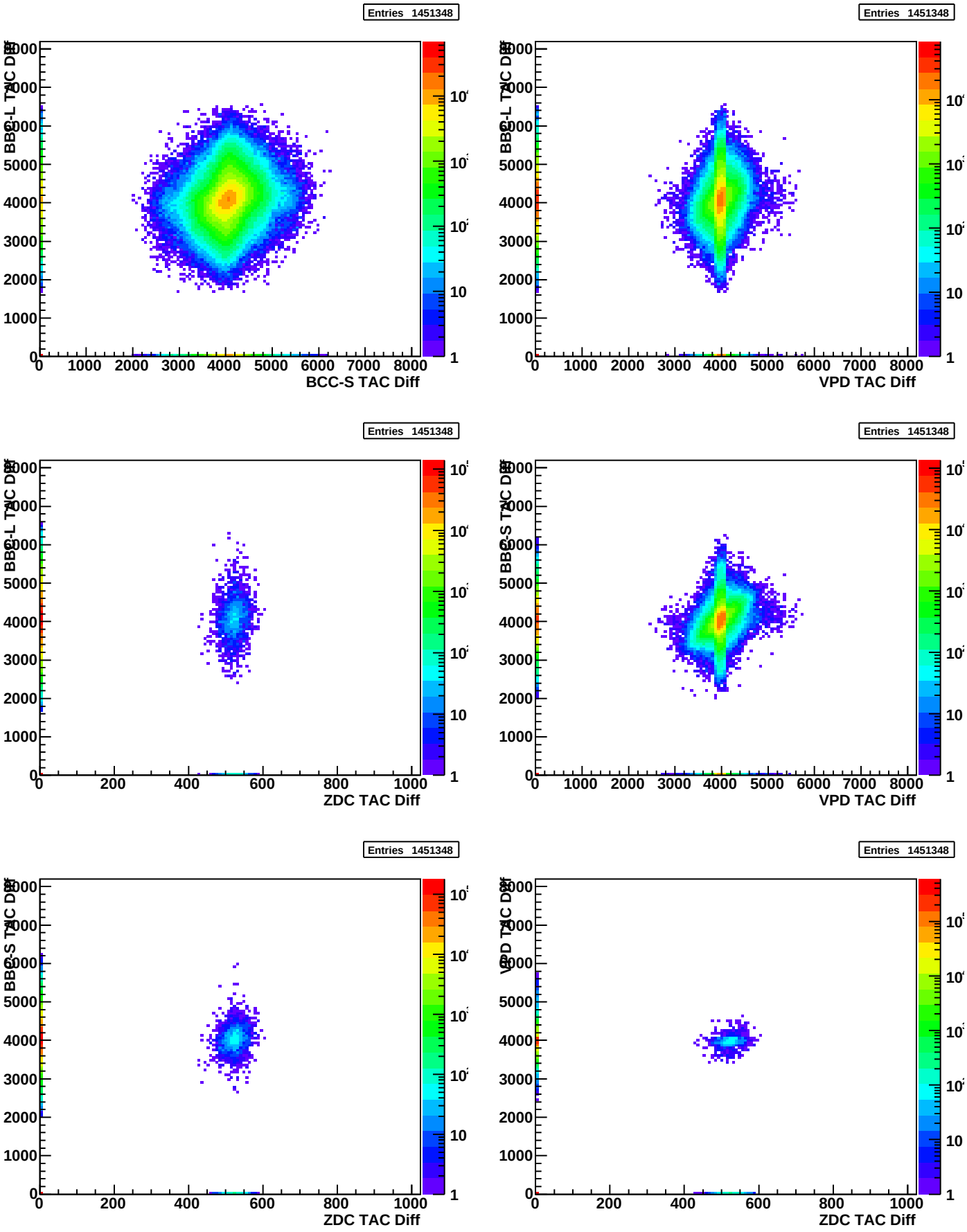
VPD-West ADC Sum
ZDC-West ADC Sum Att

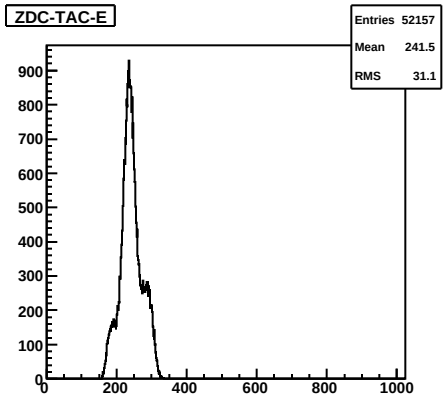

Entries 1451348
BBC-L-East ADC Sum
BBC-L-West ADC Sum

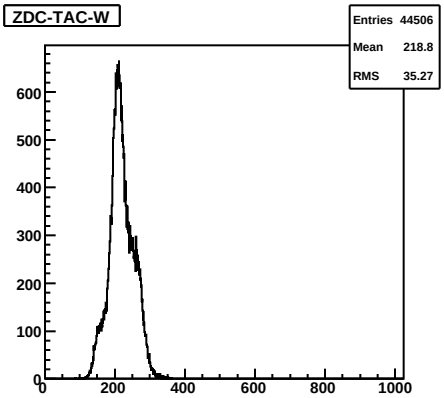

Entries 1451348
BBC-S-East ADC Sum
BBC-S-West ADC Sum

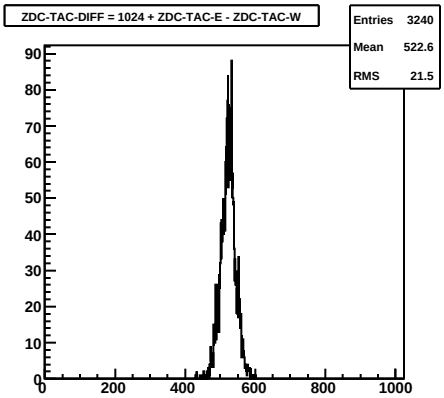

Entries 1451348
VPD East ADC Sum
VPD West ADC Sum

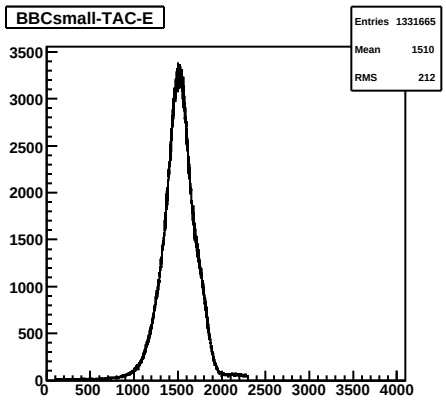

Entries 1451348
ZDC-East ADC Sum Att
ZDC West ADC Sum Att

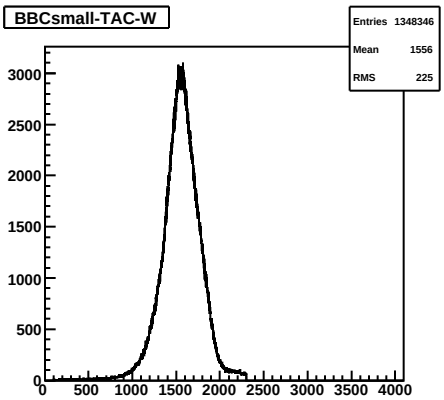

Entries 1451348
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1451348
BBC-L TAC Diff
VPD TAC Diff
Entries 1451348
BBC-L TAC Diff
ZDC TAC Diff
Entries 1451348
BBC-S TAC Diff
VPD TAC Diff
Entries 1451348
BBC-S TAC Diff
ZDC TAC Diff
Entries 1451348
VPD TAC Diff
ZDC TAC Diff

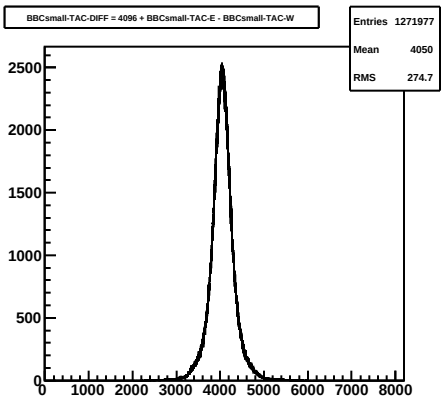

ZDC-TAC-E
Entries 52157
Mean 241.5
RMS 31.1

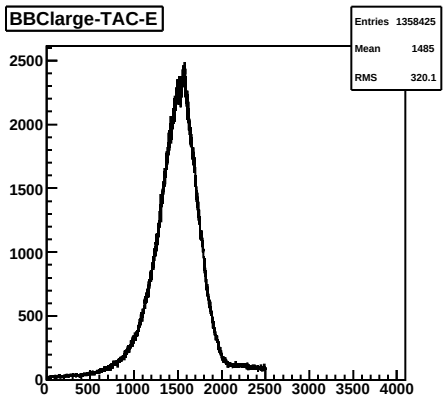

ZDC-TAC-W
Entries 44506
Mean 218.8
RMS 35.27

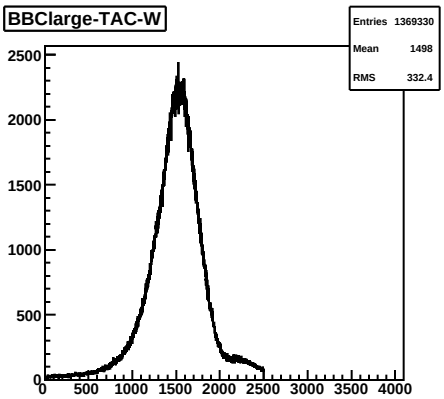

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 3240
Mean 522.6
RMS 21.5

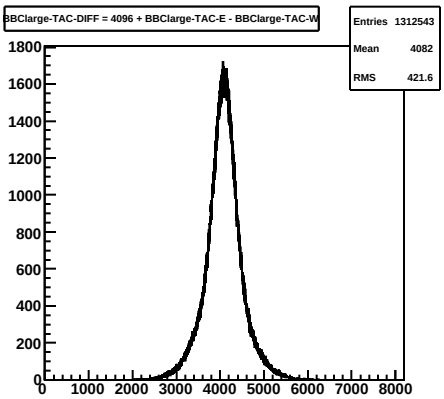

BBCsmall-TAC-E
Entries 1331665
Mean 1510
RMS 212

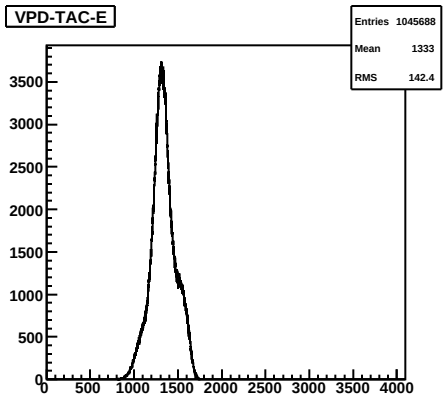

BBCsmall-TAC-W
Entries 1348346
Mean 1556
RMS 225

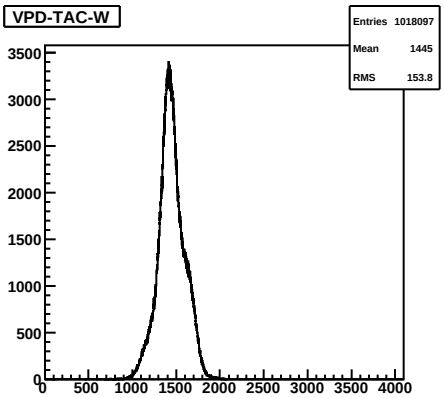

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1271977
Mean 4050
RMS 274.7

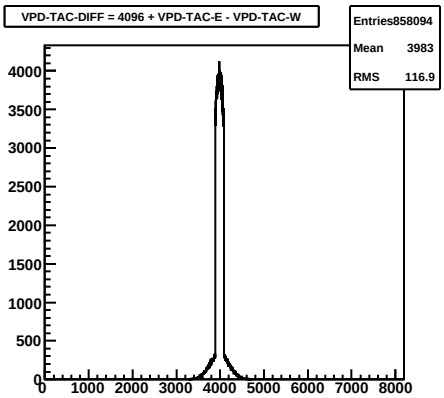

BBClarge-TAC-E
Entries 1358425
Mean 1485
RMS 320.1

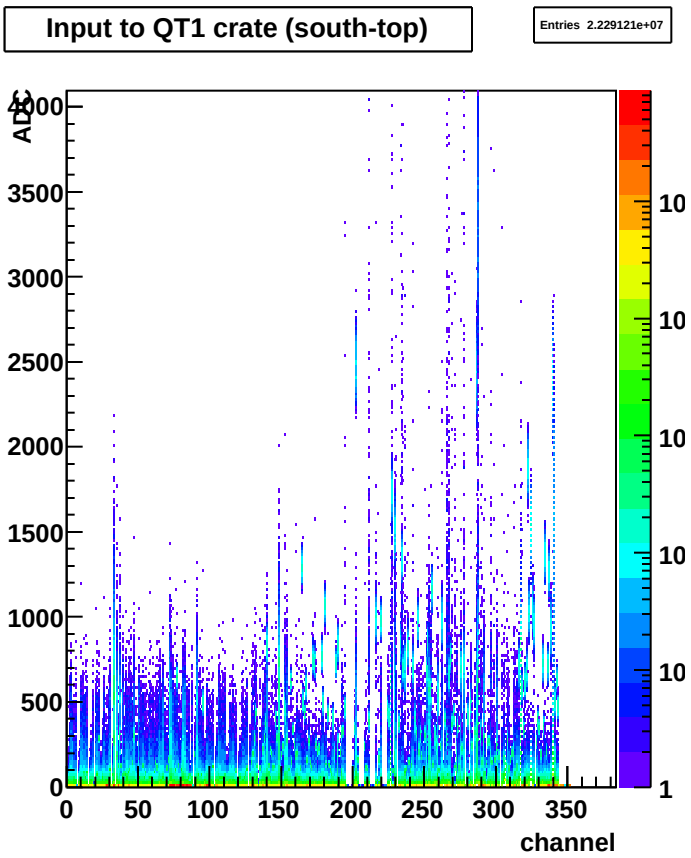

BBClarge-TAC-W
Entries 1369330
Mean 1498
RMS 332.4

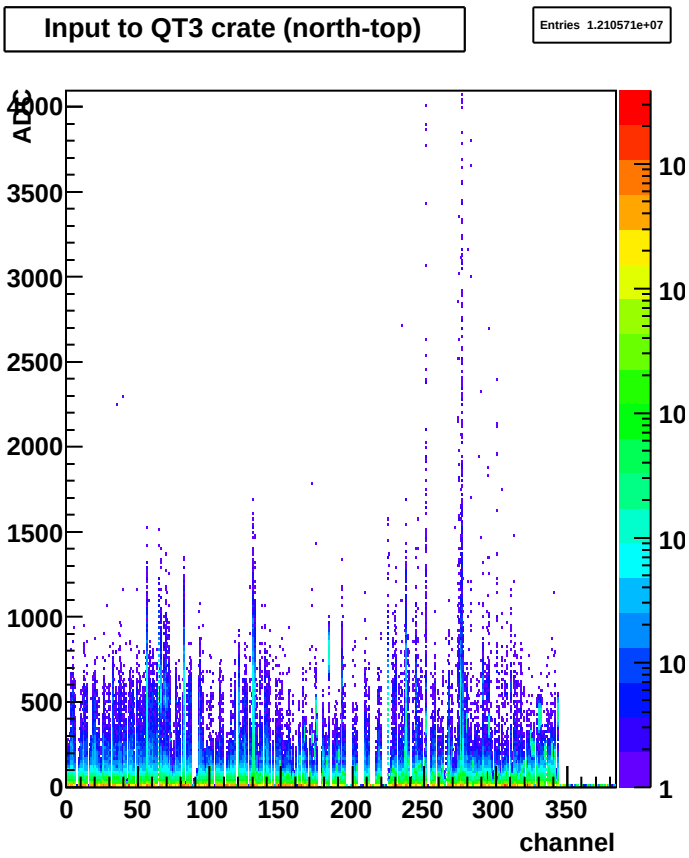

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1312543
Mean 4082
RMS 421.6

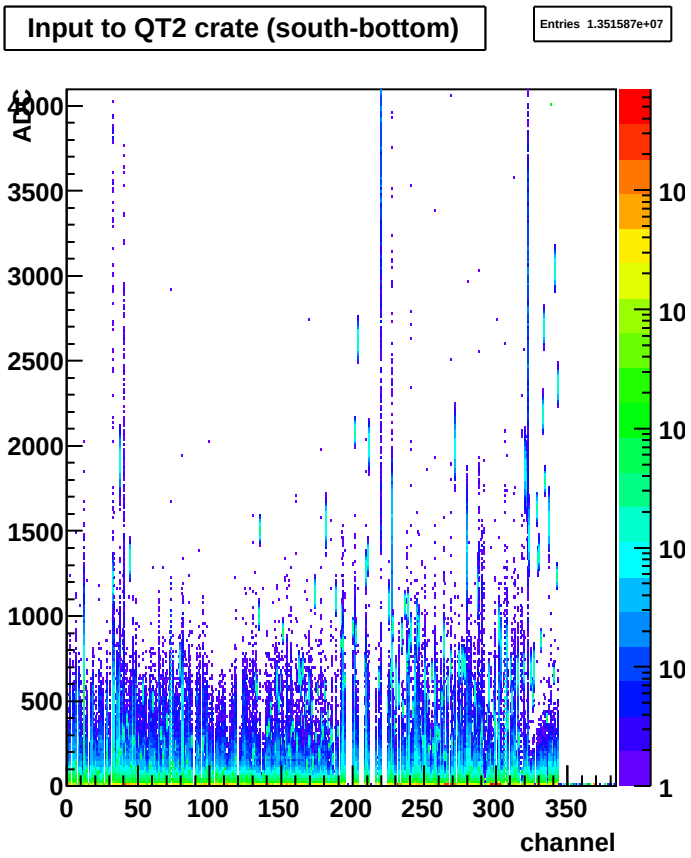

VPD-TAC-E
Entries 1045688
Mean 1333
RMS 142.4

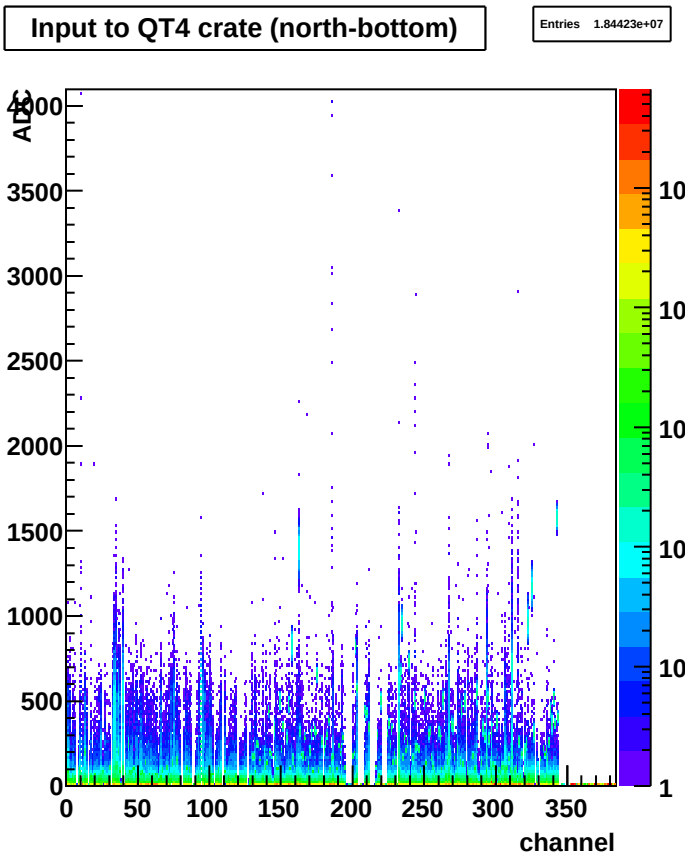

VPD-TAC-W
Entries 1018097
Mean 1445
RMS 153.8

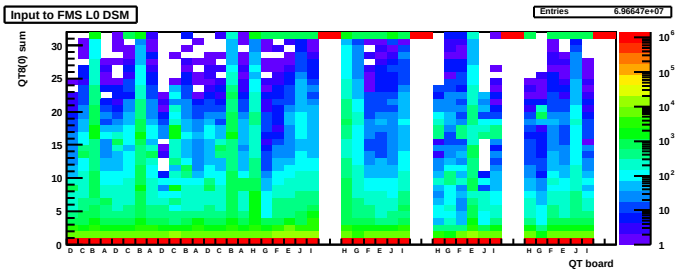

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries858094
Mean 3983
RMS 116.9

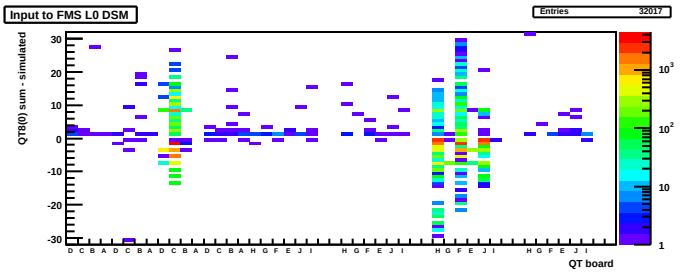

Input to QT1 crate (south-top)
Entries 2.229121e+07
ADC
channel

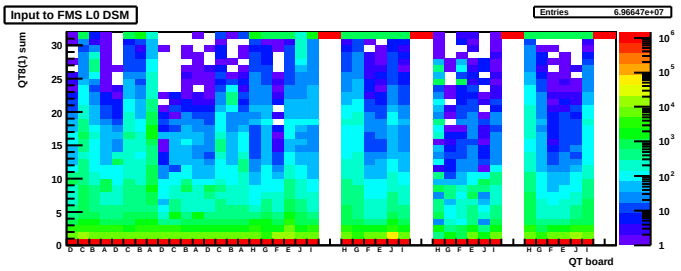

Input to QT3 crate (north-top)
Entries 1.210571e+07
ADC
channel

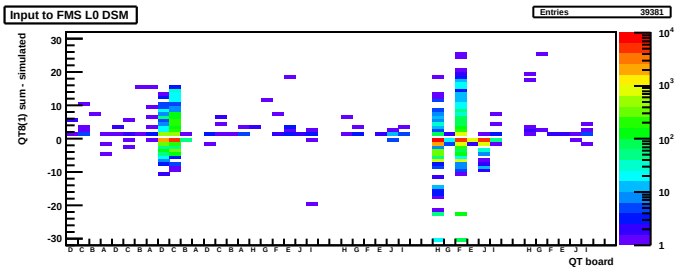

Input to QT2 crate (south-bottom)
Entries 1.351587e+07
ADC
channel

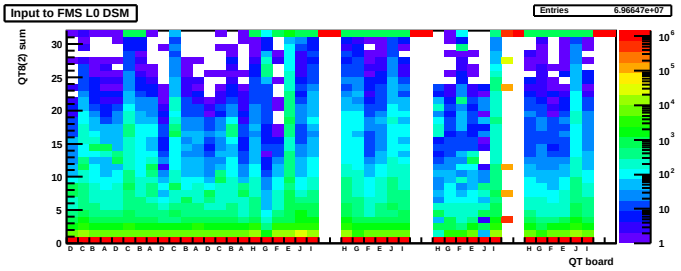

Input to QT4 crate (north-bottom)
Entries 1.84423e+07
ADC
channel

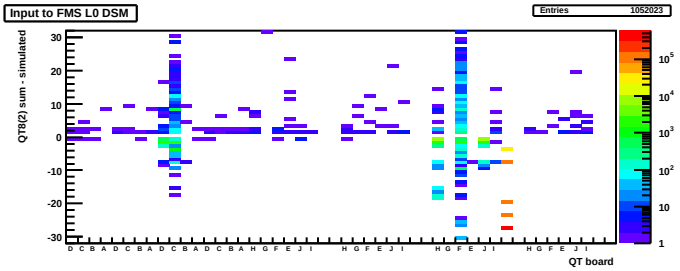

Input to FMS L0 DSM
Entries 6.96647e+07
QT8(0) sum
QT board

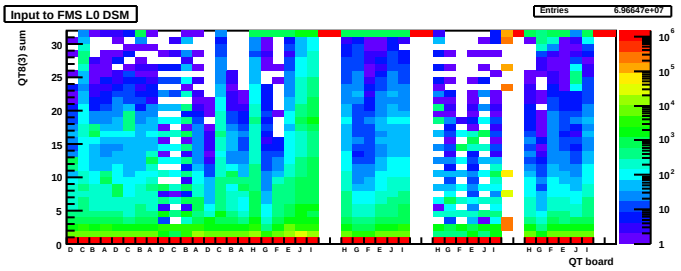

Input to FMS L0 DSM
Entries 32017
QT8(0) sum - simulated
QT board

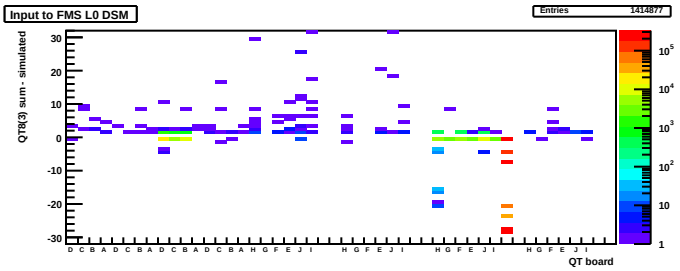

Input to FMS L0 DSM
Entries 6.96647e+07
QT8(1) sum
QT board

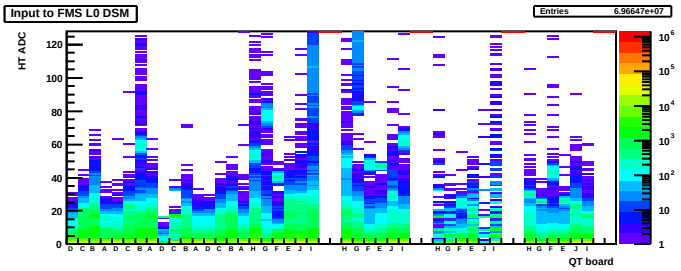

Input to FMS L0 DSM
Entries 39381
QT8(1) sum - simulated
QT board

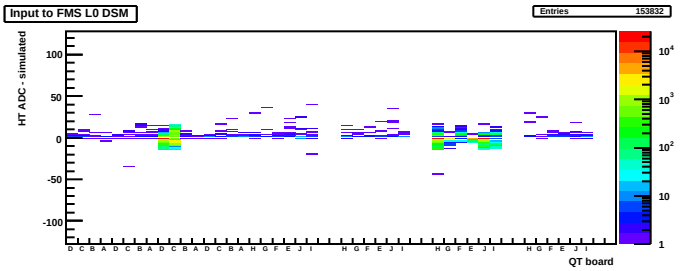

Input to FMS L0 DSM
Entries 6.96647e+07
QT8(2) sum
QT board

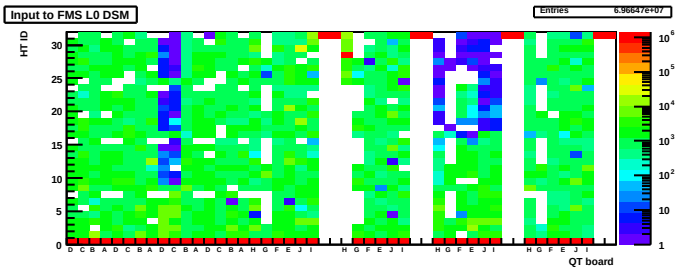

Input to FMS L0 DSM
Entries 1052023
QT8(2) sum - simulated
QT board

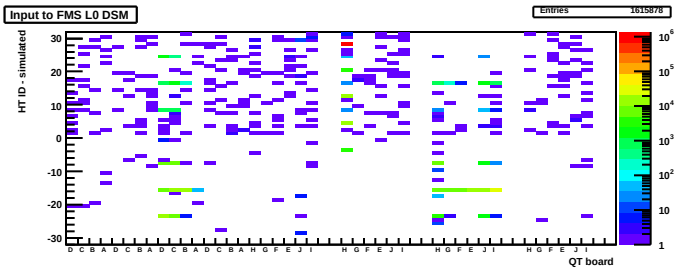

Input to FMS L0 DSM
Entries 6.96647e+07
QT8(3) sum
QT board

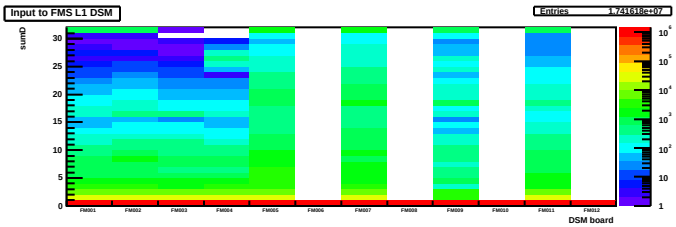

Input to FMS L0 DSM
Entries 1414877
QT8(3) sum - simulated
QT board

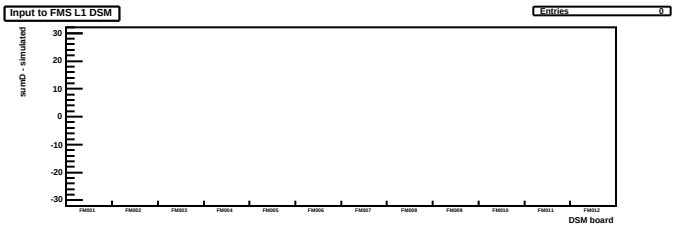

Input to FMS L0 DSM
Entries 6.96647e+07
HT ADC
QT board

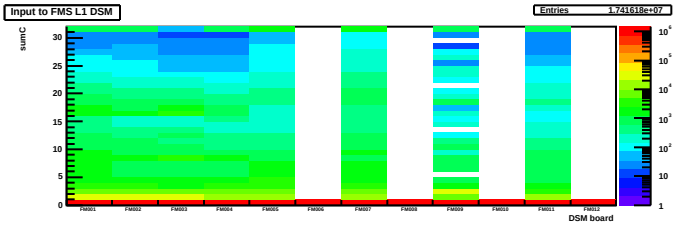

Input to FMS L0 DSM
Entries 153832
HT ADC - simulated
QT board

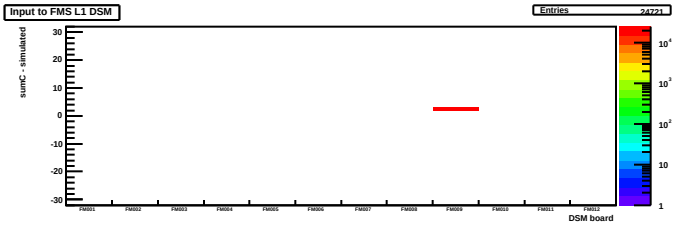

Input to FMS L0 DSM
Entries 6.96647e+07
HT ID
QT board

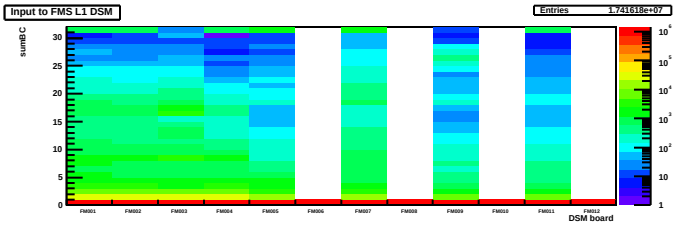

Input to FMS L0 DSM
Entries 1615878
HT ID - simulated
QT board

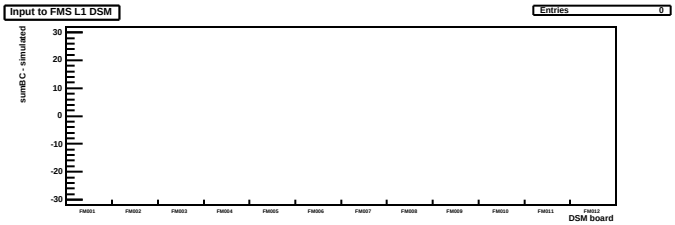

Input to FMS L1 DSM
Entries 1.741618e+07
sumD
DSM board

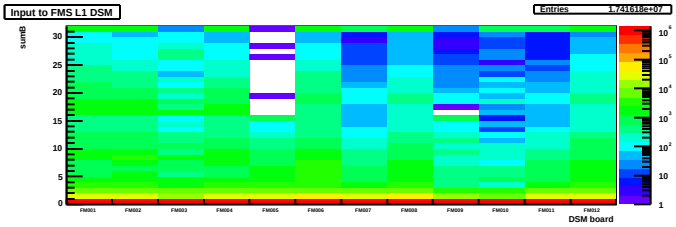

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

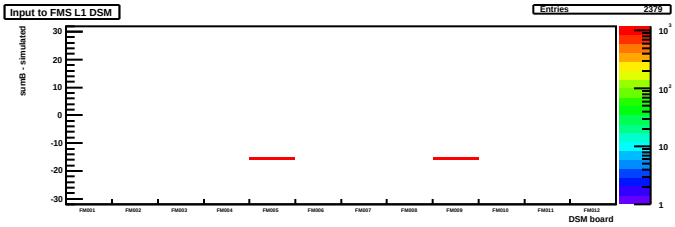

Input to FMS L1 DSM
Entries 1.741618e+07
sumC
DSM board

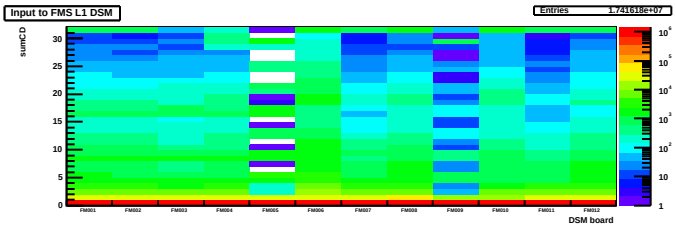

Input to FMS L1 DSM
Entries 24721
sumC - simulated
DSM board

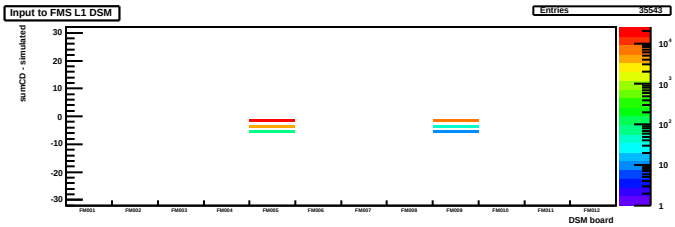

Input to FMS L1 DSM
Entries 1.741618e+07
sumBC
DSM board

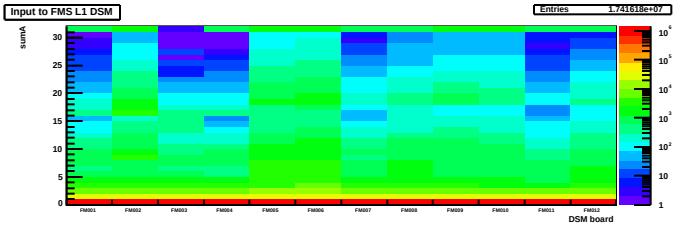

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

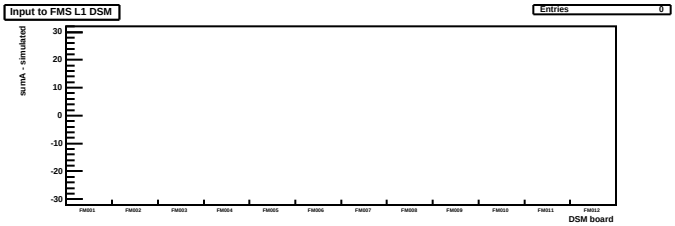

Input to FMS L1 DSM
Entries 1.741618e+07
sumB
DSM board

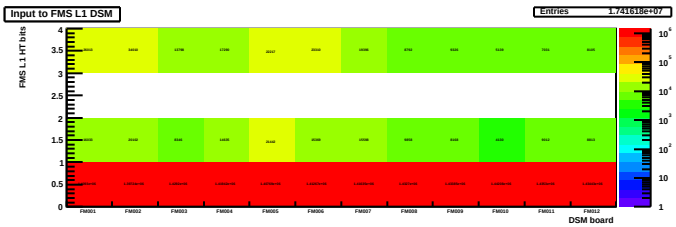

Input to FMS L1 DSM
Entries 2379
sumB - simulated
DSM board

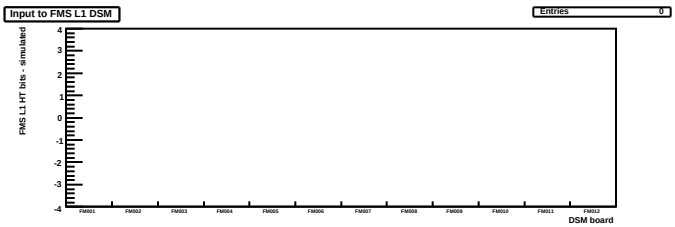

Input to FMS L1 DSM
Entries 1.741618e+07
sumCD
DSM board

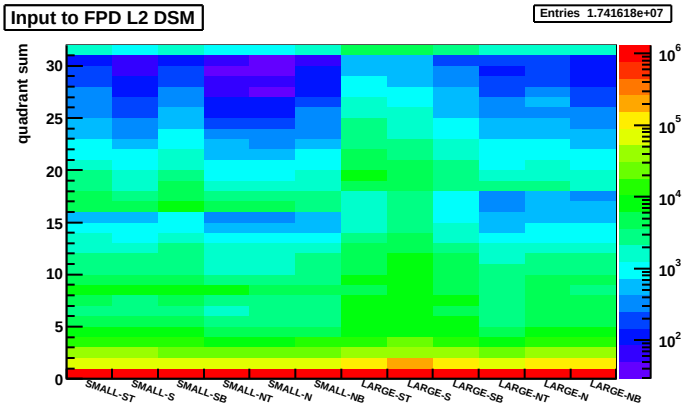

Input to FMS L1 DSM
Entries 35543
sumCD - simulated
DSM board

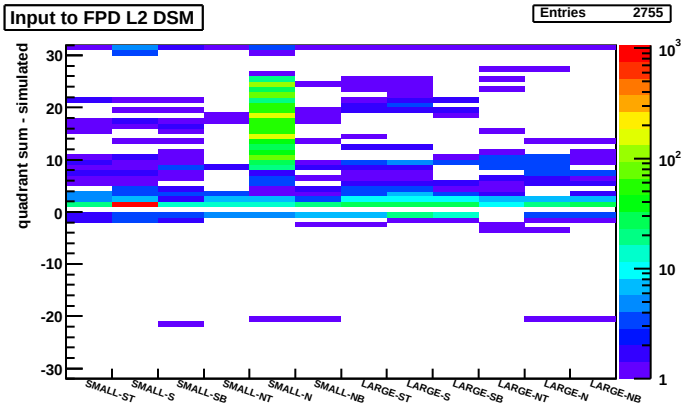

Input to FMS L1 DSM
Entries 1.741618e+07
sumA
DSM board

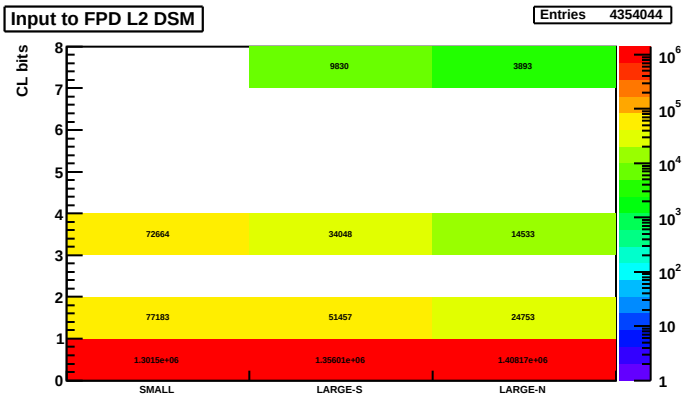

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

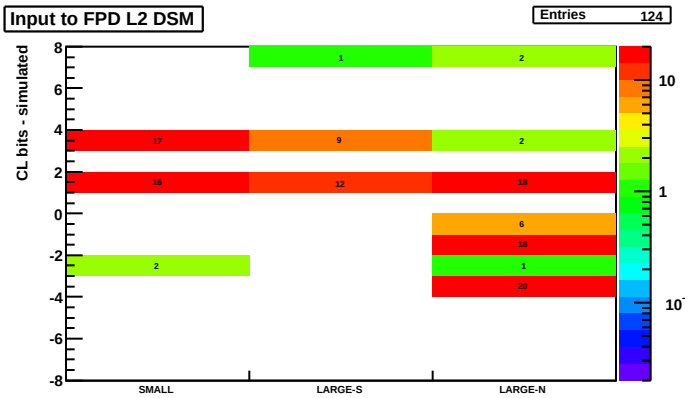

Input to FMS L1 DSM
Entries 1.741618e+07
FMS L1 HT bits
DSM board

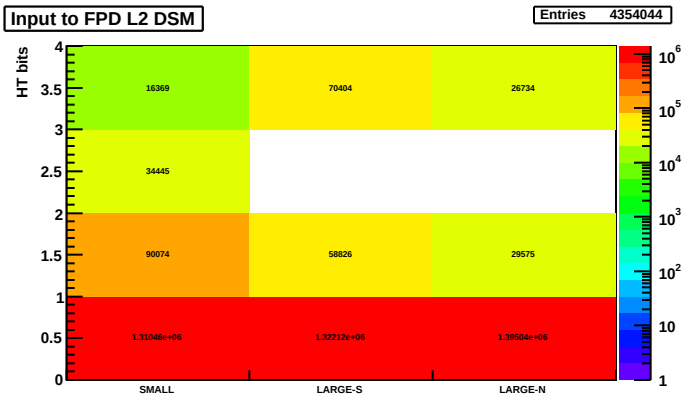

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

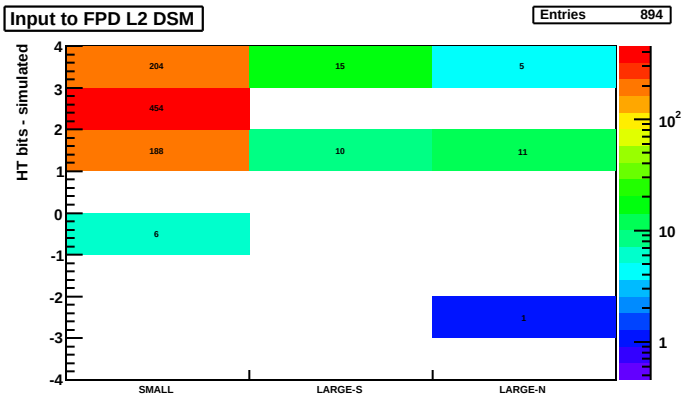

Input to FPD L2 DSM
Entries 1.741618e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

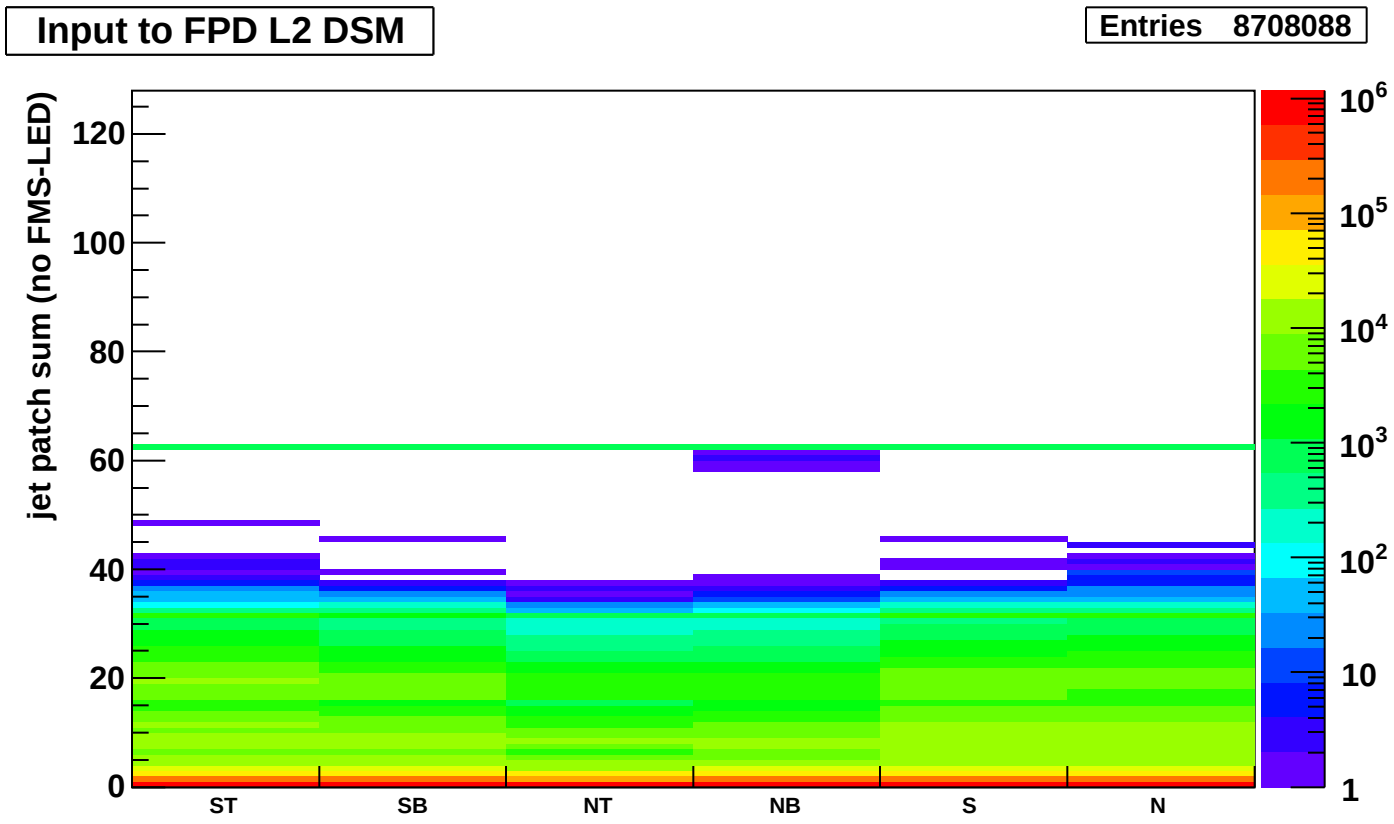

Input to FPD L2 DSM
Entries 2755
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

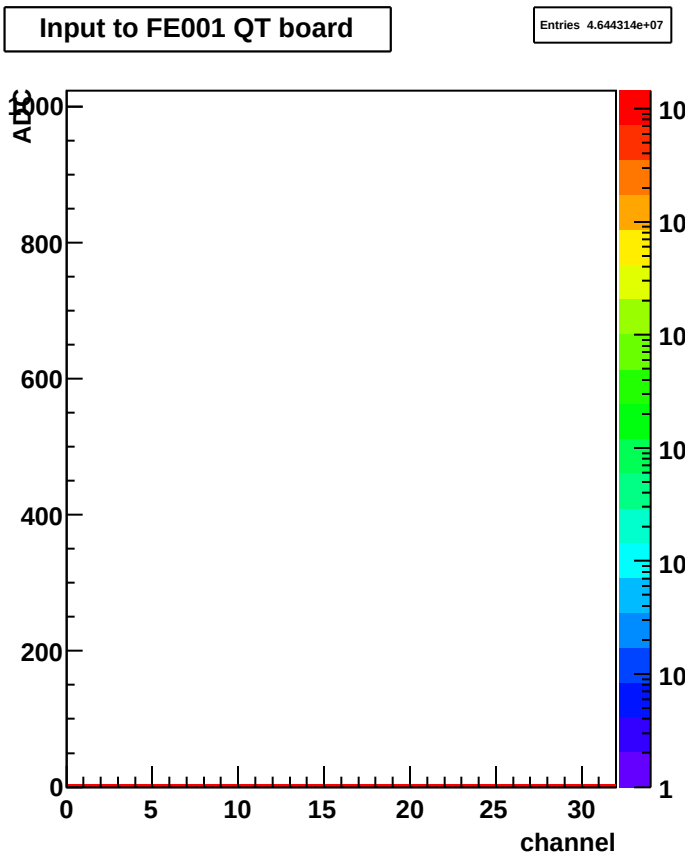

Input to FPD L2 DSM
Entries 4354044
CL bits
9830
3893
72664
34048
14533
77183
51457
24753
1.3015e+06
1.35601e+06
1.40817e+06
SMALL
LARGE-S
LARGE-N

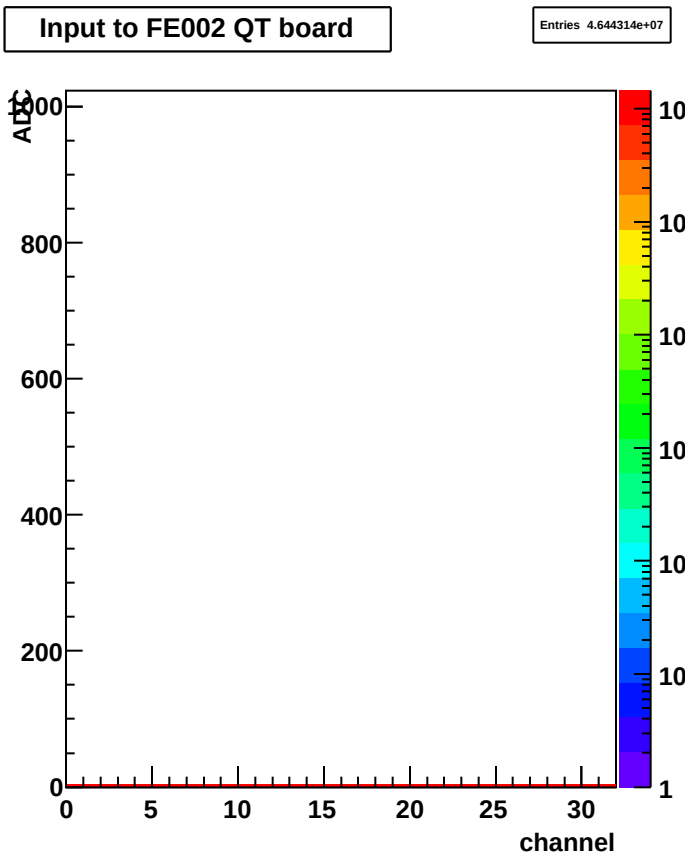

Input to FPD L2 DSM
Entries 124
CL bits - simulated
1
2
17
9
2
16
12
18
6
18
2
1
20
SMALL
LARGE-S
LARGE-N

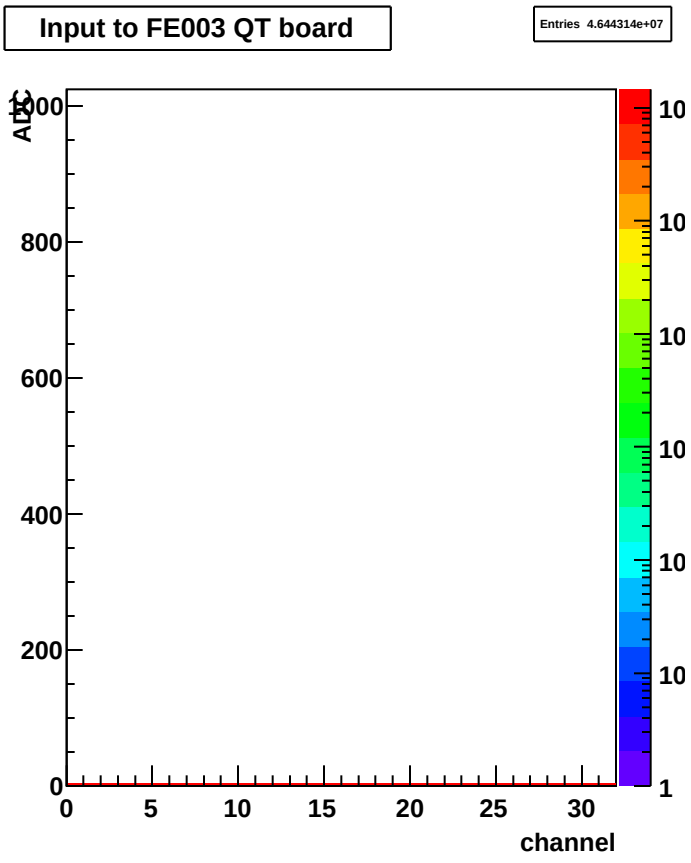

Input to FPD L2 DSM
Entries 4354044
HT bits
16369
70404
26734
34445
90074
58826
29575
1.31046e+06
1.32212e+06
1.39504e+06
SMALL
LARGE-S
LARGE-N

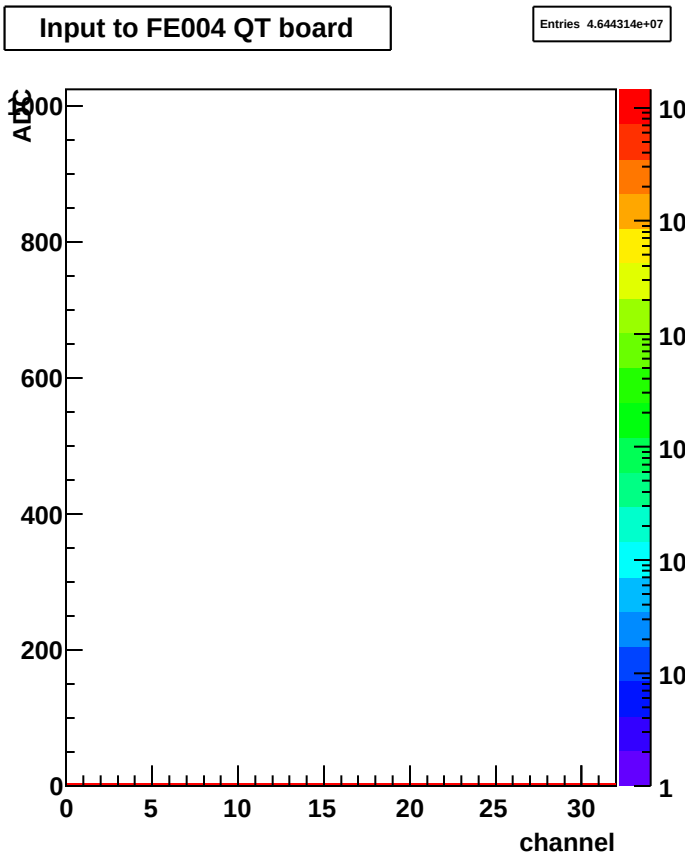

Input to FPD L2 DSM
Entries 894
HT bits - simulated
204
15
5
454
188
10
11
6
1
SMALL
LARGE-S
LARGE-N

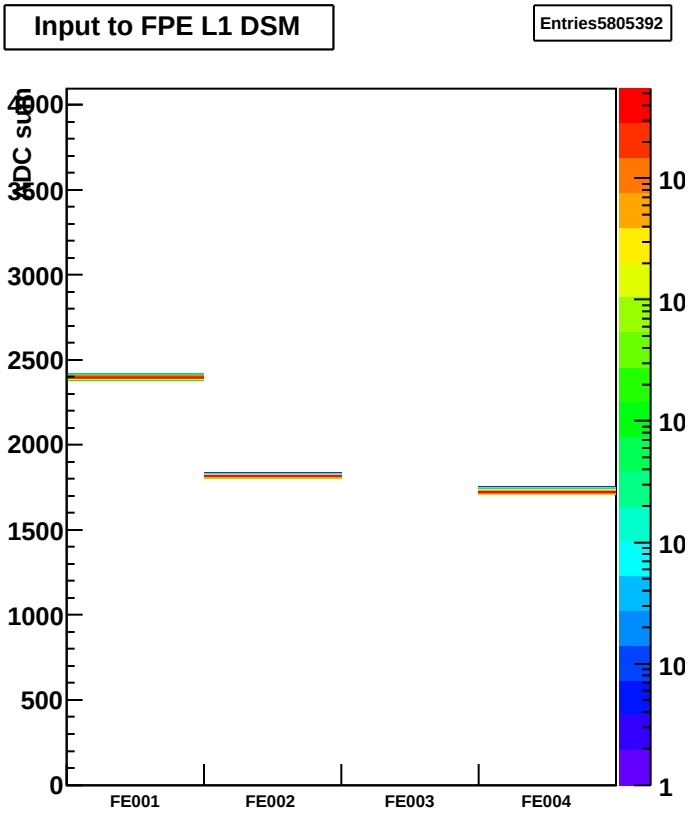

Input to FPD L2 DSM
Entries 8708088
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

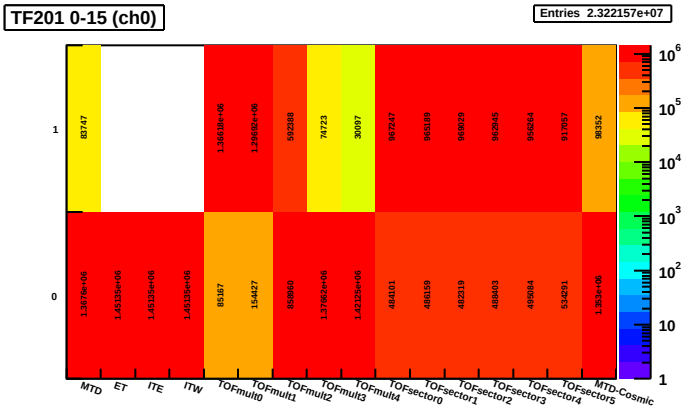

Input to FE001 QT board
Entries 4.644314e+07
ADC
channel

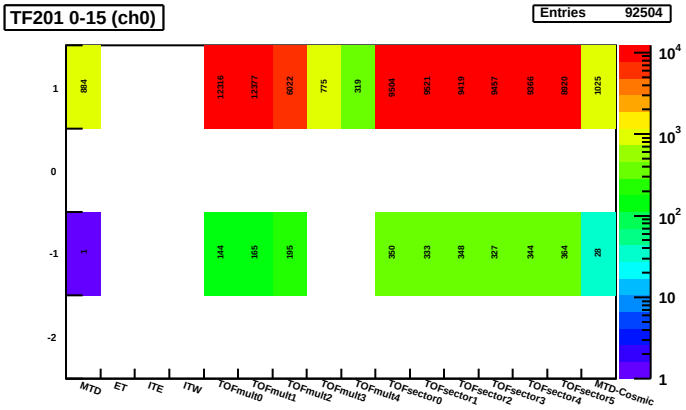

Input to FE002 QT board
Entries 4.644314e+07
ADC
channel

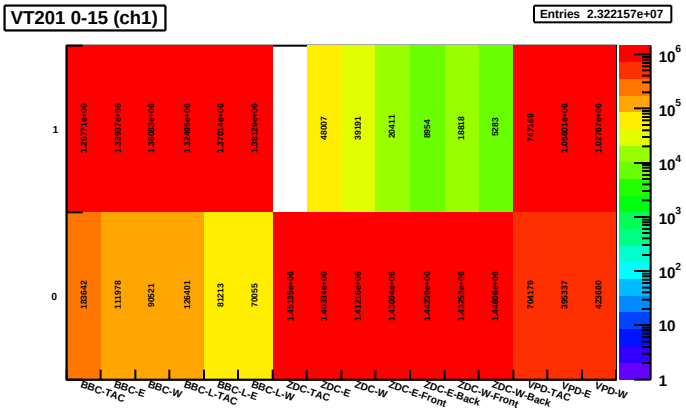

Input to FE003 QT board
Entries 4.644314e+07
ADC
channel


Input to FE004 QT board
Entries 4.644314e+07
ADC
channel


Input to FPE L1 DSM
Entries5805392
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.322157e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 92504
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic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Entries 2.322157e+07
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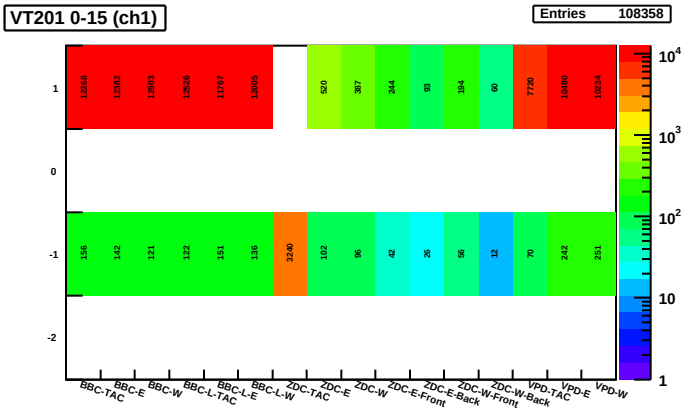
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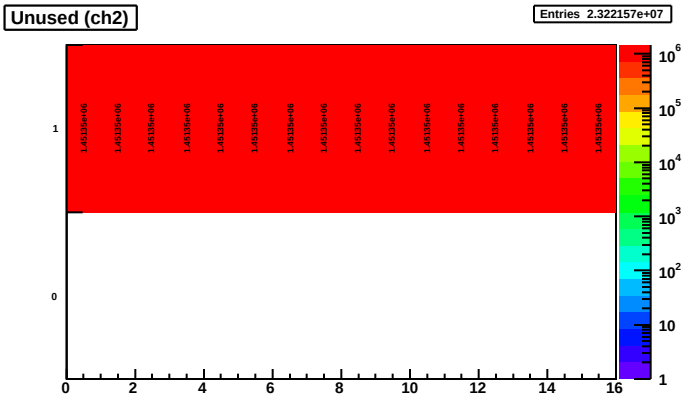
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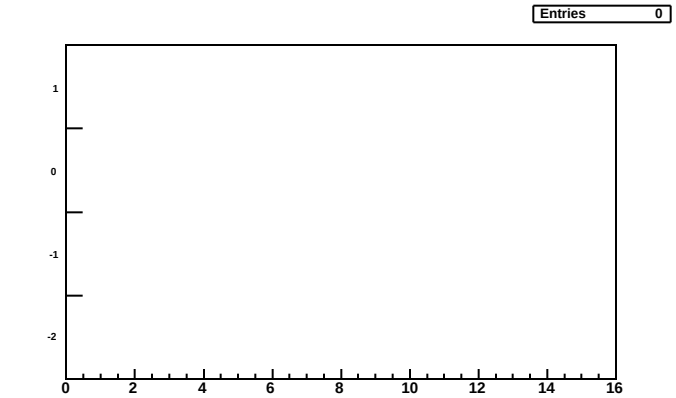
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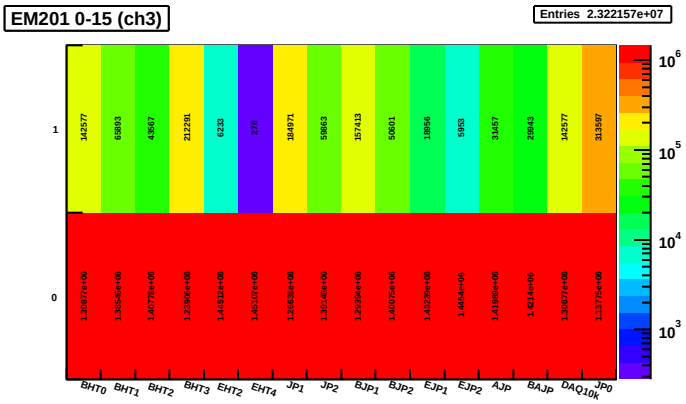
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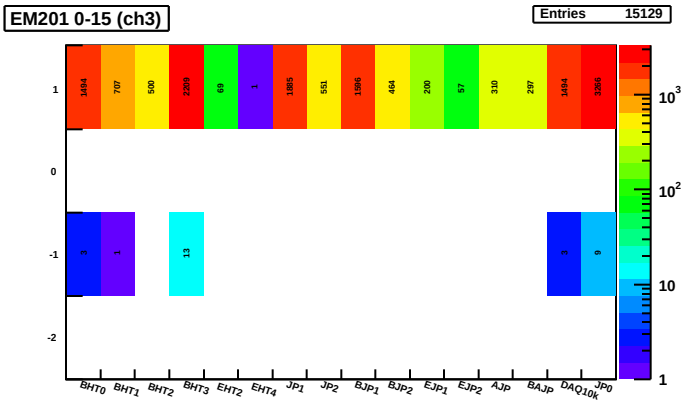
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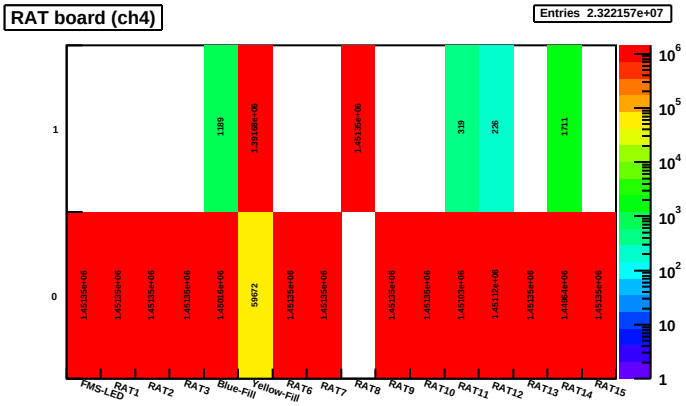
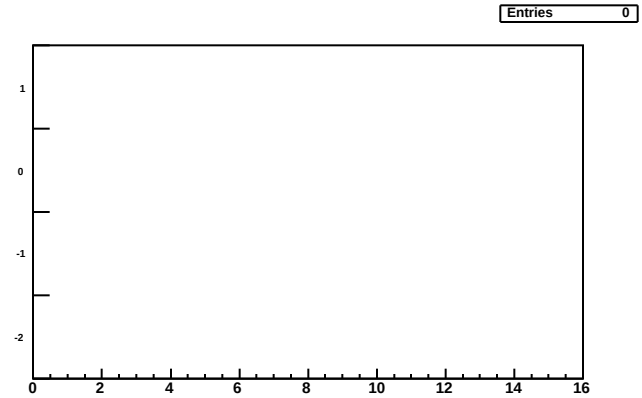
RAT board (ch4)





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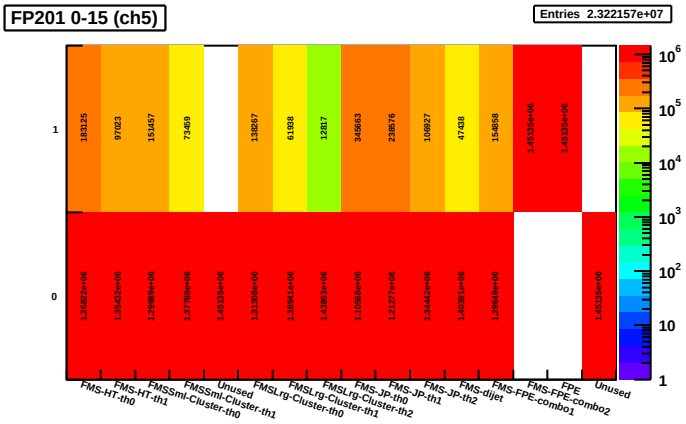

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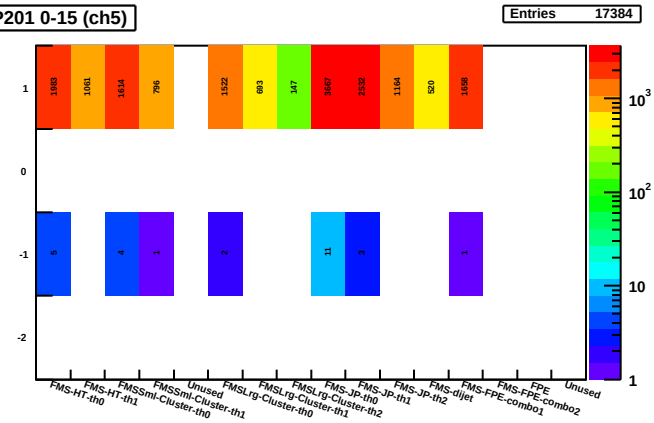
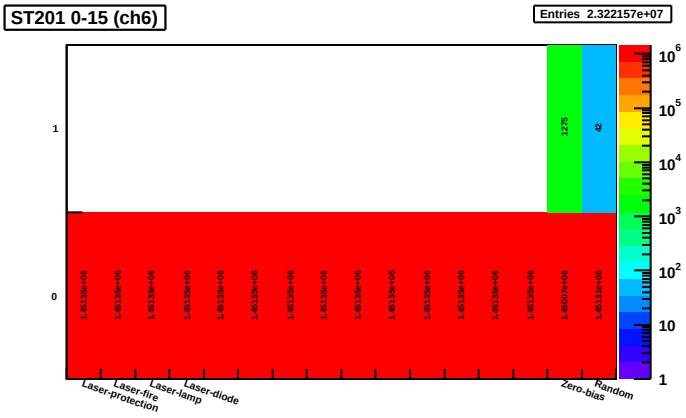
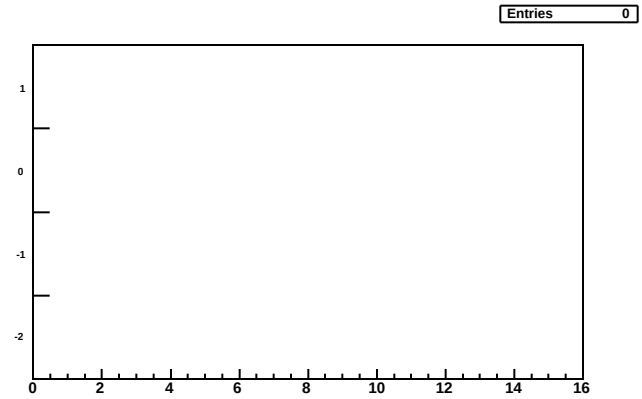
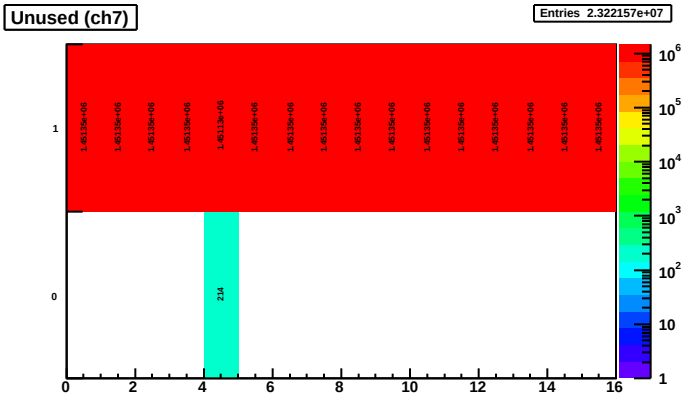
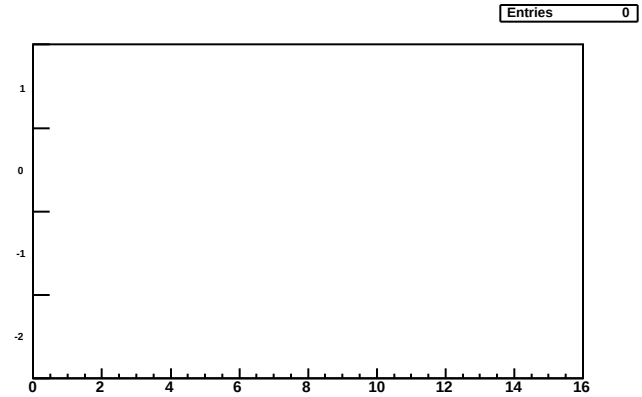


ST201 0-15 (ch6)





Unused (ch7)