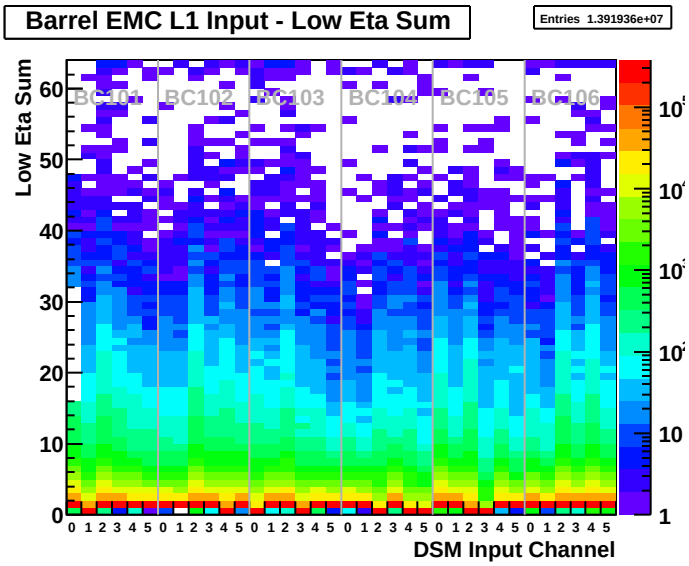
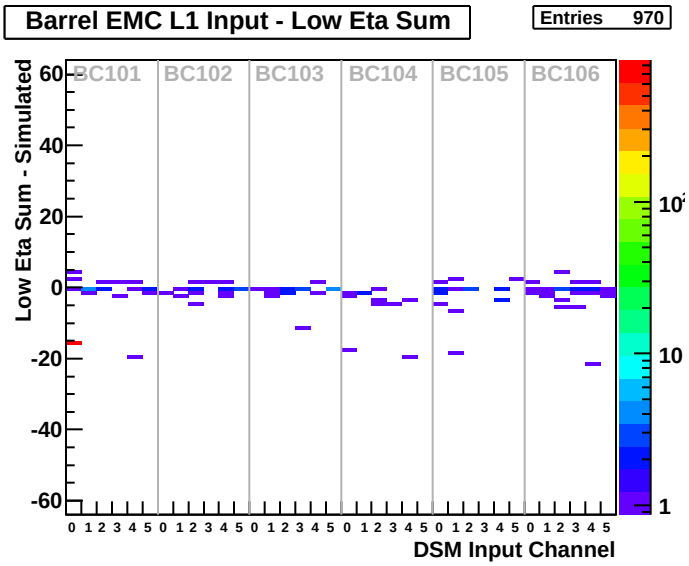
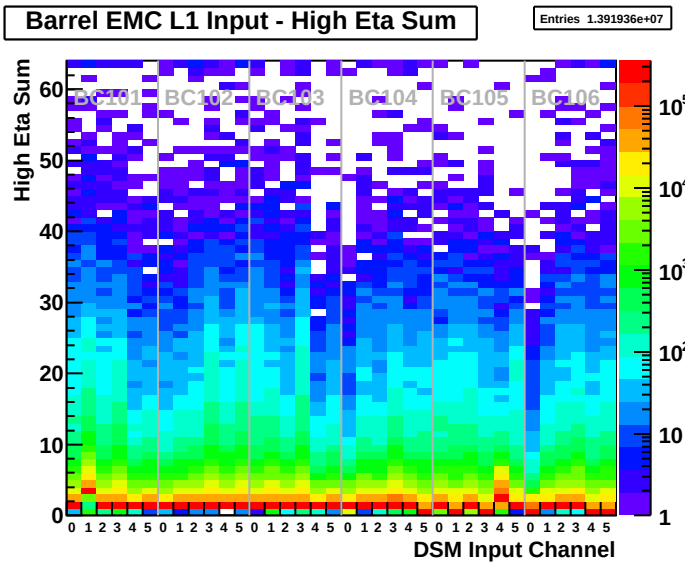
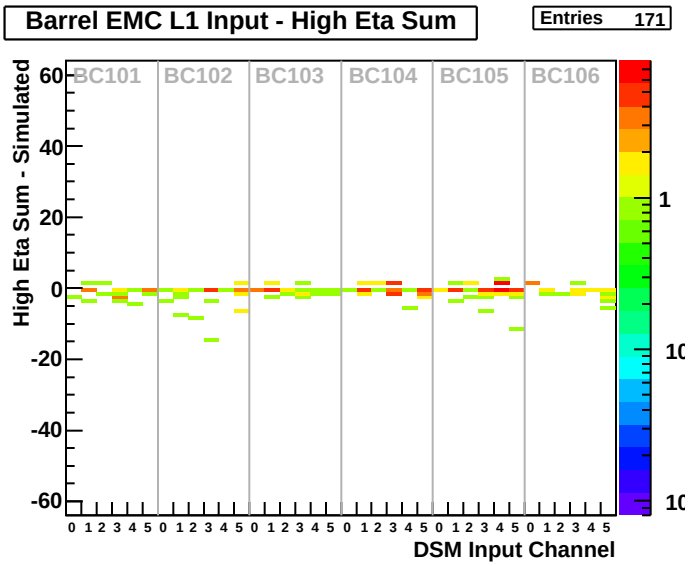
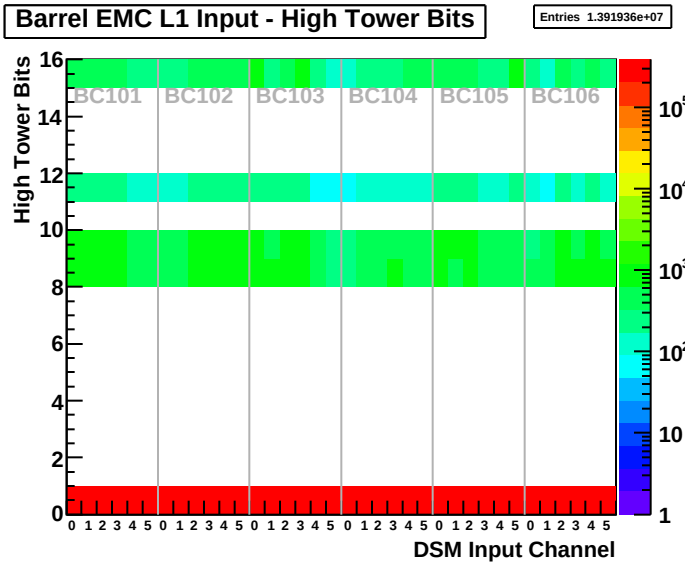
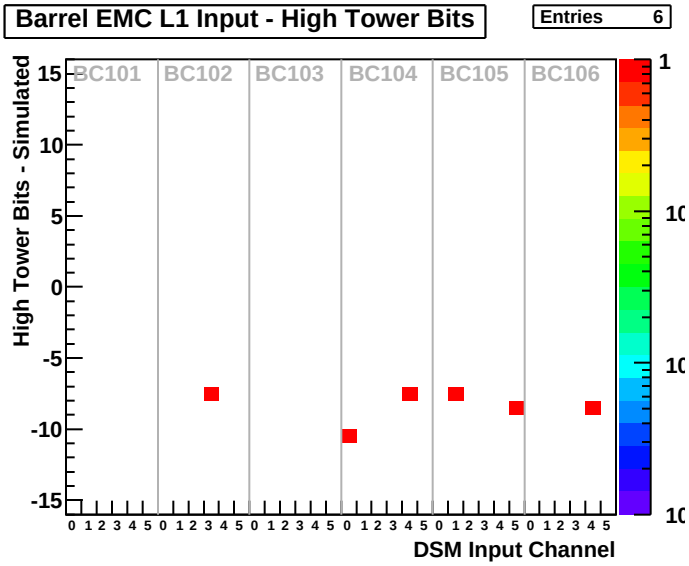

Barrel EMC L1 Input - Low Eta Sum
Entries 1.391936e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

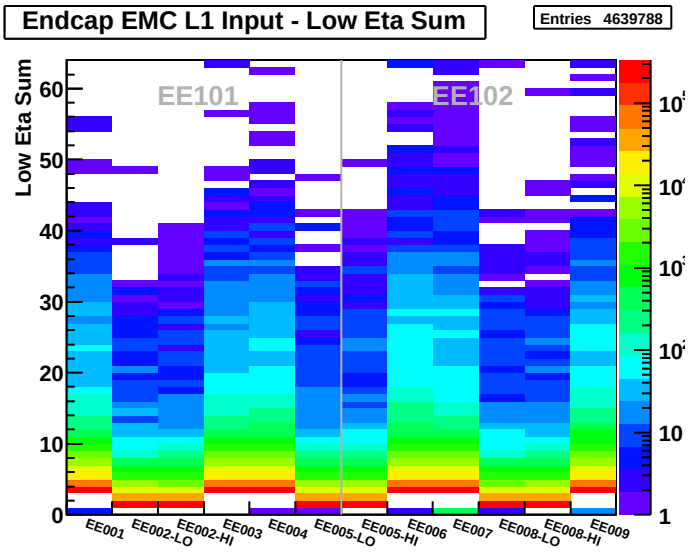

Barrel EMC L1 Input - Low Eta Sum
Entries 970
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

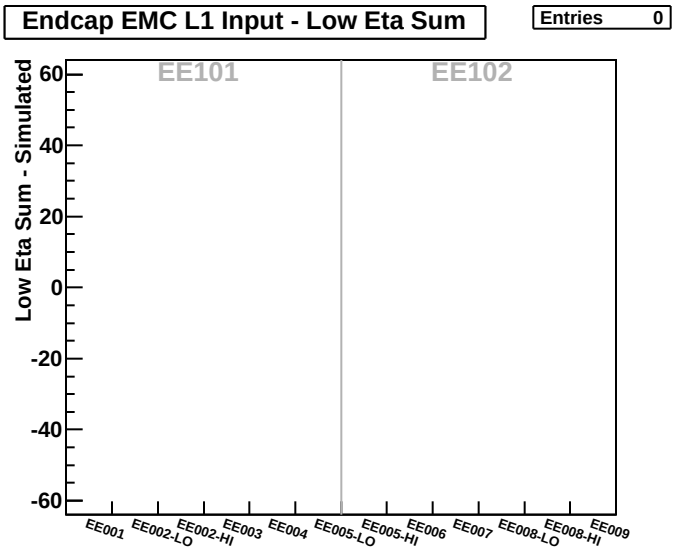

Barrel EMC L1 Input - High Eta Sum
Entries 1.391936e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

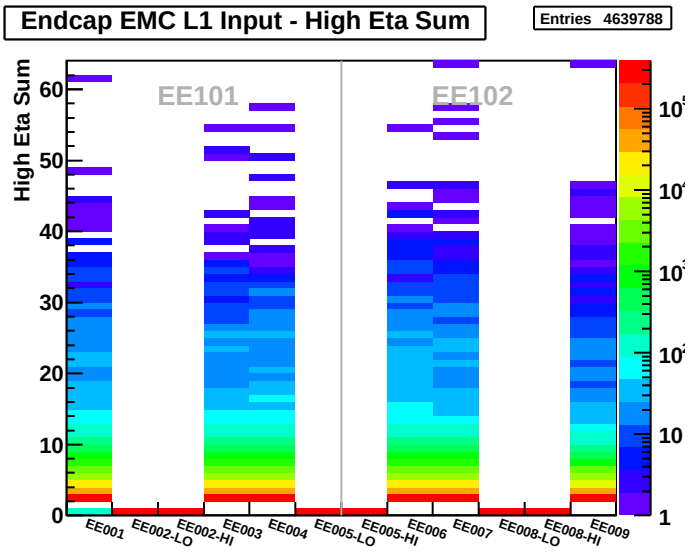

Barrel EMC L1 Input - High Eta Sum
Entries 171
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

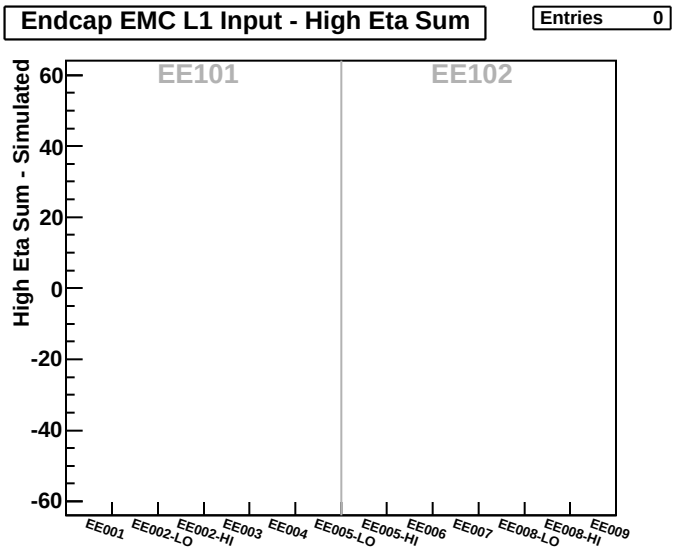

Barrel EMC L1 Input - High Tower Bits
Entries 1.391936e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

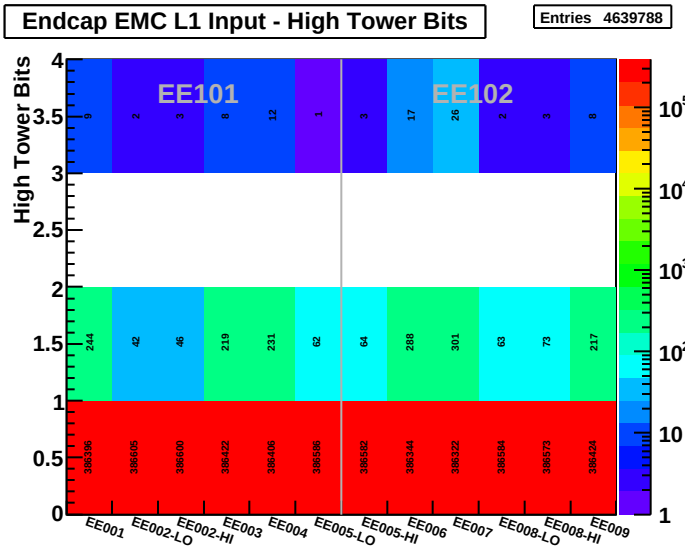

Barrel EMC L1 Input - High Tower Bits
Entries 6
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

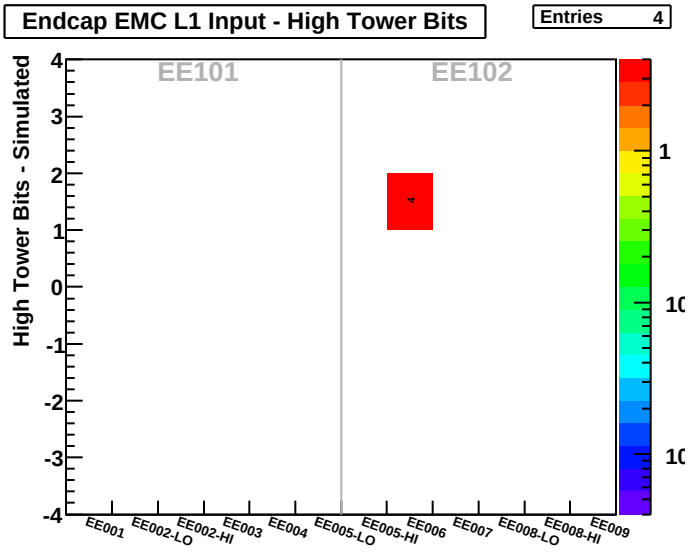

Endcap EMC L1 Input - Low Eta Sum
Entries 4639788
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

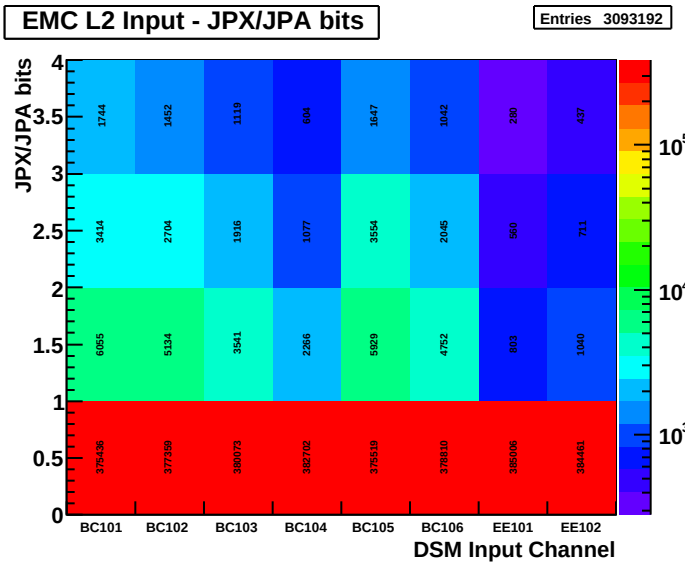

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

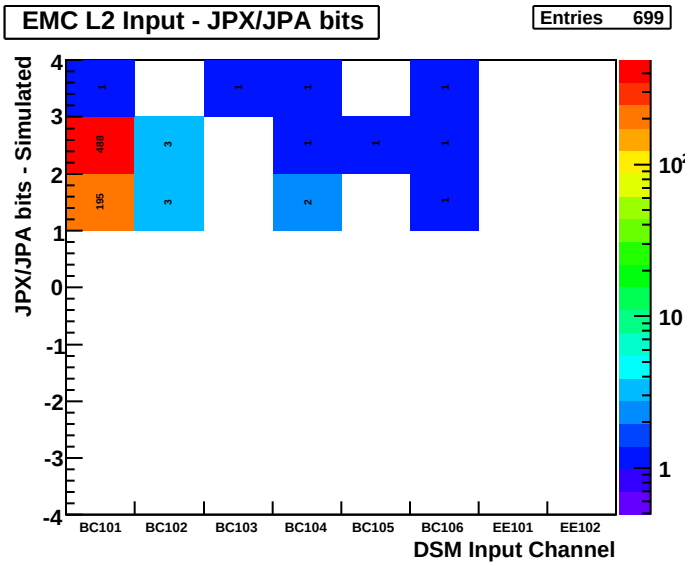

Endcap EMC L1 Input - High Eta Sum
Entries 4639788
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

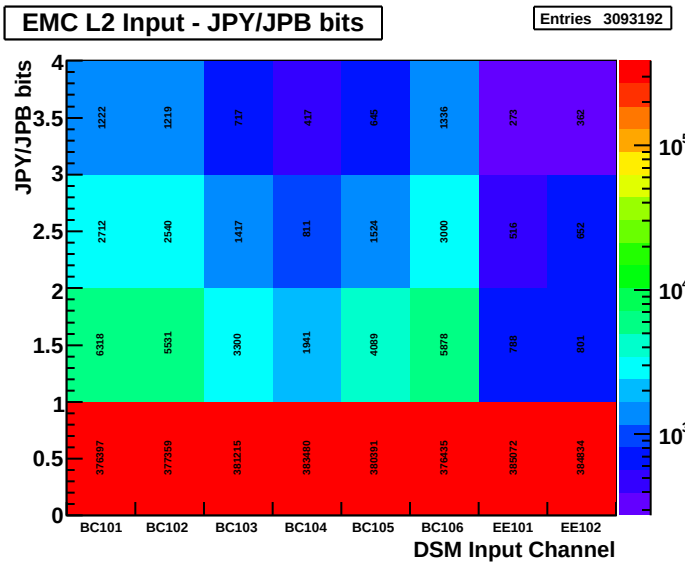

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

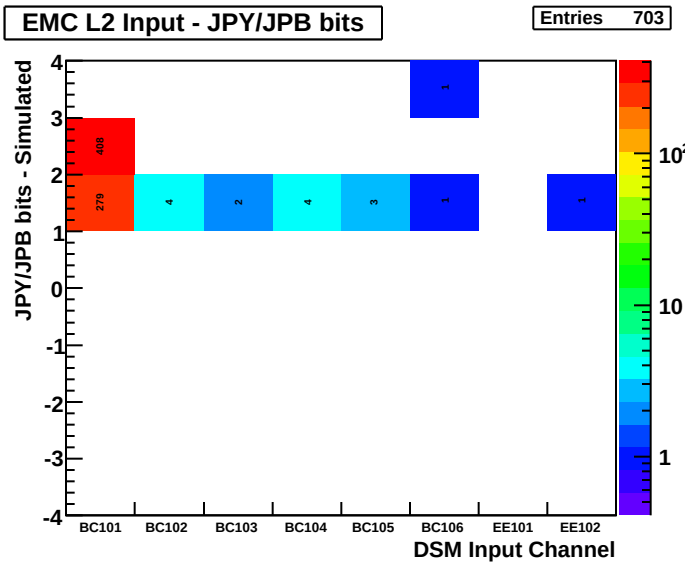

Endcap EMC L1 Input - High Tower Bits
Entries 4639788
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

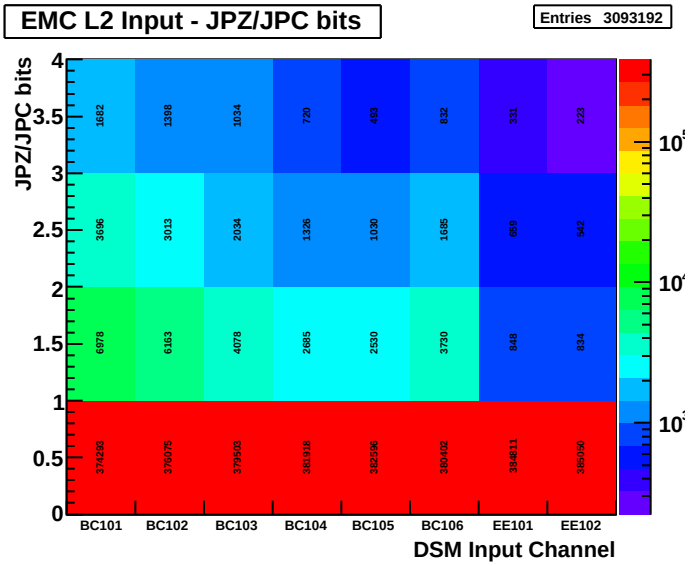

Endcap EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

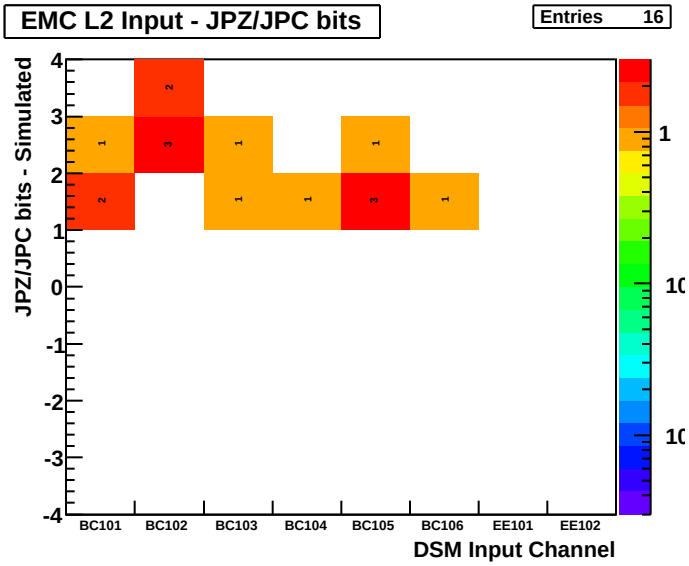

EMC L2 Input - JPX/JPA bits
Entries 3093192
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1744 1452 1119 604 1647 1042 280 437
3414 2704 1916 1077 3554 2045 560 711
6055 5134 3541 2266 5929 4752 803 1040
375436 377359 380073 382702 375519 378810 385006 384461

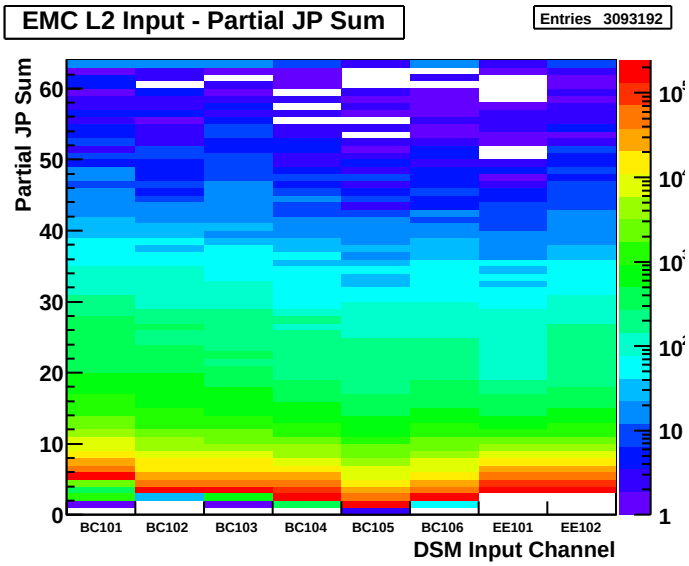

EMC L2 Input - JPX/JPA bits
Entries 699
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1 1 1
488 3 1 1 1
195 3 2 1

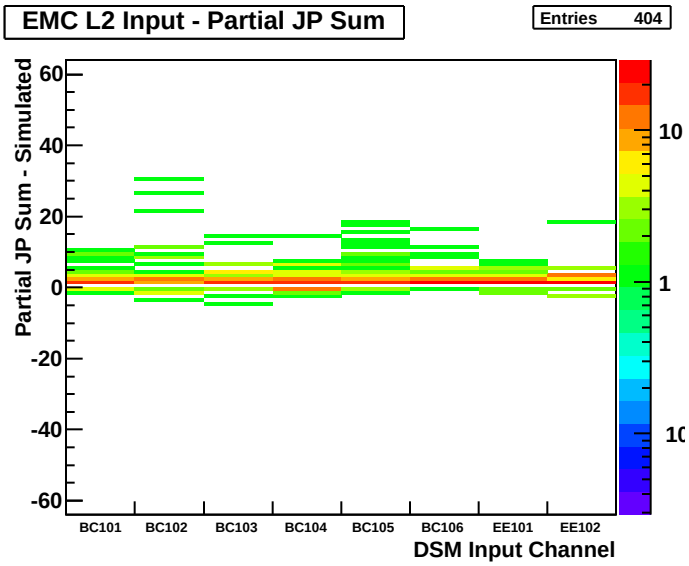

EMC L2 Input - JPY/JPB bits
Entries 3093192
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1222 1219 717 417 645 1336 273 362
2712 2540 1417 811 1524 3000 516 652
6318 5631 3300 1941 4089 5978 788 801
376397 377359 381215 383480 380391 376435 385072 384834

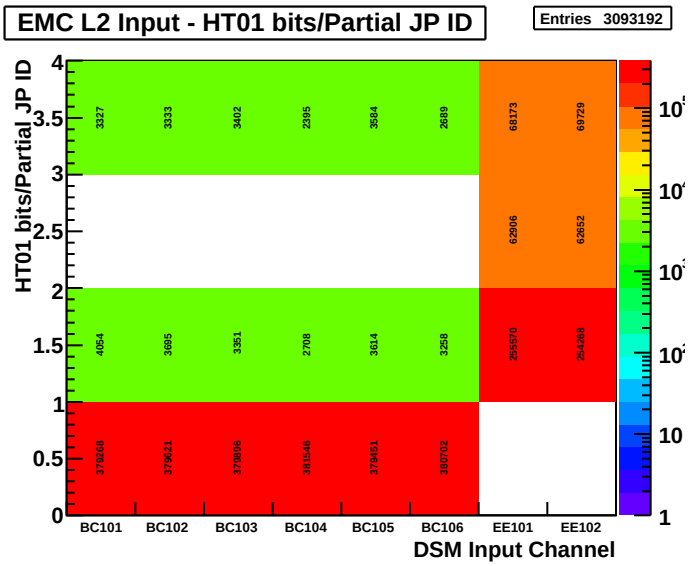

EMC L2 Input - JPY/JPB bits
Entries 703
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
408
279 4 2 4 3 1 1

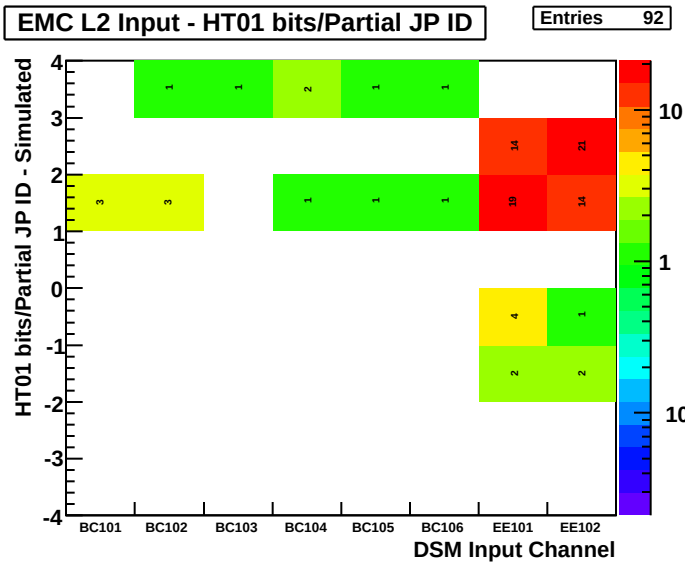

EMC L2 Input - JPZ/JPC bits
Entries 3093192
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1682 1398 1034 720 493 832 331 223
3696 3013 2034 1326 1030 1685 659 542
6978 6163 4078 2685 2530 3730 848 834
374293 376075 379503 381918 382596 380402 384811 385050

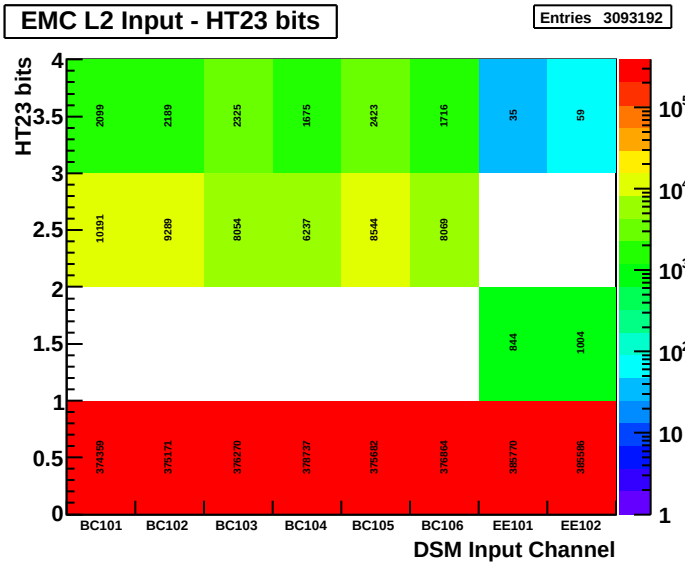

EMC L2 Input - JPZ/JPC bits
Entries 16
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2
1 3 1 1
2 1 1 3 1

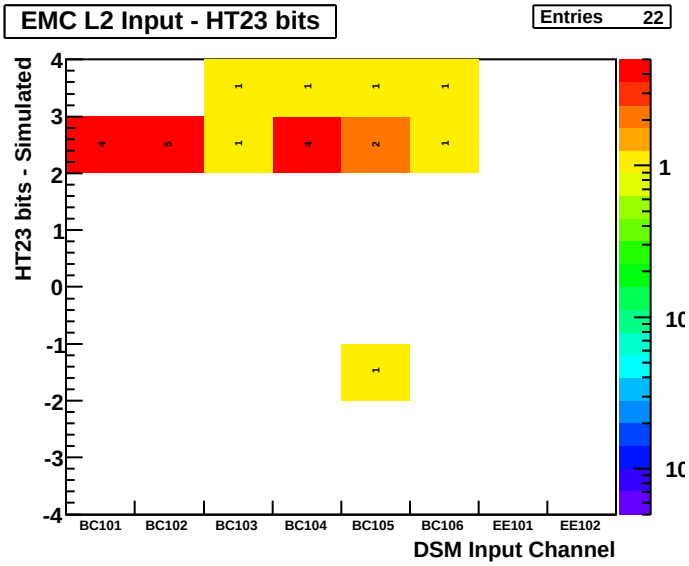

EMC L2 Input - Partial JP Sum
Entries 3093192
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

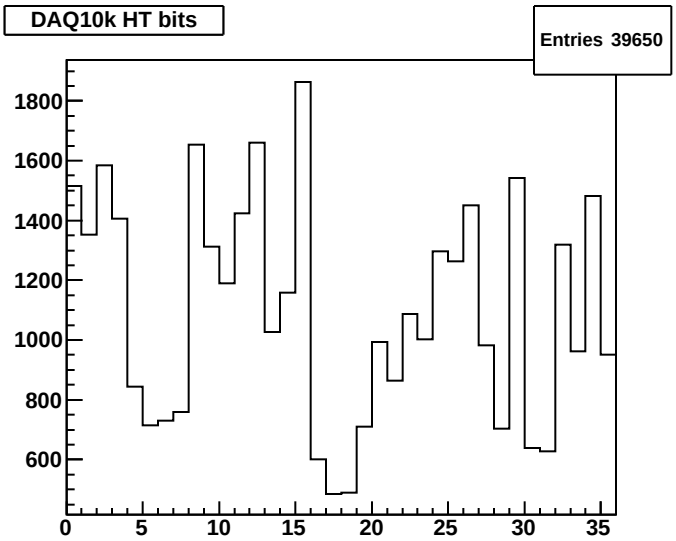

EMC L2 Input - Partial JP Sum
Entries 404
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

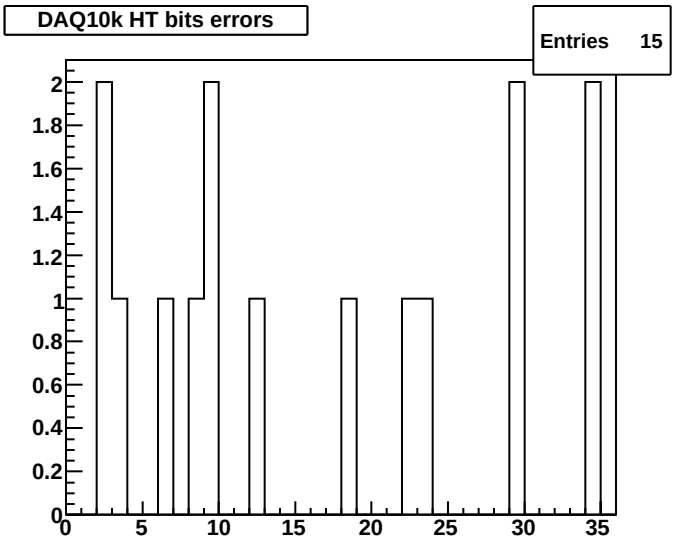

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3093192
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

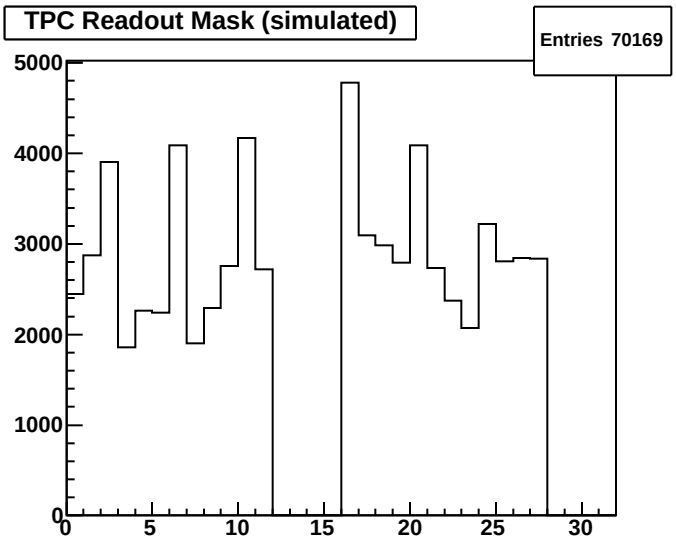

EMC L2 Input - HT01 bits/Partial JP ID
Entries 92
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

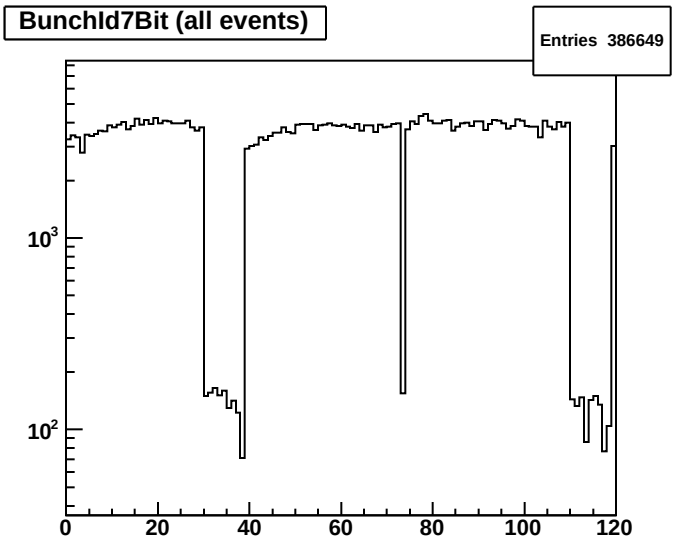

EMC L2 Input - HT23 bits
Entries 3093192
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

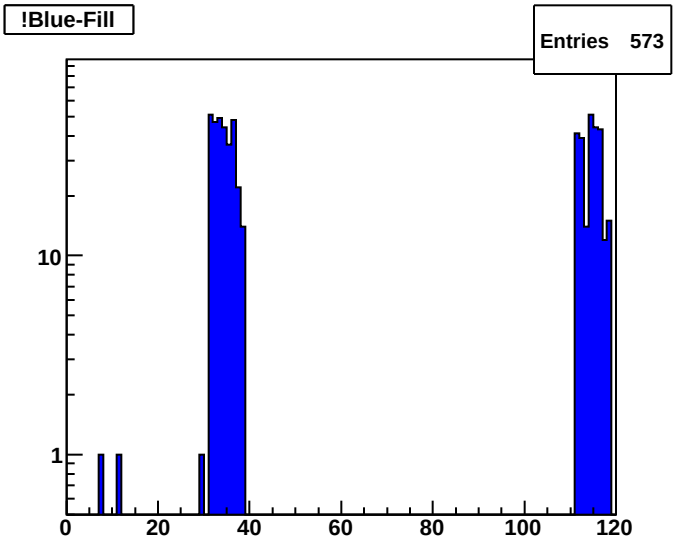

EMC L2 Input - HT23 bits
Entries 22
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

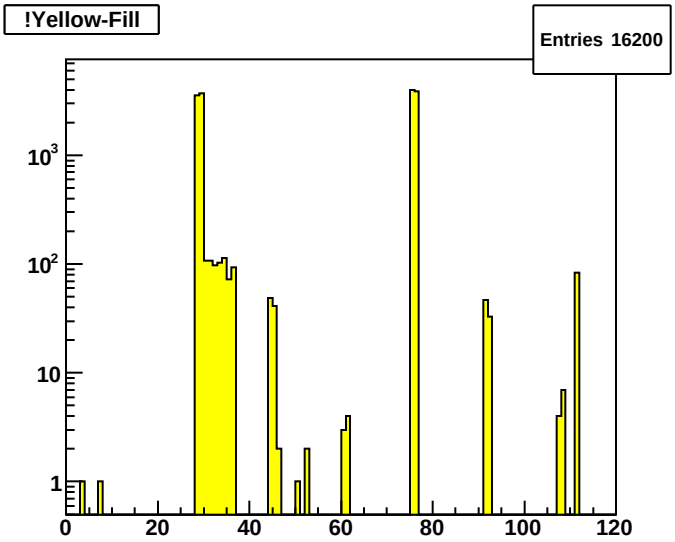

DAQ10k HT bits
Entries 39650

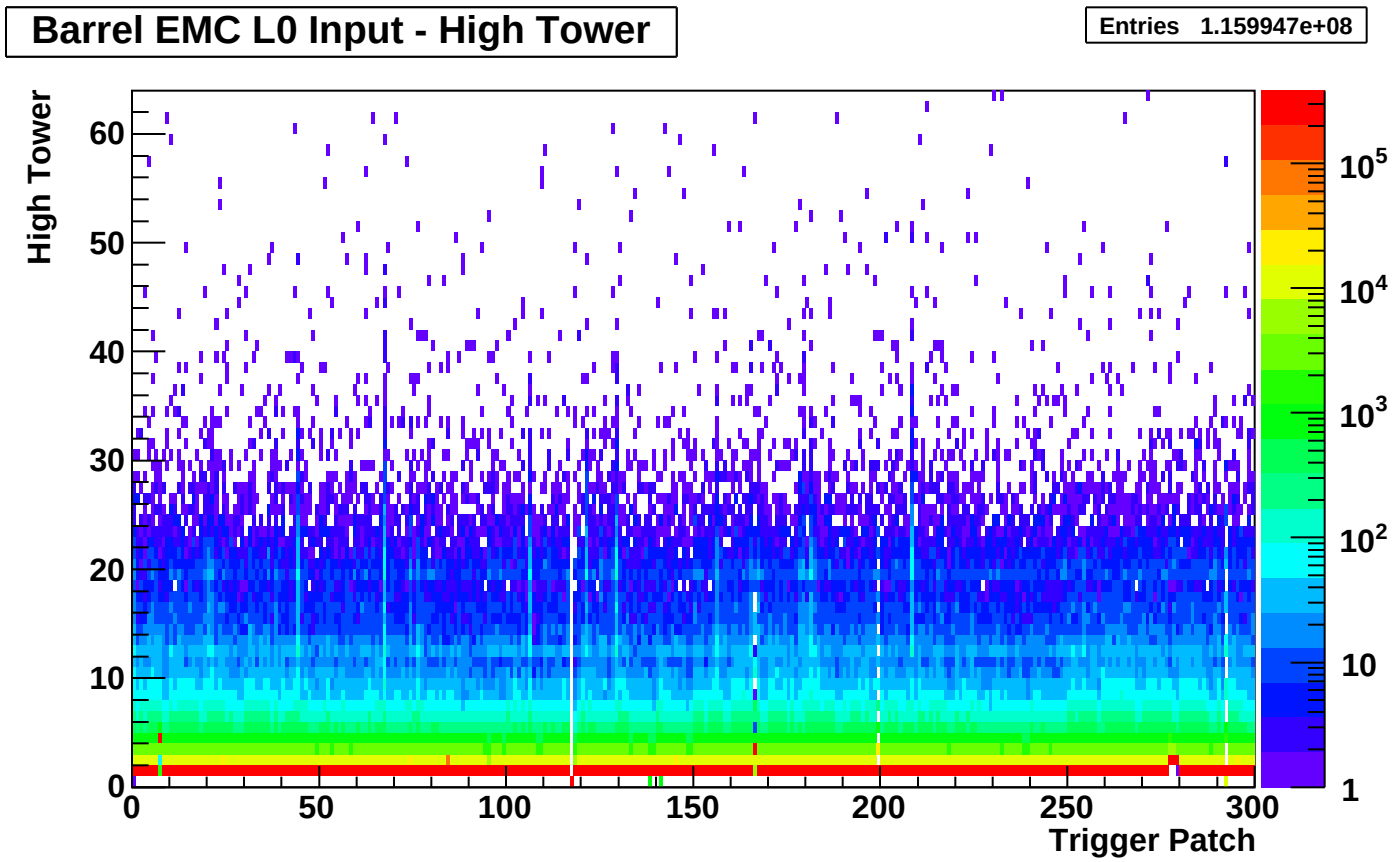

DAQ10k HT bits errors
Entries 15

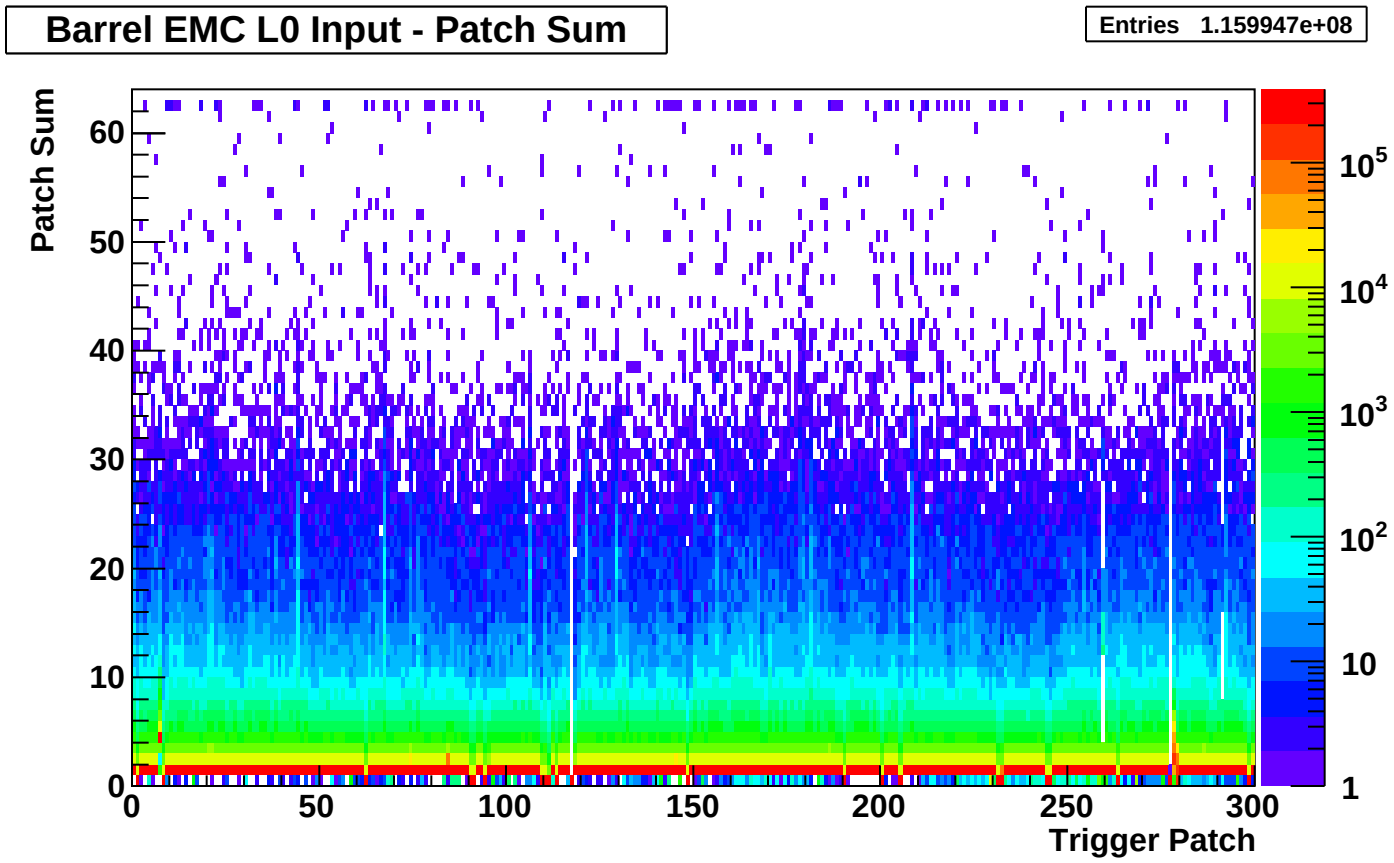

TPC Readout Mask (simulated)
Entries 70169

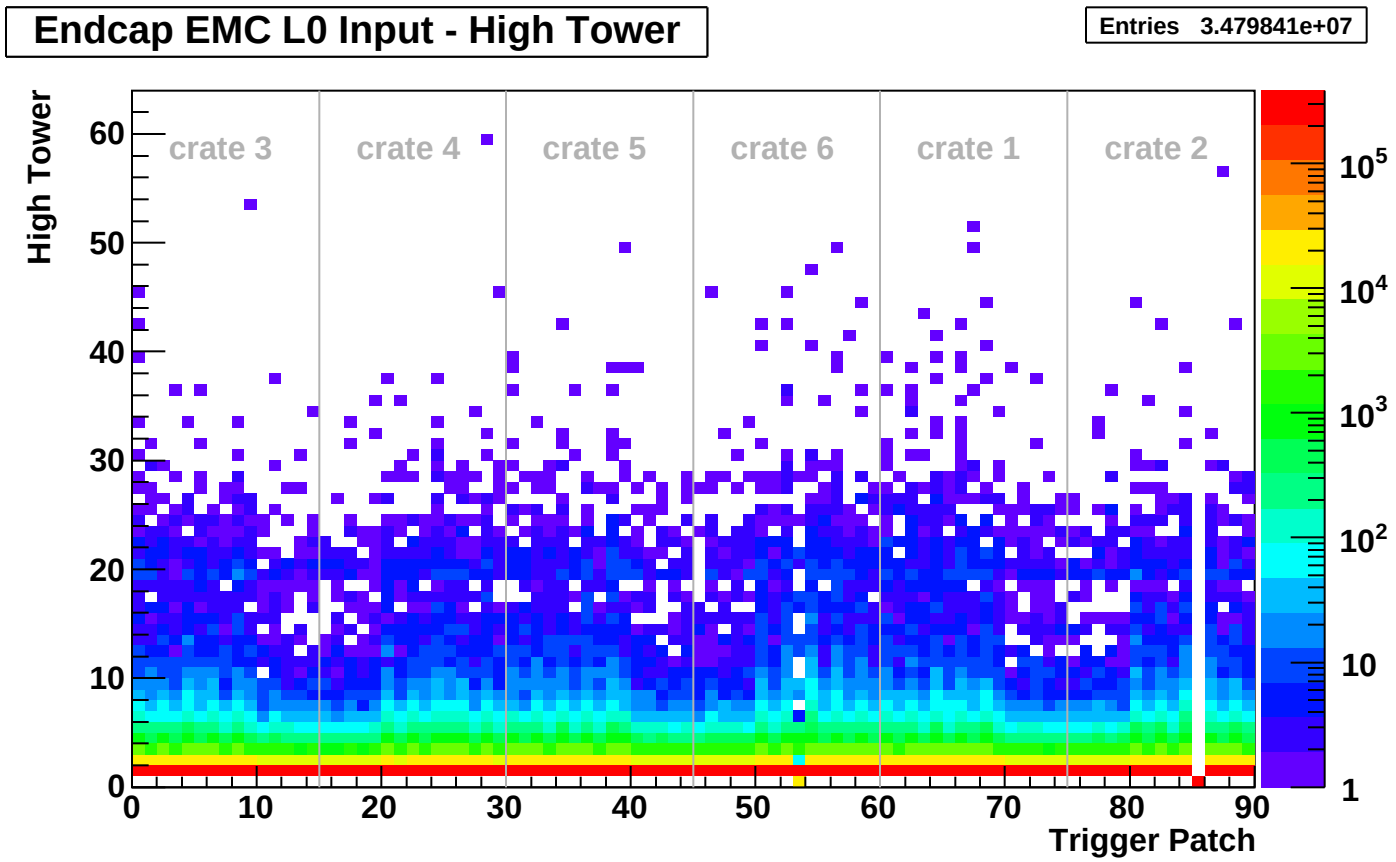

BunchId7Bit (all events)
Entries 386649

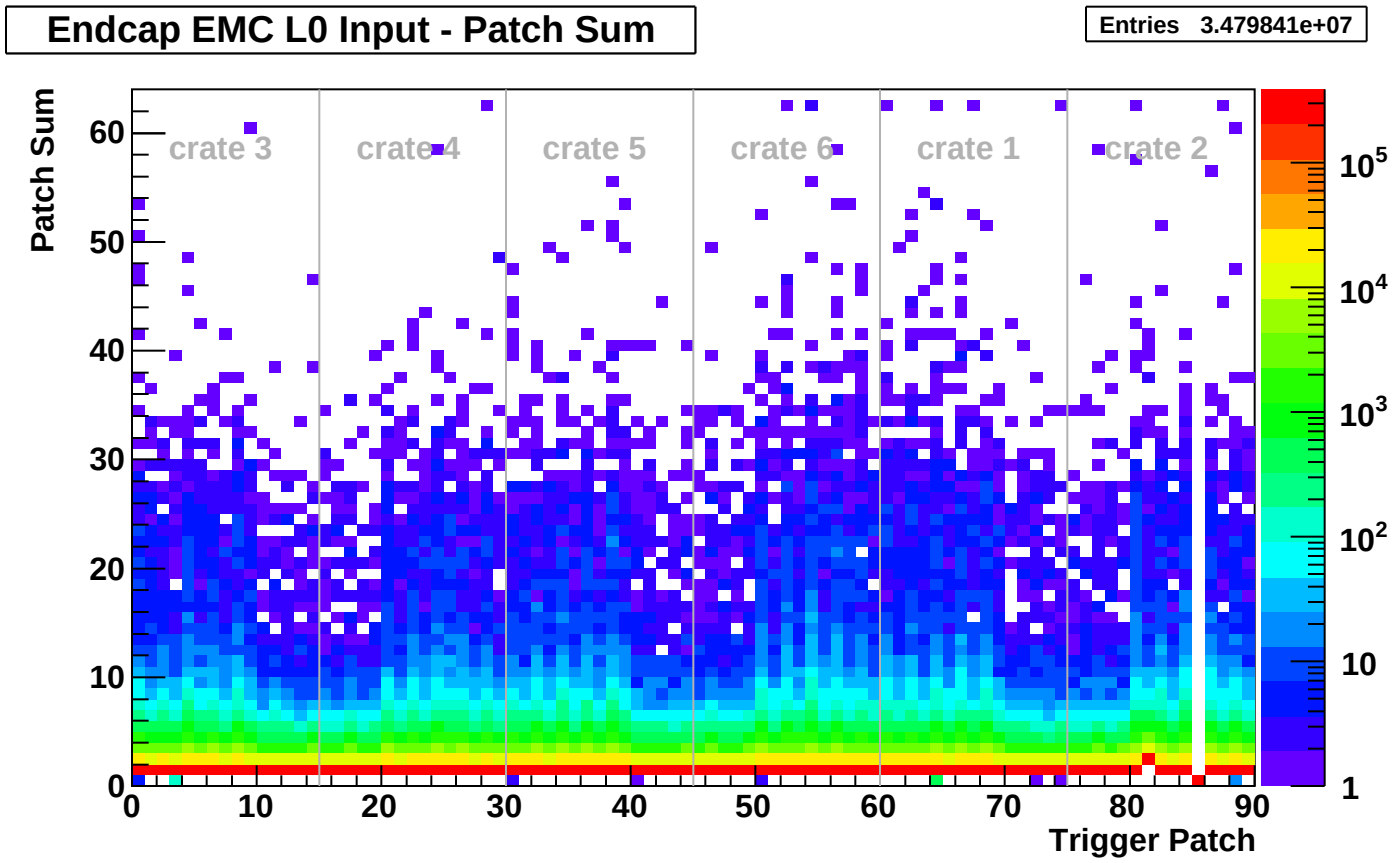

!Blue-Fill
Entries 573

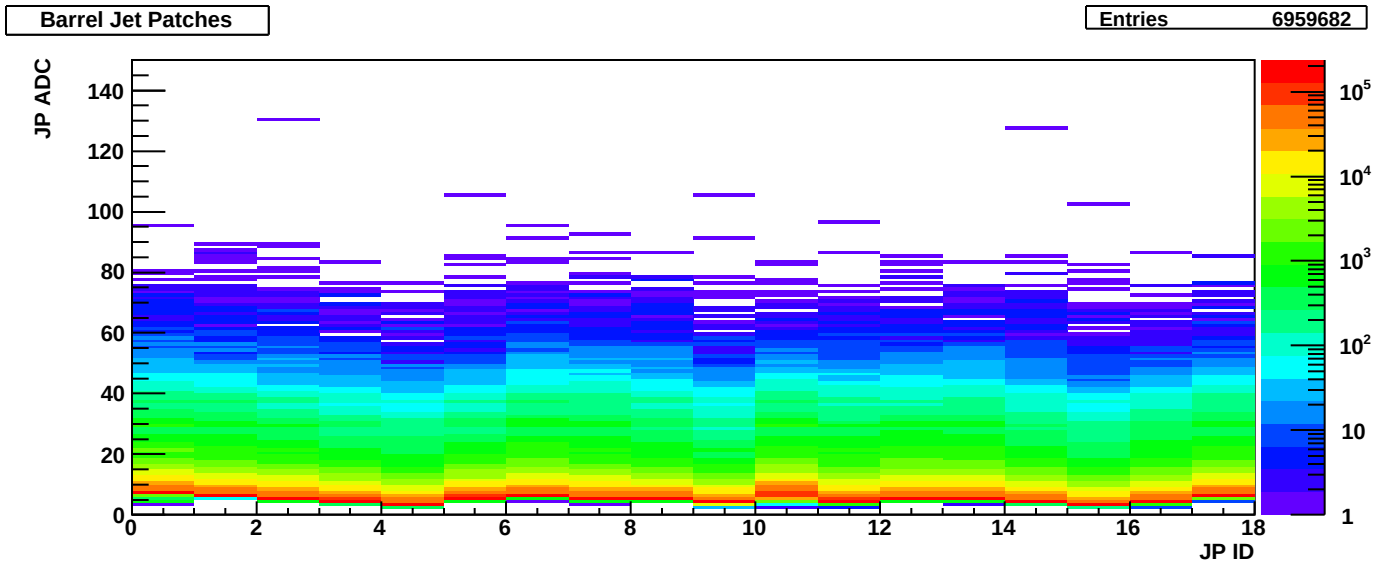

!Yellow-Fill
Entries 16200

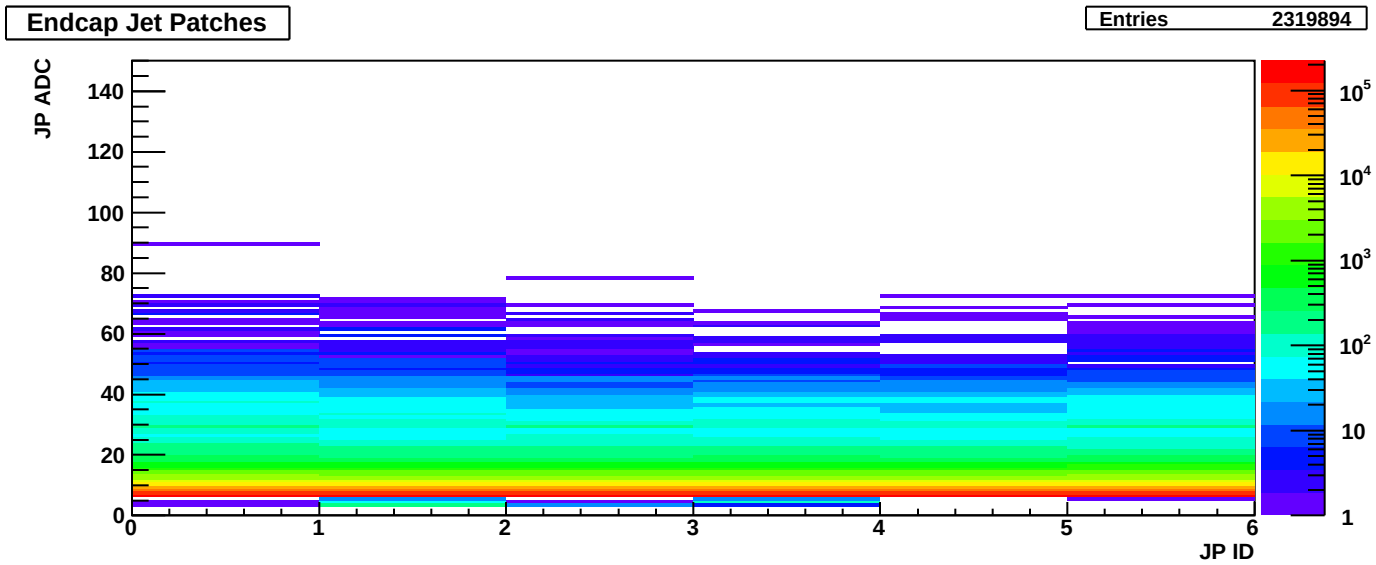

Barrel EMC L0 Input - High Tower
Entries 1.159947e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

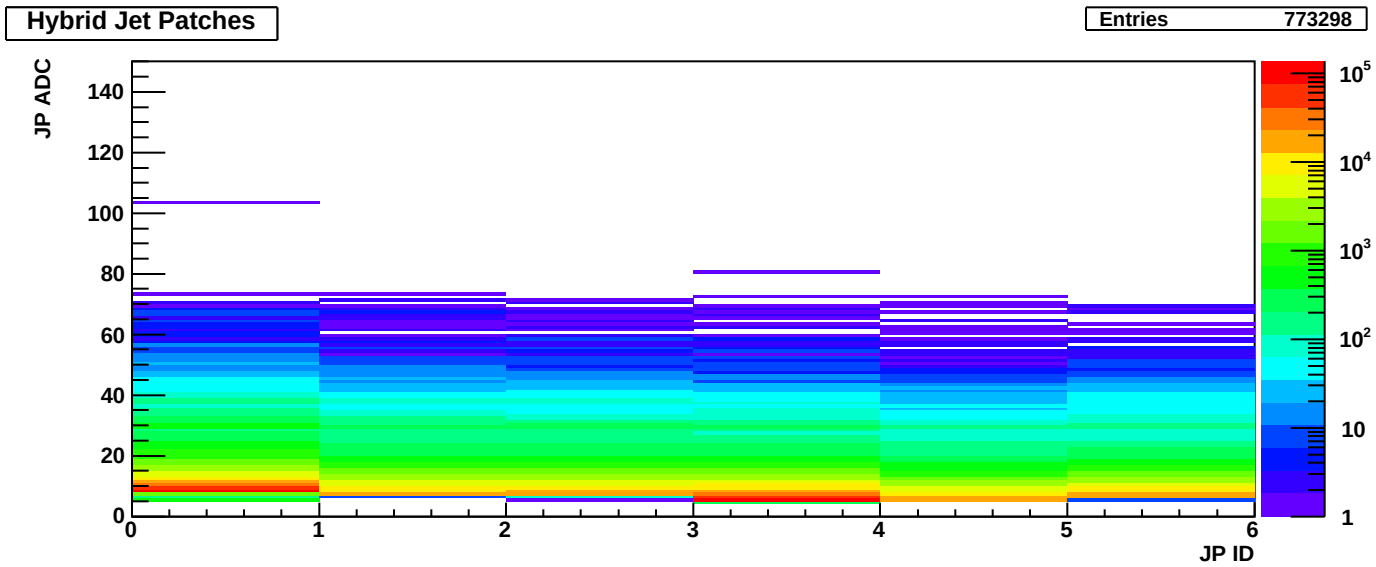

Barrel EMC L0 Input - Patch Sum
Entries 1.159947e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

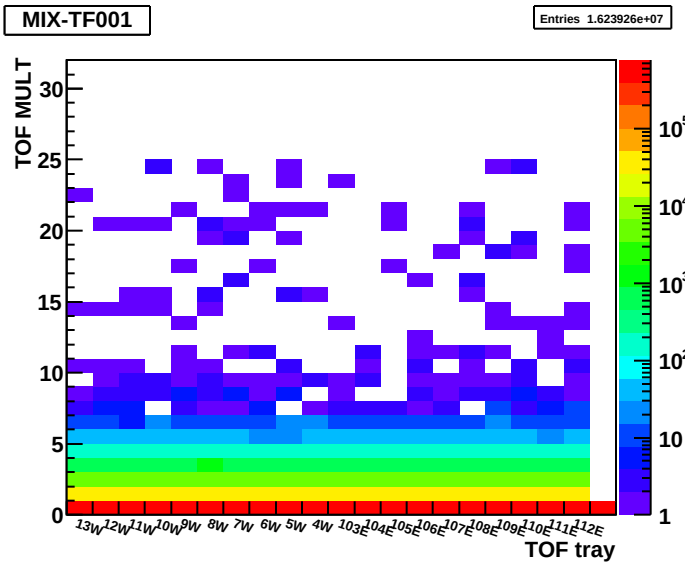

Endcap EMC L0 Input - High Tower
Entries 3.479841e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

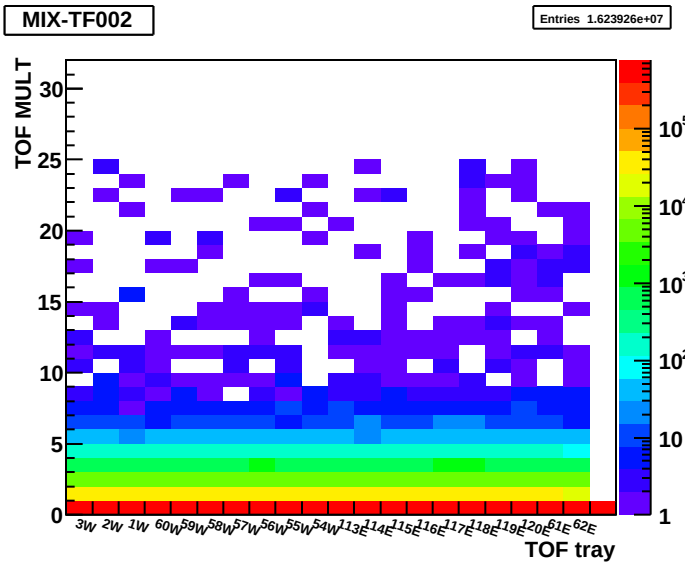

Endcap EMC L0 Input - Patch Sum
Entries 3.479841e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

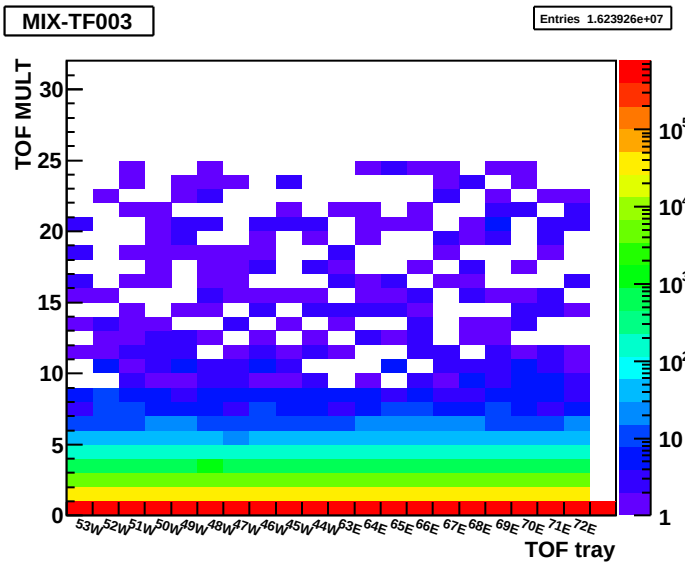

Barrel Jet Patches
Entries 6959682
JP ADC
JP ID

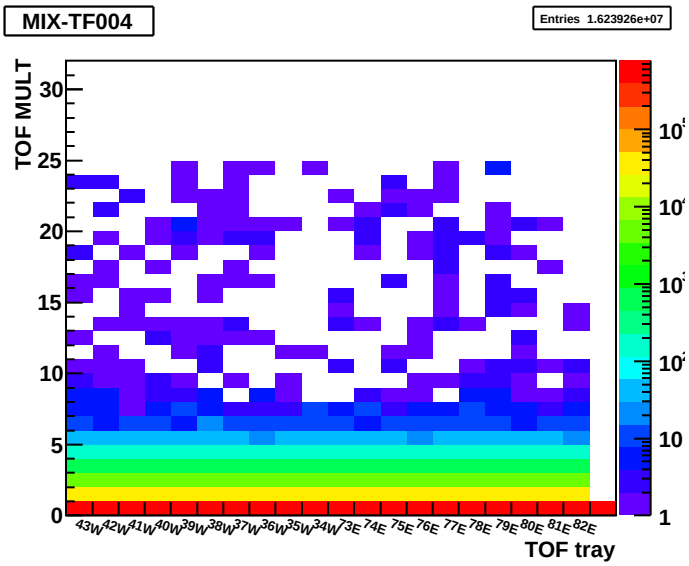

Endcap Jet Patches
Entries 2319894
JP ADC
JP ID

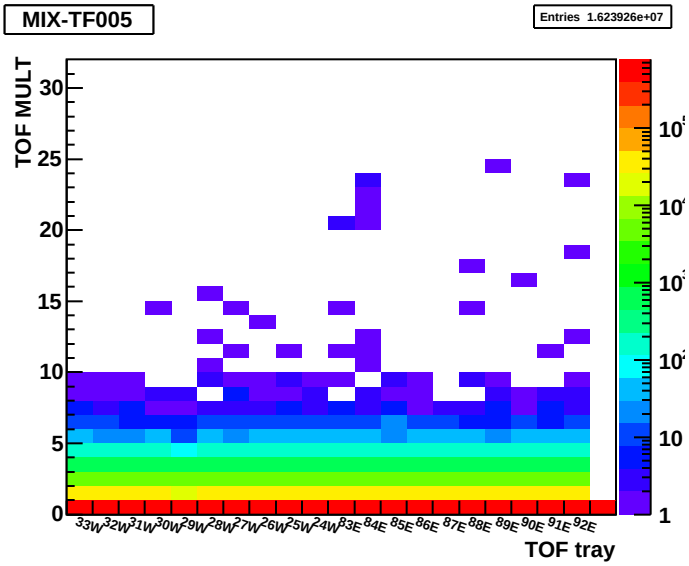

Hybrid Jet Patches
Entries 773298
JP ADC
JP ID

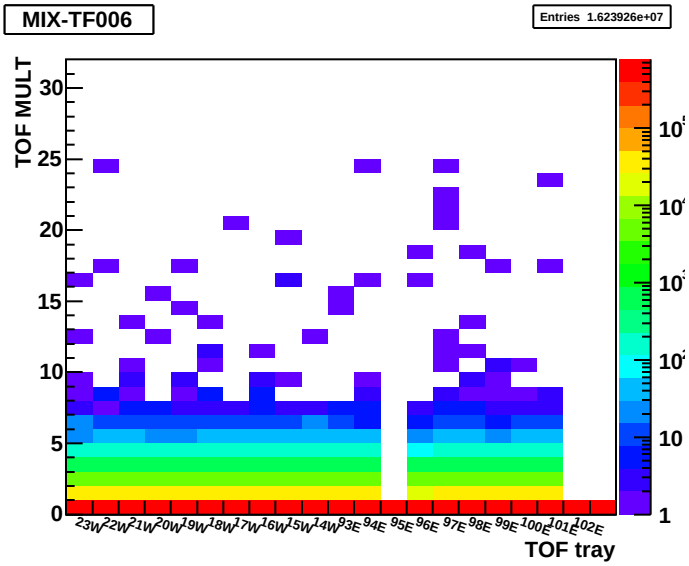

MIX-TF001
Entries 1.623926e+07
TOF MULT
TOF tray

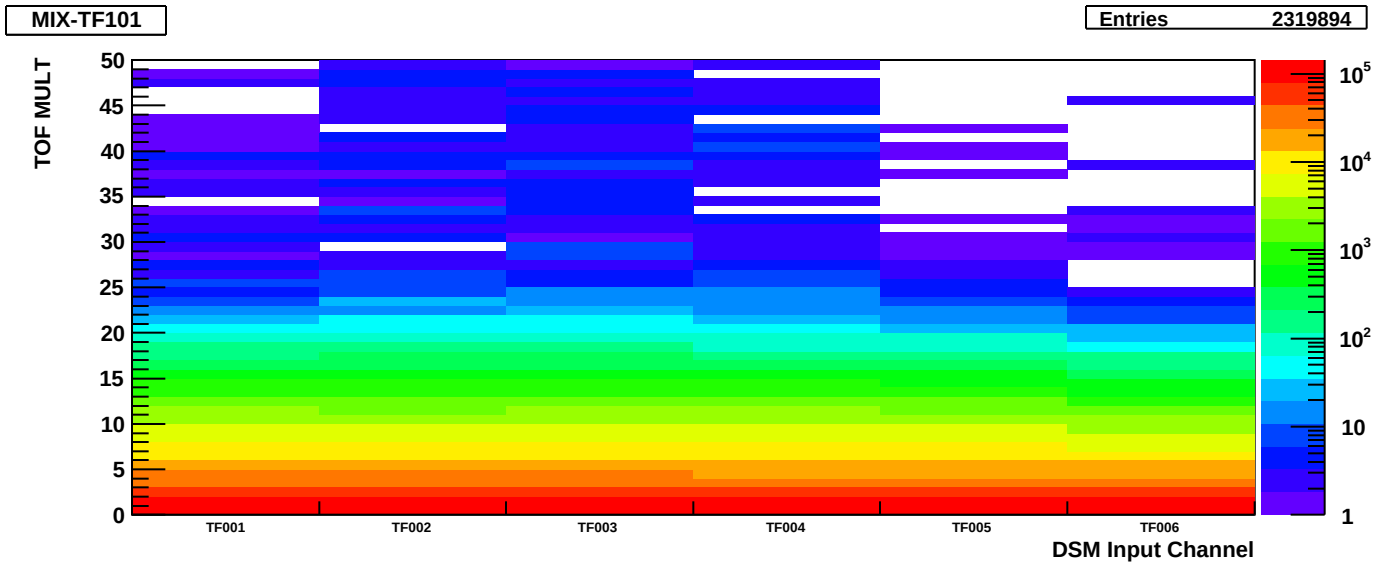

MIX-TF002
Entries 1.623926e+07
TOF MULT
TOF tray

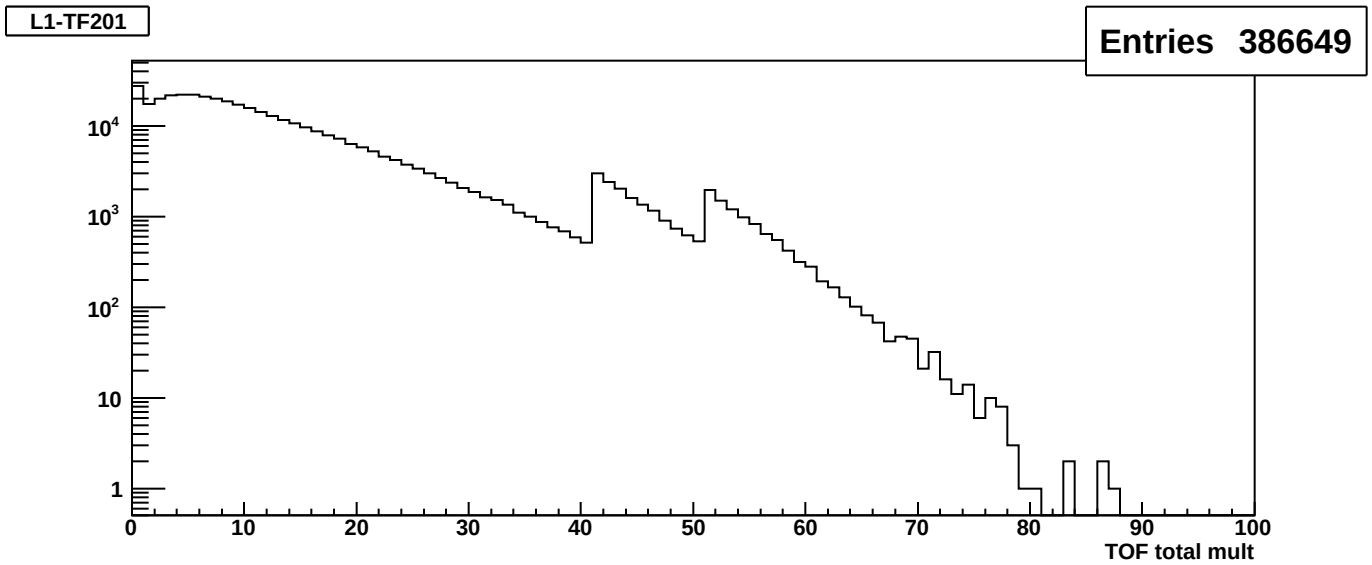

MIX-TF003
Entries 1.623926e+07
TOF MULT
TOF tray

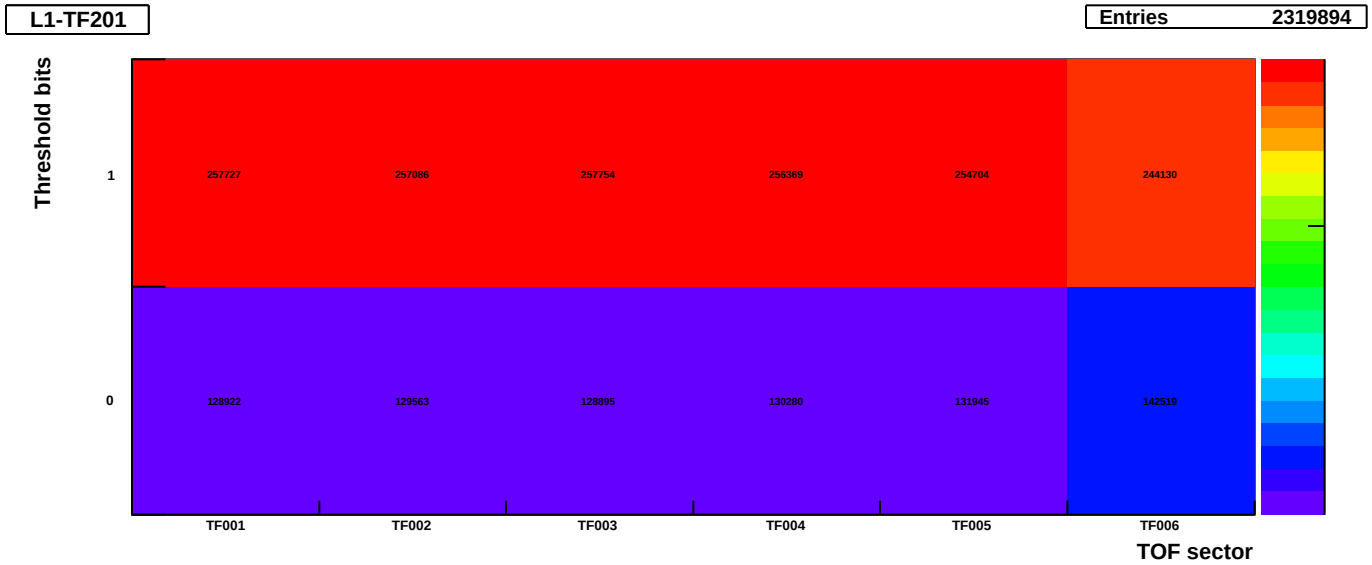

MIX-TF004
Entries 1.623926e+07
TOF MULT
TOF tray

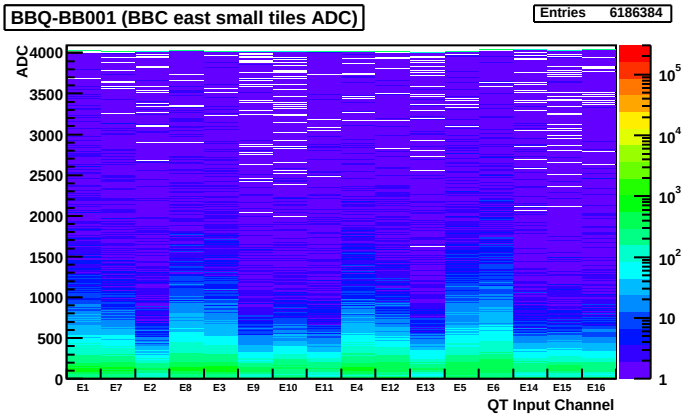

MIX-TF005
Entries 1.623926e+07
TOF MULT
TOF tray

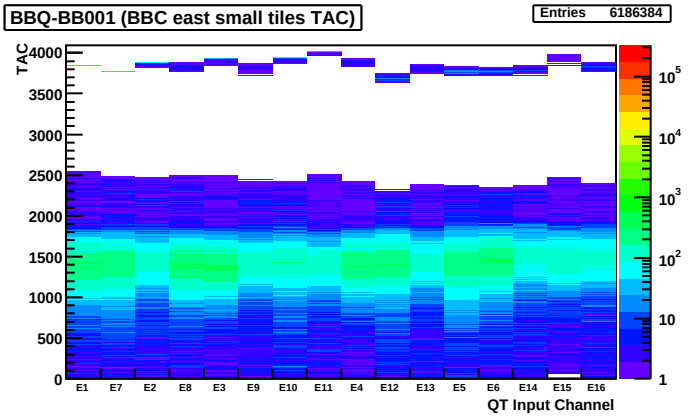

MIX-TF006
Entries 1.623926e+07
TOF MULT
TOF tray

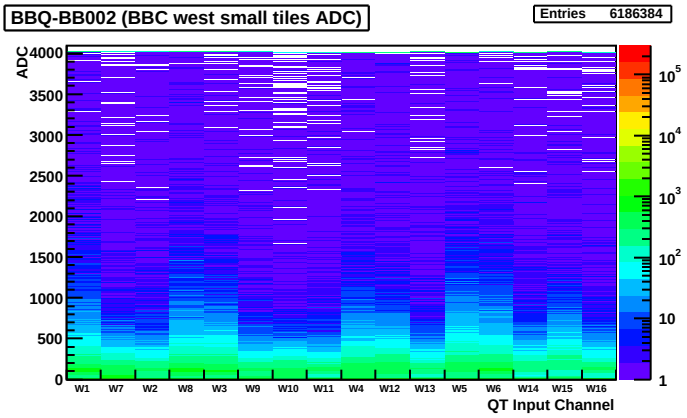

MIX-TF101
Entries 2319894
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

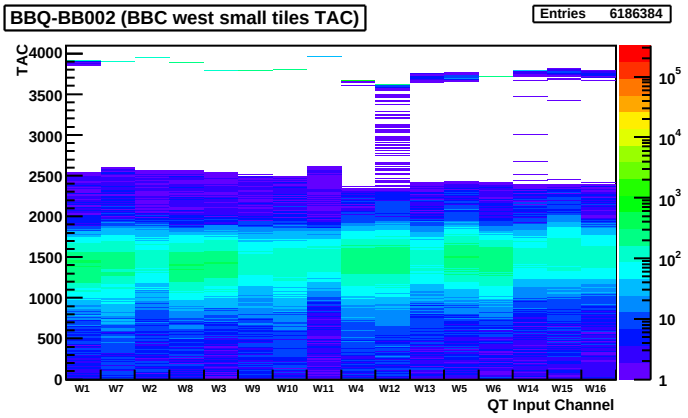

L1-TF201
Entries 386649
TOF total mult

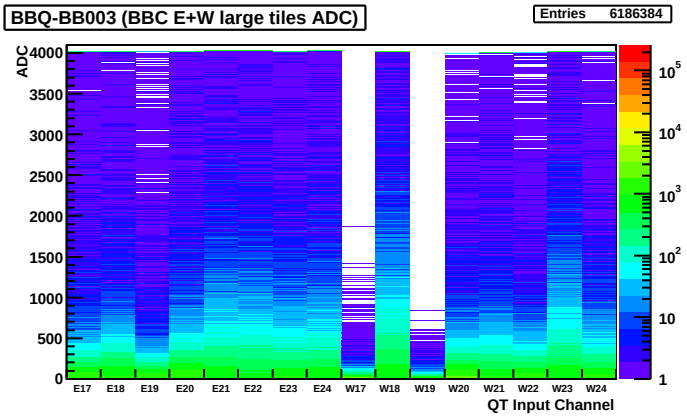

L1-TF201
Entries 2319894
Threshold bits
257727
257086
257754
256369
254704
244130
128922
129563
128895
130280
131945
142519
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

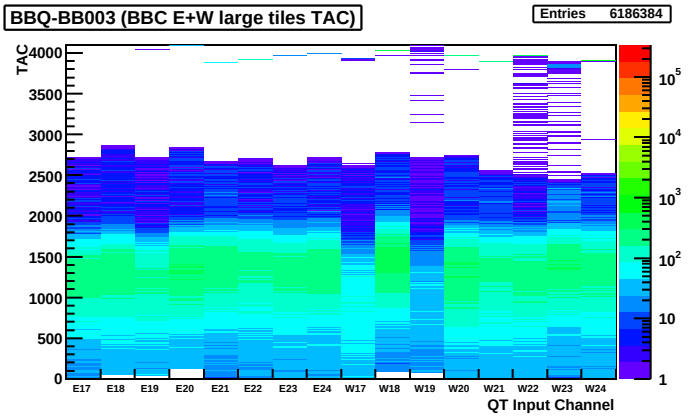

BBQ-BB001 (BBC east small tiles ADC)
Entries 6186384
ADC
QT Input Channel

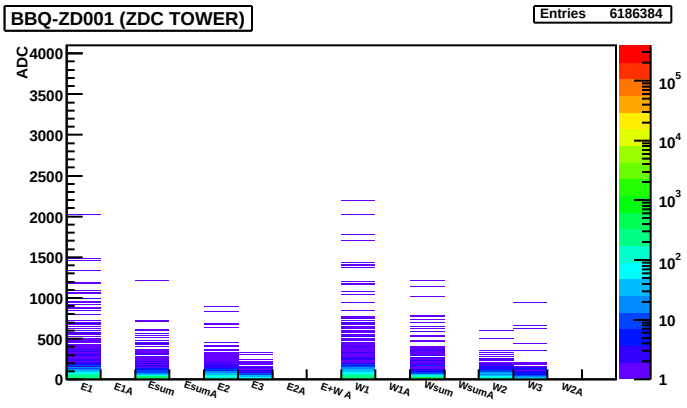

BBQ-BB001 (BBC east small tiles TAC)
Entries 6186384
TAC
QT Input Channel

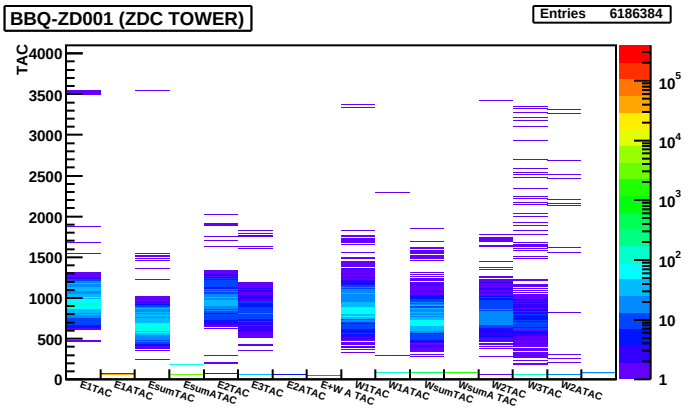

BBQ-BB002 (BBC west small tiles ADC)
Entries 6186384
ADC
QT Input Channel

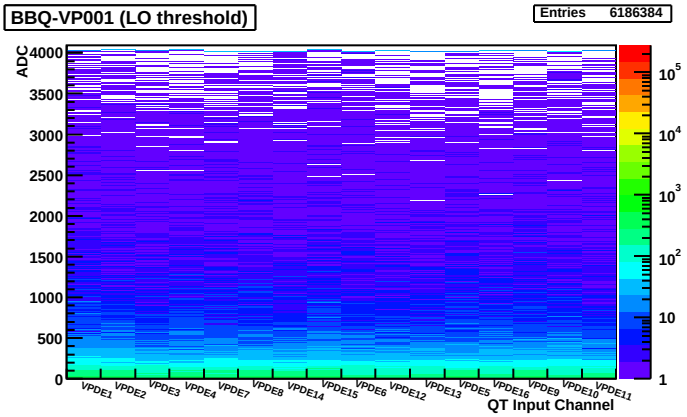

BBQ-BB002 (BBC west small tiles TAC)
Entries 6186384
TAC
QT Input Channel

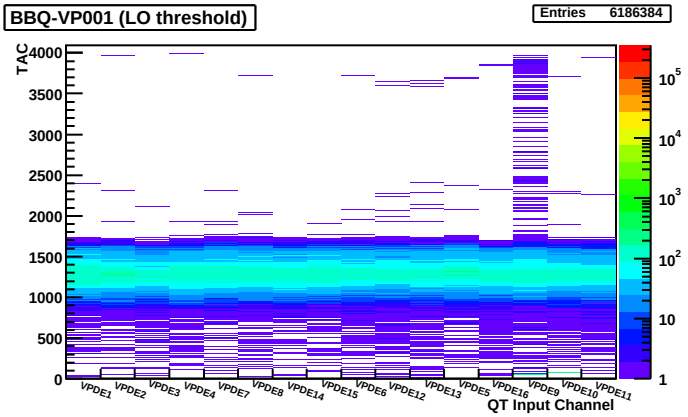

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6186384
ADC
QT Input Channel

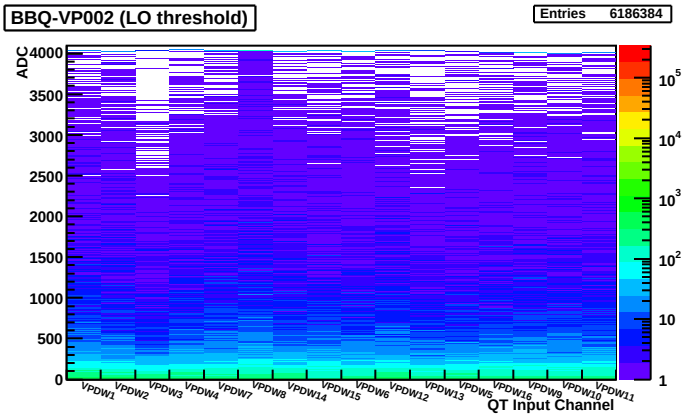

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6186384
TAC
QT Input Channel

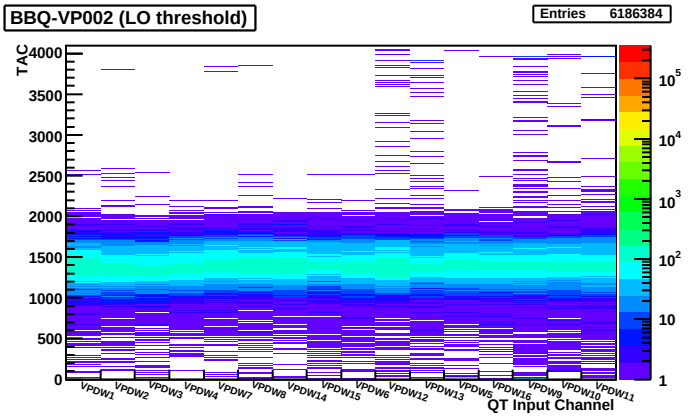

BBQ-ZD001 (ZDC TOWER)
Entries 6186384
ADC

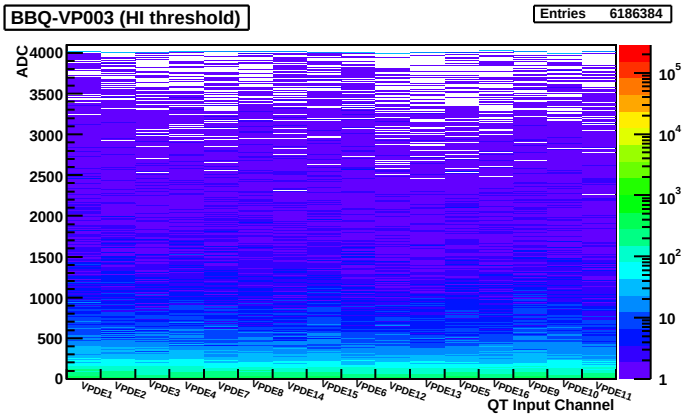

BBQ-ZD001 (ZDC TOWER)
Entries 6186384
TAC

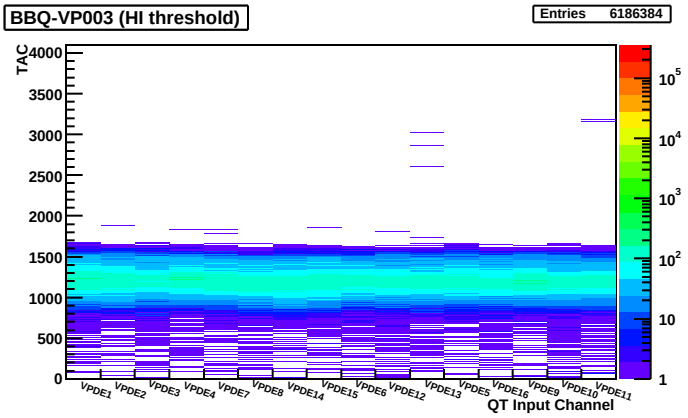

BBQ-VP001 (LO threshold)
Entries 6186384
ADC
QT Input Channel

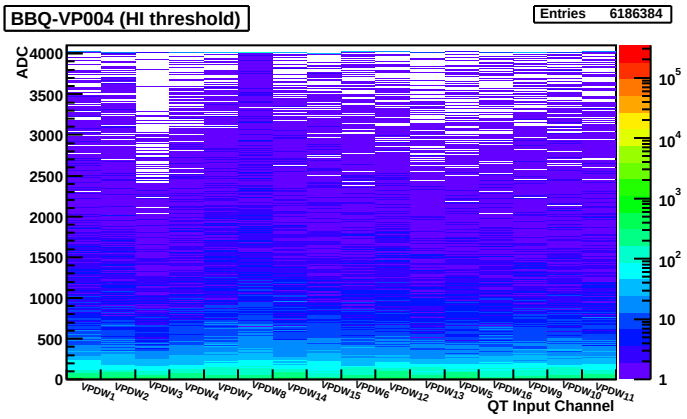

BBQ-VP001 (LO threshold)
Entries 6186384
TAC
QT Input Channel

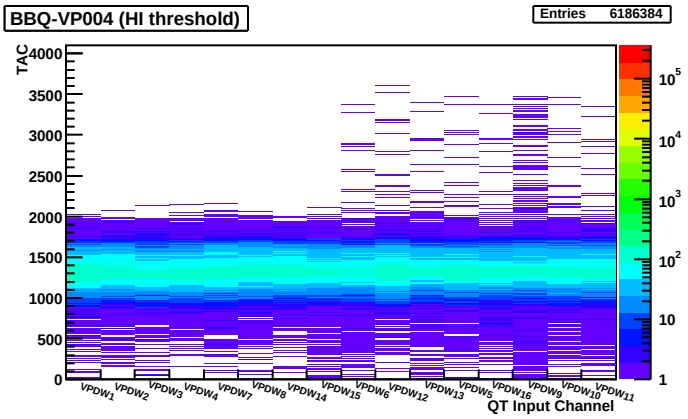

BBQ-VP002 (LO threshold)
Entries 6186384
ADC
QT Input Channel

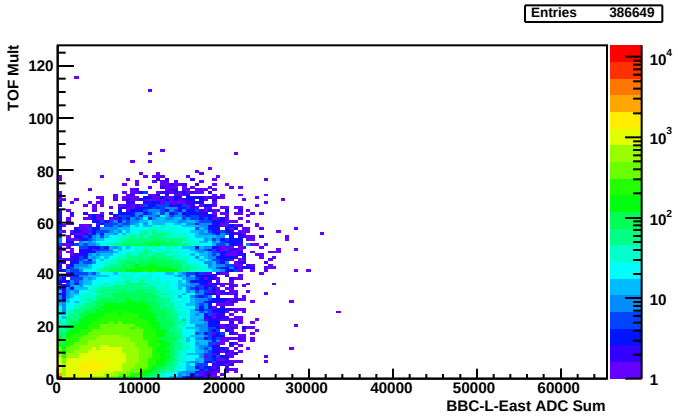

BBQ-VP002 (LO threshold)
Entries 6186384
TAC
QT Input Channel

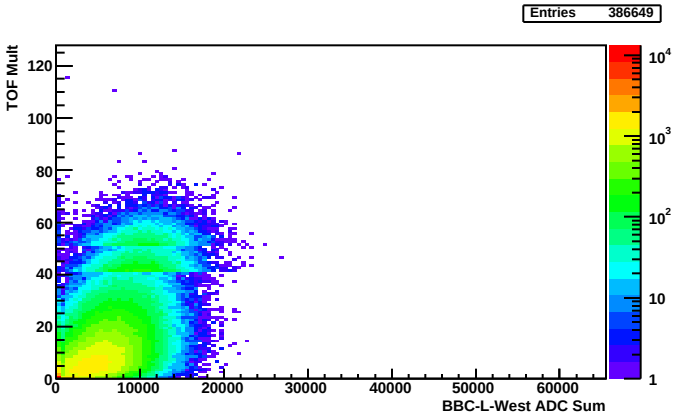

BBQ-VP003 (HI threshold)
Entries 6186384
ADC
QT Input Channel

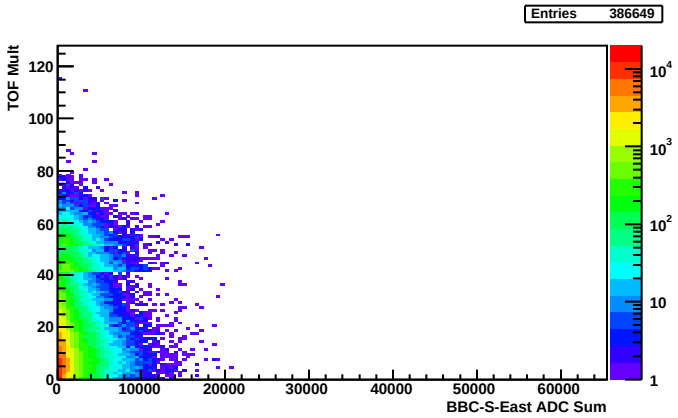

BBQ-VP003 (HI threshold)
Entries 6186384
TAC
QT Input Channel

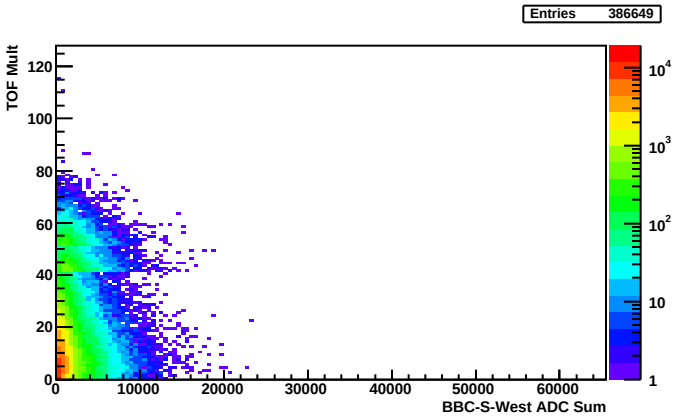

BBQ-VP004 (HI threshold)
Entries 6186384
ADC
QT Input Channel

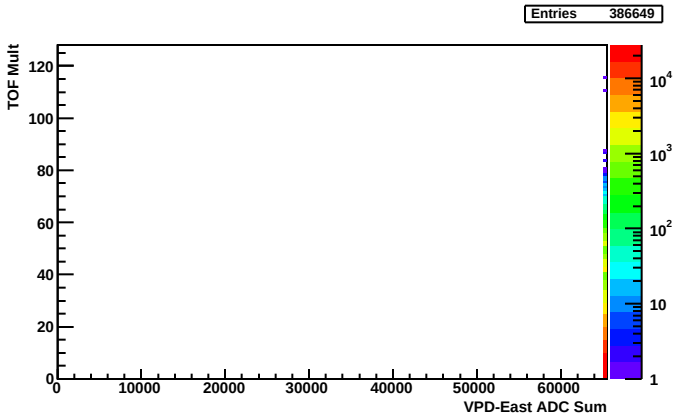

BBQ-VP004 (HI threshold)
Entries 6186384
TAC
QT Input Channel

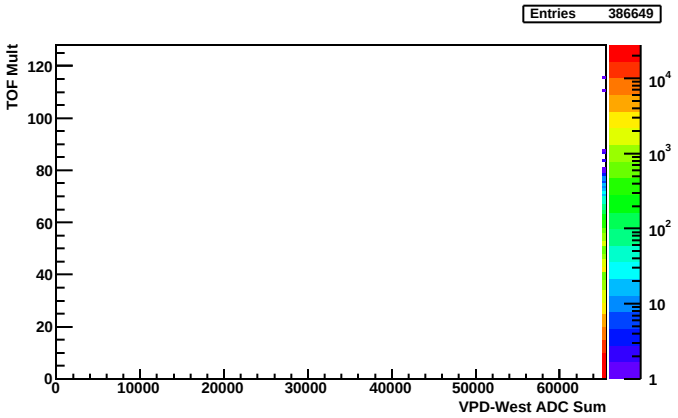

Entries 386649
TOF Mult
BBC-L-East ADC Sum

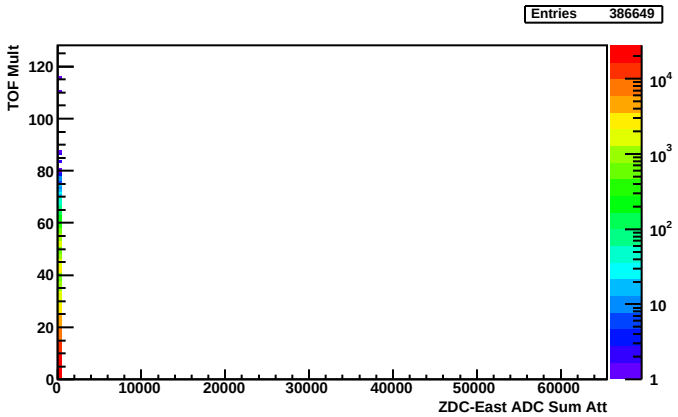

Entries 386649
TOF Mult
BBC-L-West ADC Sum

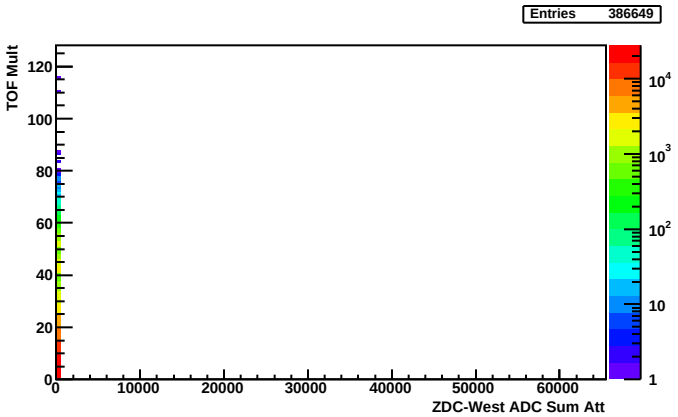

Entries 386649
TOF Mult
BBC-S-East ADC Sum

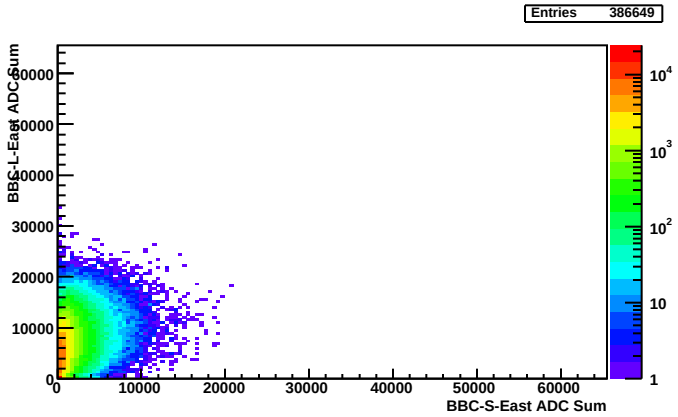

Entries 386649
TOF Mult
BBC-S-West ADC Sum

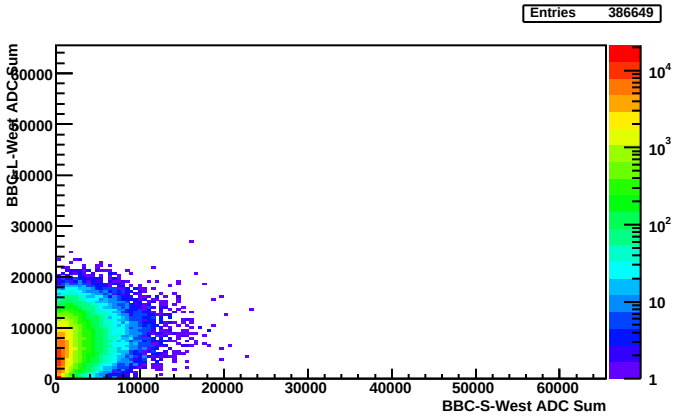

Entries 386649
TOF Mult
VPD-East ADC Sum

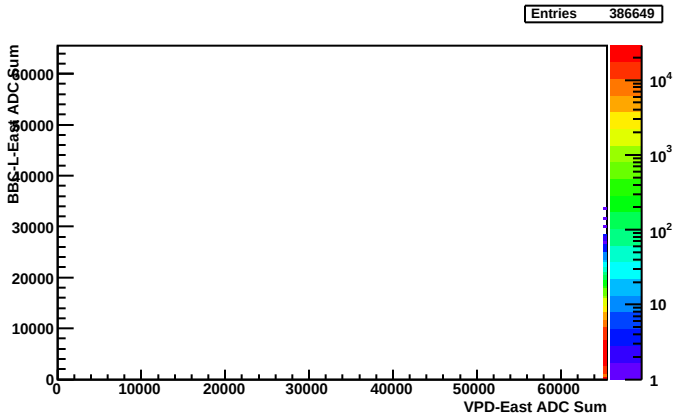

Entries 386649
TOF Mult
VPD-West ADC Sum

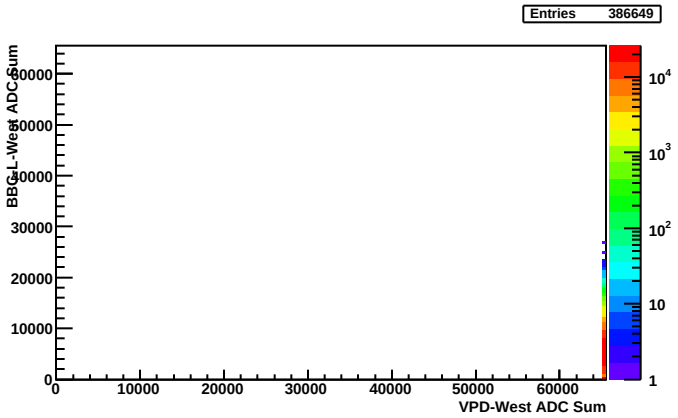

Entries 386649
TOF Mult
ZDC-East ADC Sum Att

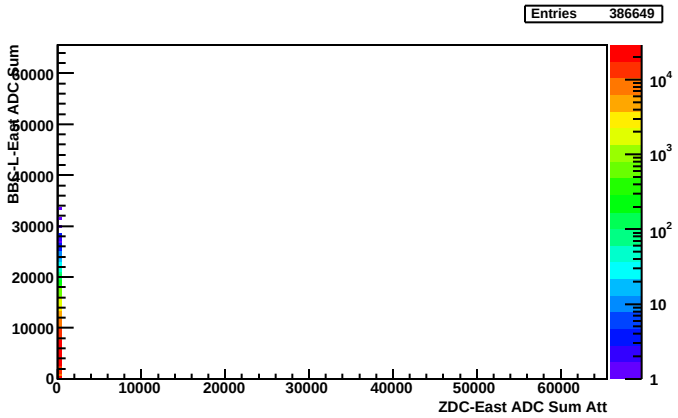

Entries 386649
TOF Mult
ZDC-West ADC Sum Att

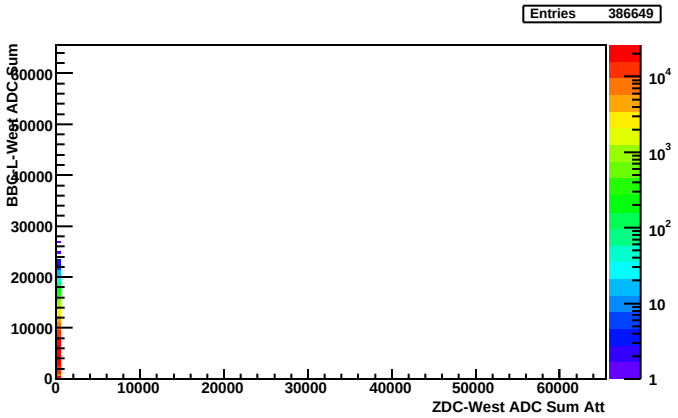

Entries 386649
BBC-L-East ADC Sum
BBC-S-East ADC Sum

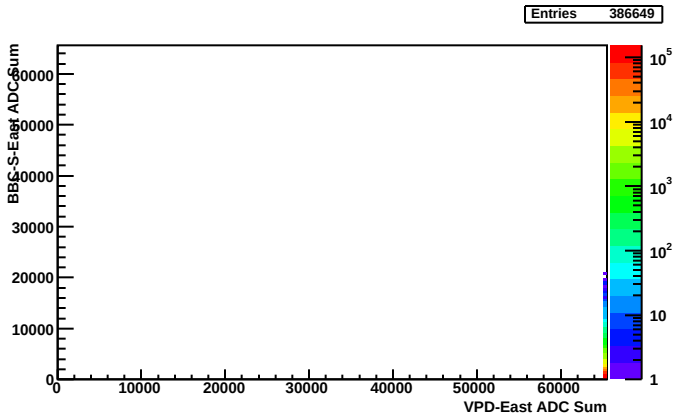

Entries 386649
BBC-L-West ADC Sum
BBC-S-West ADC Sum

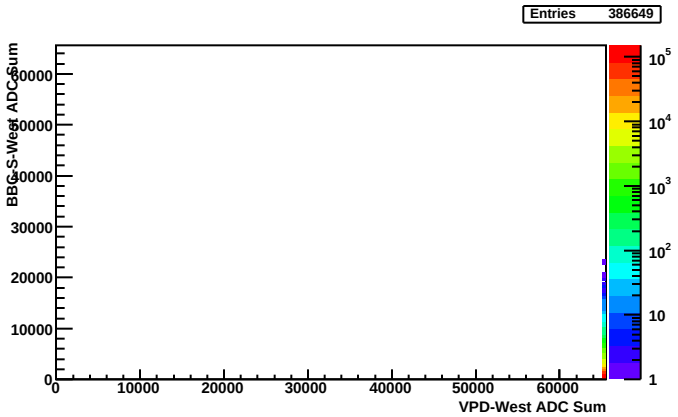

Entries 386649
BBC-L-East ADC Sum
VPD-East ADC Sum

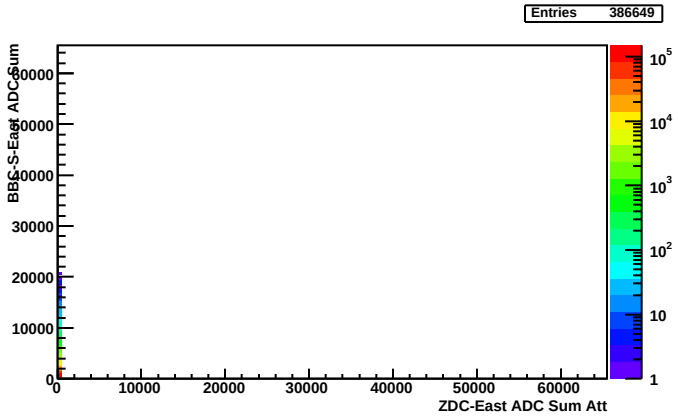

Entries 386649
BBC-L-West ADC Sum
VPD-West ADC Sum

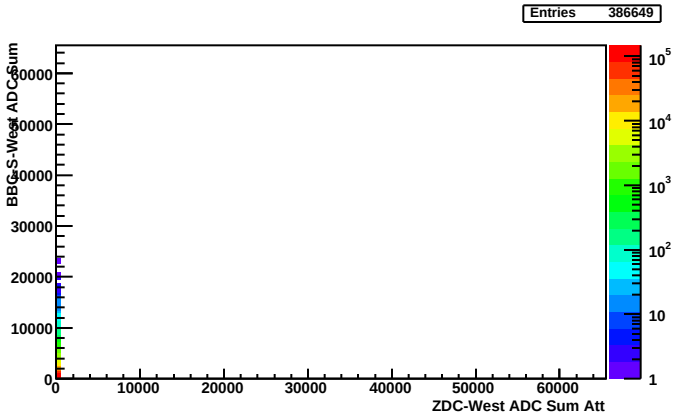

Entries 386649
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

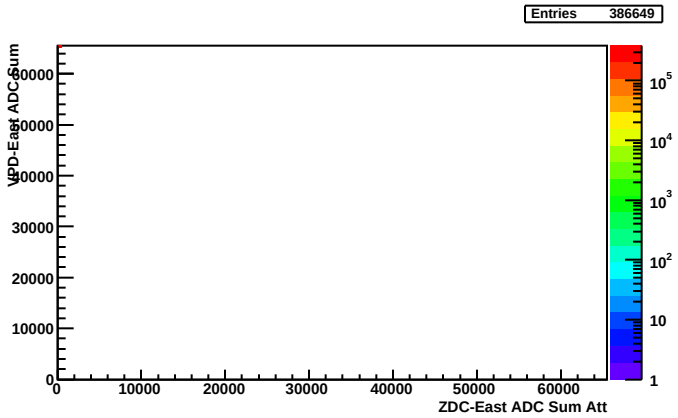

Entries 386649
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

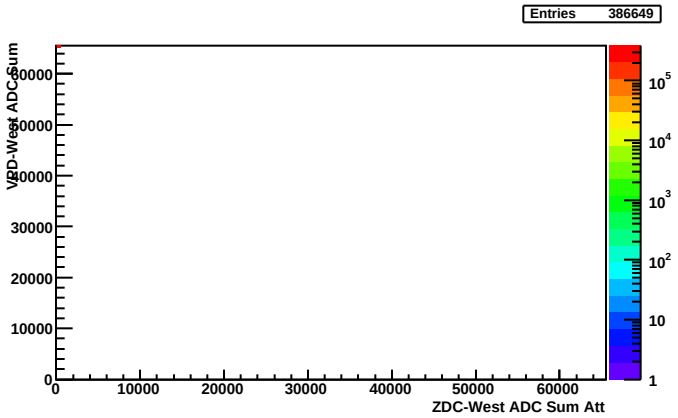

Entries 386649
BBC-S-East ADC Sum
VPD-East ADC Sum

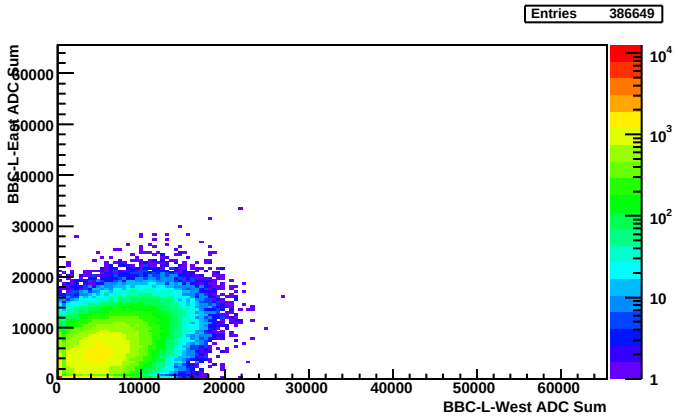

Entries 386649
BBC-S-West ADC Sum
VPD-West ADC Sum

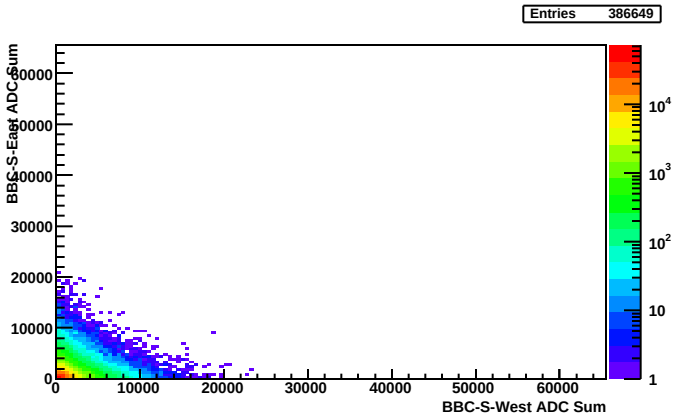

Entries 386649
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

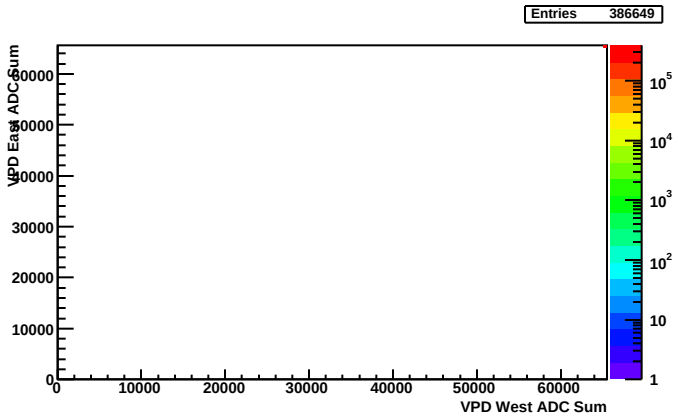

Entries 386649
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

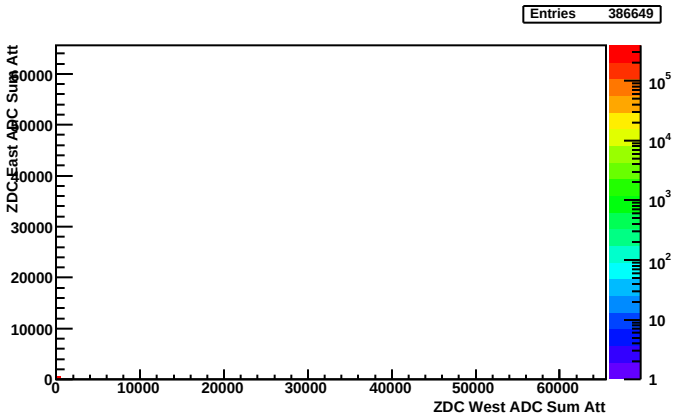

Entries 386649
VPD-East ADC Sum
ZDC-East ADC Sum Att

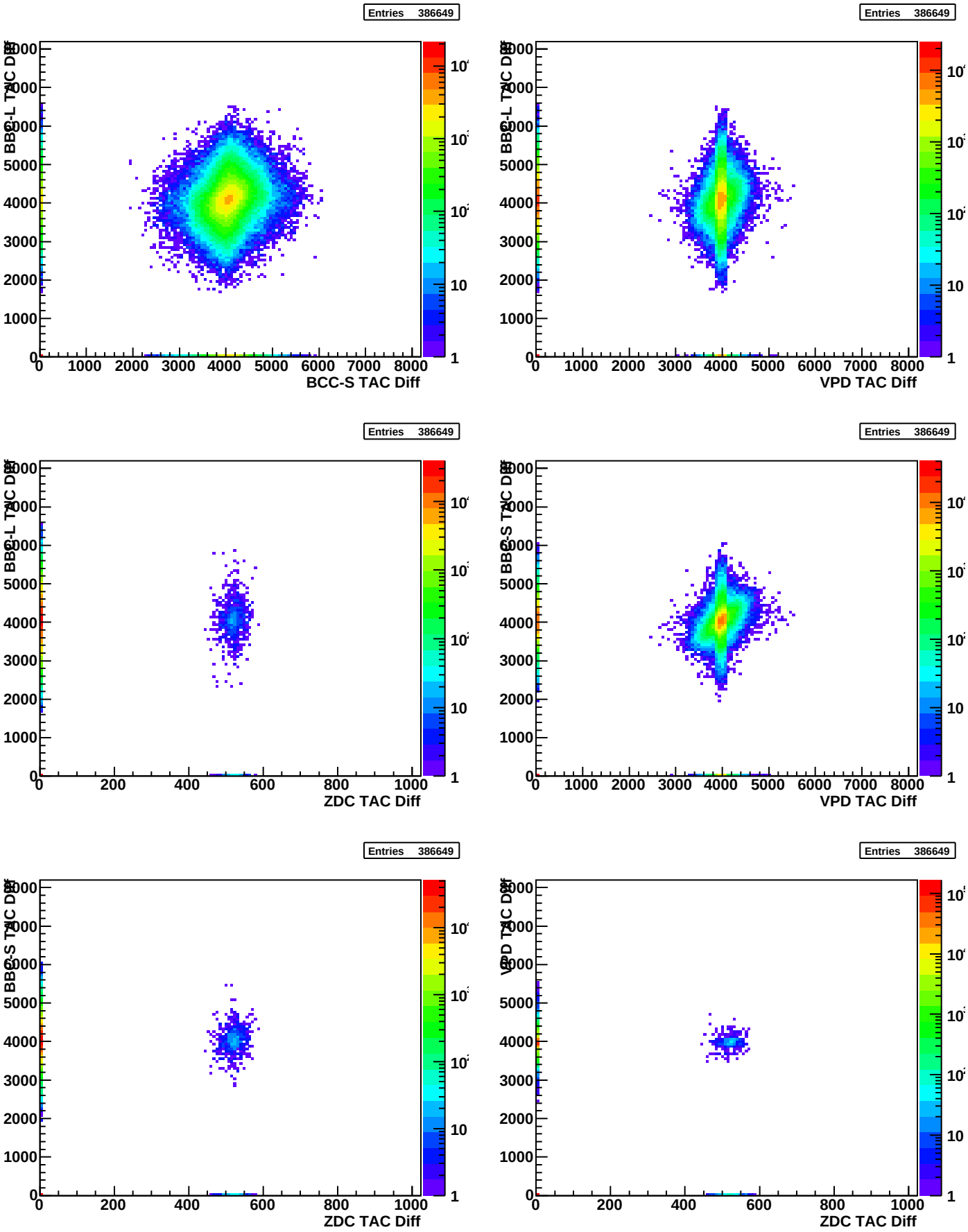

Entries 386649
VPD-West ADC Sum
ZDC-West ADC Sum Att

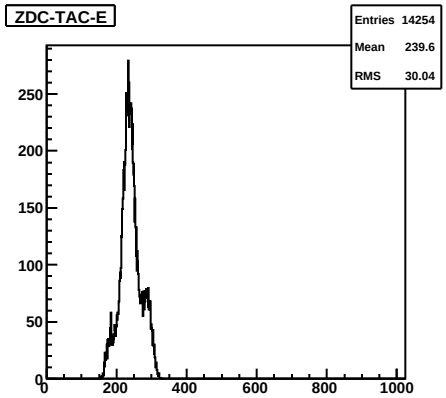

Entries 386649
BBC-L-East ADC Sum
BBC-L-West ADC Sum

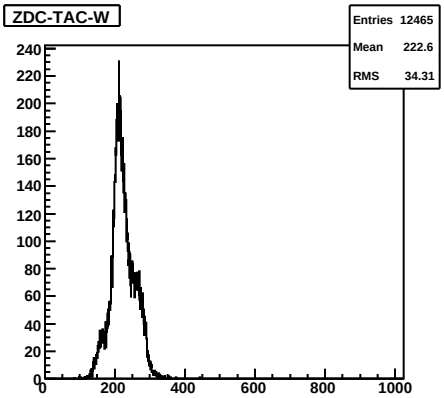

Entries 386649
BBC-S-East ADC Sum
BBC-S-West ADC Sum

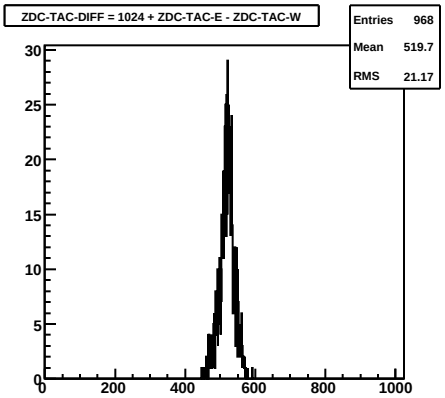

Entries 386649
VPD East ADC Sum
VPD West ADC Sum

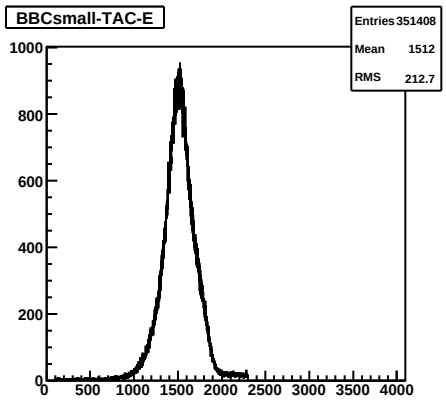

Entries 386649
ZDC East ADC Sum Att
ZDC West ADC Sum Att

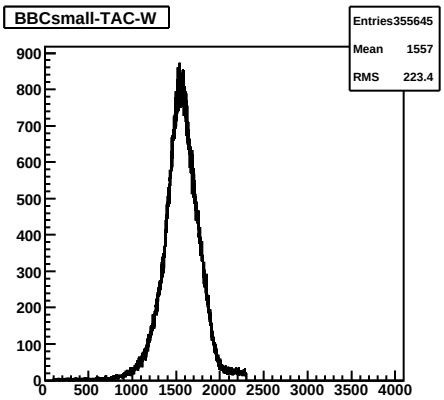

Entries 386649
BBC-L TAC Diff
BCC-S TAC Diff
Entries 386649
BBC-L TAC Diff
VPD TAC Diff
Entries 386649
BBC-L TAC Diff
ZDC TAC Diff
Entries 386649
BBC-S TAC Diff
VPD TAC Diff
Entries 386649
BBC-S TAC Diff
ZDC TAC Diff
Entries 386649
VPD TAC Diff
ZDC TAC Diff

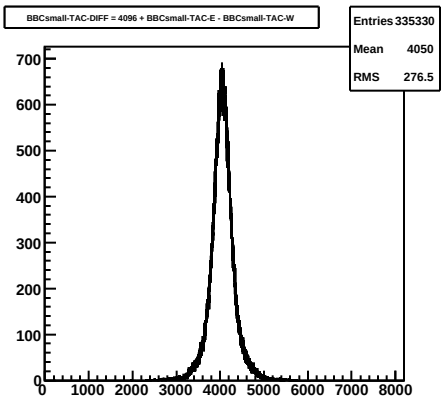

ZDC-TAC-E
Entries 14254
Mean 239.6
RMS 30.04

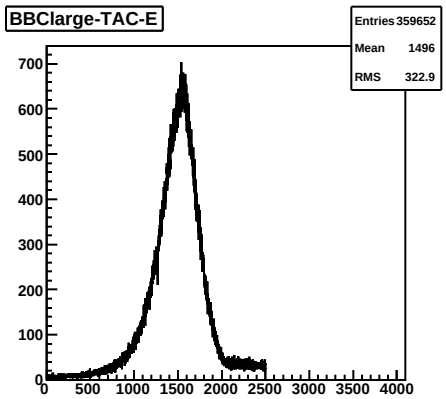

ZDC-TAC-W
Entries 12465
Mean 222.6
RMS 34.31

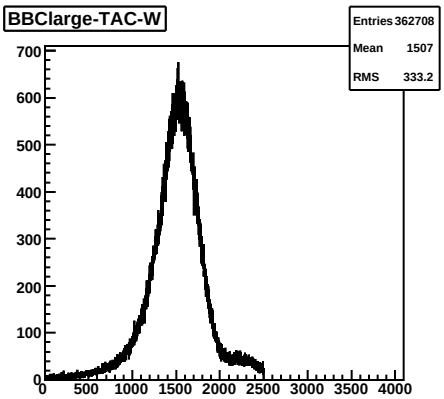

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 968
Mean 519.7
RMS 21.17

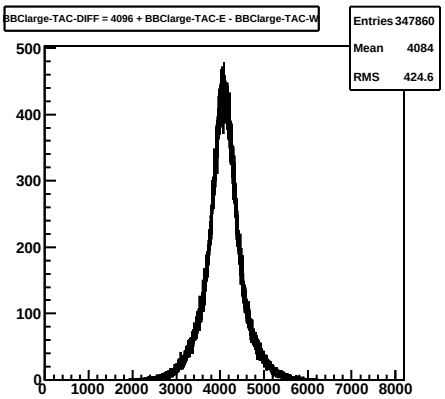

BBCsmall-TAC-E
Entries 351408
Mean 1512
RMS 212.7

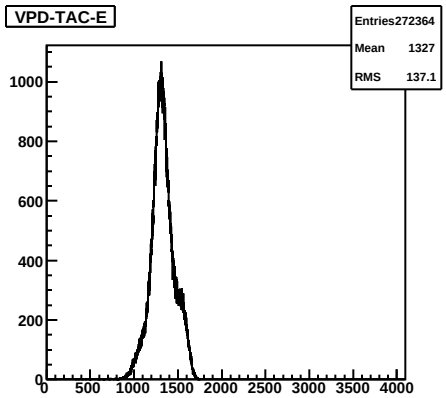

BBCsmall-TAC-W
Entries 355645
Mean 1557
RMS 223.4

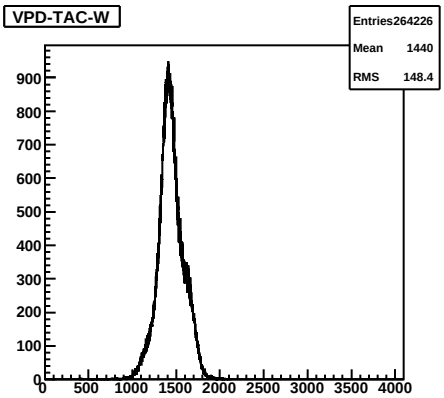

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 335330
Mean 4050
RMS 276.5

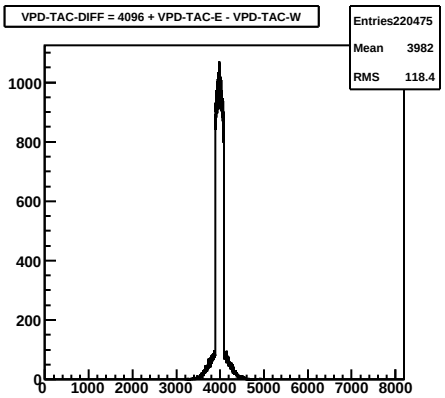

BBClarge-TAC-E
Entries 359652
Mean 1496
RMS 322.9

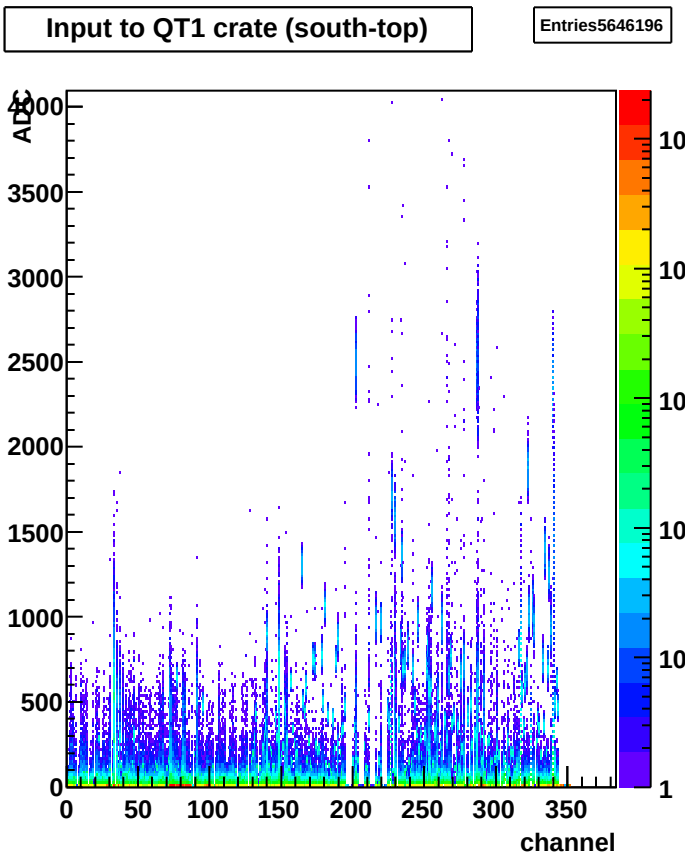

BBClarge-TAC-W
Entries 362708
Mean 1507
RMS 333.2

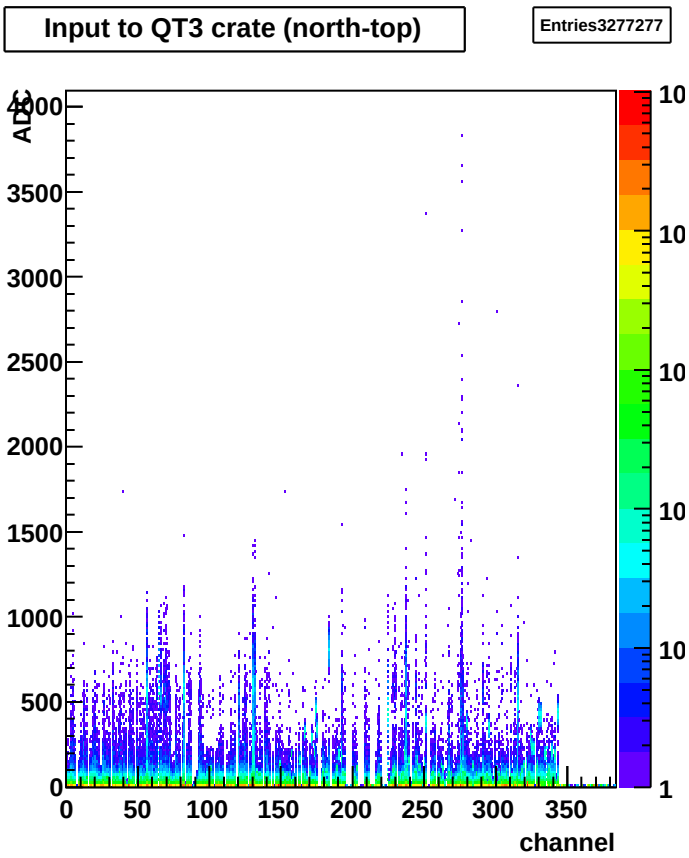

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 347860
Mean 4084
RMS 424.6

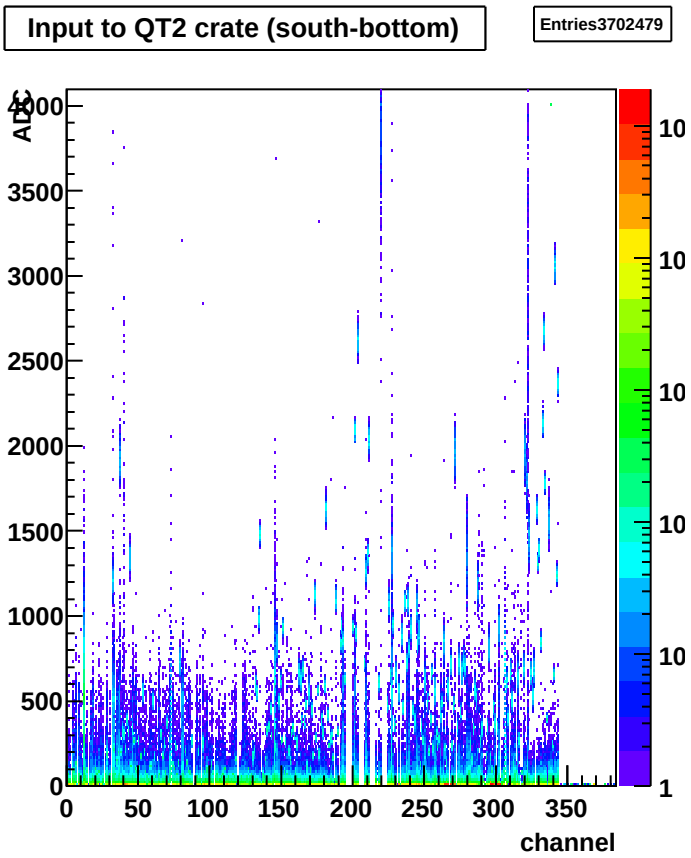

VPD-TAC-E
Entries 272364
Mean 1327
RMS 137.1

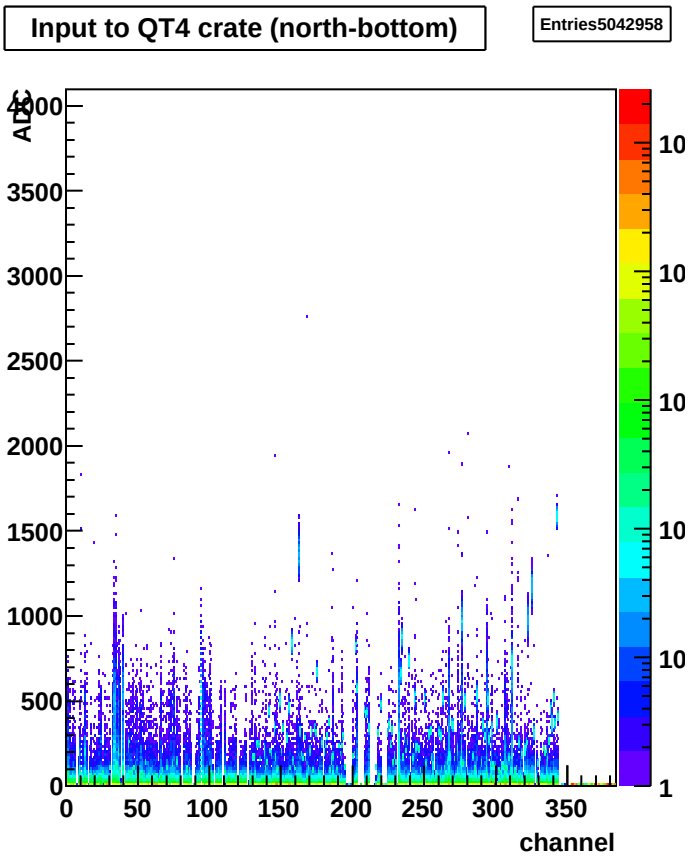

VPD-TAC-W
Entries 264226
Mean 1440
RMS 148.4

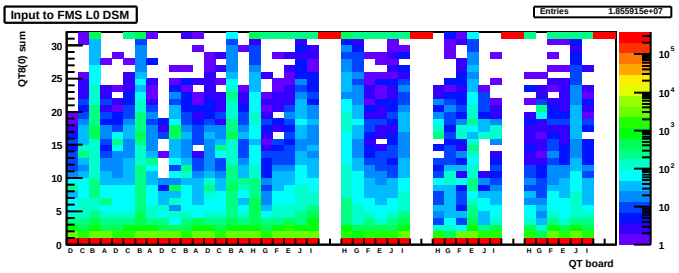

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 220475
Mean 3982
RMS 118.4

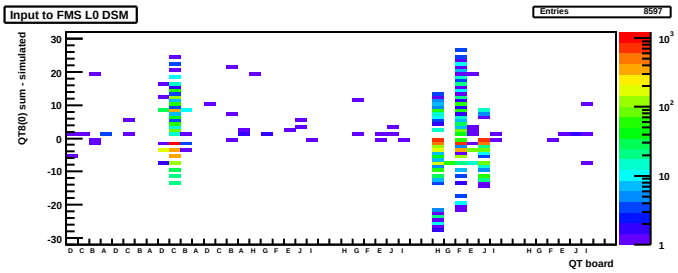

Input to QT1 crate (south-top)
Entries5646196
ADC
channel

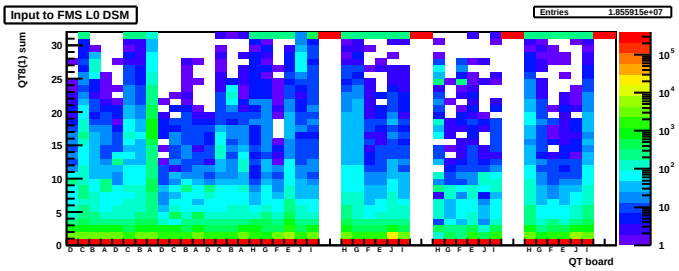

Input to QT3 crate (north-top)
Entries3277277
ADC
channel

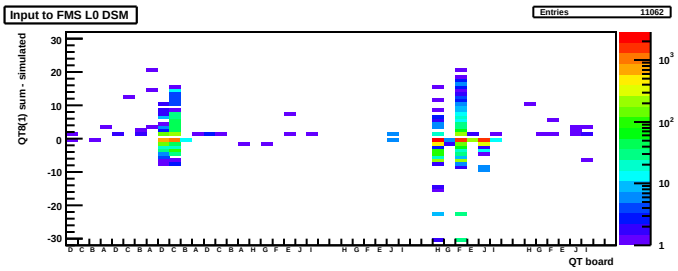

Input to QT2 crate (south-bottom)
Entries3702479
ADC
channel

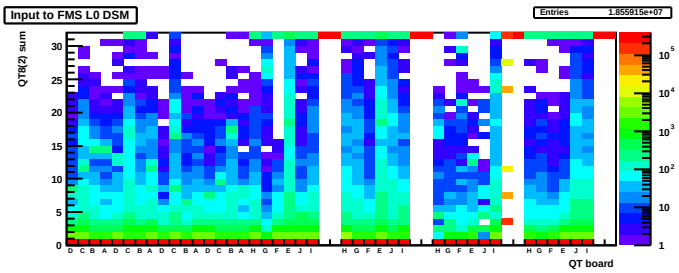

Input to QT4 crate (north-bottom)
Entries5042958
ADC
channel

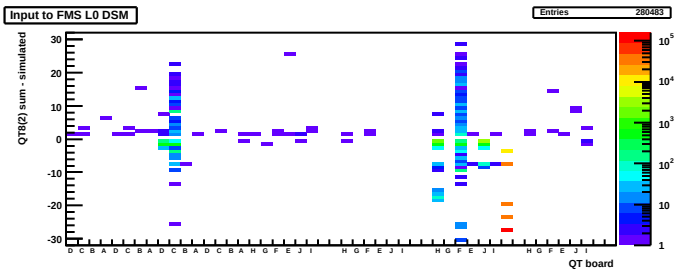

Input to FMS L0 DSM
Entries 1.855915e+07
QT8(0) sum
QT board

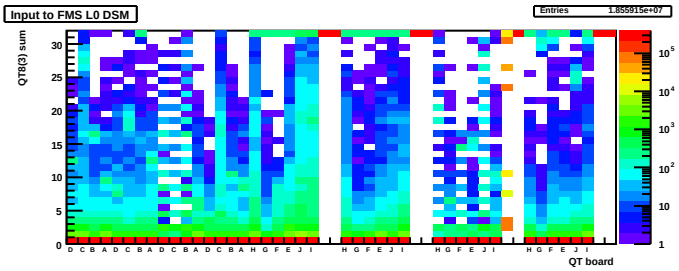

Input to FMS L0 DSM
Entries 8597
QT8(0) sum - simulated
QT board

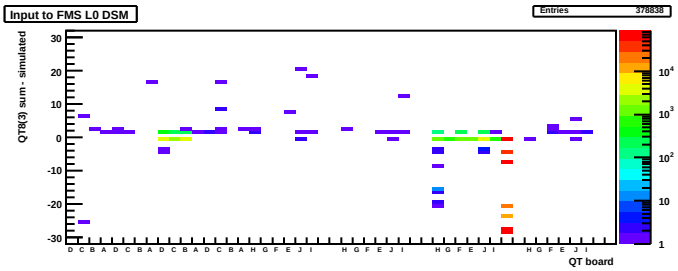

Input to FMS L0 DSM
Entries 1.855915e+07
QT8(1) sum
QT board

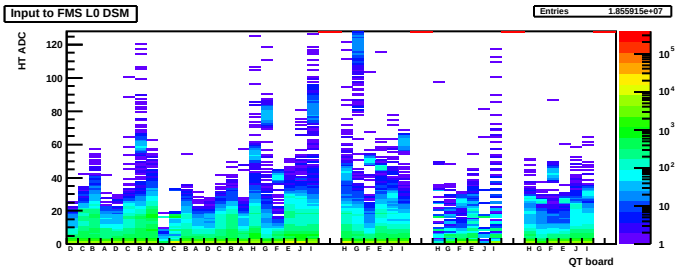

Input to FMS L0 DSM
Entries 11062
QT8(1) sum - simulated
QT board

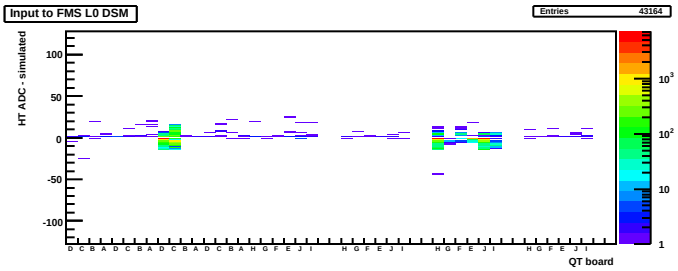

Input to FMS L0 DSM
Entries 1.855915e+07
QT8(2) sum
QT board

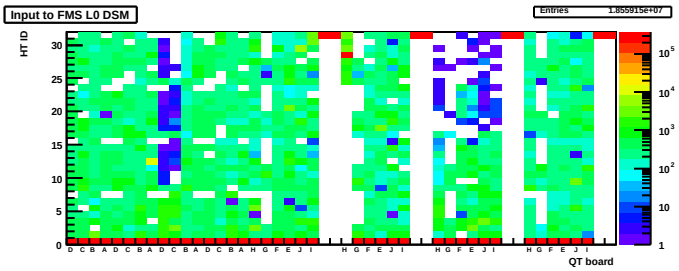

Input to FMS L0 DSM
Entries 280483
QT8(2) sum - simulated
QT board

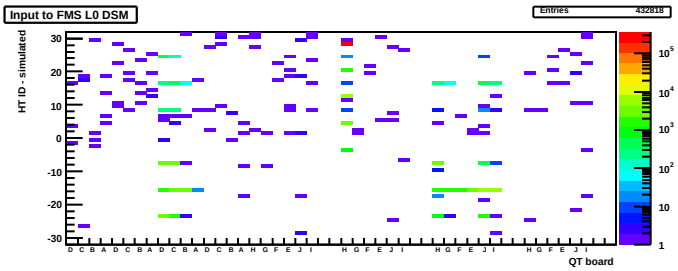

Input to FMS L0 DSM
Entries 1.855915e+07
QT8(3) sum
QT board

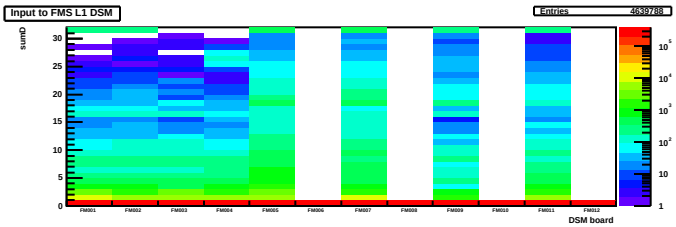

Input to FMS L0 DSM
Entries 378838
QT8(3) sum - simulated
QT board

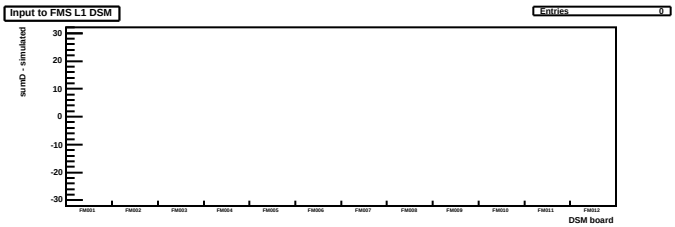

Input to FMS L0 DSM
Entries 1.855915e+07
HT ADC
QT board

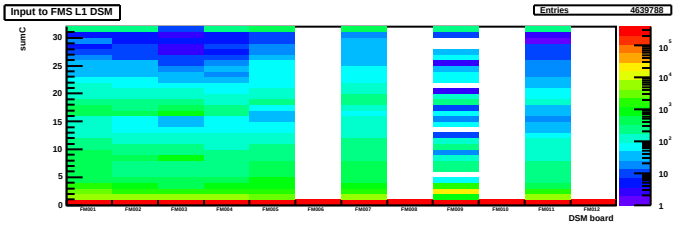

Input to FMS L0 DSM
Entries 43164
HT ADC - simulated
QT board

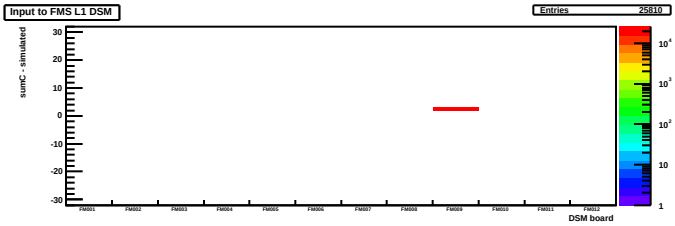

Input to FMS L0 DSM
Entries 1.855915e+07
HT ID
QT board

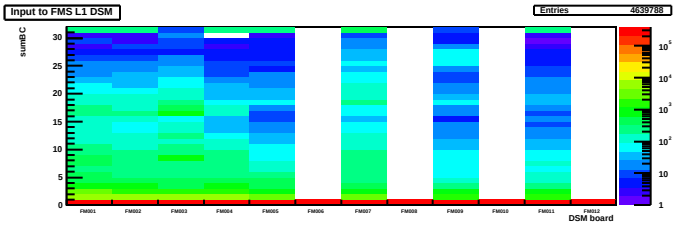

Input to FMS L0 DSM
Entries 432818
HT ID - simulated
QT board

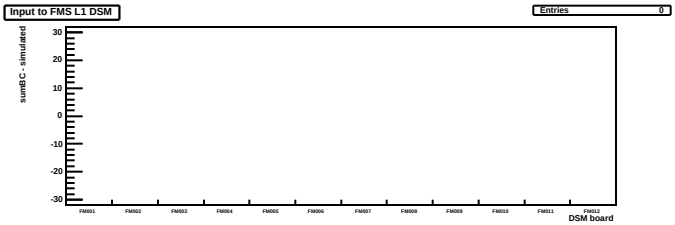

Input to FMS L1 DSM
Entries 4639788
sumD
DSM board

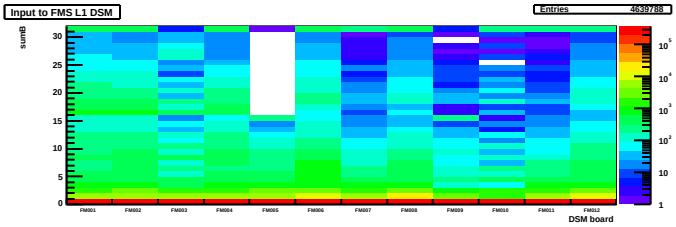

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

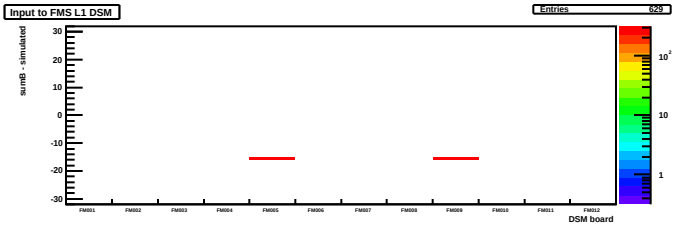

Input to FMS L1 DSM
Entries 4639788
sumC
DSM board

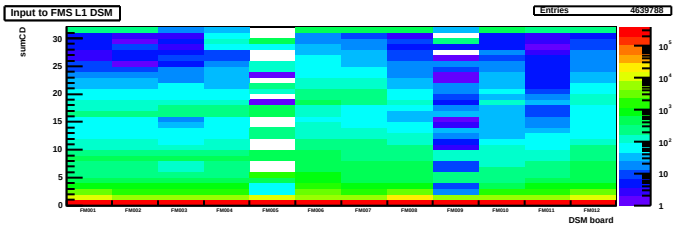

Input to FMS L1 DSM
Entries 25810
sumC - simulated
DSM board

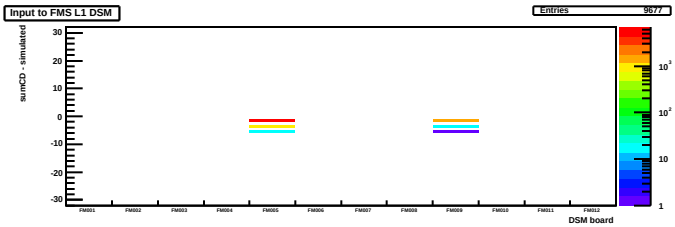

Input to FMS L1 DSM
Entries 4639788
sumBC
DSM board

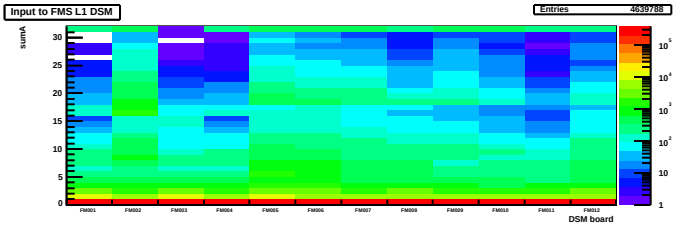

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

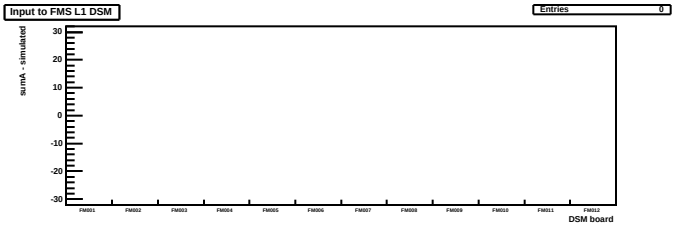

Input to FMS L1 DSM
Entries 4639788
sumB
DSM board

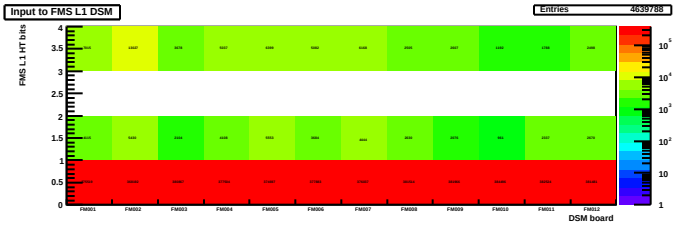

Input to FMS L1 DSM
Entries 629
sumB - simulated
DSM board

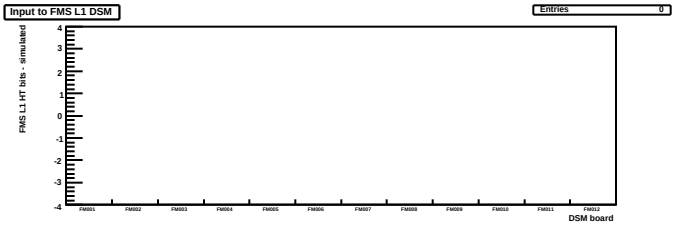

Input to FMS L1 DSM
Entries 4639788
sumCD
DSM board

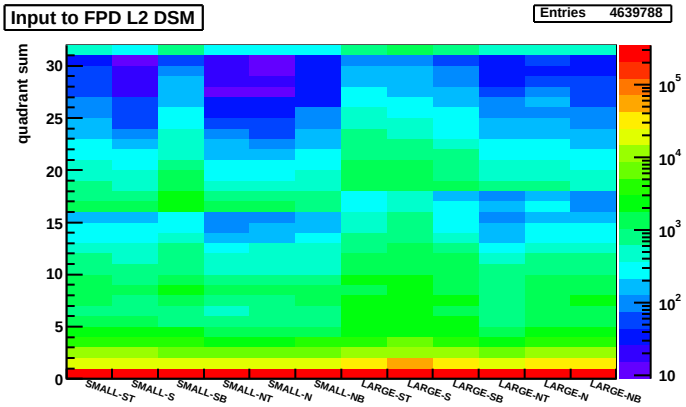

Input to FMS L1 DSM
Entries 9677
sumCD - simulated
DSM board

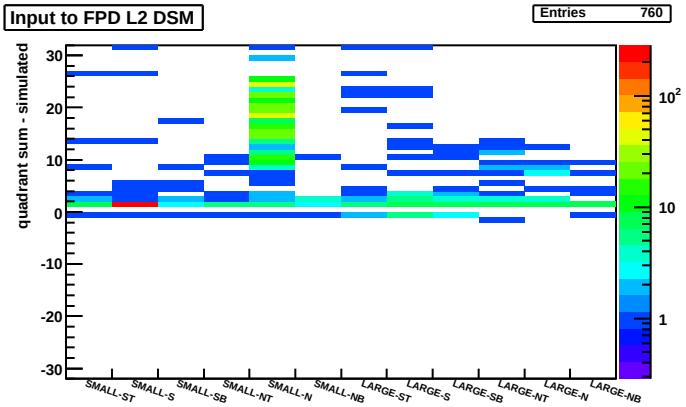

Input to FMS L1 DSM
Entries 4639788
sumA
DSM board

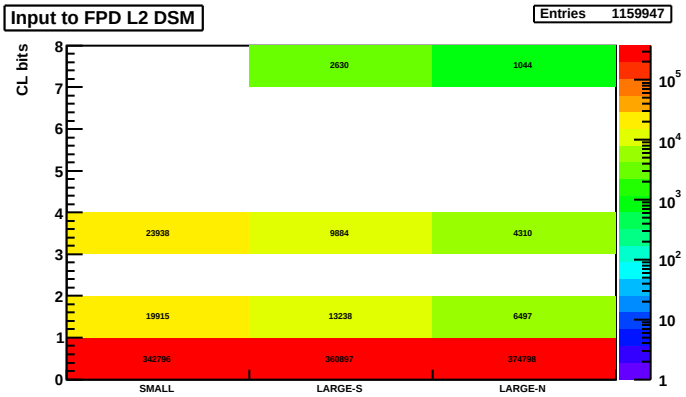

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

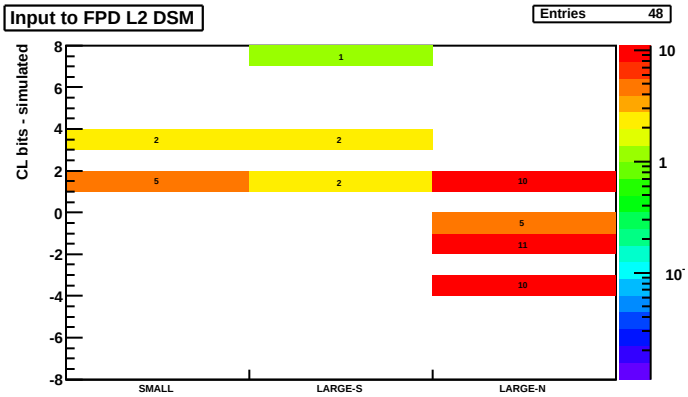

Input to FMS L1 DSM
Entries 4639788
FMS L1 HT bits
DSM board

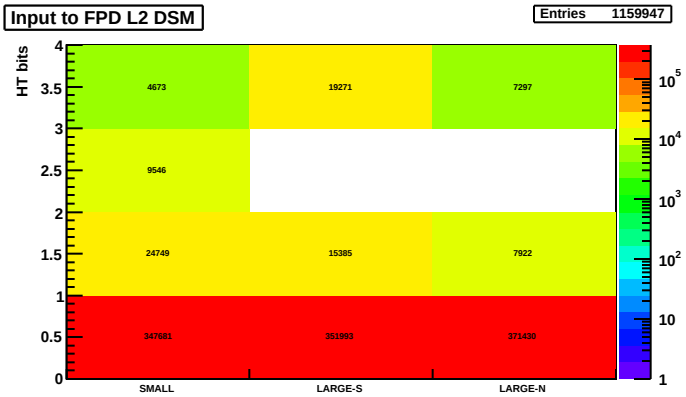

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

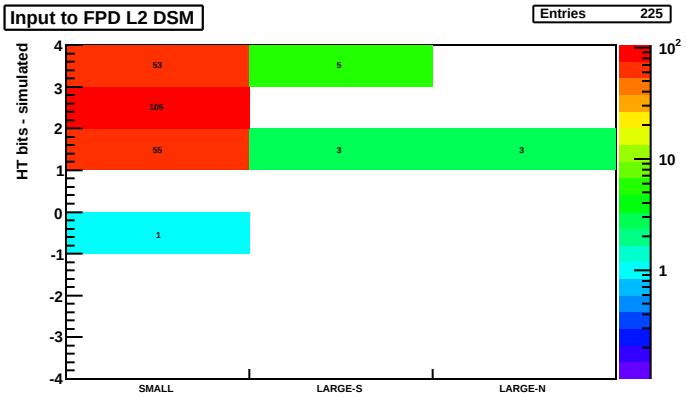

Input to FPD L2 DSM
Entries 4639788
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

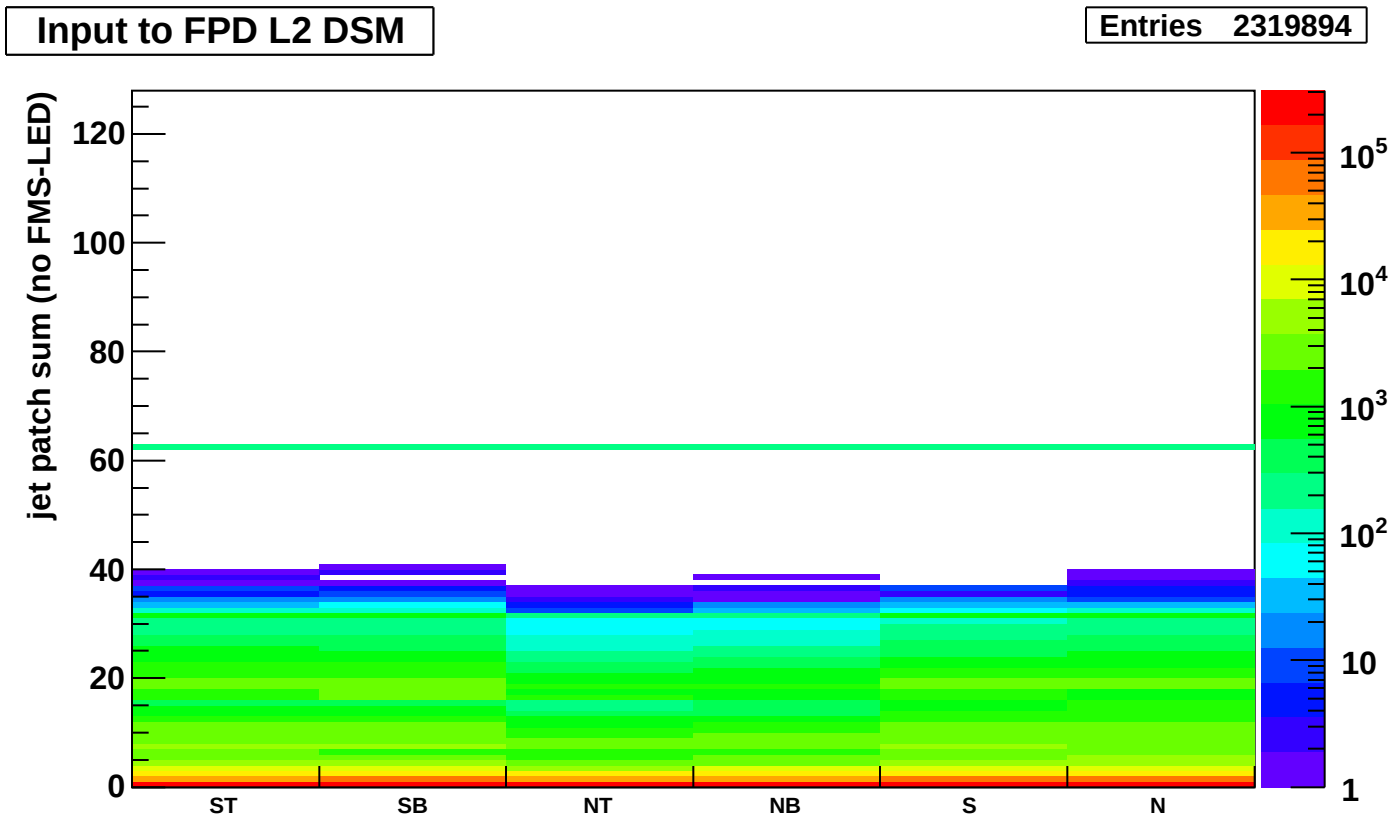

Input to FPD L2 DSM
Entries 760
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

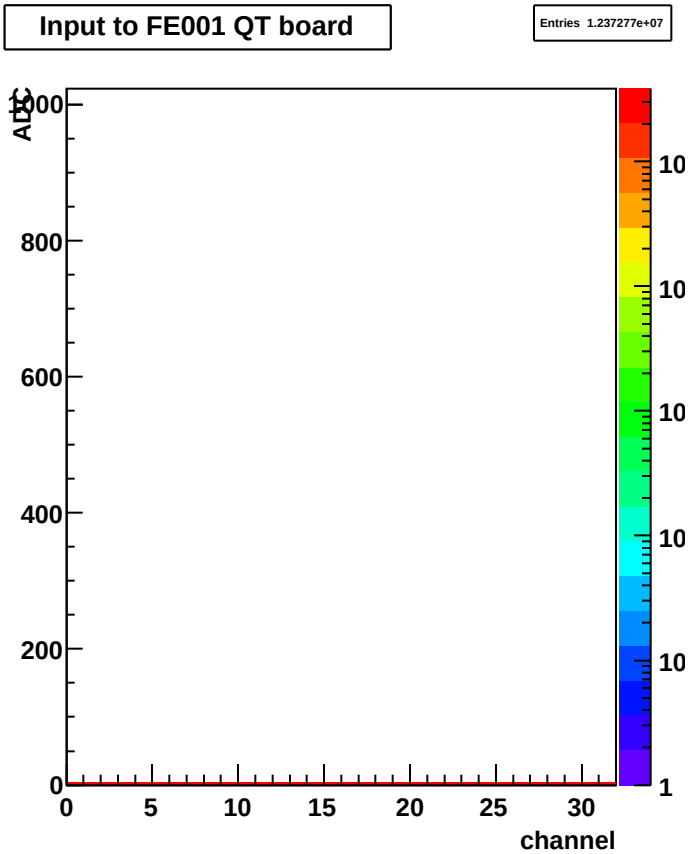

Input to FPD L2 DSM
Entries 1159947
CL bits
2630
1044
23938
9884
4310
19915
13238
6497
342796
360897
374798
SMALL
LARGE-S
LARGE-N

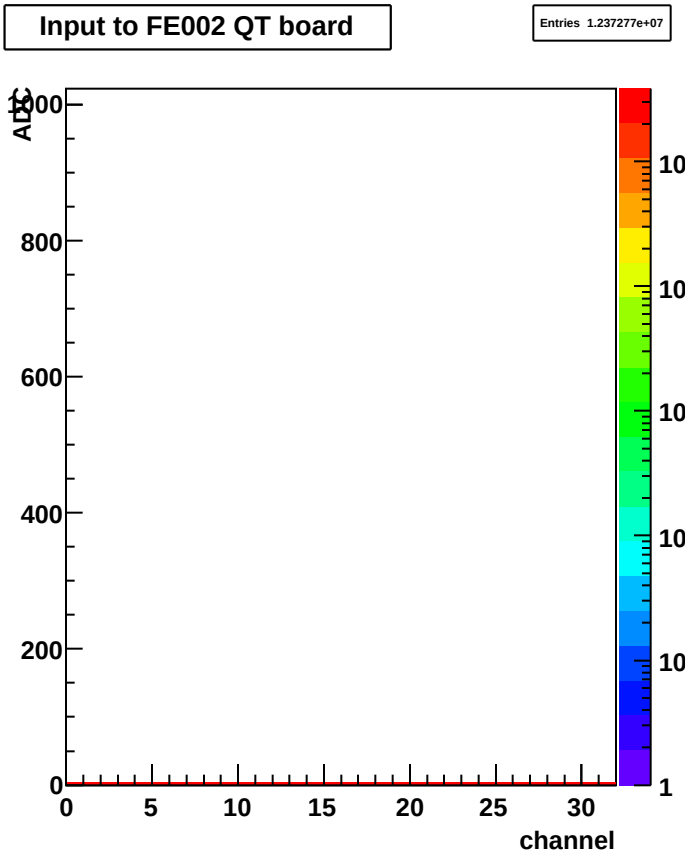

Input to FPD L2 DSM
Entries 48
CL bits - simulated
1
2
2
5
2
10
5
11
10
SMALL
LARGE-S
LARGE-N

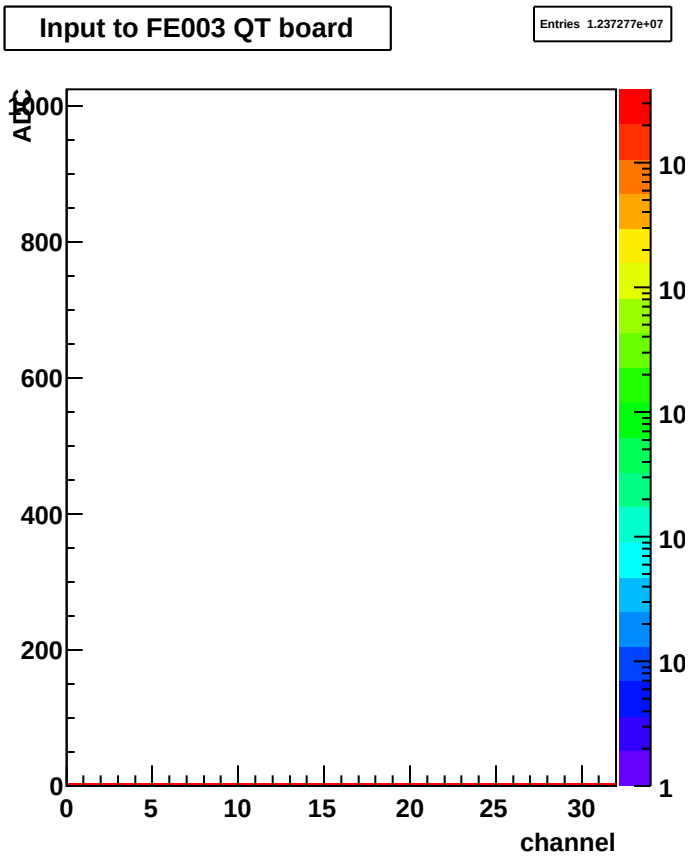

Input to FPD L2 DSM
Entries 1159947
HT bits
4673
19271
7297
9546
24749
15385
7922
347681
351993
371430
SMALL
LARGE-S
LARGE-N

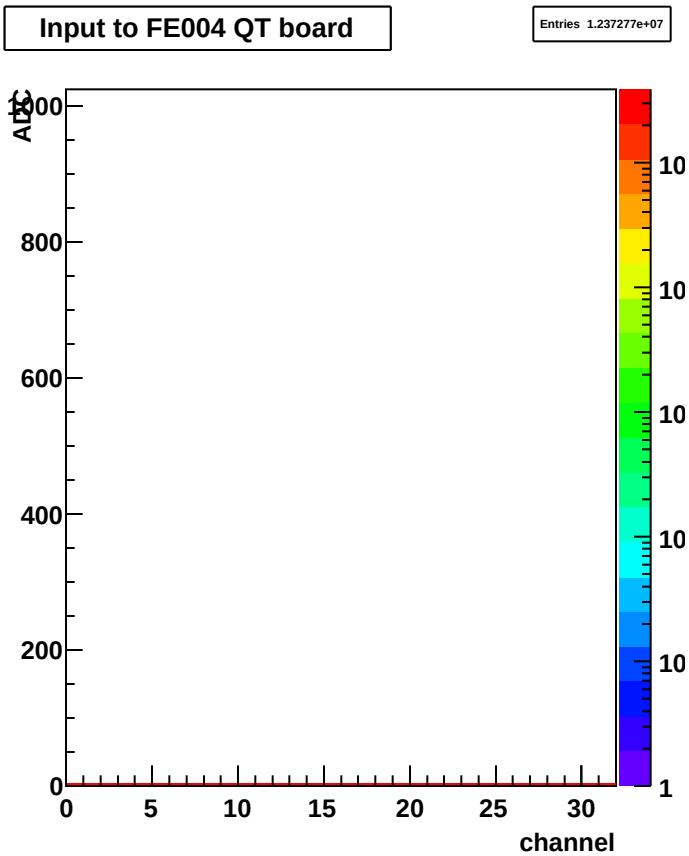

Input to FPD L2 DSM
Entries 225
HT bits - simulated
53
5
105
55
3
3
1
SMALL
LARGE-S
LARGE-N

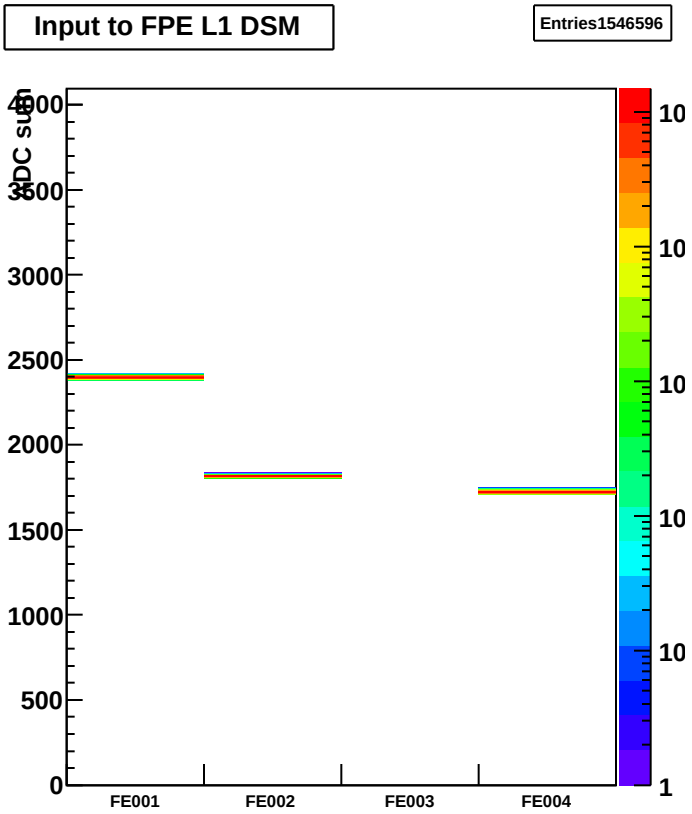

Input to FPD L2 DSM
Entries 2319894
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5


Input to FE001 QT board
Entries 1.237277e+07
ADC
channel

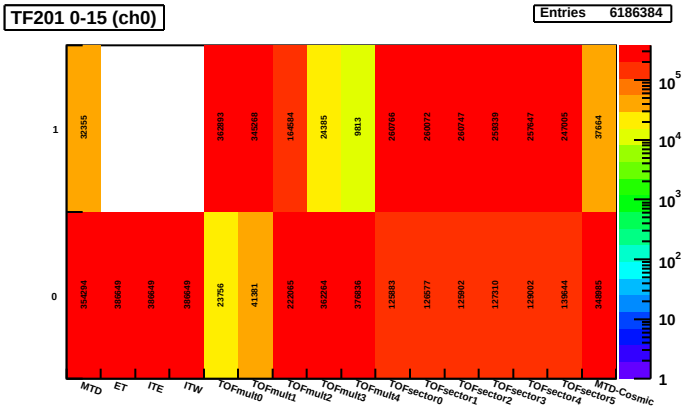

Input to FE002 QT board
Entries 1.237277e+07
ADC
channel


Input to FE003 QT board
Entries 1.237277e+07
ADC
channel

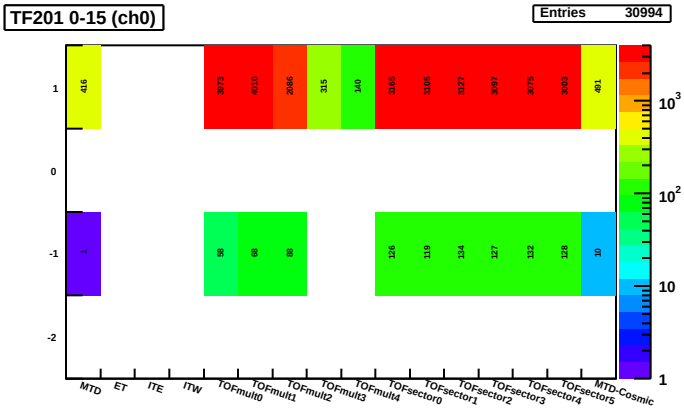

Input to FE004 QT board
Entries 1.237277e+07
ADC
channel


Input to FPE L1 DSM
Entries1546596
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

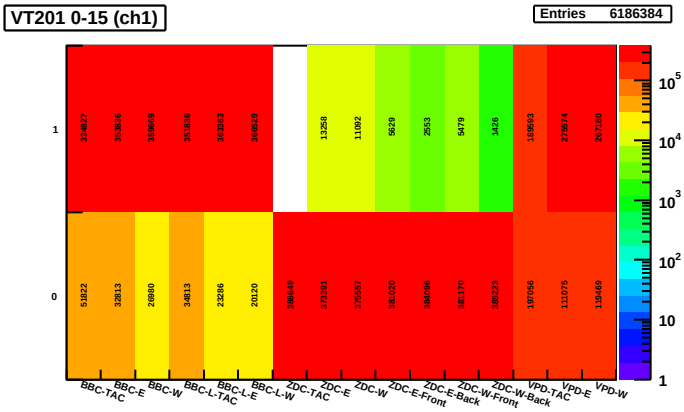
TF201 0-15 (ch0)



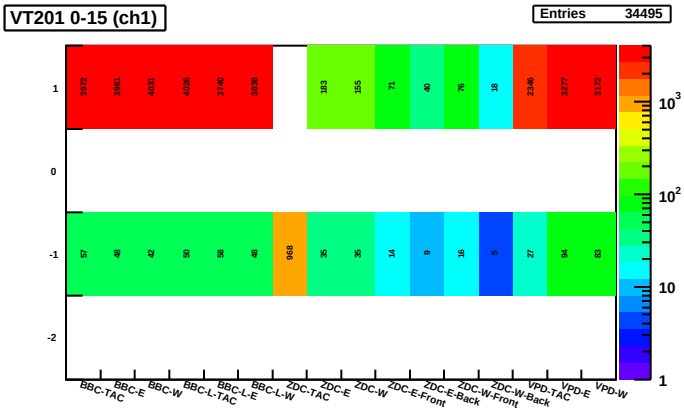
TF201 0-15 (ch0)



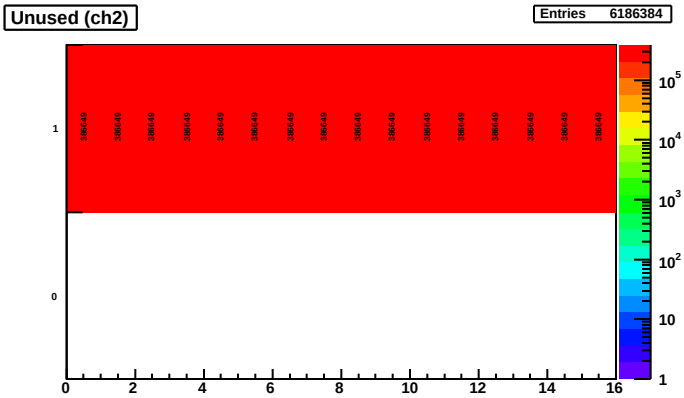
VT201 0-15 (ch1)

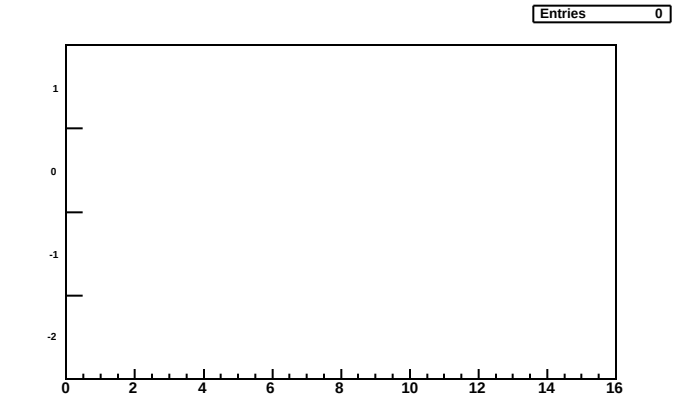


VT201 0-15 (ch1)

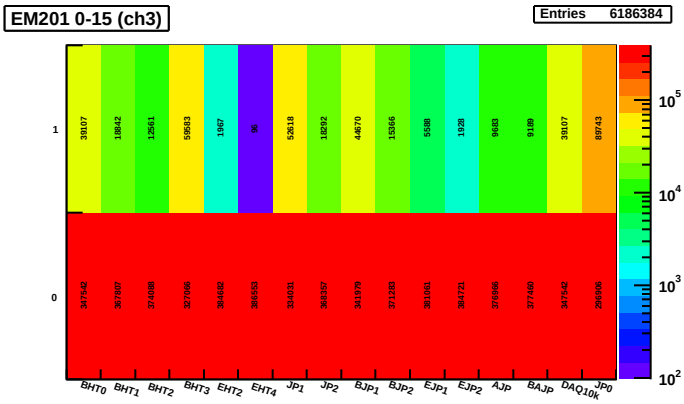


Unused (ch2)





EM201 0-15 (ch3)



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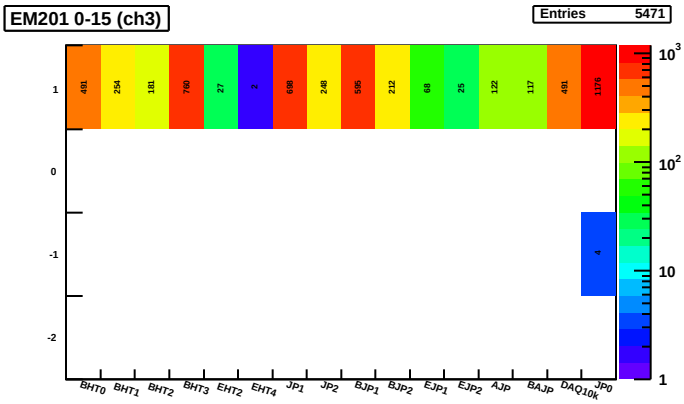
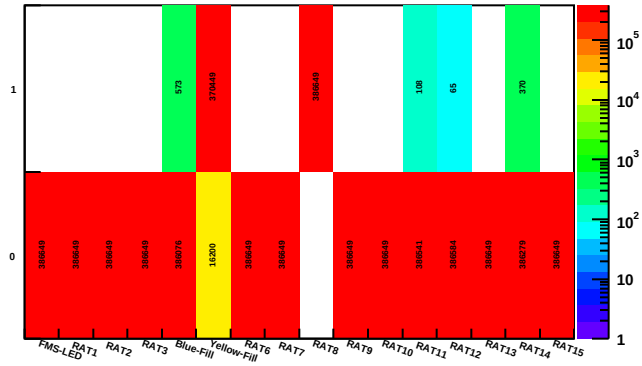
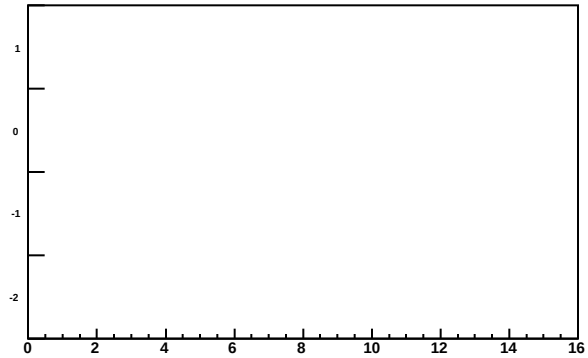
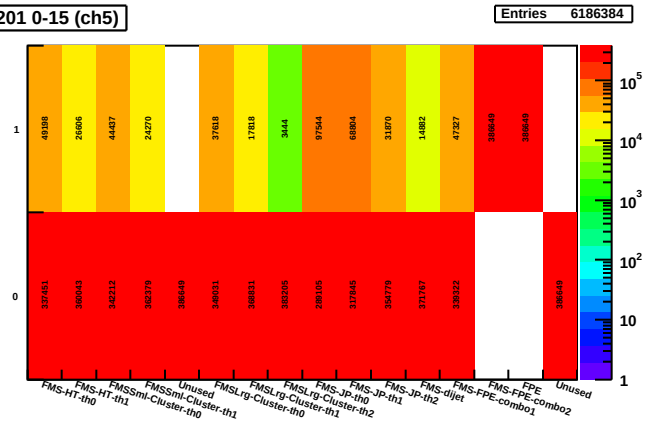


RAT board (ch4)





FP201 0-15 (ch5)



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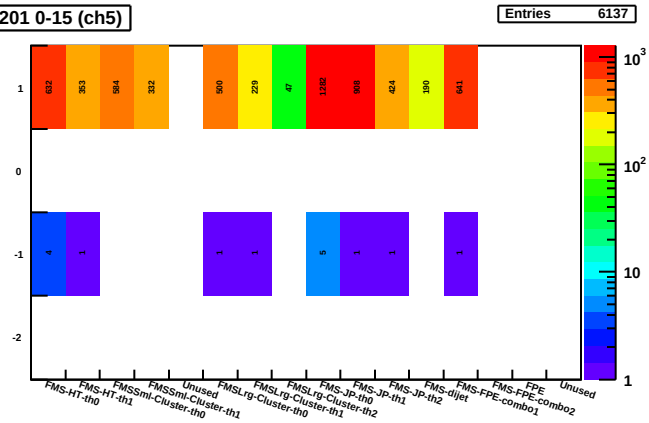

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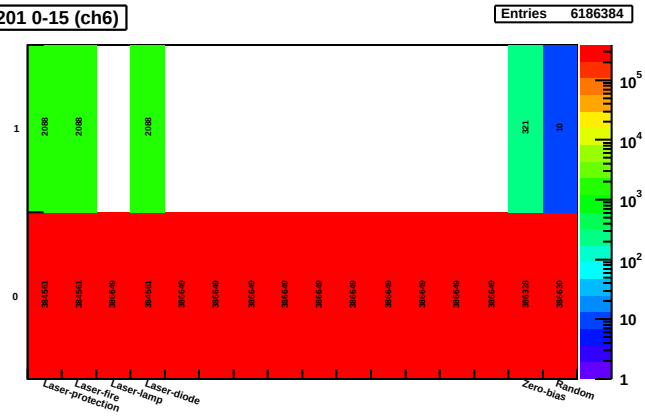

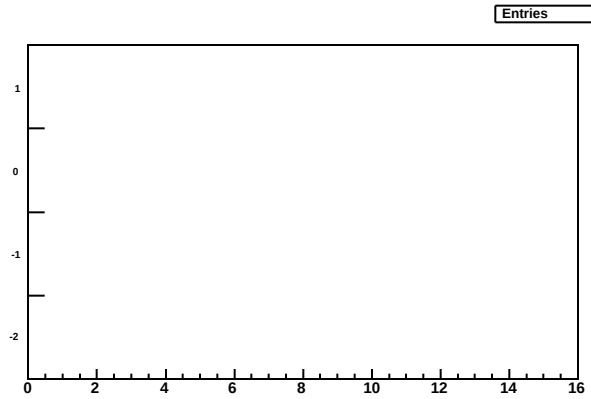
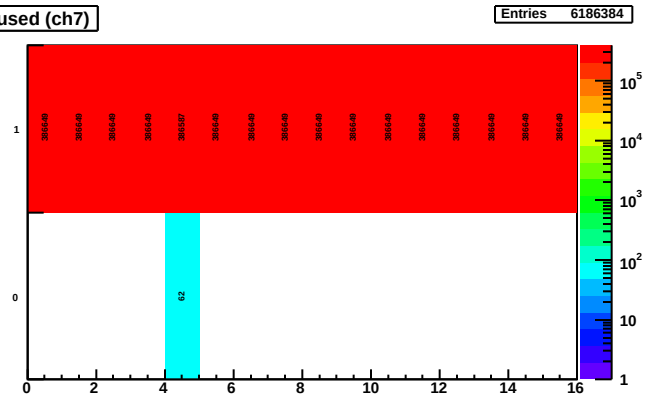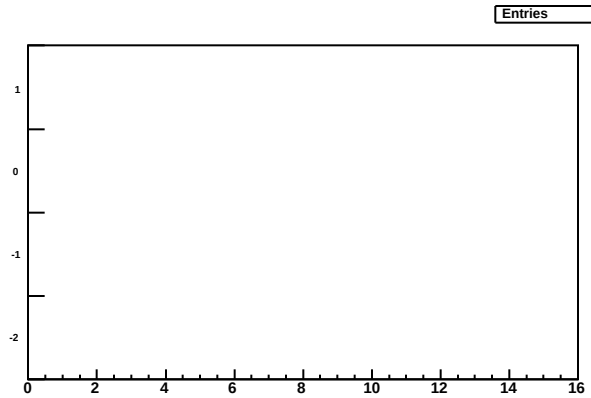
Unused (ch7)