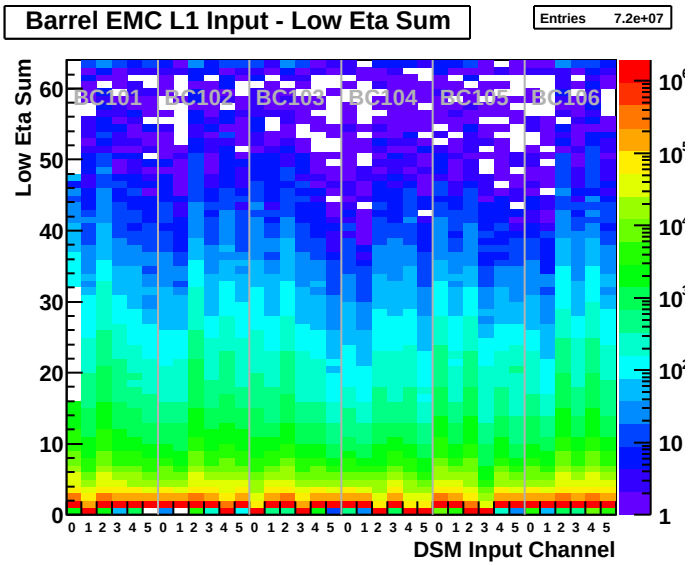
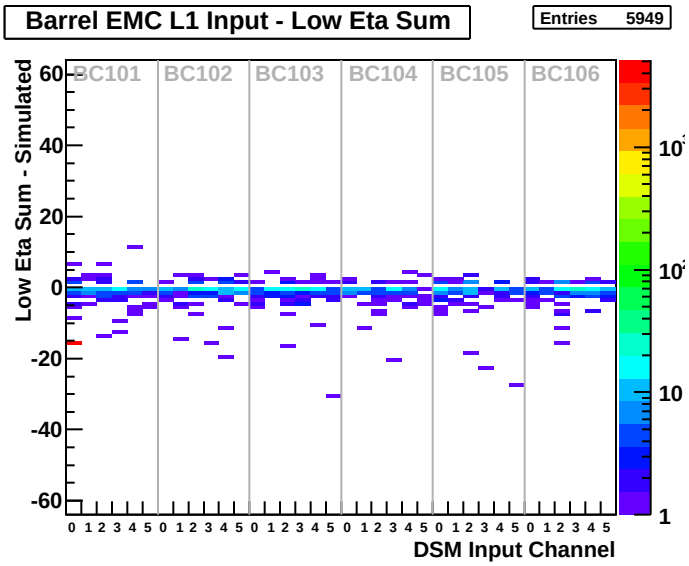
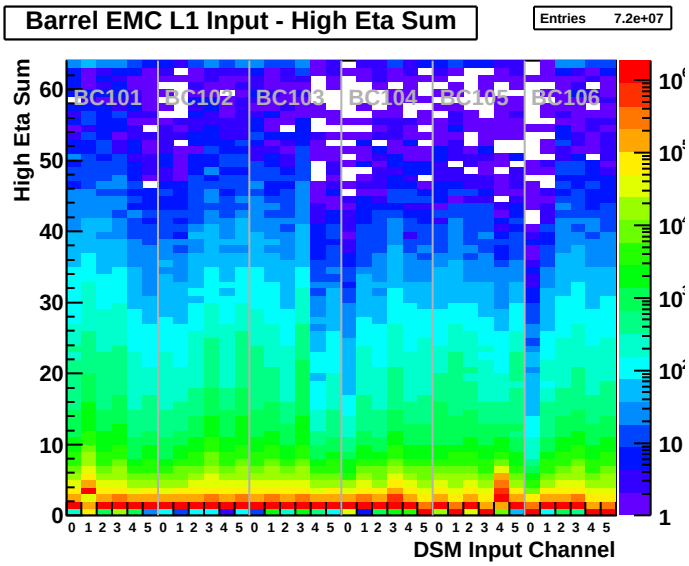
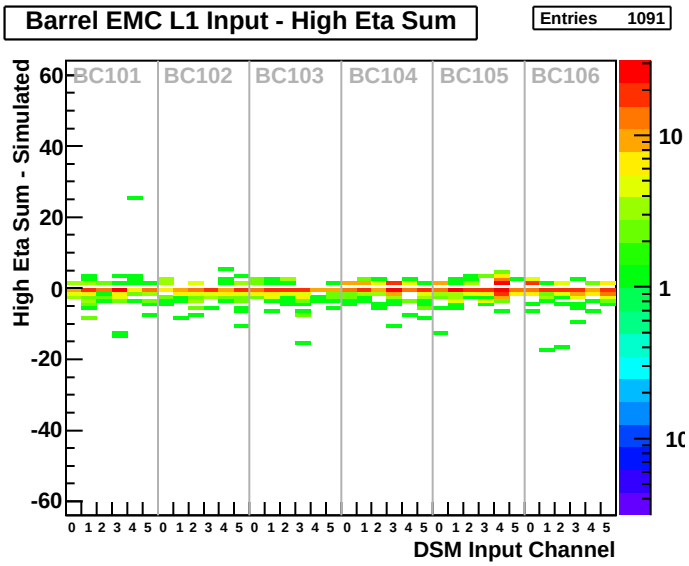
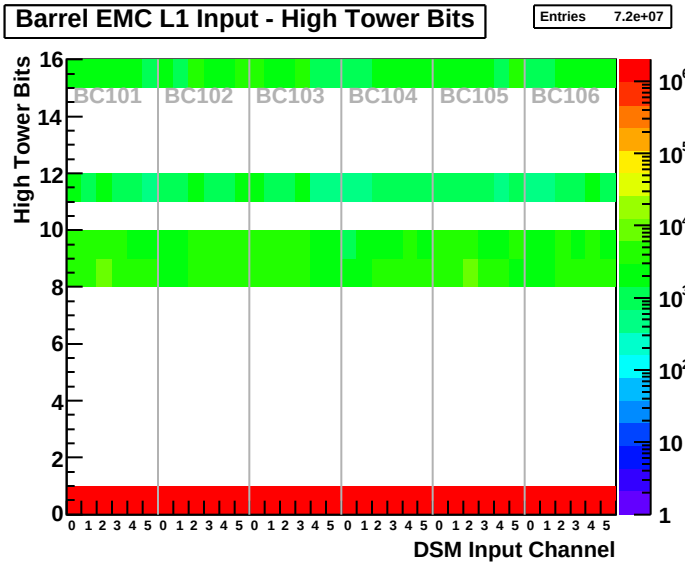
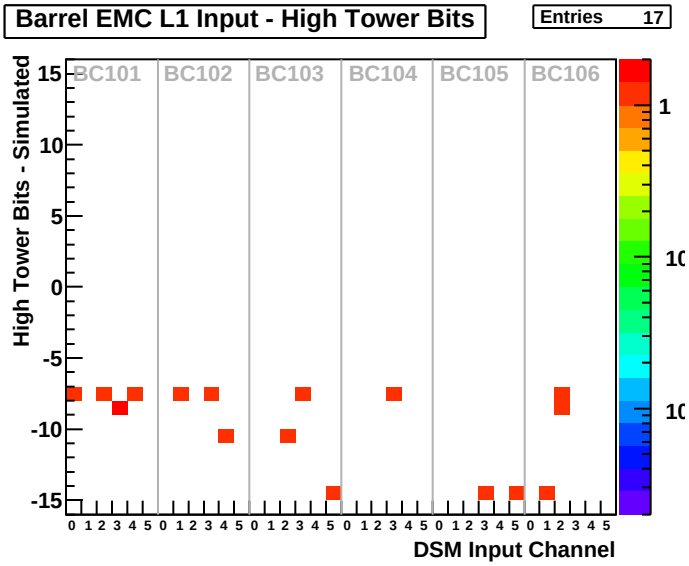

Barrel EMC L1 Input - Low Eta Sum
Entries 7.2e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

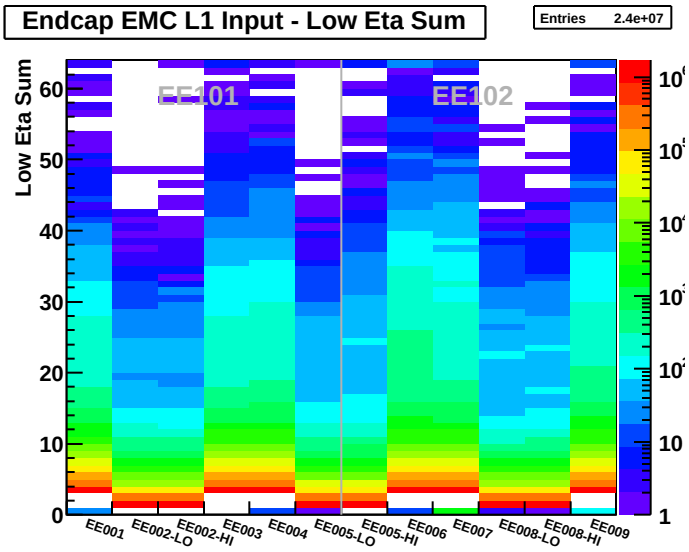

Barrel EMC L1 Input - Low Eta Sum
Entries 5949
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

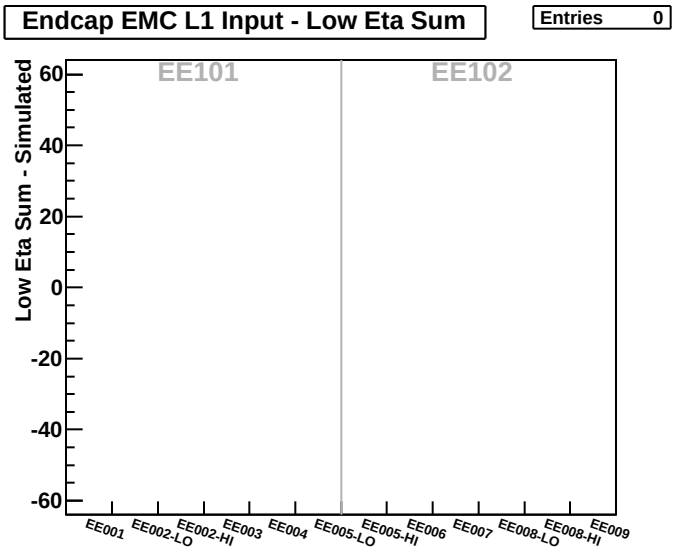

Barrel EMC L1 Input - High Eta Sum
Entries 7.2e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

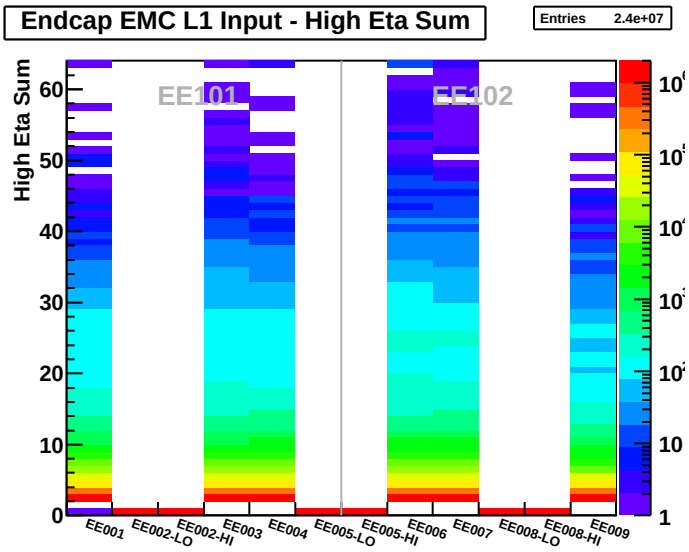

Barrel EMC L1 Input - High Eta Sum
Entries 1091
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

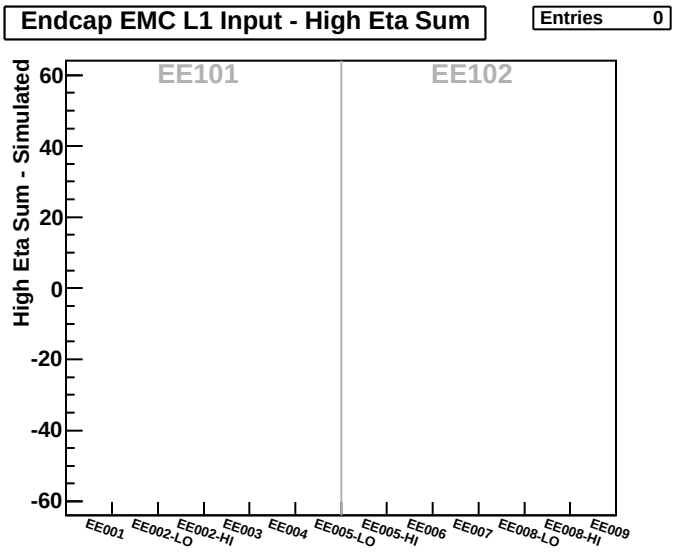

Barrel EMC L1 Input - High Tower Bits
Entries 7.2e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

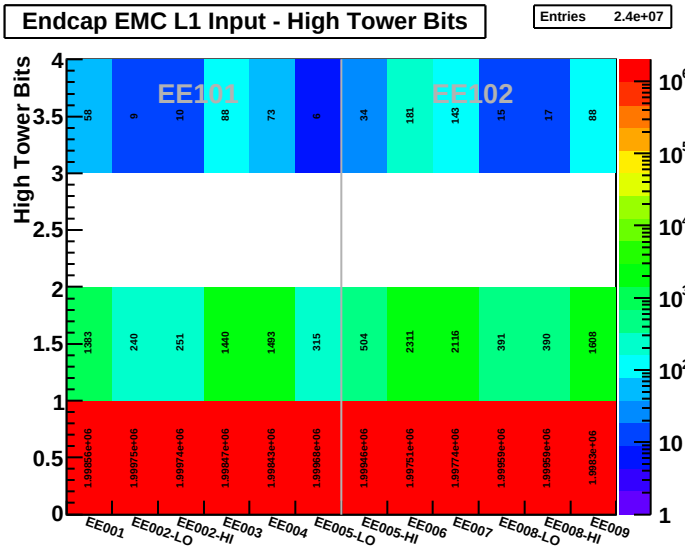

Barrel EMC L1 Input - High Tower Bits
Entries 17
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

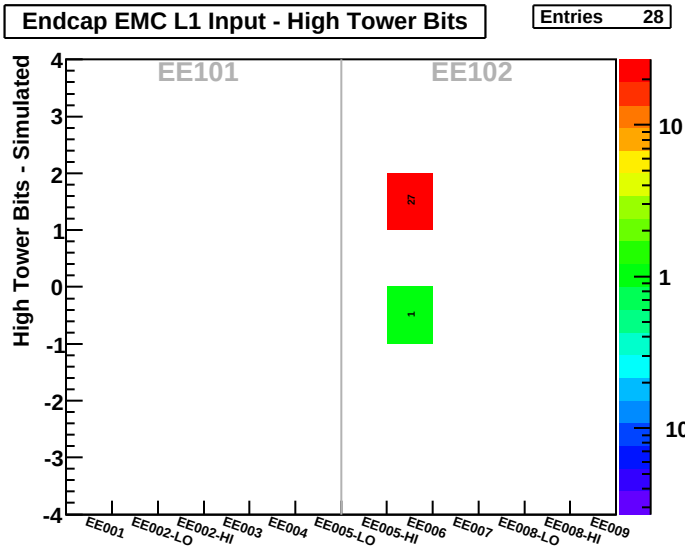

Endcap EMC L1 Input - Low Eta Sum
Entries 2.4e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

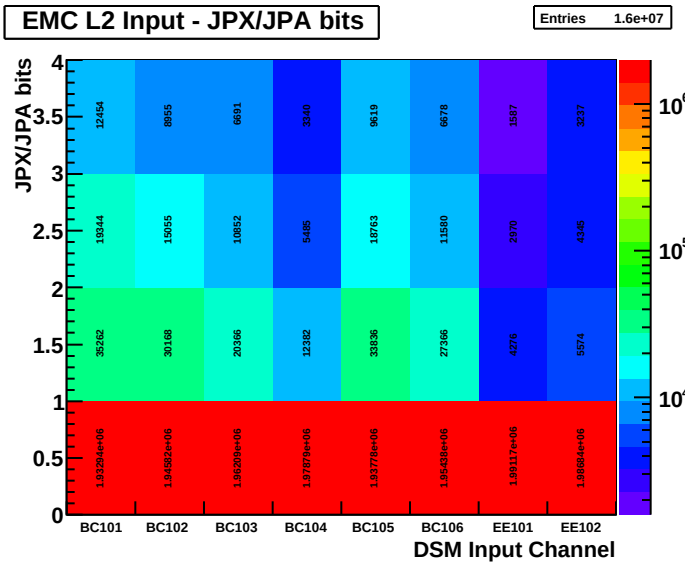

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

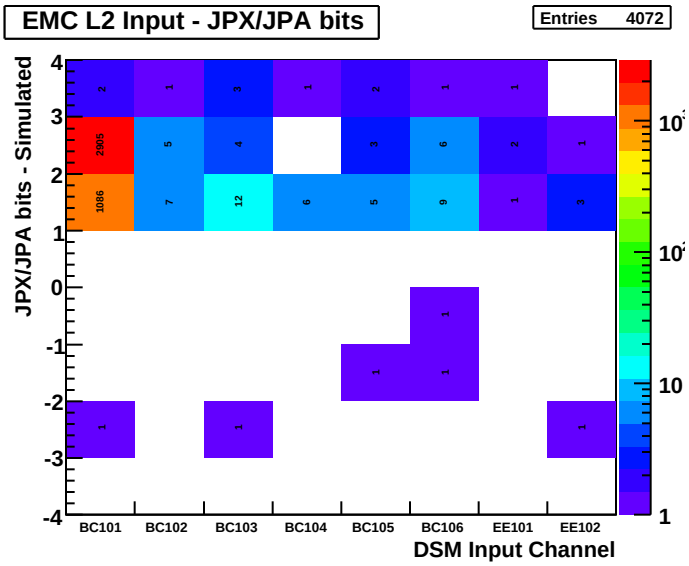

Endcap EMC L1 Input - High Eta Sum
Entries 2.4e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

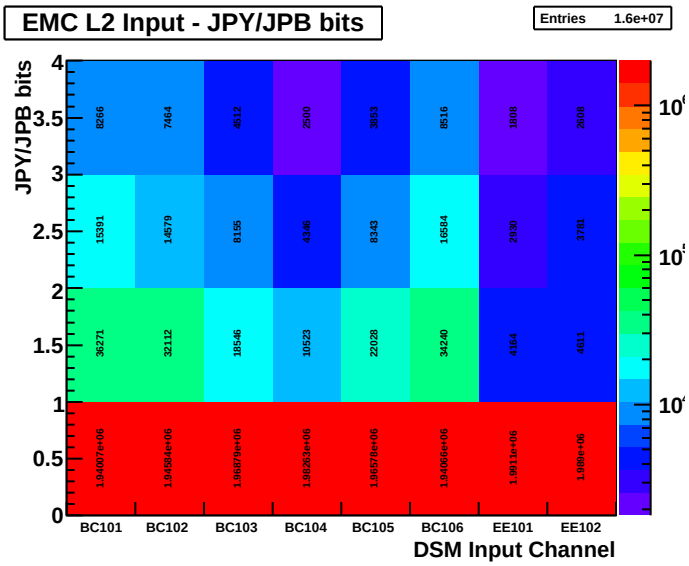

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

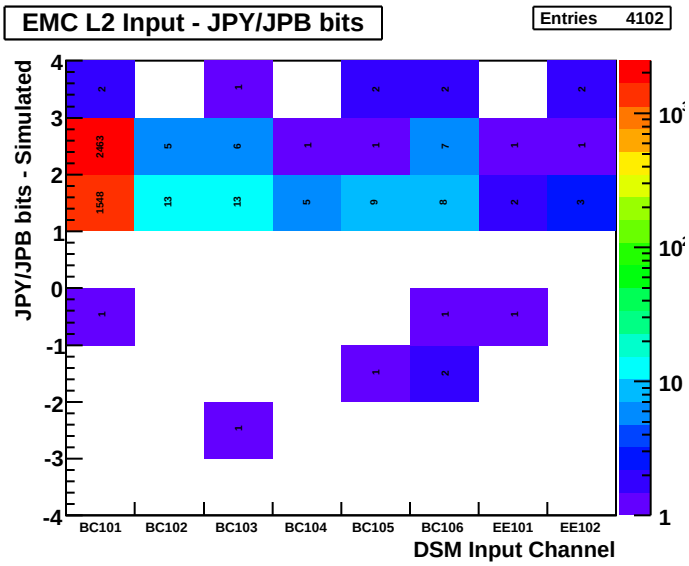

Endcap EMC L1 Input - High Tower Bits
Entries 2.4e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

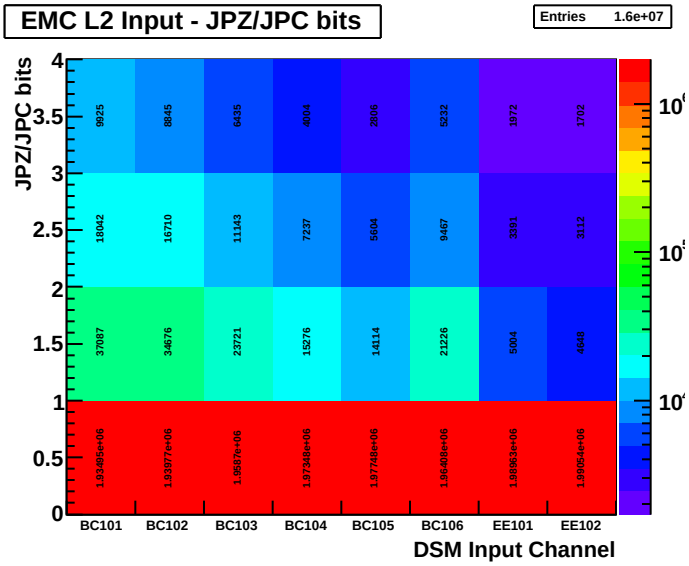

Endcap EMC L1 Input - High Tower Bits
Entries 28
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

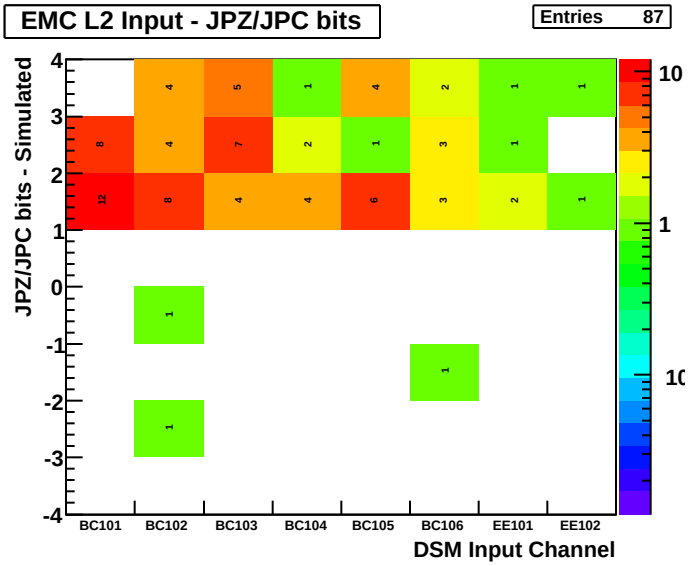

EMC L2 Input - JPX/JPA bits
Entries 1.6e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

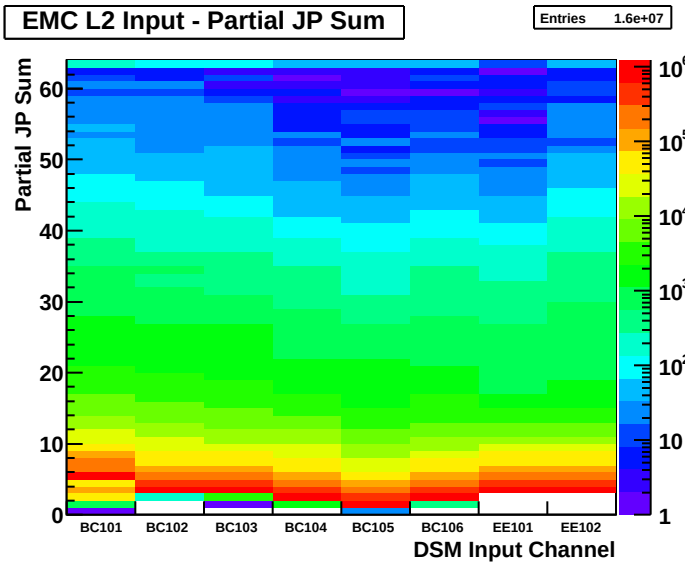

EMC L2 Input - JPX/JPA bits
Entries 4072
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

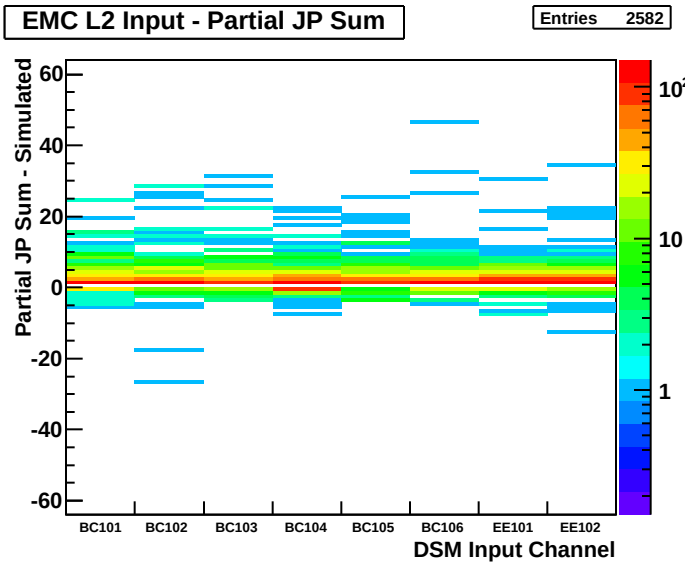

EMC L2 Input - JPY/JPB bits
Entries 1.6e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

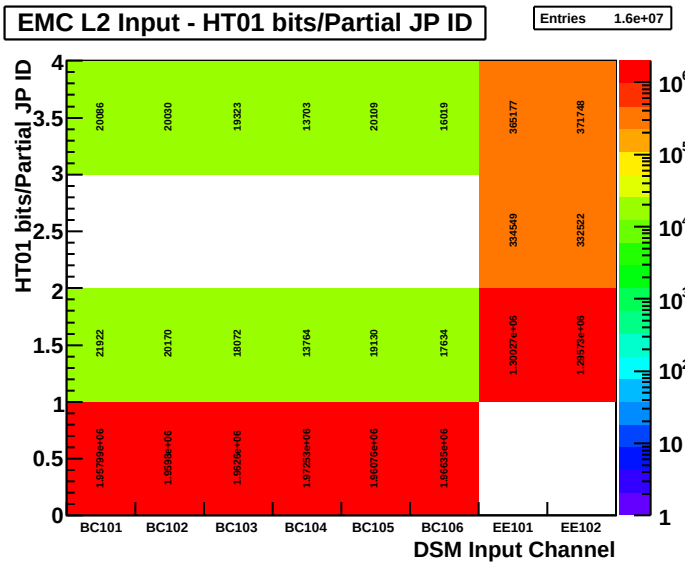

EMC L2 Input - JPY/JPB bits
Entries 4102
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

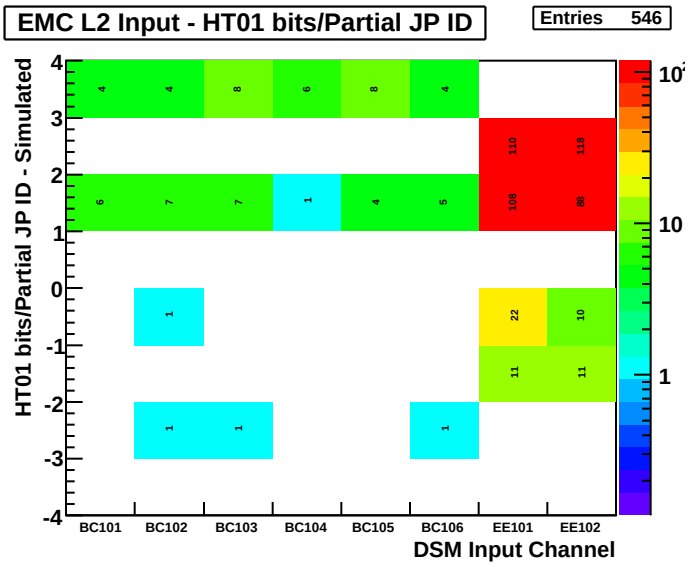

EMC L2 Input - JPZ/JPC bits
Entries 1.6e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

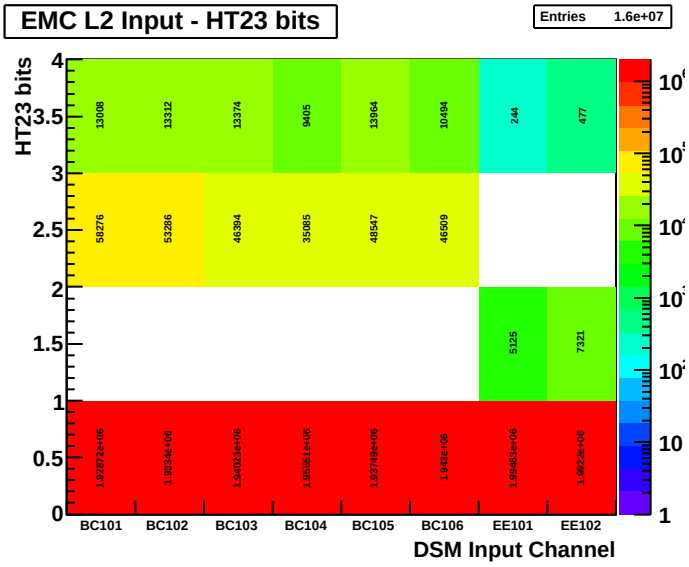

EMC L2 Input - JPZ/JPC bits
Entries 87
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

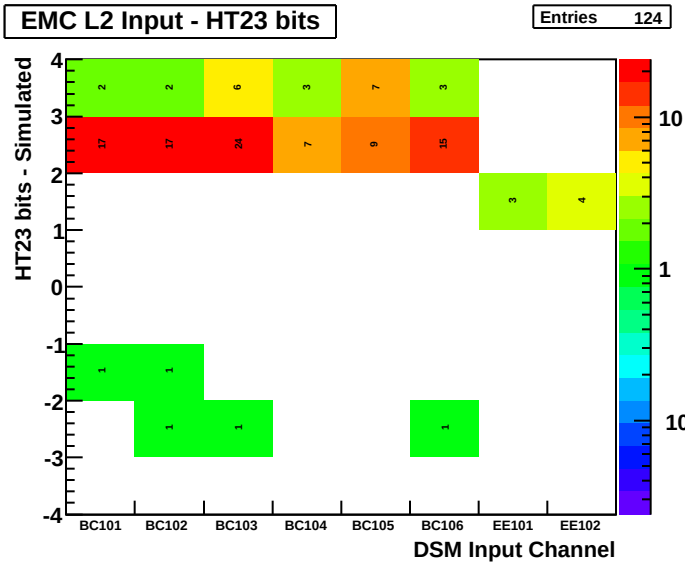

EMC L2 Input - Partial JP Sum
Entries 1.6e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

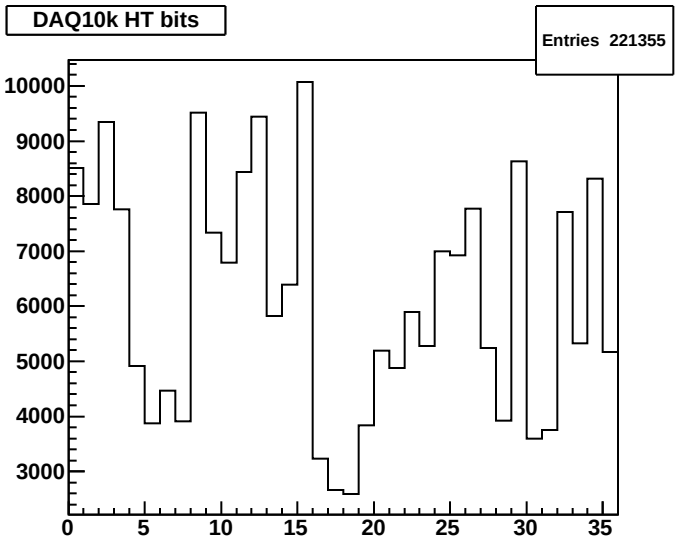

EMC L2 Input - Partial JP Sum
Entries 2582
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

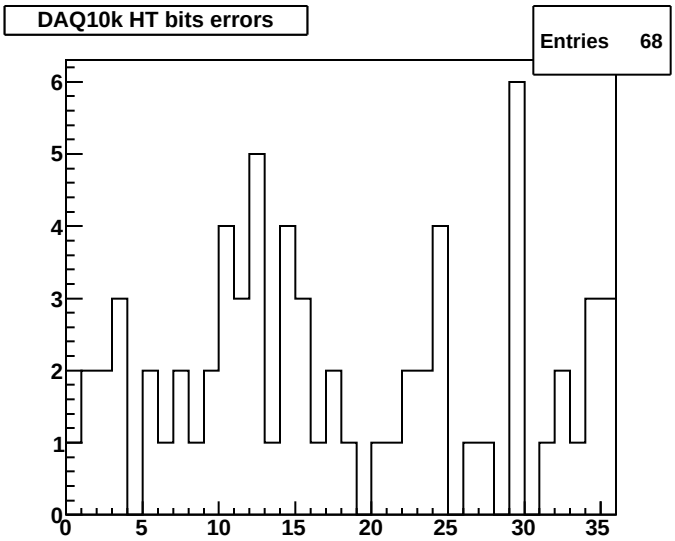

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.6e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

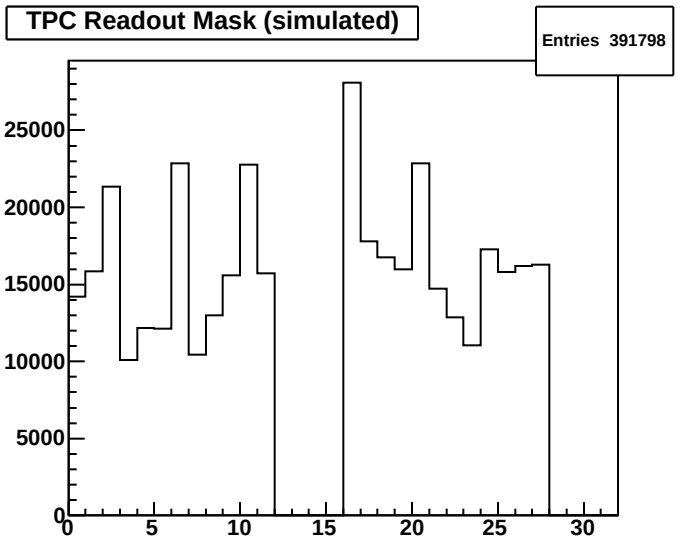

EMC L2 Input - HT01 bits/Partial JP ID
Entries 546
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

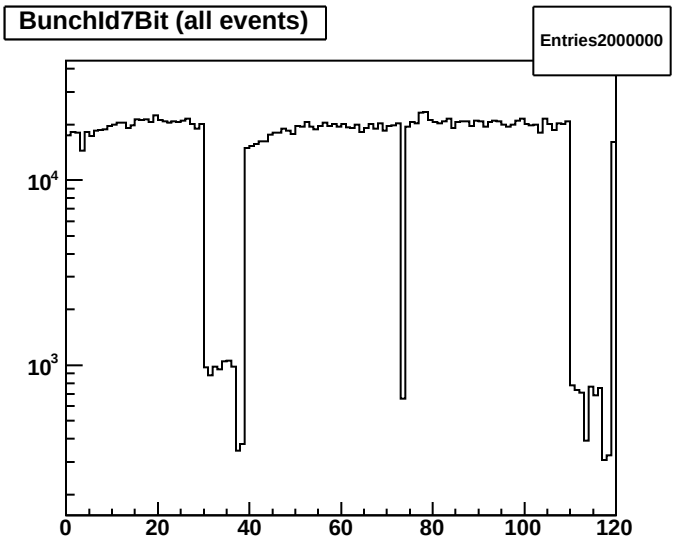

EMC L2 Input - HT23 bits
Entries 1.6e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

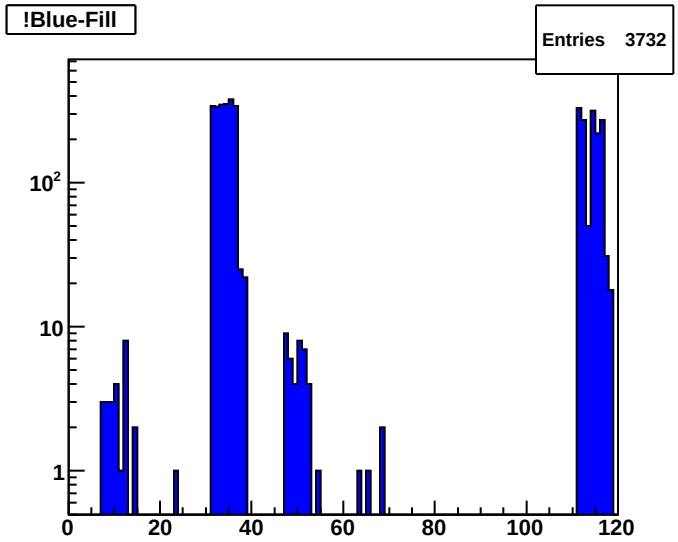

EMC L2 Input - HT23 bits
Entries 124
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

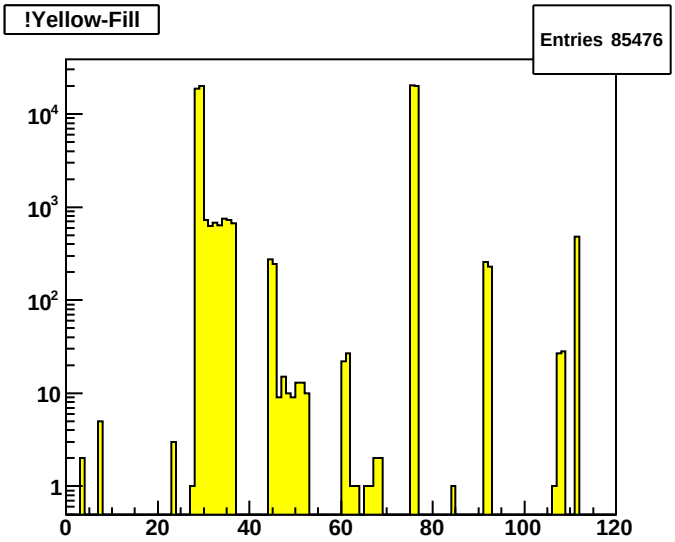

DAQ10k HT bits
Entries 221355

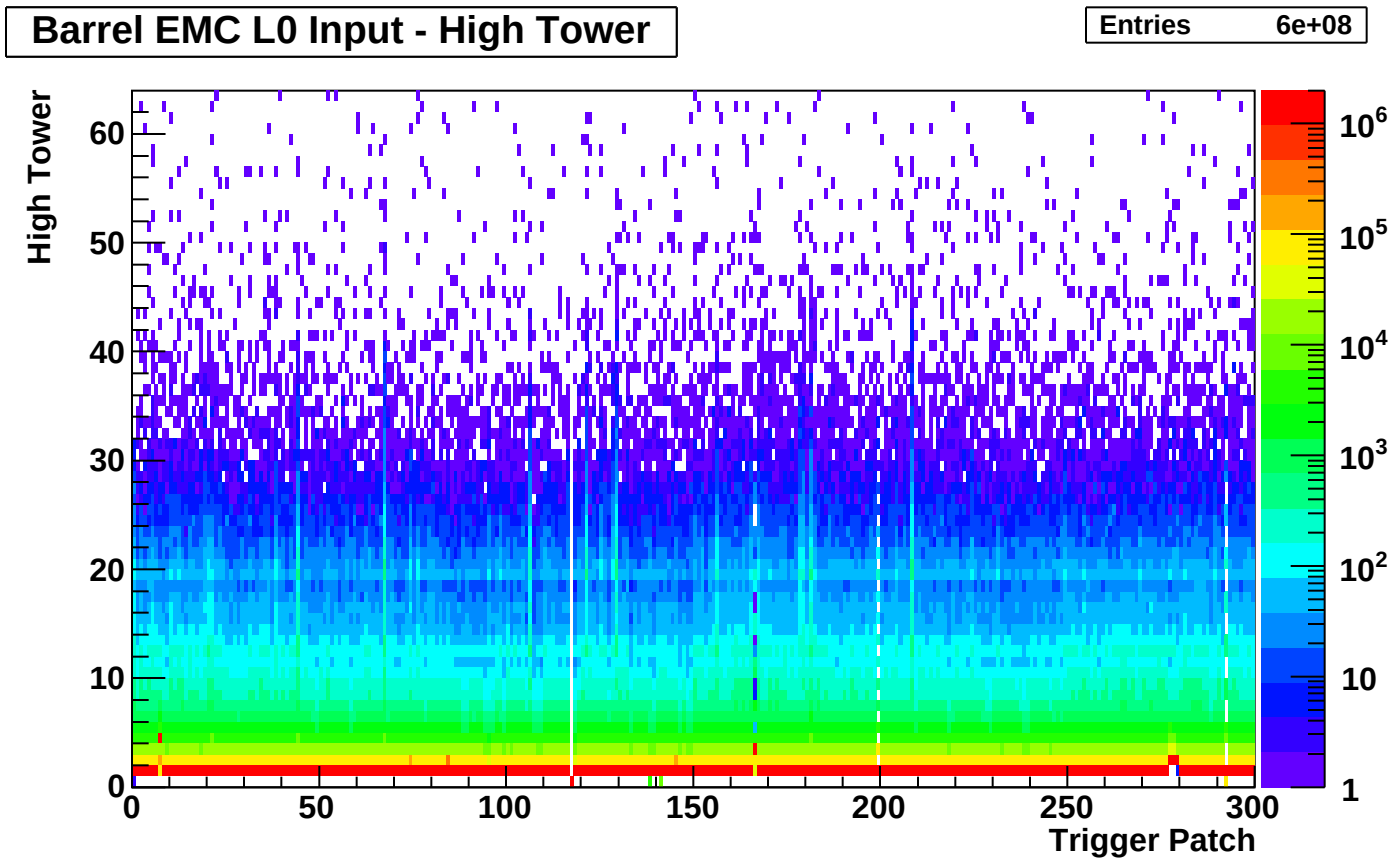

DAQ10k HT bits errors
Entries 68

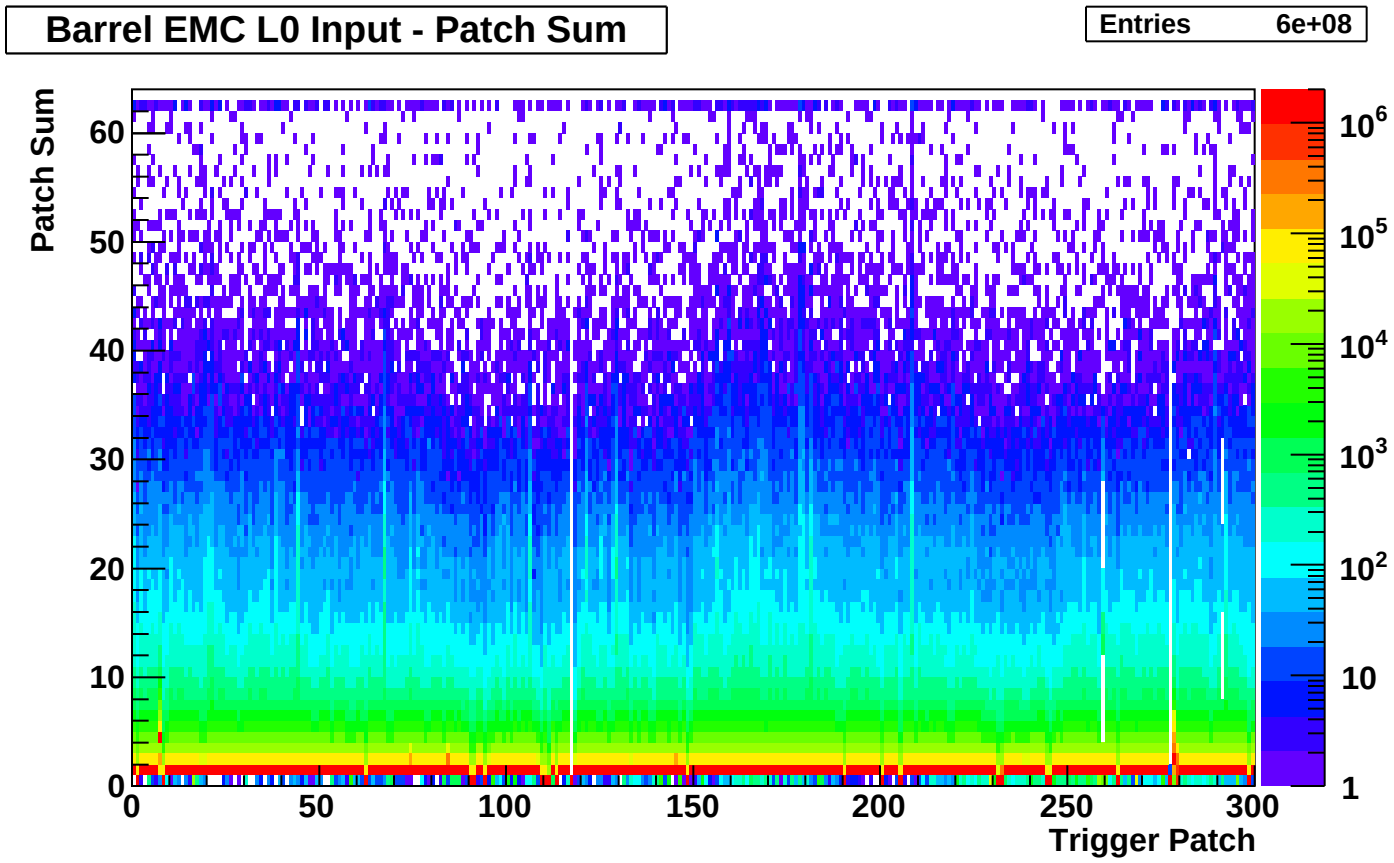

TPC Readout Mask (simulated)
Entries 391798

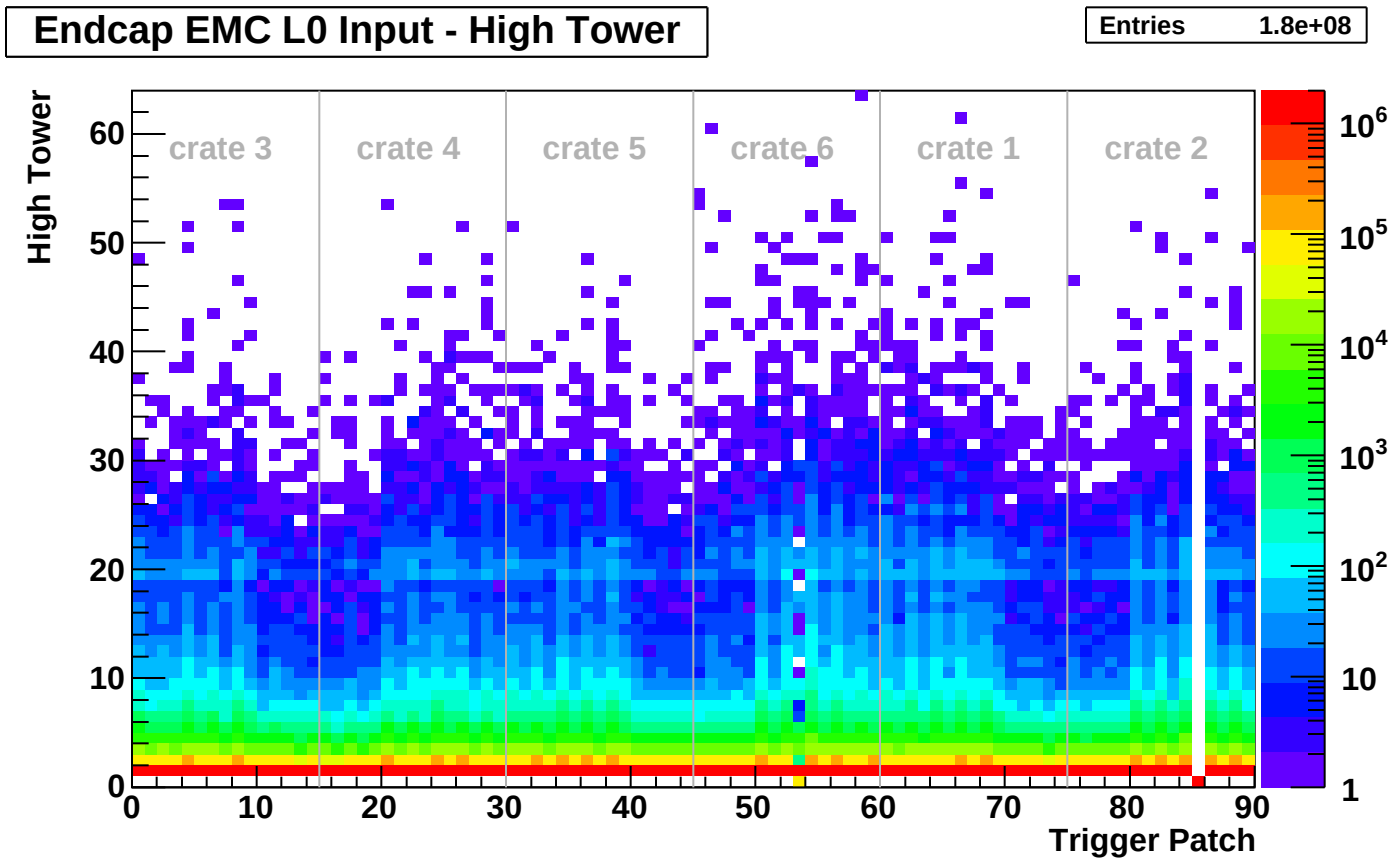

BunchId7Bit (all events)
Entries2000000

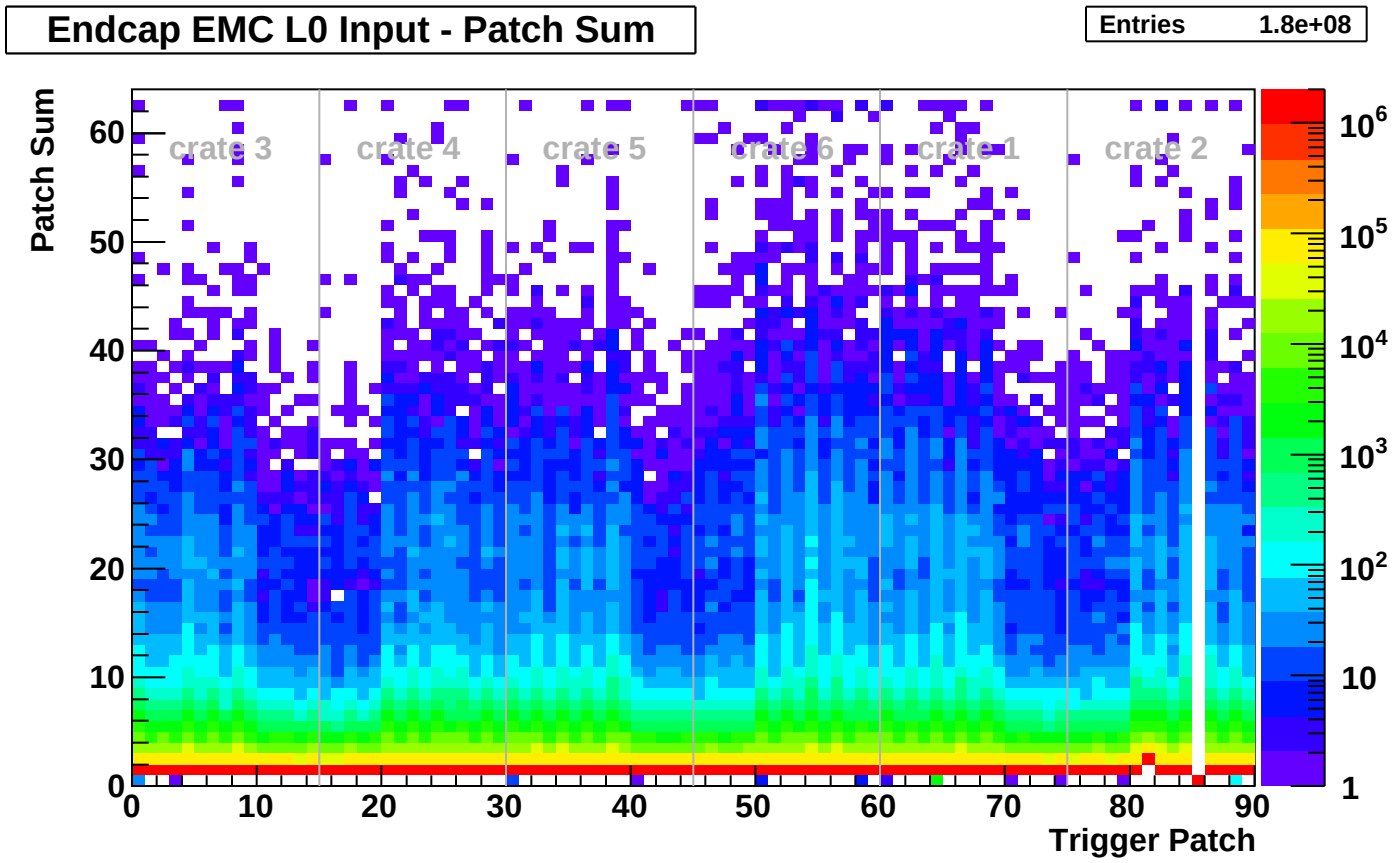

!Blue-Fill
Entries 3732

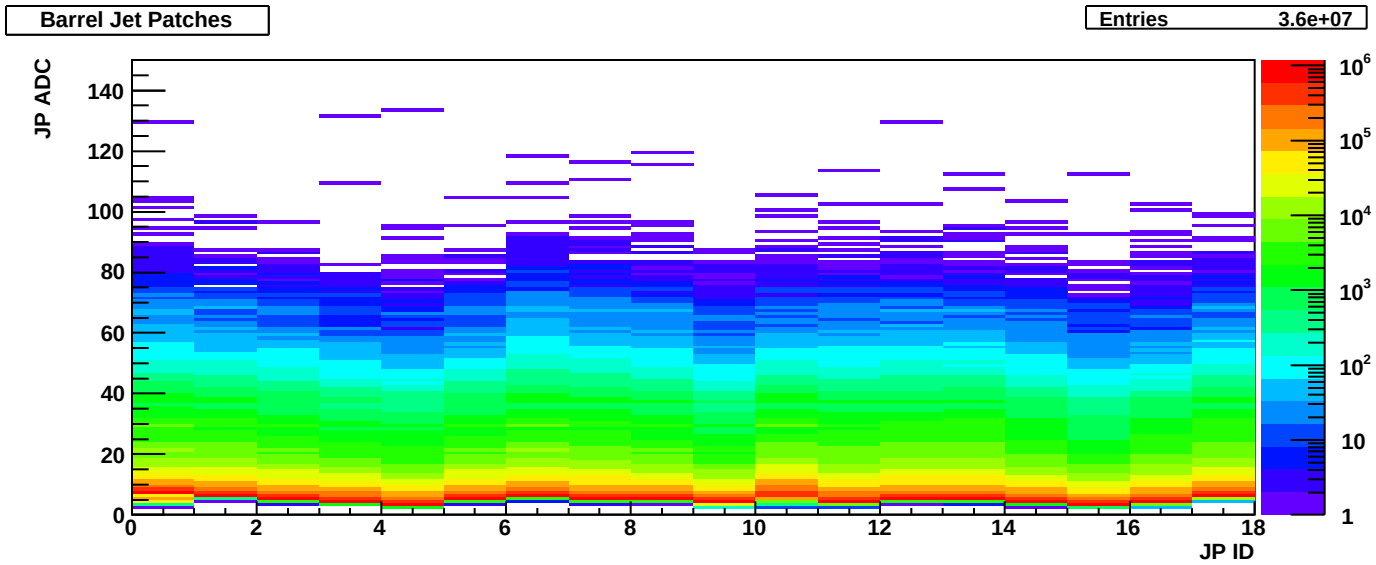

!Yellow-Fill
Entries 85476

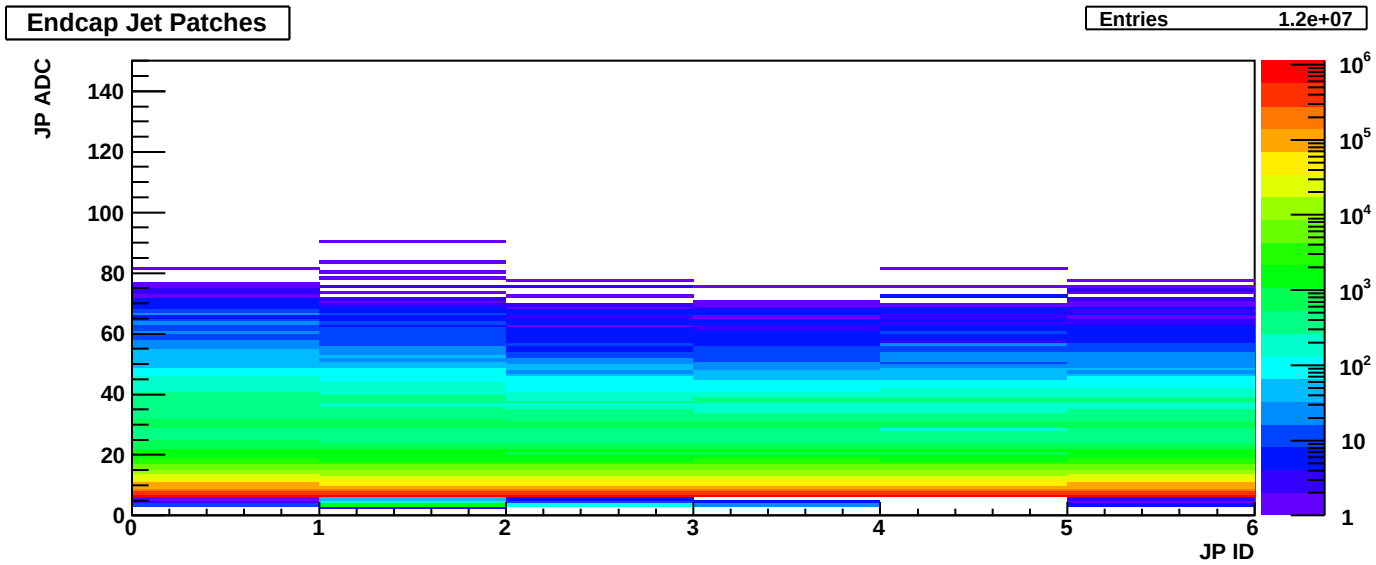

Barrel EMC L0 Input - High Tower
Entries 6e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

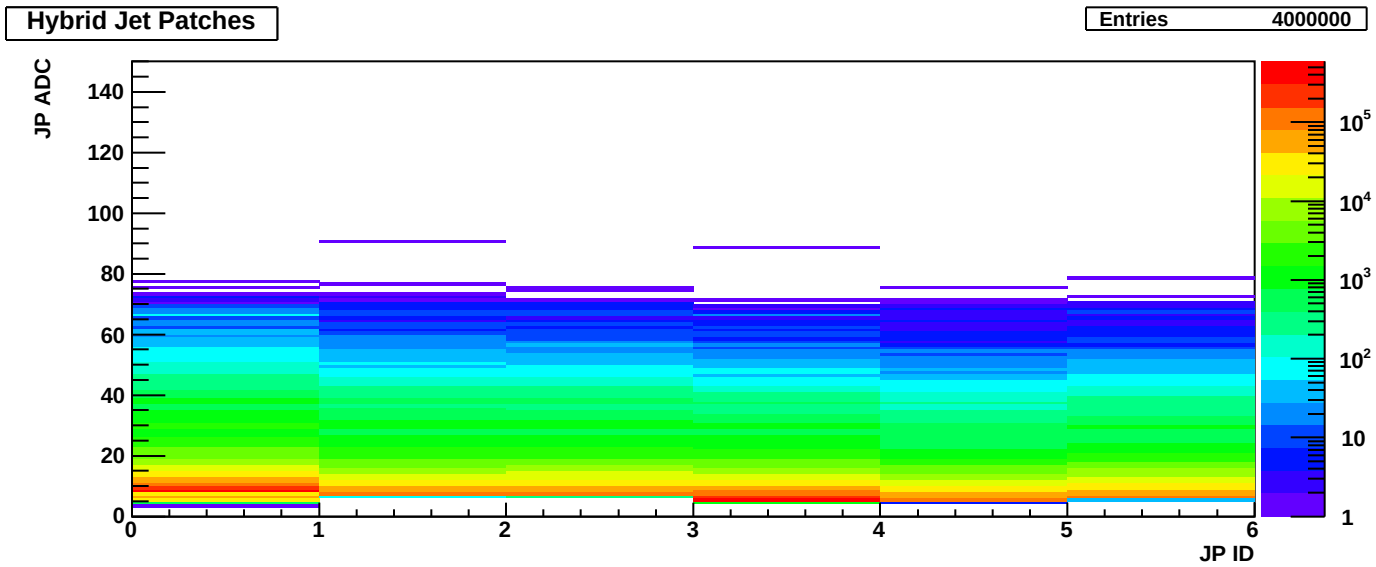

Barrel EMC L0 Input - Patch Sum
Entries 6e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

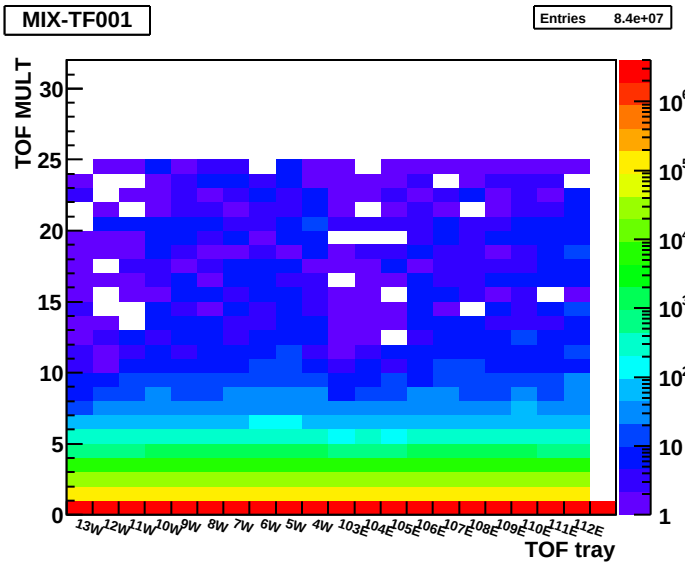

Endcap EMC L0 Input - High Tower
Entries 1.8e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

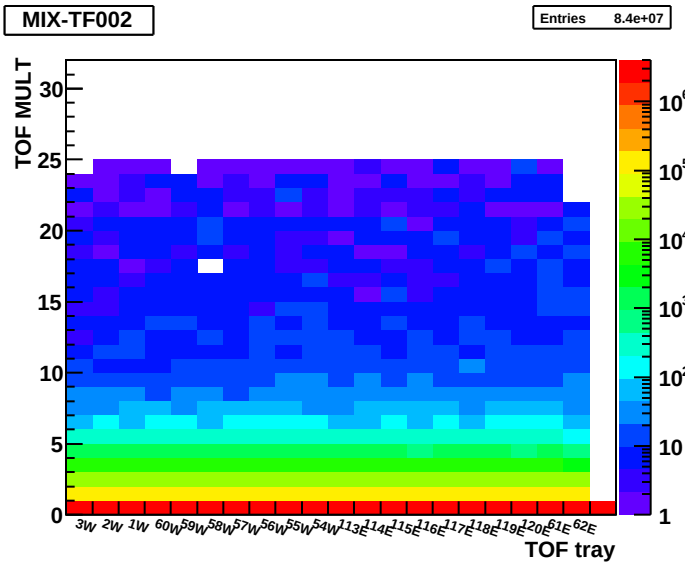

Endcap EMC L0 Input - Patch Sum
Entries 1.8e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

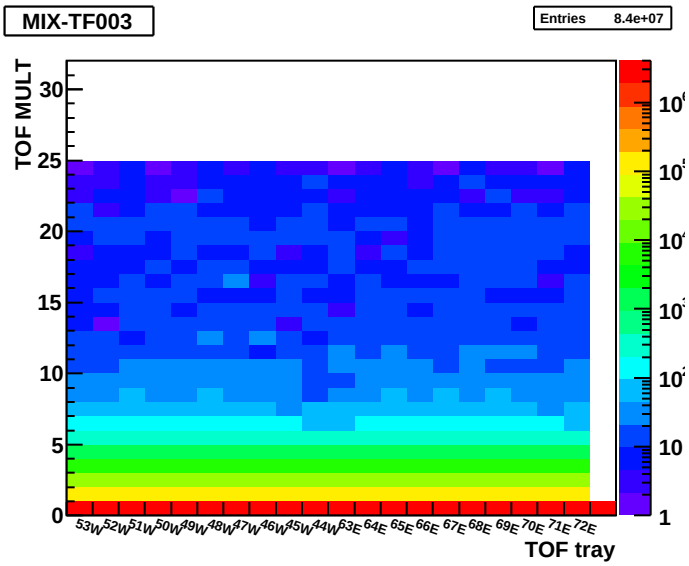

Barrel Jet Patches
Entries 3.6e+07
JP ADC
JP ID

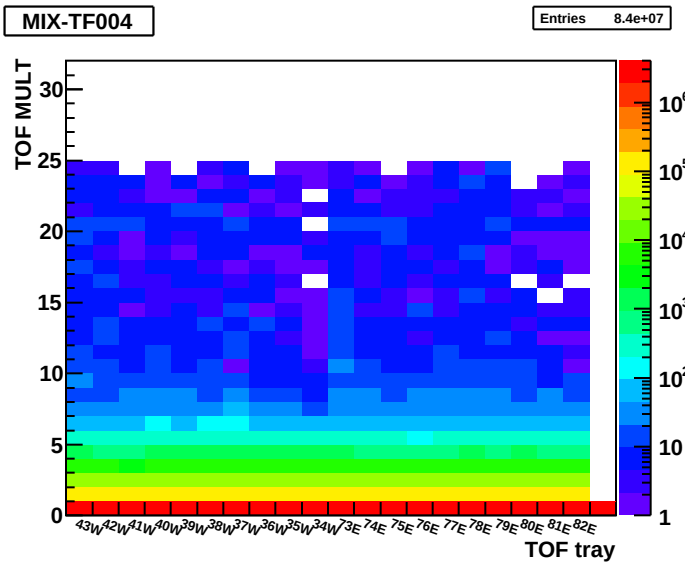

Endcap Jet Patches
Entries 1.2e+07
JP ADC
JP ID

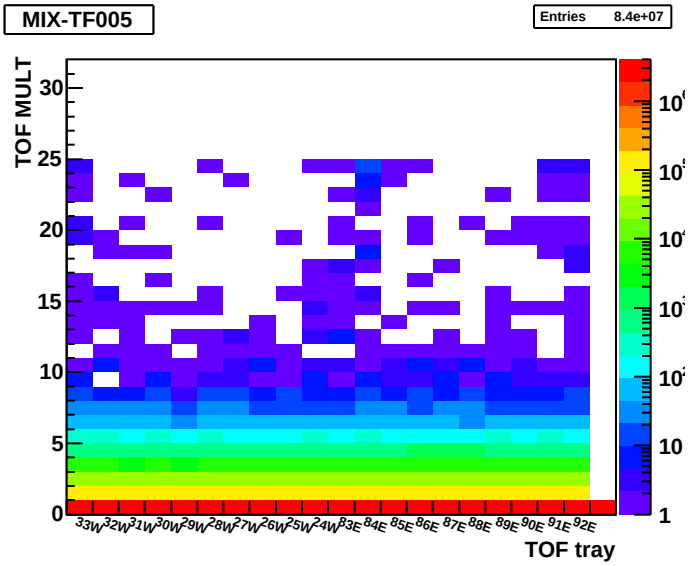

Hybrid Jet Patches
Entries 4000000
JP ADC
JP ID

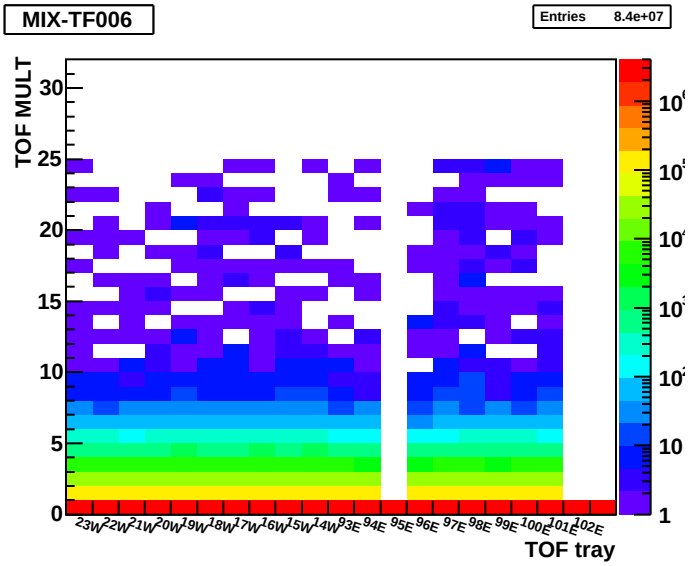

MIX-TF001
Entries 8.4e+07
TOF MULT
TOF tray

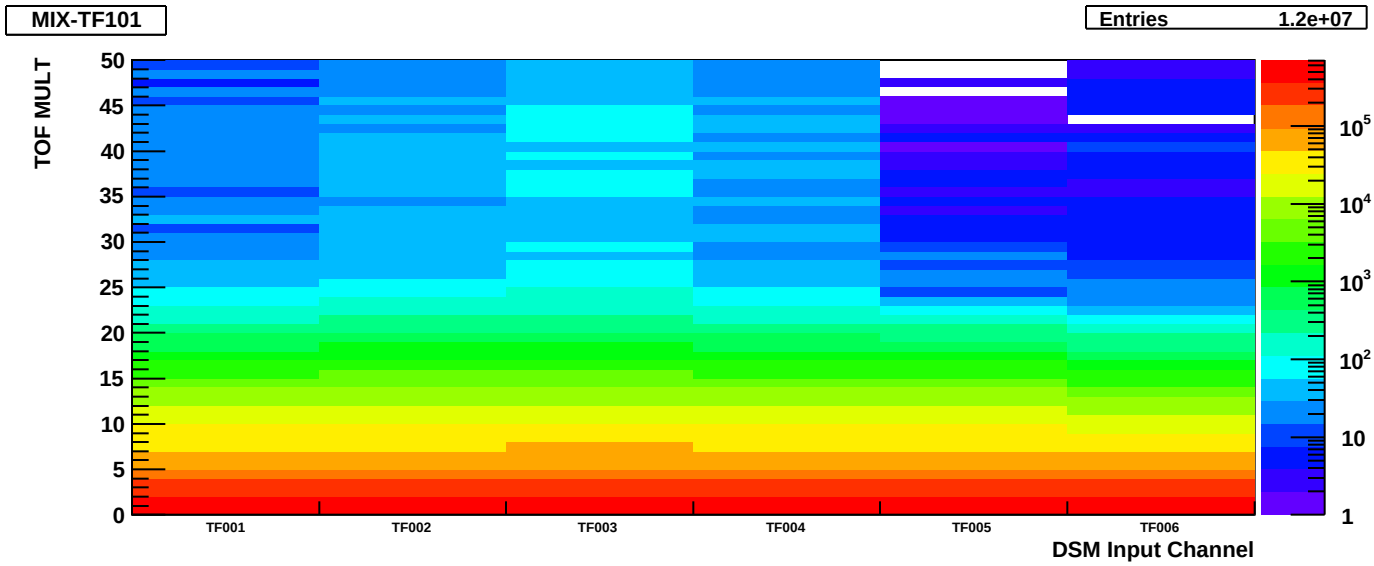

MIX-TF002
Entries 8.4e+07
TOF MULT
TOF tray

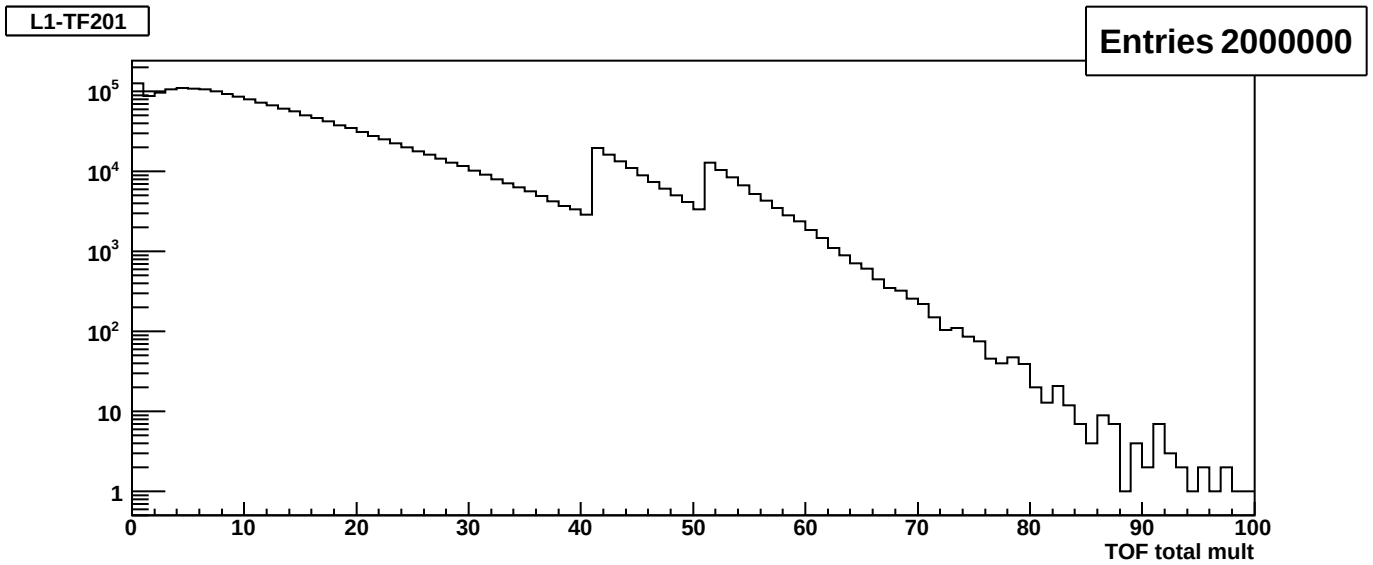

MIX-TF003
Entries 8.4e+07
TOF MULT
TOF tray

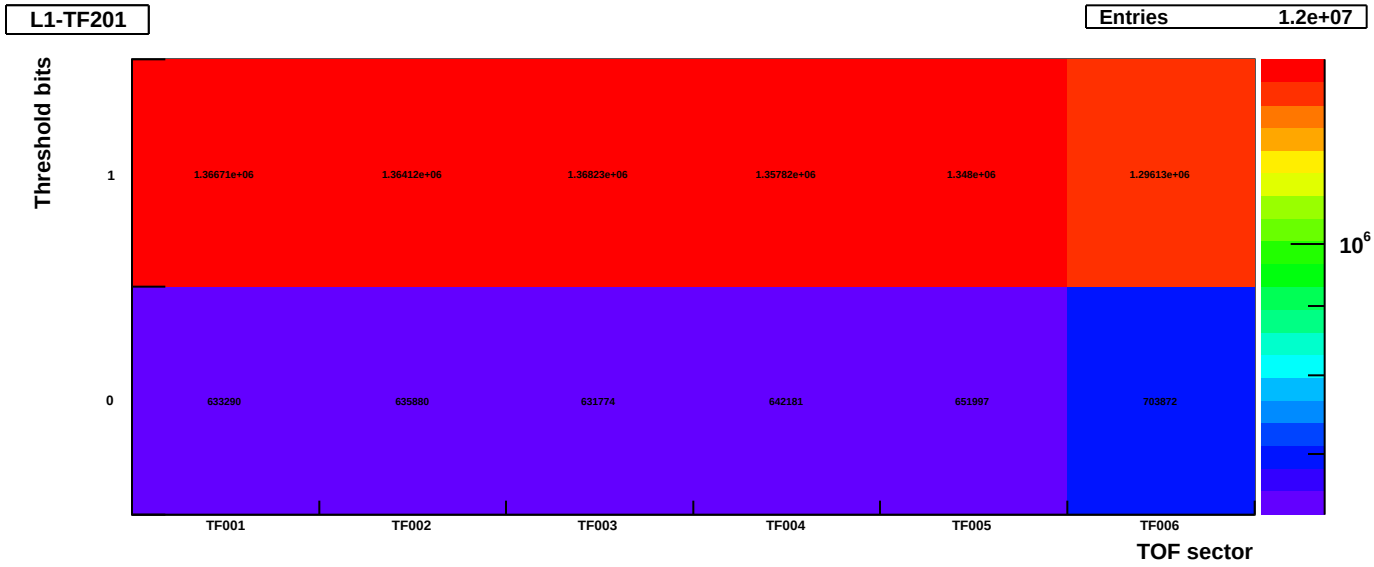

MIX-TF004
Entries 8.4e+07
TOF MULT
TOF tray

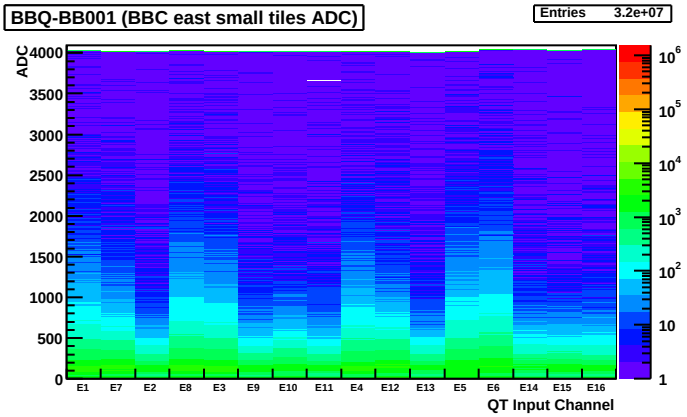

MIX-TF005
Entries 8.4e+07
TOF MULT
TOF tray

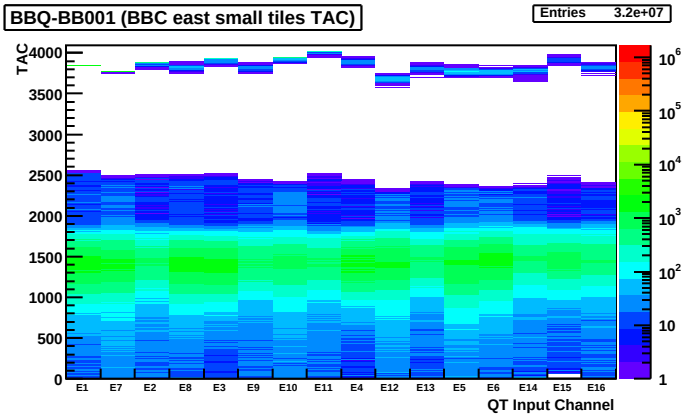

MIX-TF006
Entries 8.4e+07
TOF MULT
TOF tray

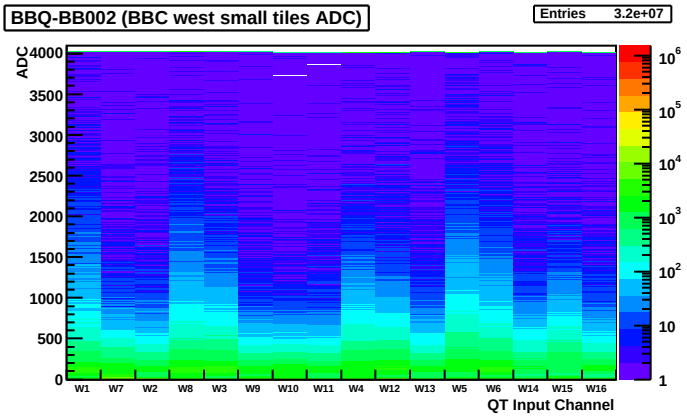

MIX-TF101
Entries 1.2e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

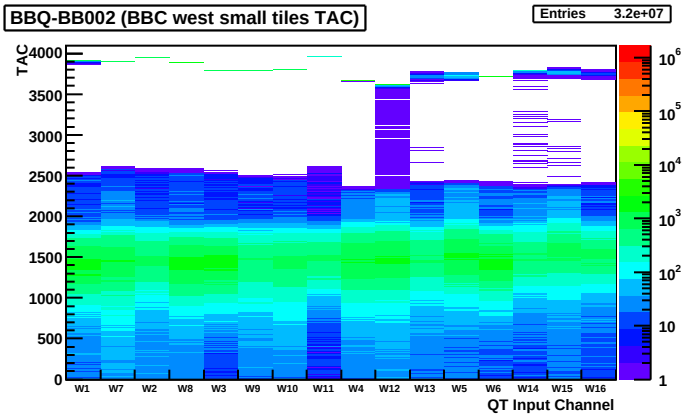

L1-TF201
Entries 2000000
TOF total mult

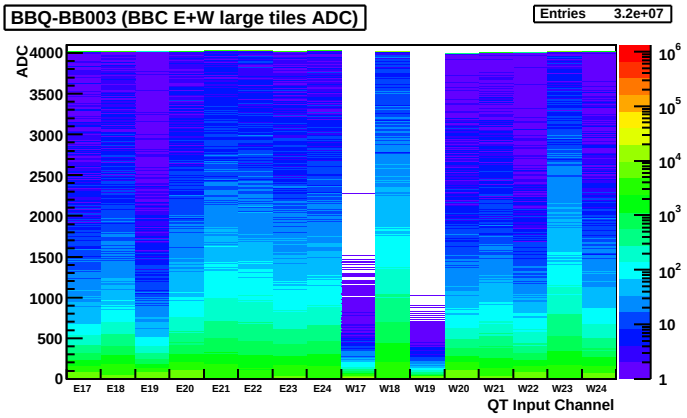

L1-TF201
Entries 1.2e+07
Threshold bits
1.36671e+06
1.36412e+06
1.36823e+06
1.35782e+06
1.348e+06
1.29613e+06
633290
635880
631774
642181
651997
703872
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

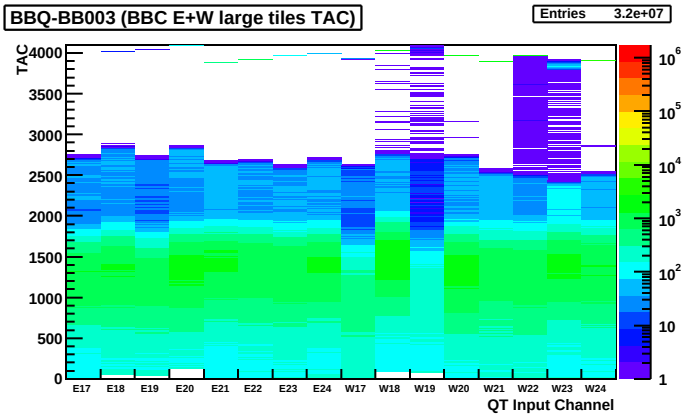

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

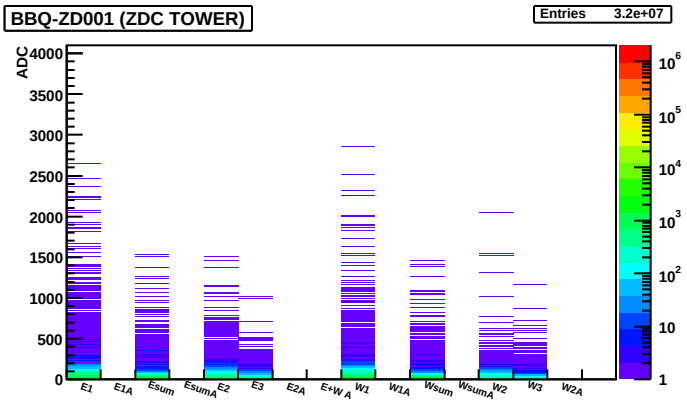

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

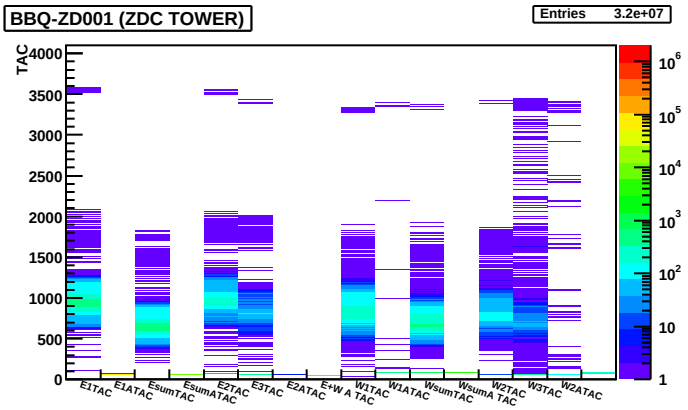

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

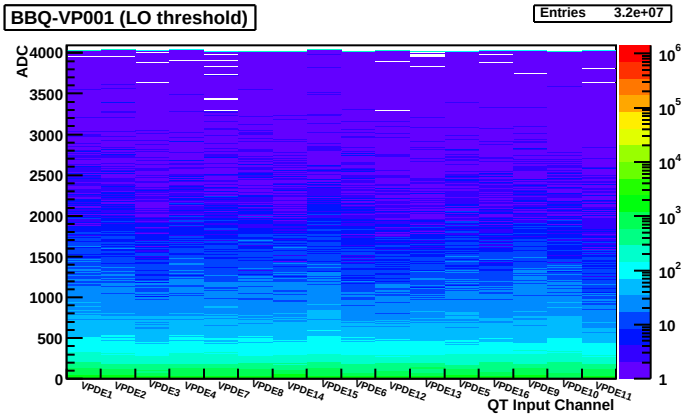

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

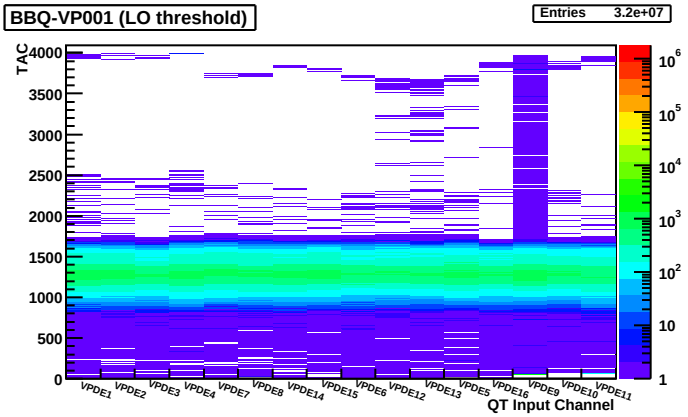

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

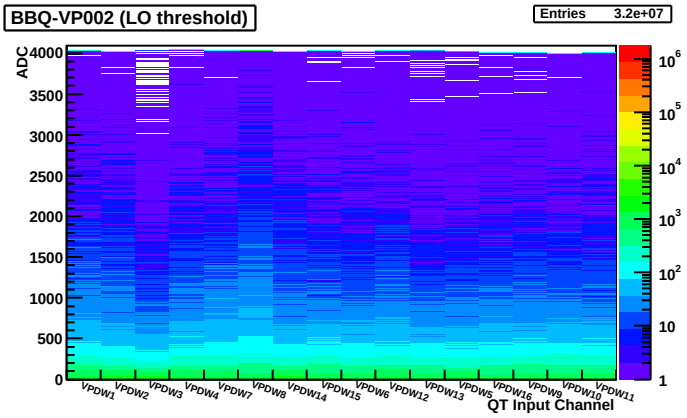

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

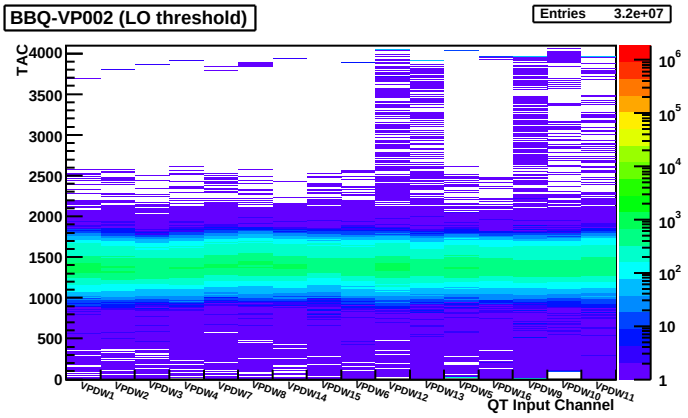

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
ADC

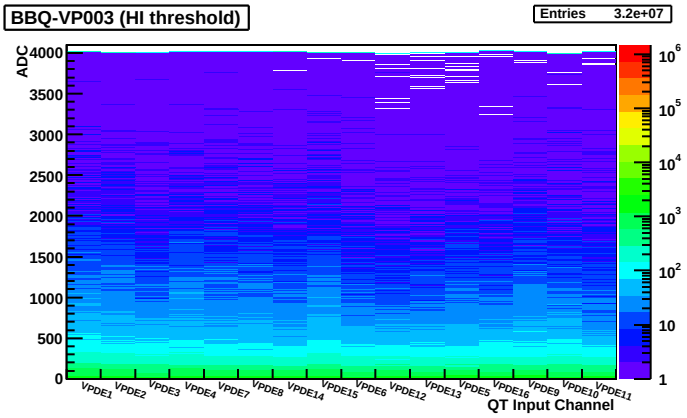

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
TAC

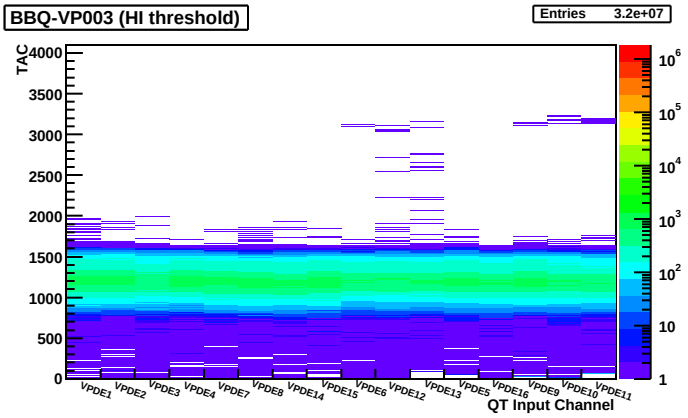

BBQ-VP001 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

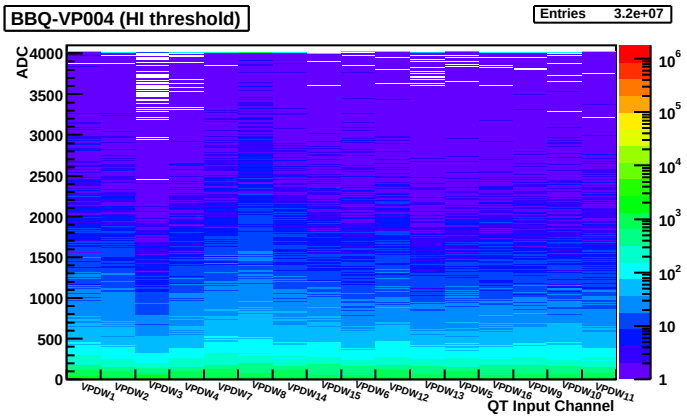

BBQ-VP001 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

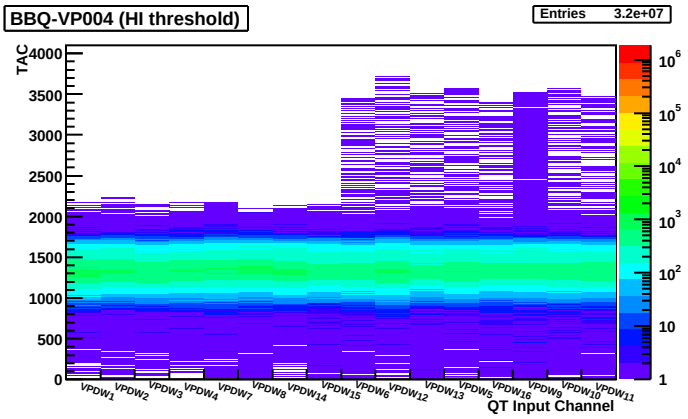

BBQ-VP002 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

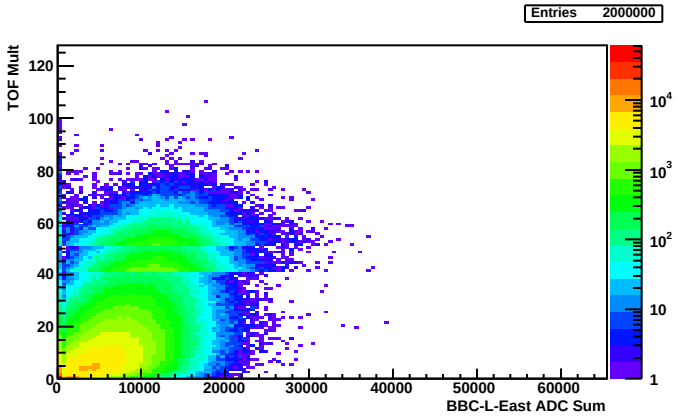

BBQ-VP002 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

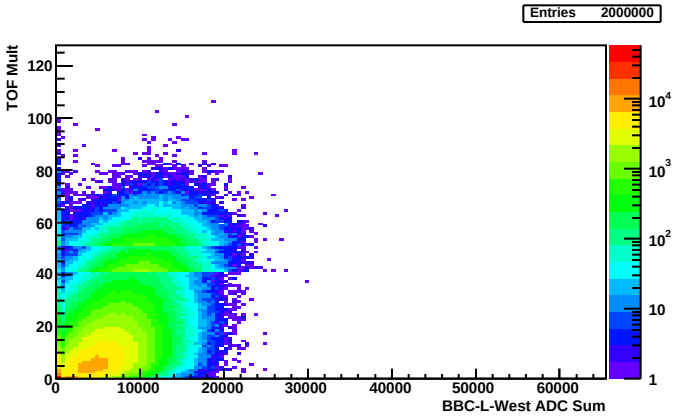

BBQ-VP003 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

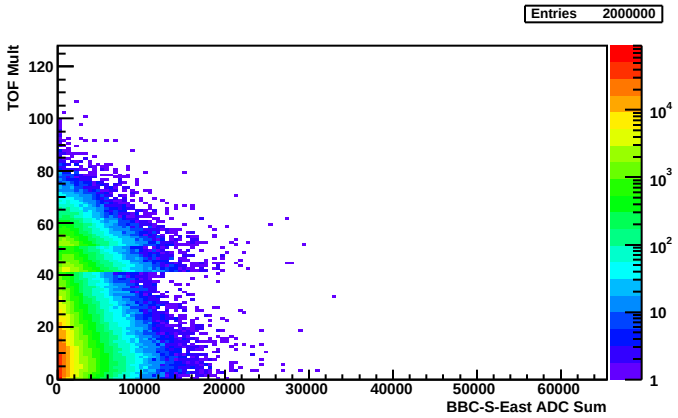

BBQ-VP003 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

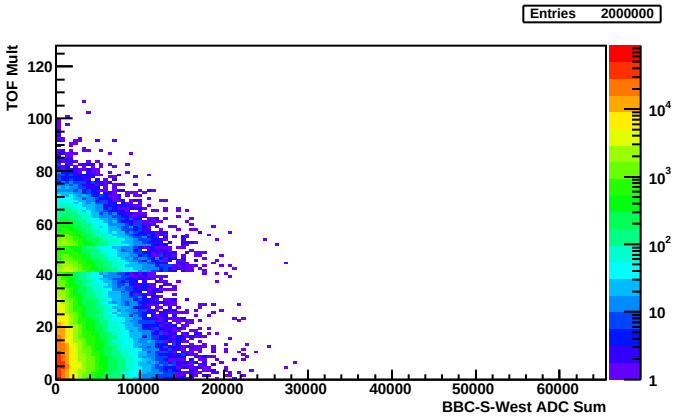

BBQ-VP004 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

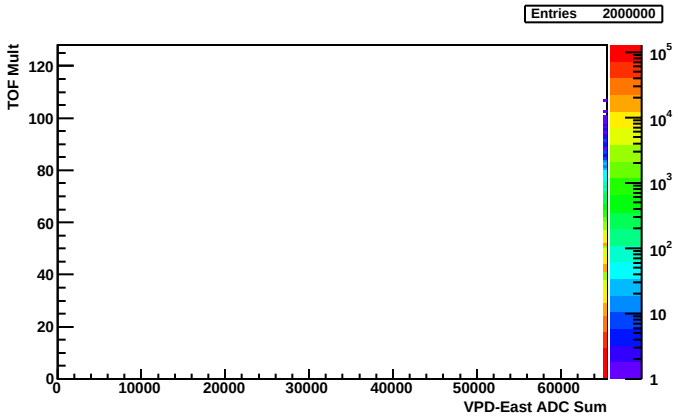

BBQ-VP004 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

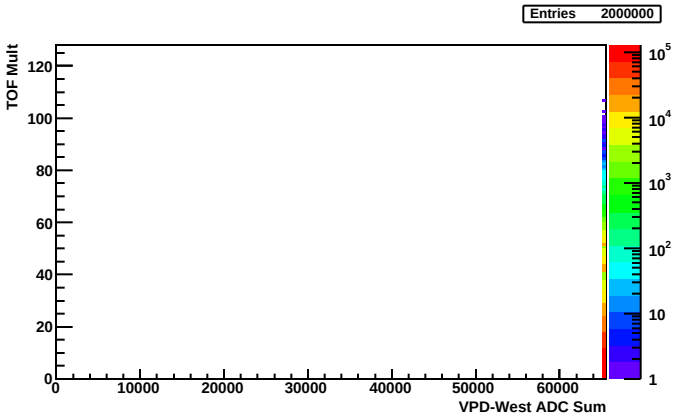

Entries 2000000
TOF Mult
BBC-L-East ADC Sum

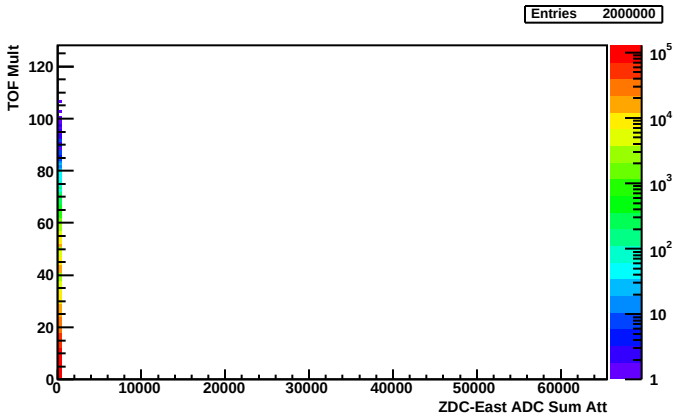

Entries 2000000
TOF Mult
BBC-L-West ADC Sum

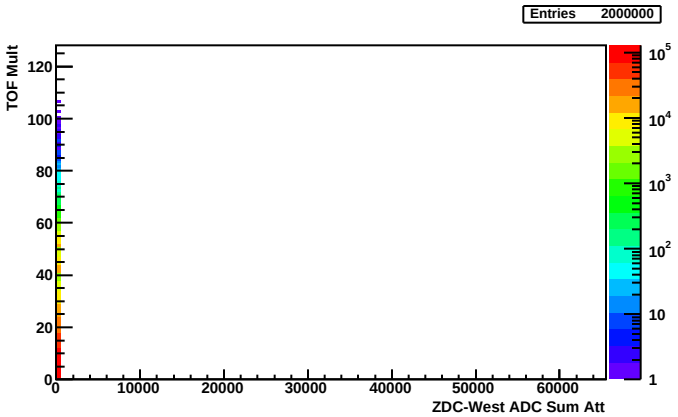

Entries 2000000
TOF Mult
BBC-S-East ADC Sum

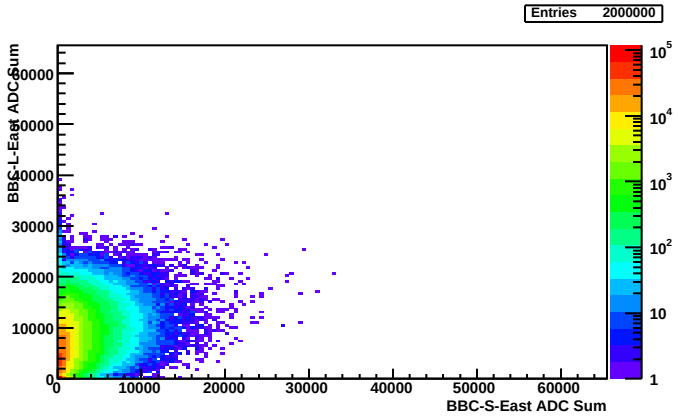

Entries 2000000
TOF Mult
BBC-S-West ADC Sum

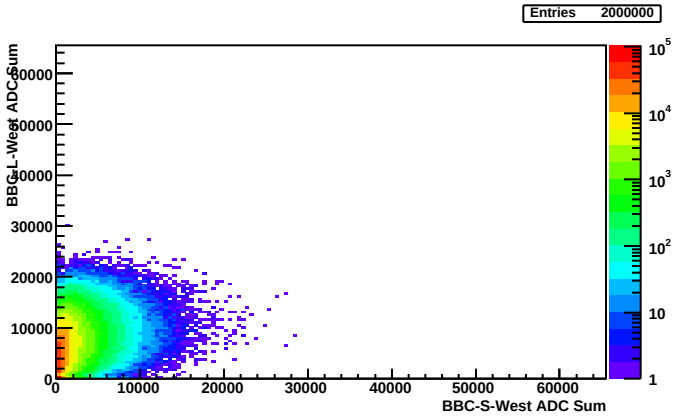

Entries 2000000
TOF Mult
VPD-East ADC Sum

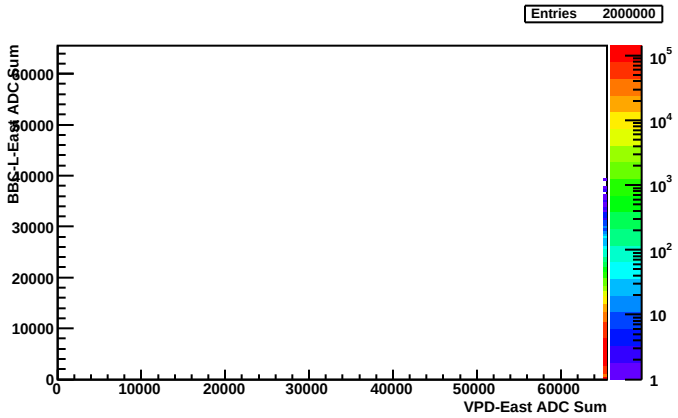

Entries 2000000
TOF Mult
VPD-West ADC Sum

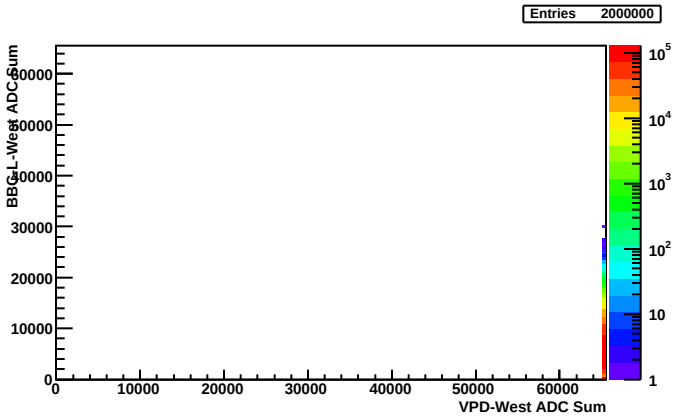

Entries 2000000
TOF Mult
ZDC-East ADC Sum Att

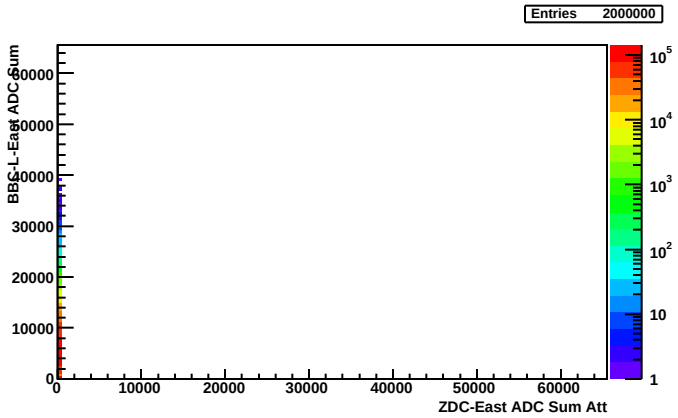

Entries 2000000
TOF Mult
ZDC-West ADC Sum Att

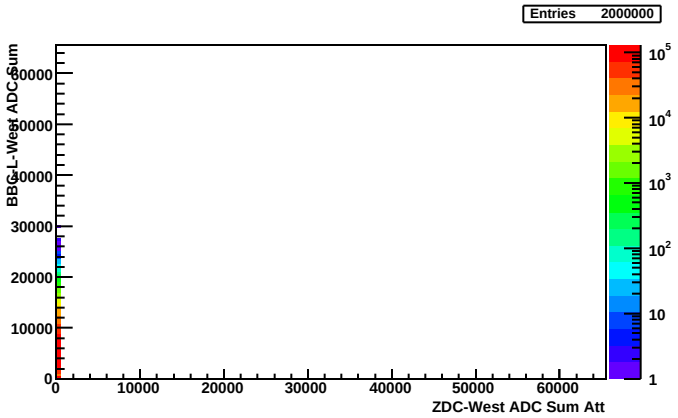

Entries 2000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

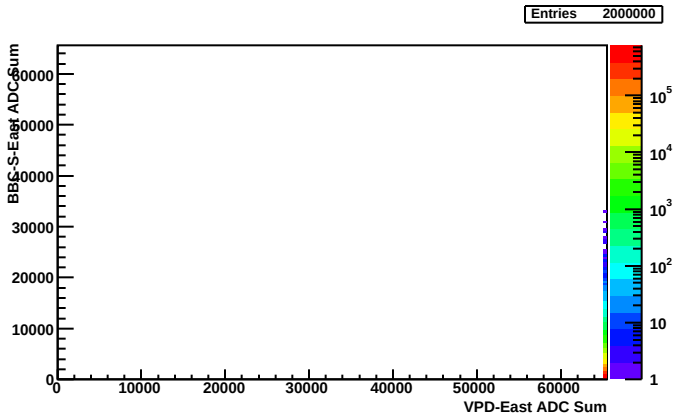

Entries 2000000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

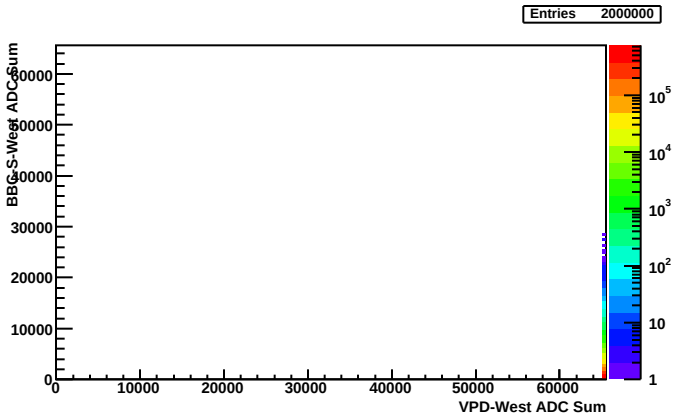

Entries 2000000
BBC-L-East ADC Sum
VPD-East ADC Sum

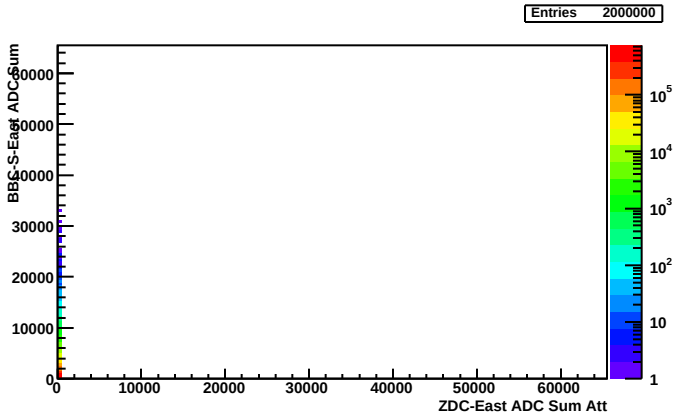

Entries 2000000
BBC-L-West ADC Sum
VPD-West ADC Sum

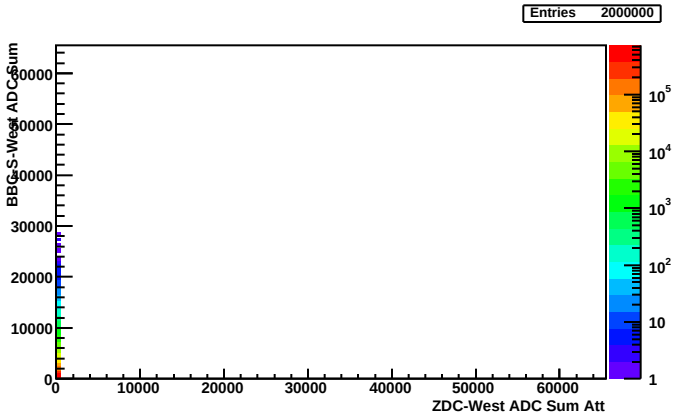

Entries 2000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

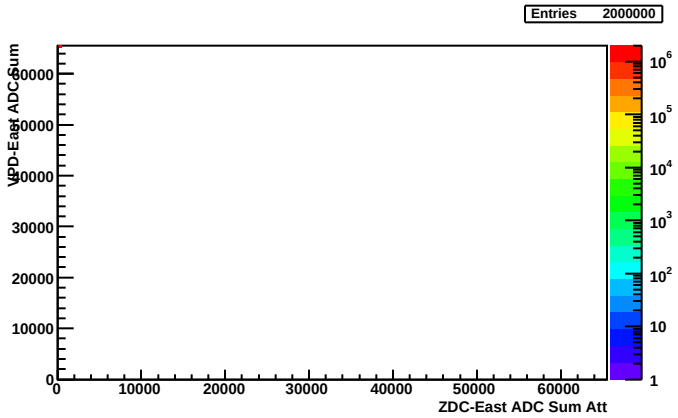

Entries 2000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

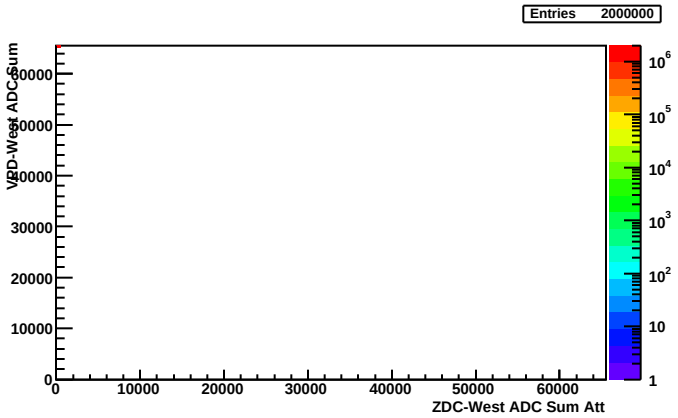

Entries 2000000
BBC-S-East ADC Sum
VPD-East ADC Sum

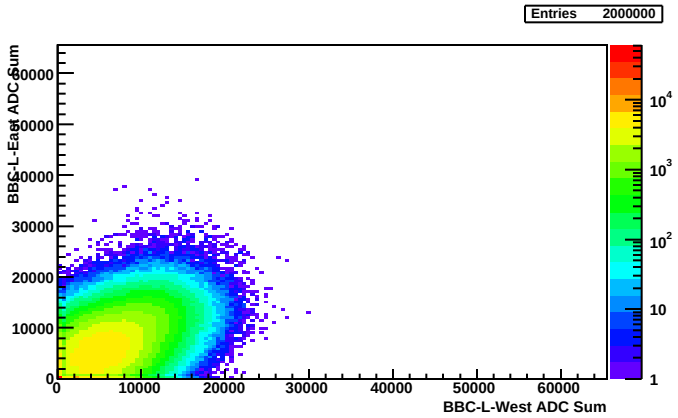

Entries 2000000
BBC-S-West ADC Sum
VPD-West ADC Sum

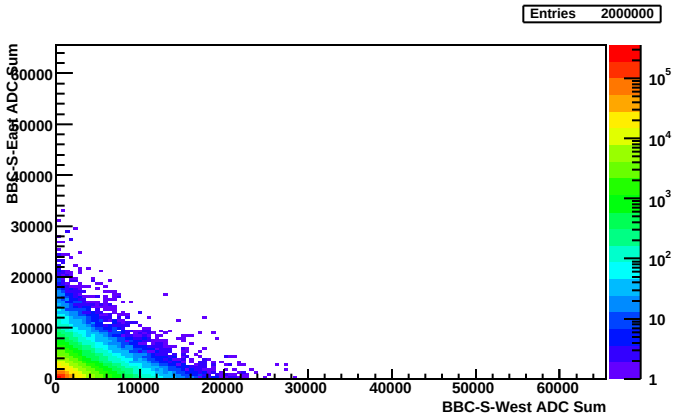

Entries 2000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

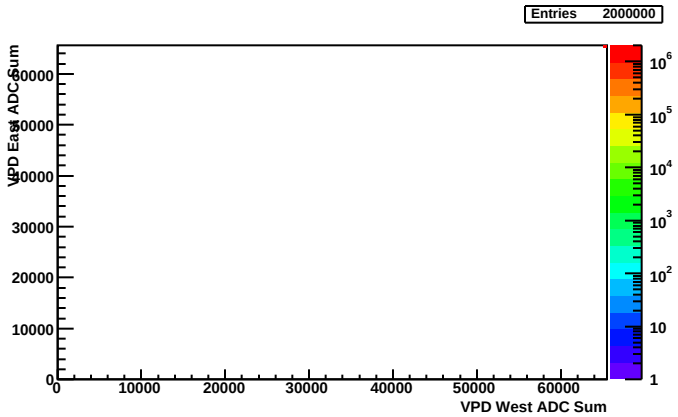

Entries 2000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

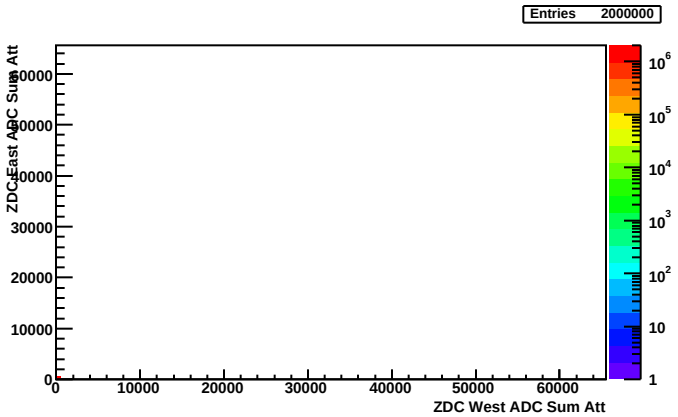

Entries 2000000
VPD-East ADC Sum
ZDC-East ADC Sum Att

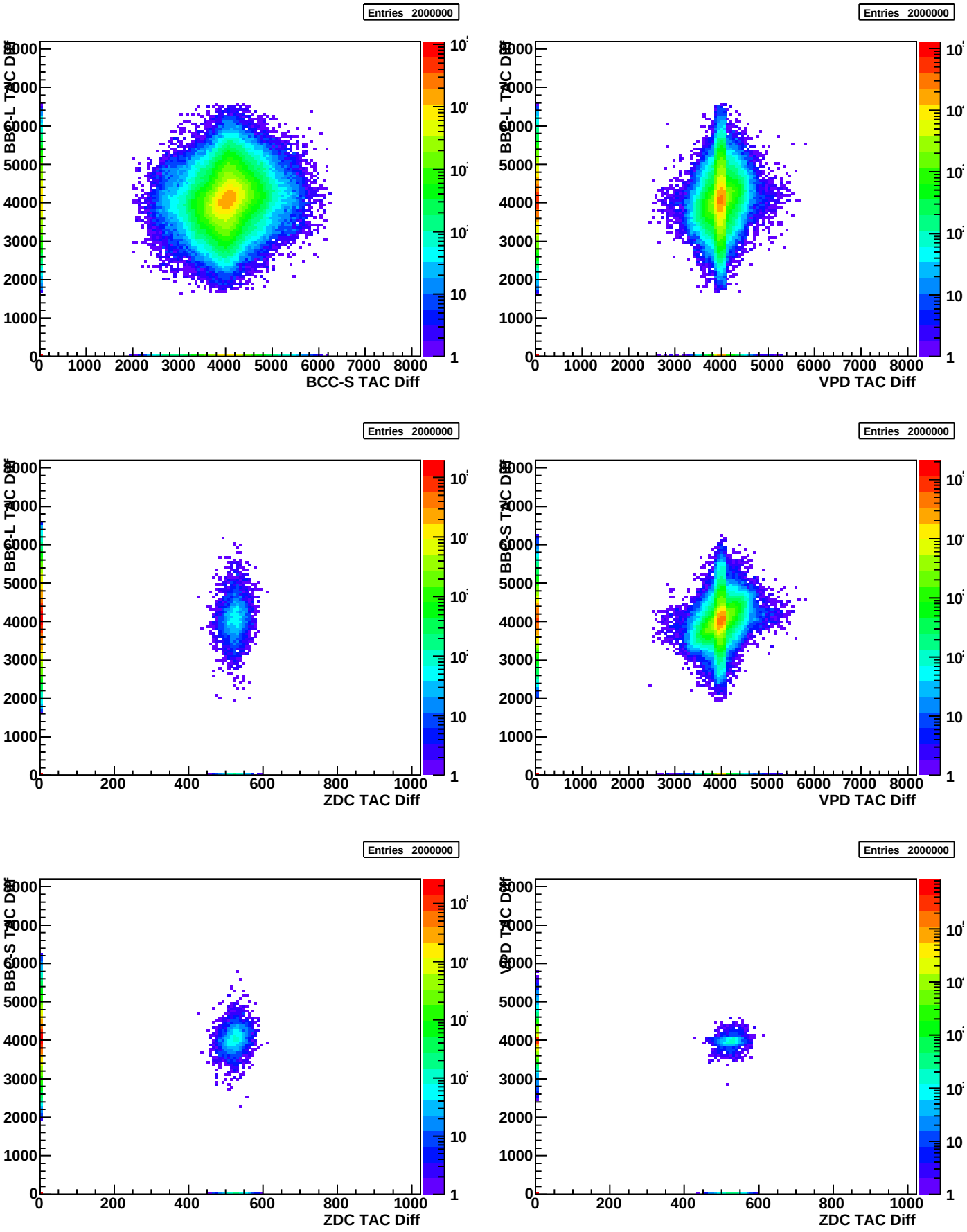

Entries 2000000
VPD-West ADC Sum
ZDC-West ADC Sum Att

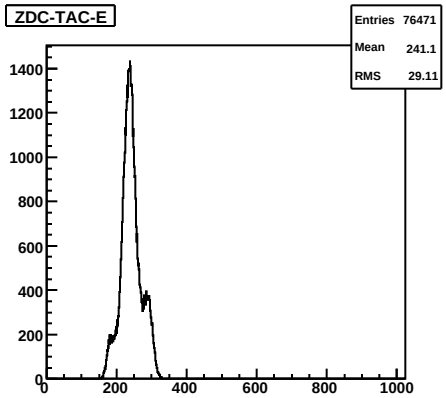

Entries 2000000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

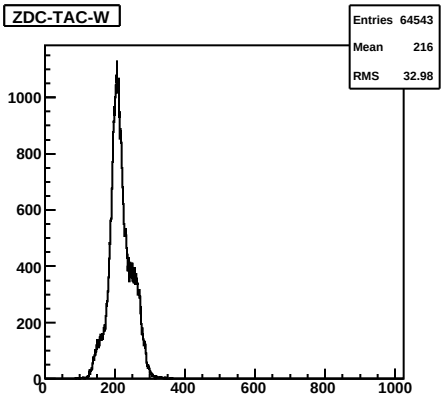

Entries 2000000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

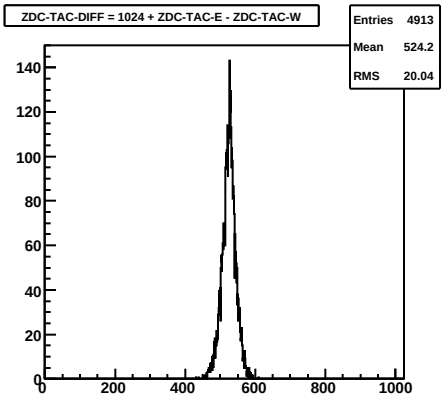

Entries 2000000
VPD East ADC Sum
VPD West ADC Sum

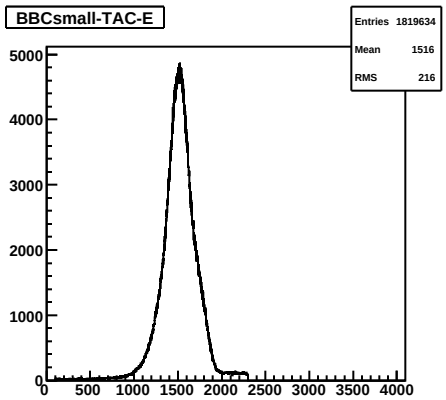

Entries 2000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

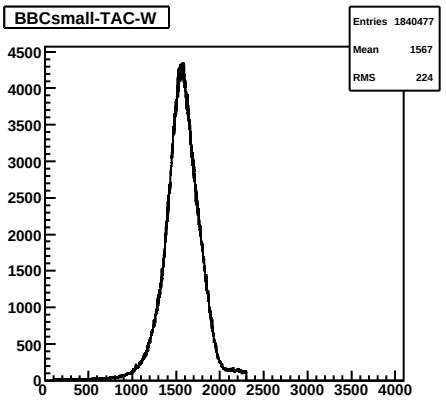

Entries 2000000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2000000
BBC-L TAC Diff
VPD TAC Diff
Entries 2000000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2000000
BBC-S TAC Diff
VPD TAC Diff
Entries 2000000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2000000
VPD TAC Diff
ZDC TAC Diff

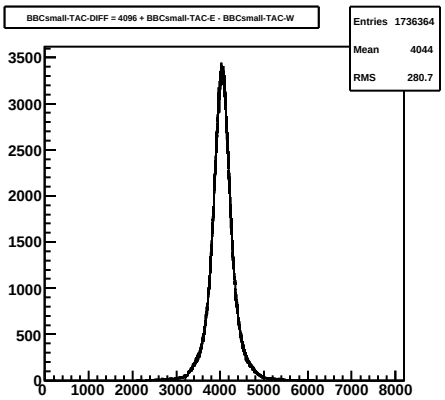

ZDC-TAC-E
Entries 76471
Mean 241.1
RMS 29.11

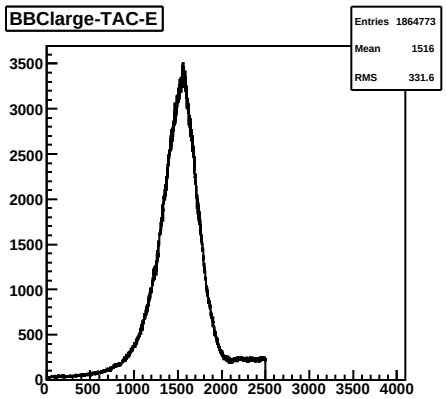

ZDC-TAC-W
Entries 64543
Mean 216
RMS 32.98

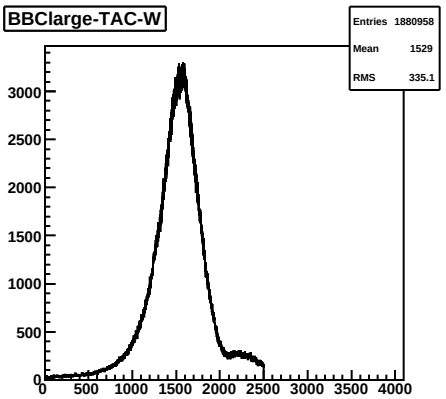

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 4913
Mean 524.2
RMS 20.04

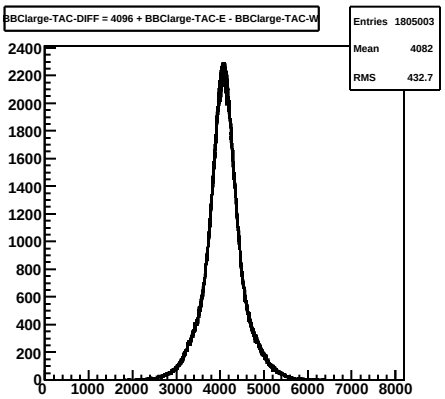

BBCsmall-TAC-E
Entries 1819634
Mean 1516
RMS 216

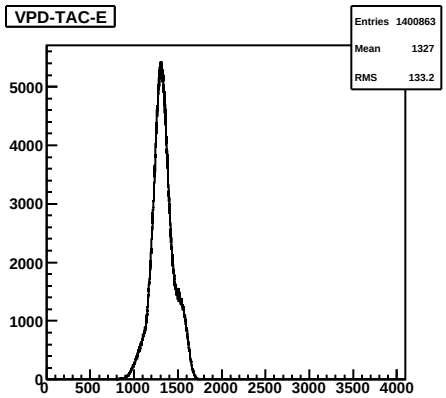

BBCsmall-TAC-W
Entries 1840477
Mean 1567
RMS 224

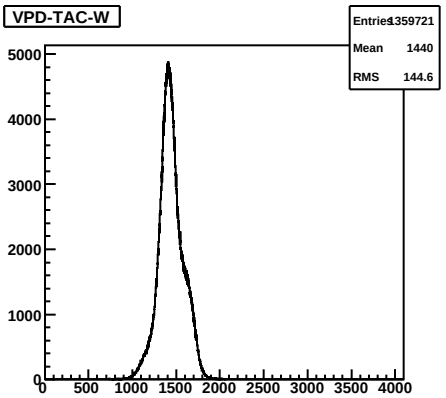

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1736364
Mean 4044
RMS 280.7

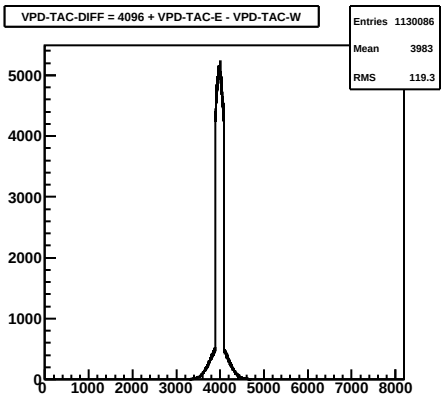

BBClarge-TAC-E
Entries 1864773
Mean 1516
RMS 331.6

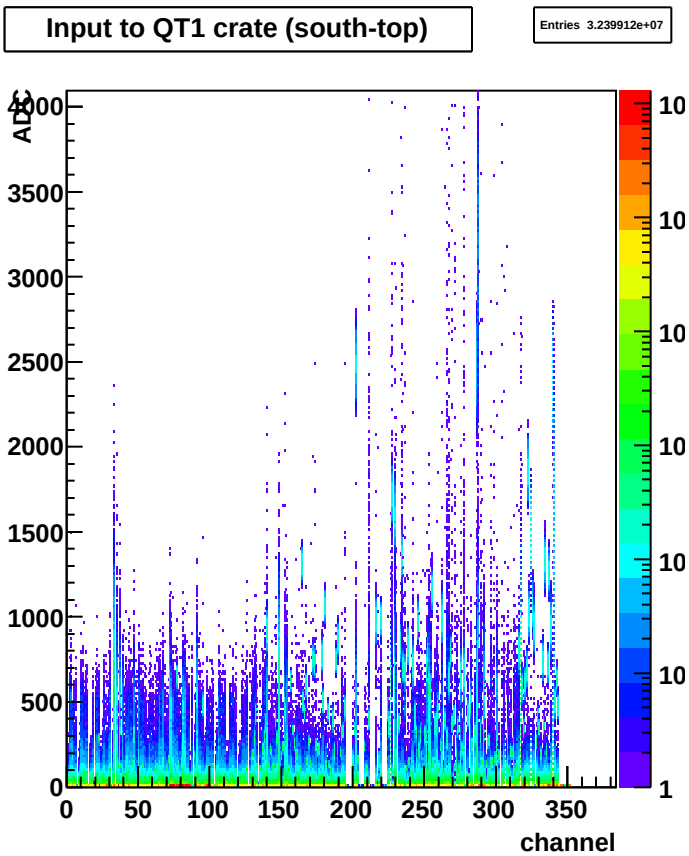

BBClarge-TAC-W
Entries 1880958
Mean 1529
RMS 335.1

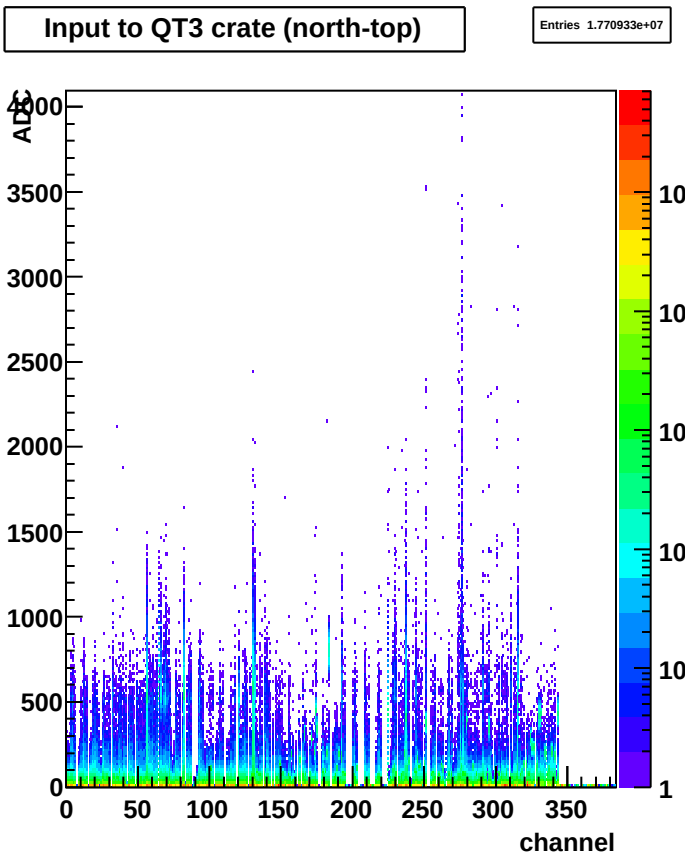

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1805003
Mean 4082
RMS 432.7

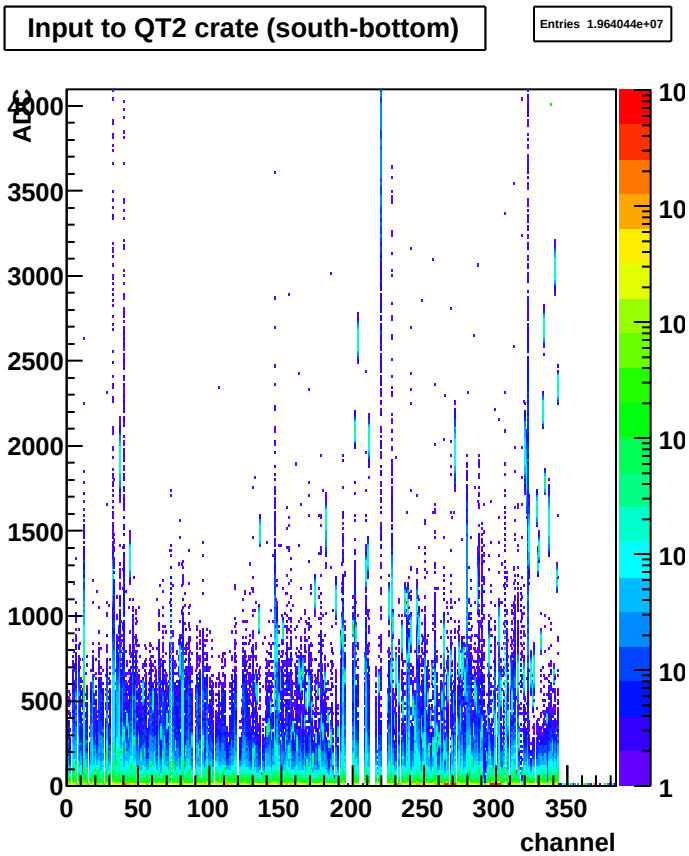

VPD-TAC-E
Entries 1400863
Mean 1327
RMS 133.2

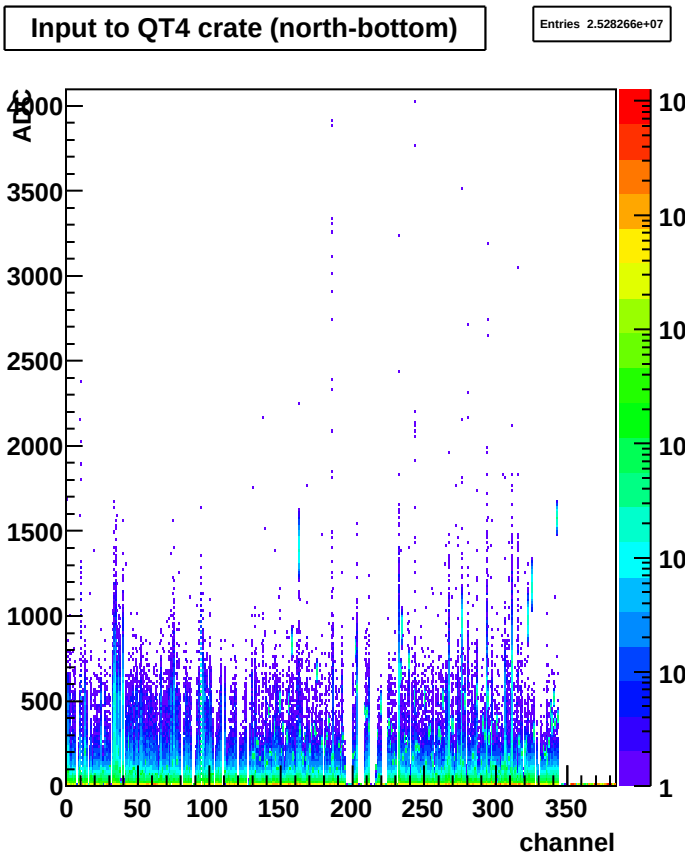

VPD-TAC-W
Entries 1359721
Mean 1440
RMS 144.6

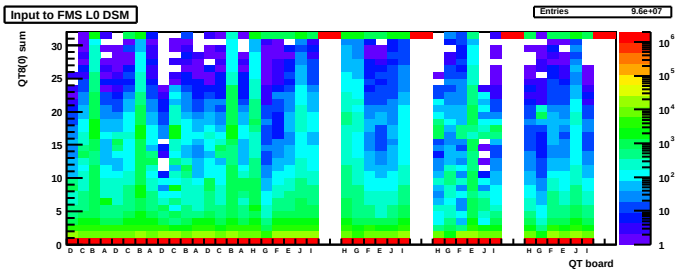

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1130086
Mean 3983
RMS 119.3

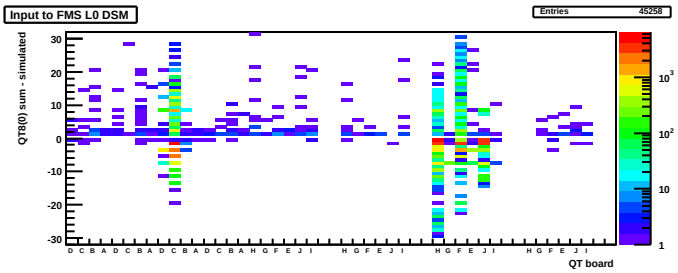

Input to QT1 crate (south-top)
Entries 3.239912e+07
ADC
channel

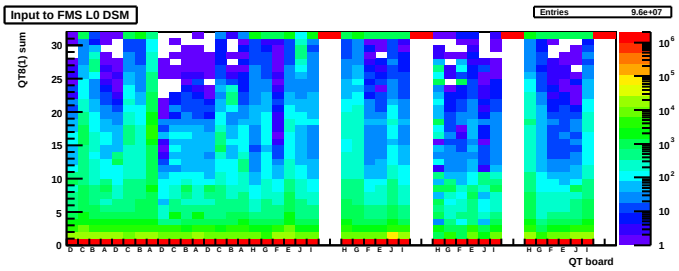

Input to QT3 crate (north-top)
Entries 1.770933e+07
ADC
channel

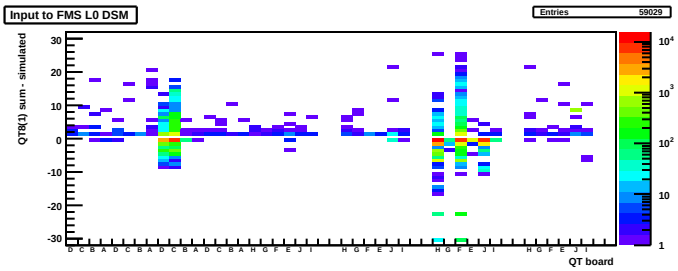

Input to QT2 crate (south-bottom)
Entries 1.964044e+07
ADC
channel

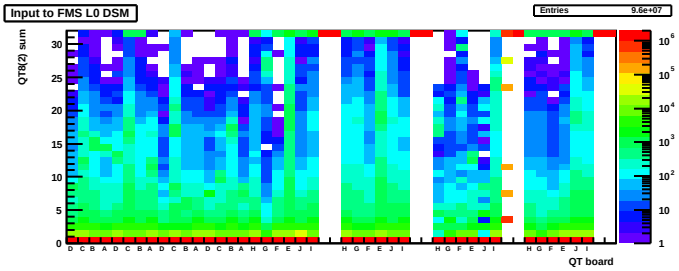

Input to QT4 crate (north-bottom)
Entries 2.528266e+07
ADC
channel

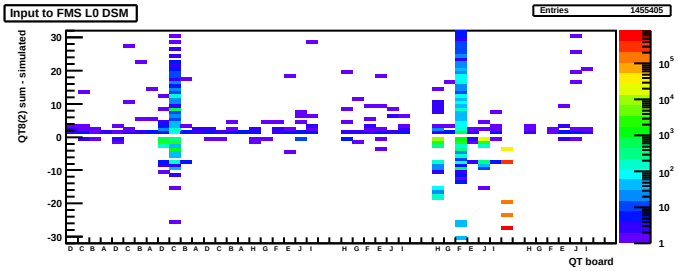

Input to FMS L0 DSM
Entries 9.6e+07
QT8(0) sum
QT board

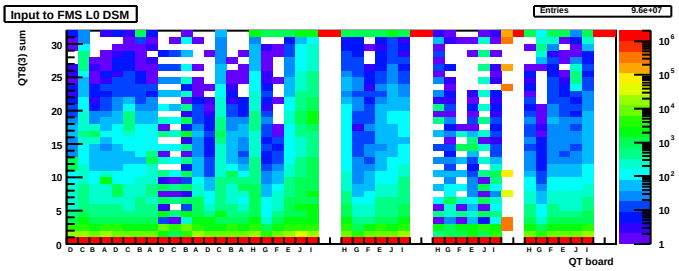

Input to FMS L0 DSM
Entries 45258
QT8(0) sum - simulated
QT board

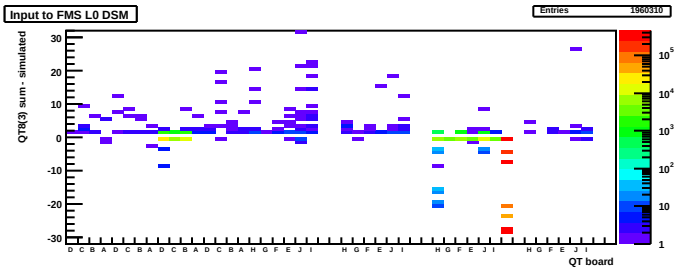

Input to FMS L0 DSM
Entries 9.6e+07
QT8(1) sum
QT board

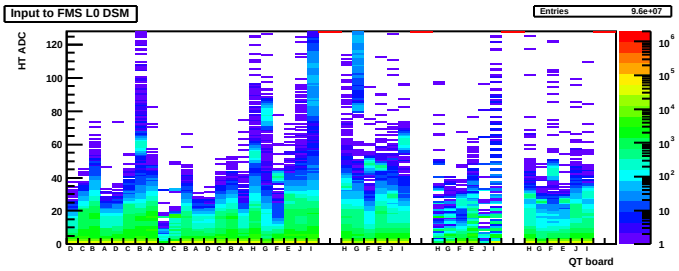

Input to FMS L0 DSM
Entries 59029
QT8(1) sum - simulated
QT board

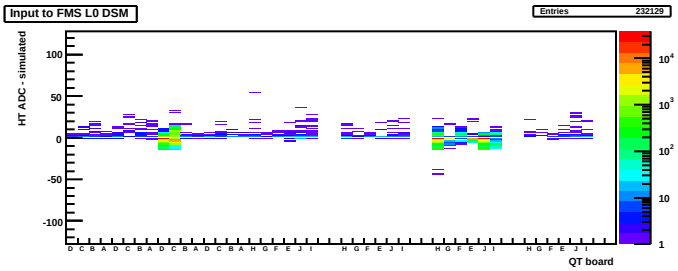

Input to FMS L0 DSM
Entries 9.6e+07
QT8(2) sum
QT board

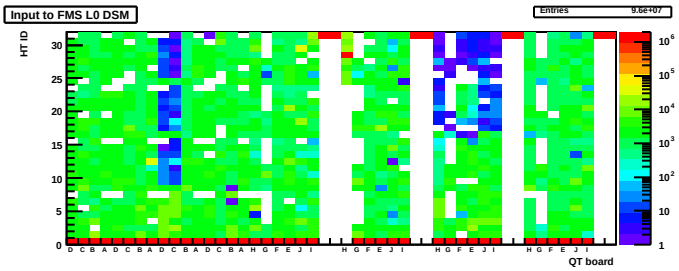

Input to FMS L0 DSM
Entries 1455405
QT8(2) sum - simulated
QT board

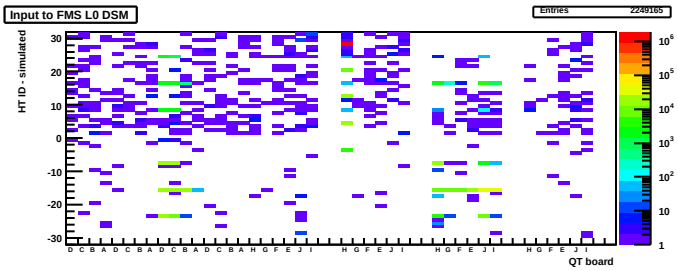

Input to FMS L0 DSM
Entries 9.6e+07
QT8(3) sum
QT board

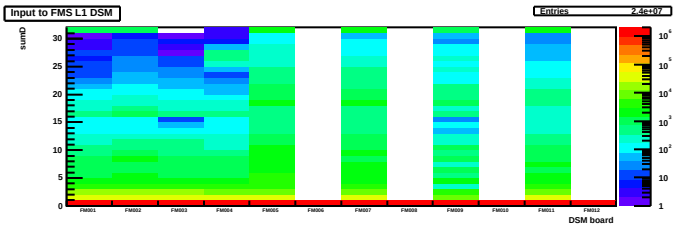

Input to FMS L0 DSM
Entries 1960310
QT8(3) sum - simulated
QT board

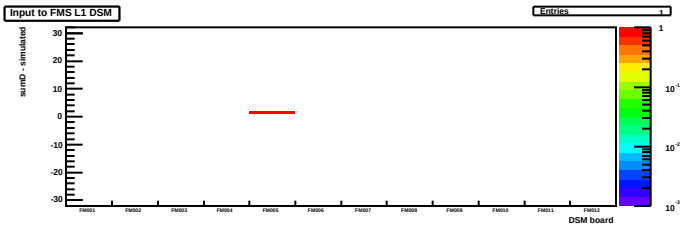

Input to FMS L0 DSM
Entries 9.6e+07
HT ADC
QT board

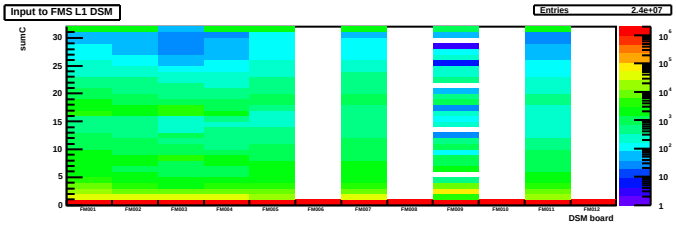

Input to FMS L0 DSM
Entries 232129
HT ADC - simulated
QT board

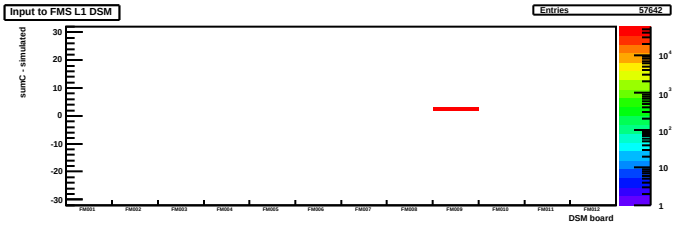

Input to FMS L0 DSM
Entries 9.6e+07
HT ID
QT board

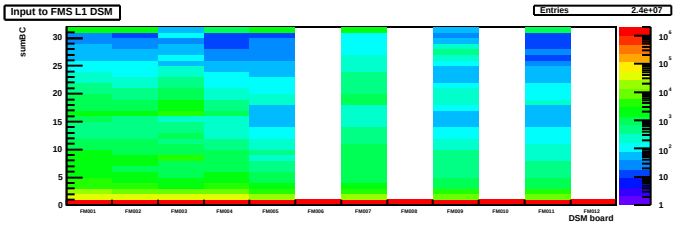

Input to FMS L0 DSM
Entries 2249165
HT ID - simulated
QT board

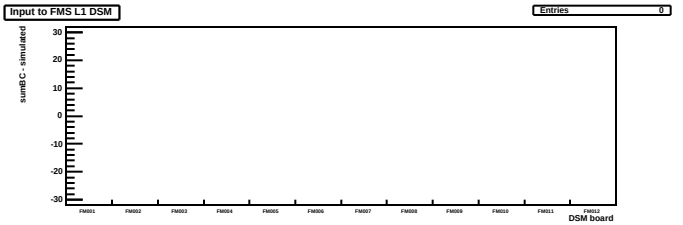

Input to FMS L1 DSM
Entries 2.4e+07
sumD
DSM board

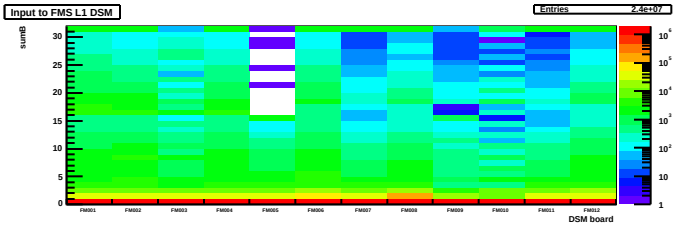

Input to FMS L1 DSM
Entries 1
sumD - simulated
DSM board

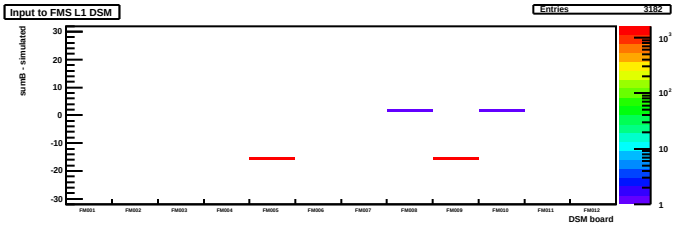

Input to FMS L1 DSM
Entries 2.4e+07
sumC
DSM board

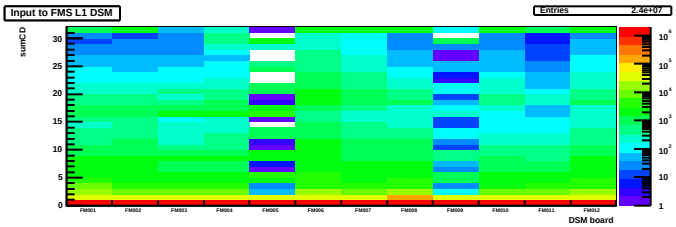

Input to FMS L1 DSM
Entries 57642
sumC - simulated
DSM board

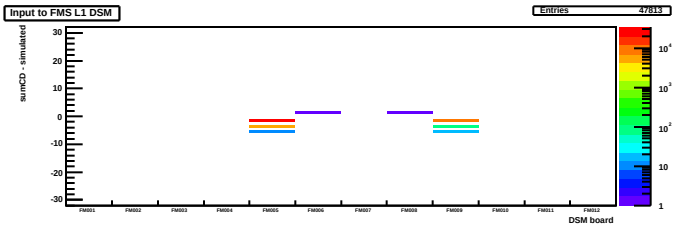

Input to FMS L1 DSM
Entries 2.4e+07
sumBC
DSM board

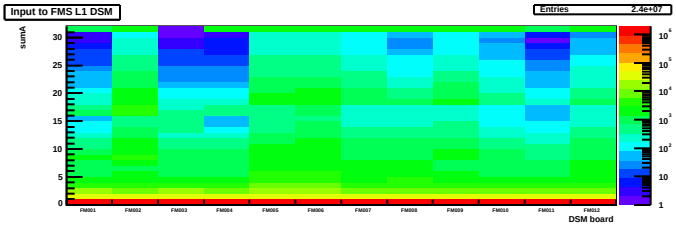

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

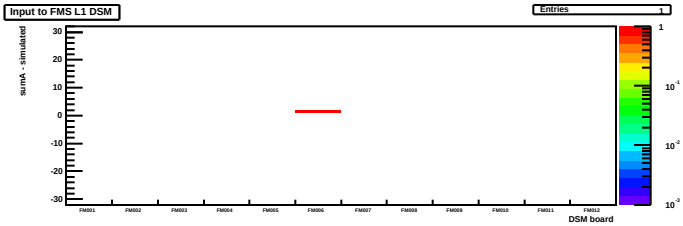

Input to FMS L1 DSM
Entries 2.4e+07
sumB
DSM board

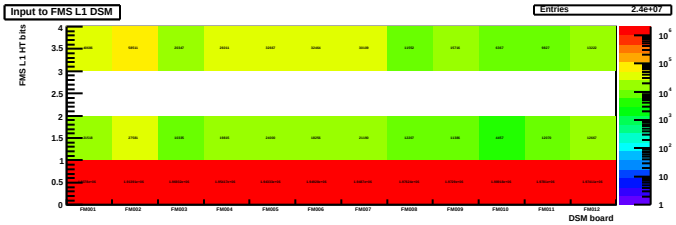

Input to FMS L1 DSM
Entries 3182
sumB - simulated
DSM board

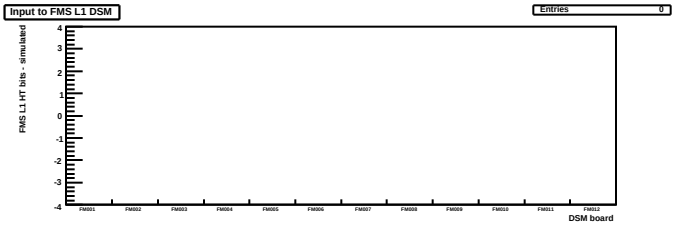

Input to FMS L1 DSM
Entries 2.4e+07
sumCD
DSM board

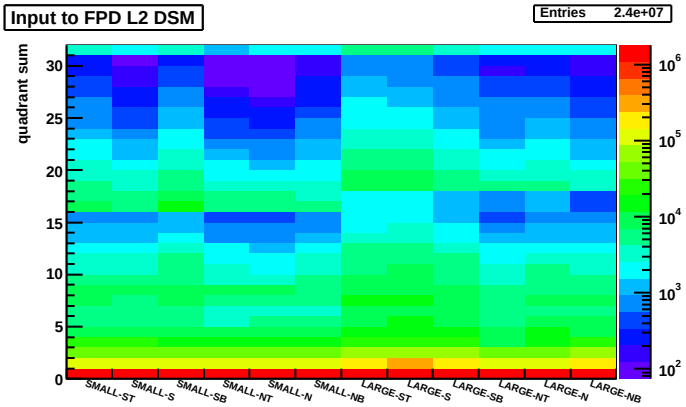

Input to FMS L1 DSM
Entries 47813
sumCD - simulated
DSM board

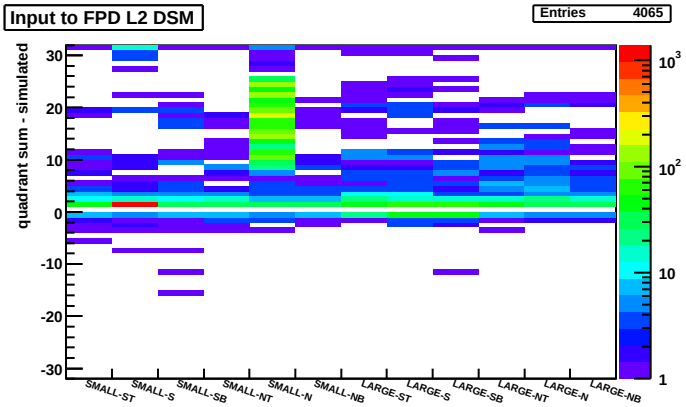

Input to FMS L1 DSM
Entries 2.4e+07
sumA
DSM board

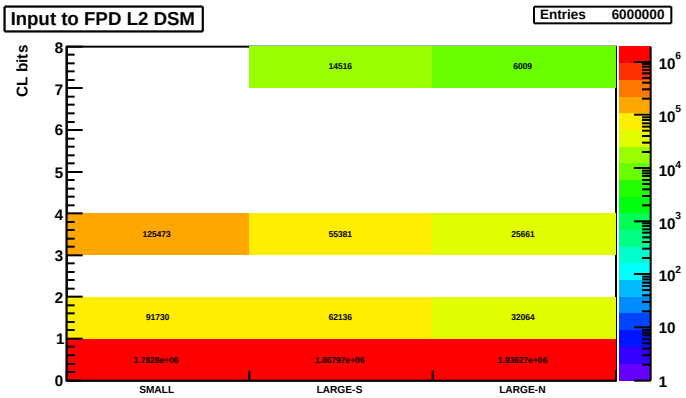

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

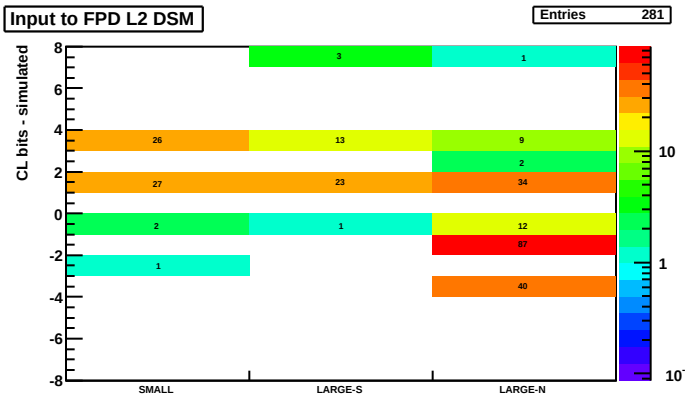

Input to FMS L1 DSM
Entries 2.4e+07
FMS L1 HT bits
DSM board

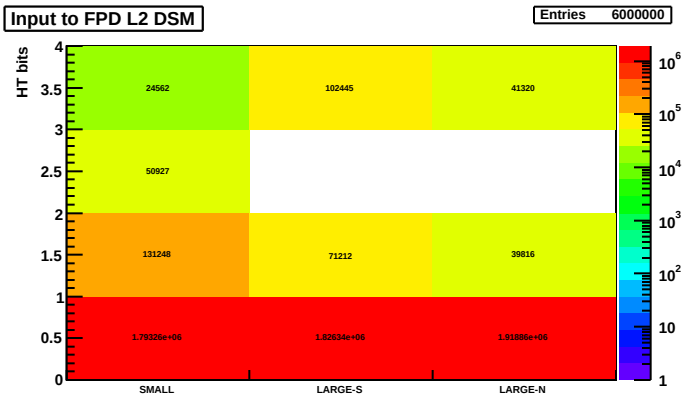

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

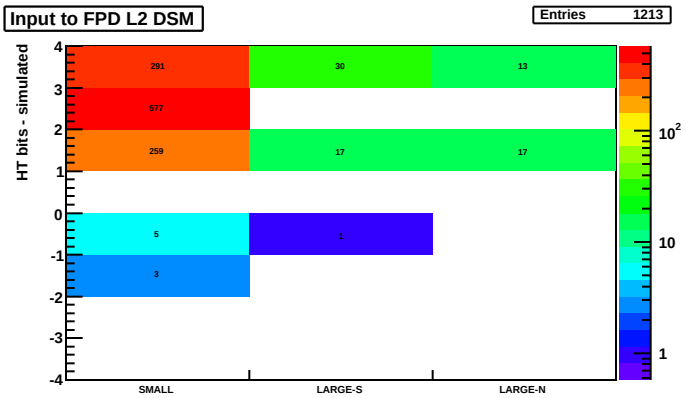

Input to FPD L2 DSM
Entries 2.4e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

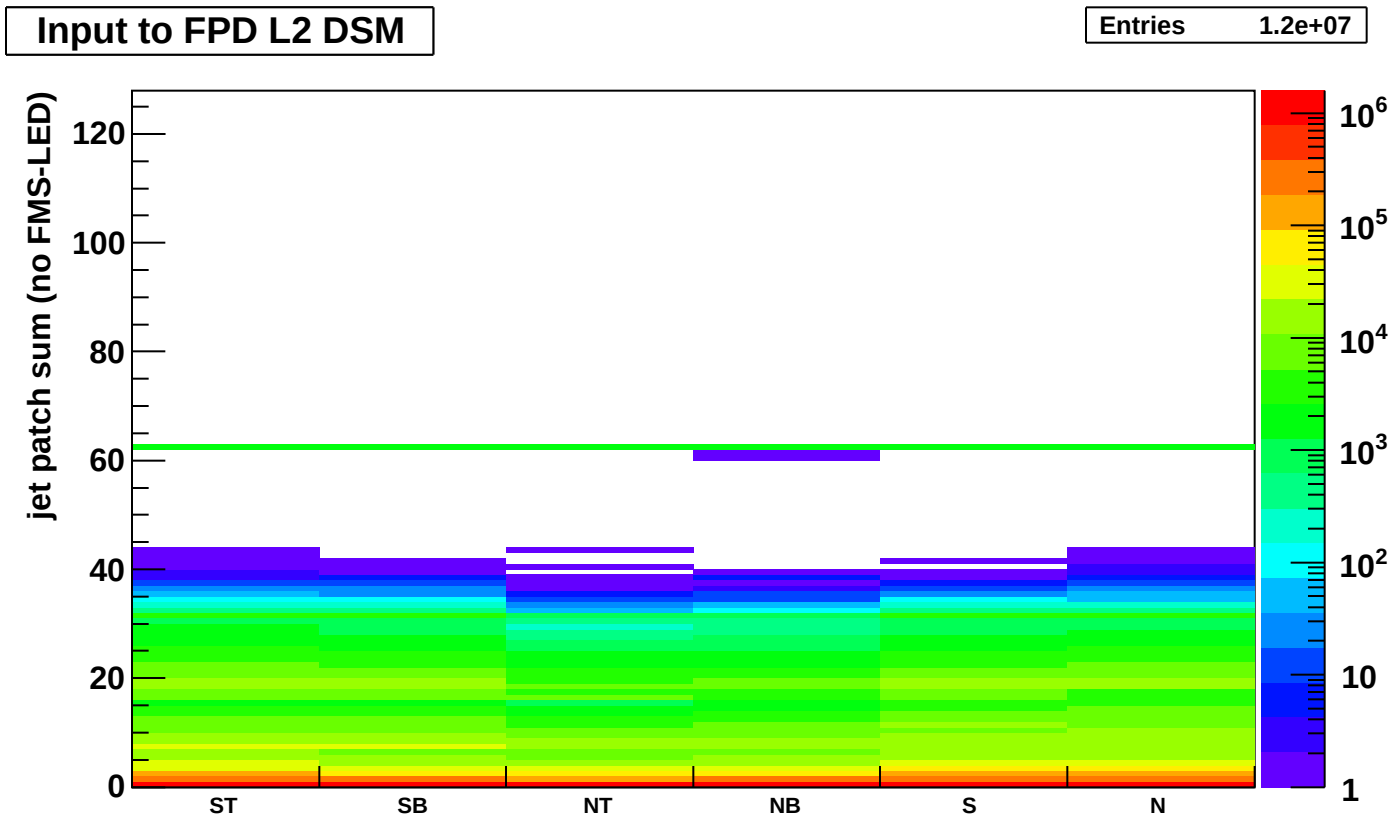

Input to FPD L2 DSM
Entries 4065
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

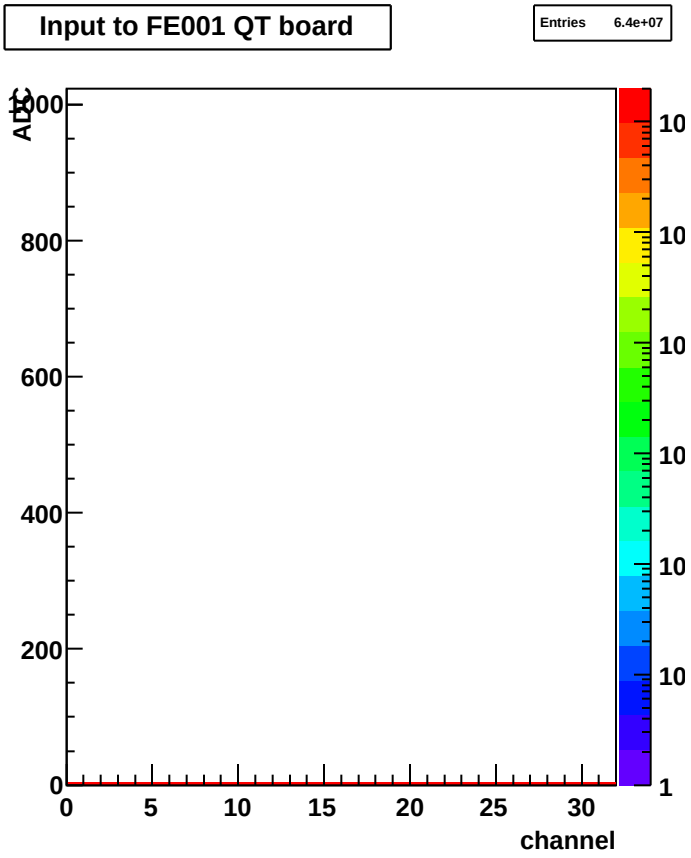

Input to FPD L2 DSM
Entries 6000000
CL bits
14516
6009
125473
55381
25661
91730
62136
32064
1.7828e+06
1.86797e+06
1.93627e+06
SMALL
LARGE-S
LARGE-N

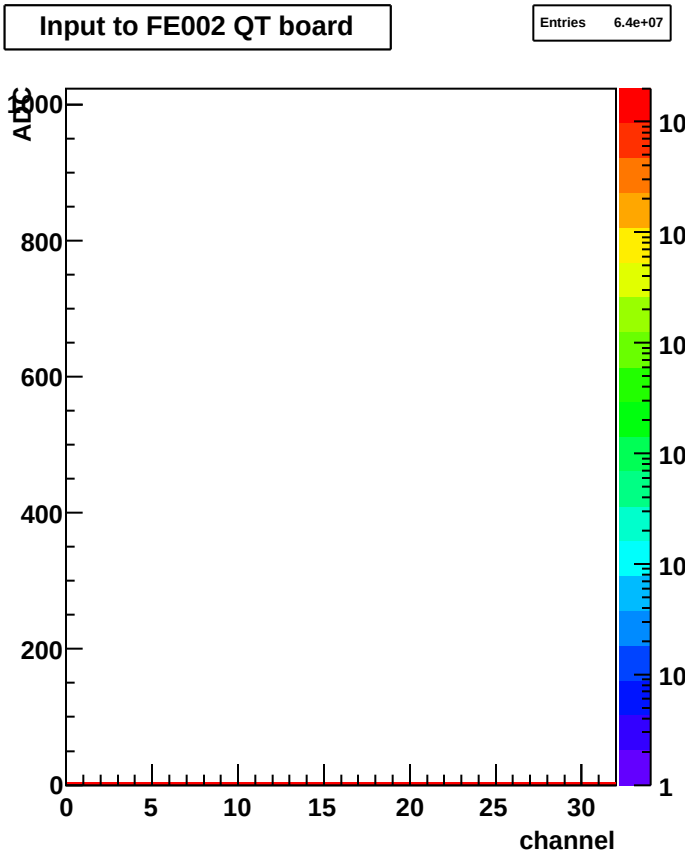

Input to FPD L2 DSM
Entries 281
CL bits - simulated
3
1
26
13
9
2
27
23
34
2
1
12
87
1
40
SMALL
LARGE-S
LARGE-N

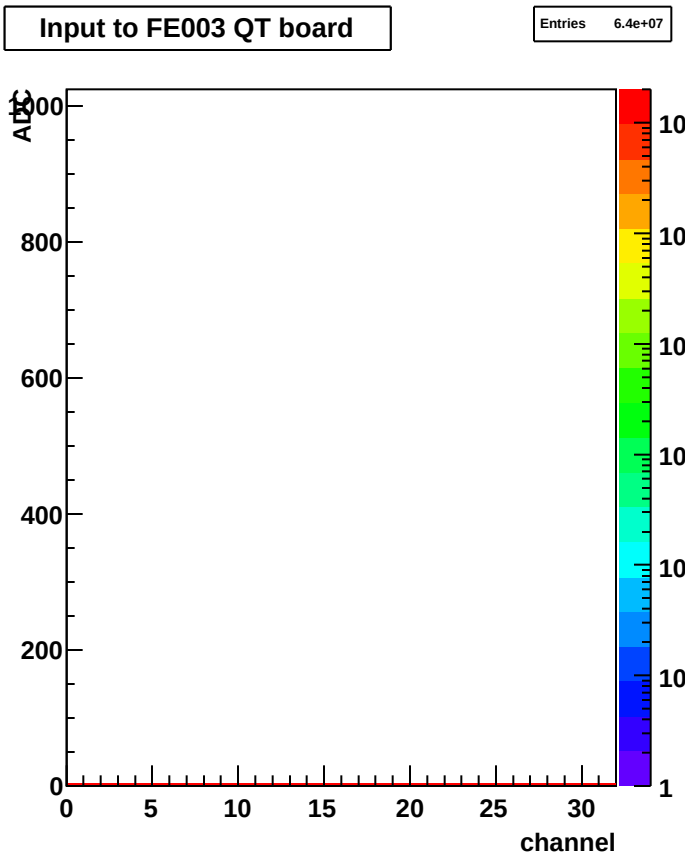

Input to FPD L2 DSM
Entries 6000000
HT bits
24562
102445
41320
50927
131248
71212
39816
1.79326e+06
1.82634e+06
1.91886e+06
SMALL
LARGE-S
LARGE-N

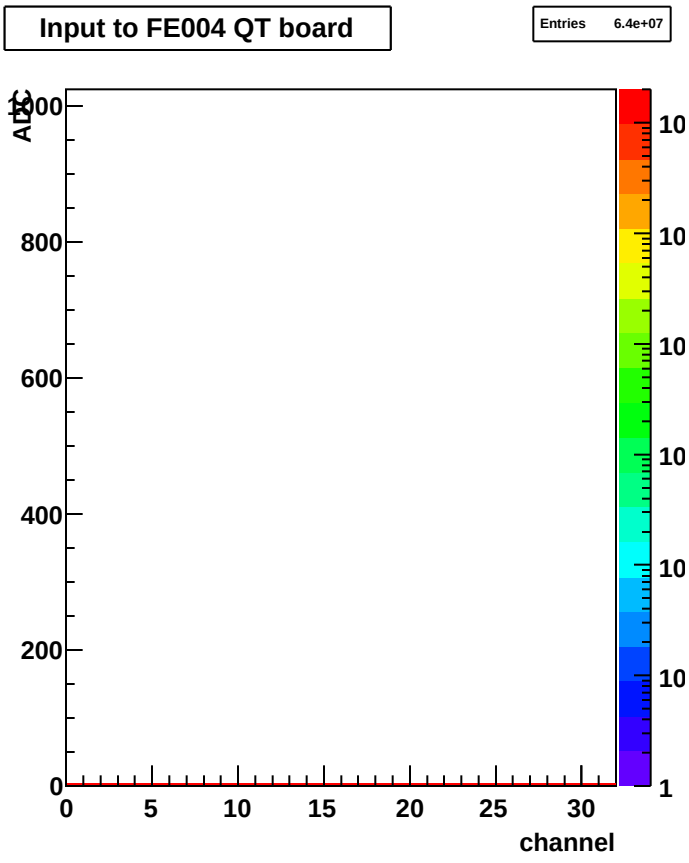

Input to FPD L2 DSM
Entries 1213
HT bits - simulated
291
30
13
577
259
17
17
5
1
3
SMALL
LARGE-S
LARGE-N

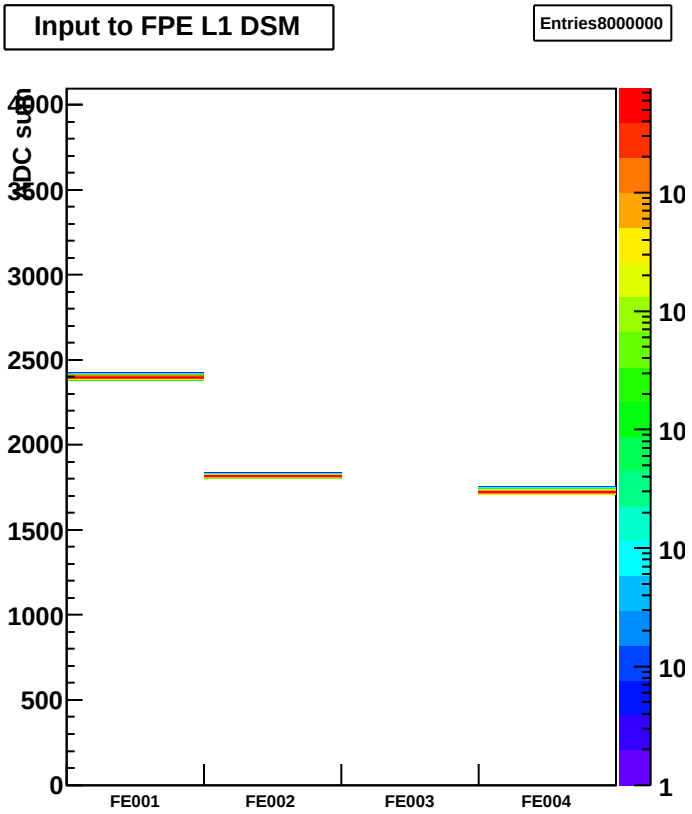

Input to FPD L2 DSM
Entries 1.2e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

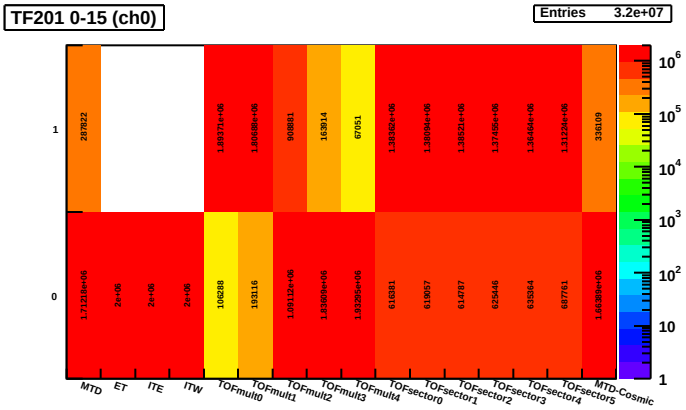

Input to FE001 QT board
Entries 6.4e+07
ADC
channel

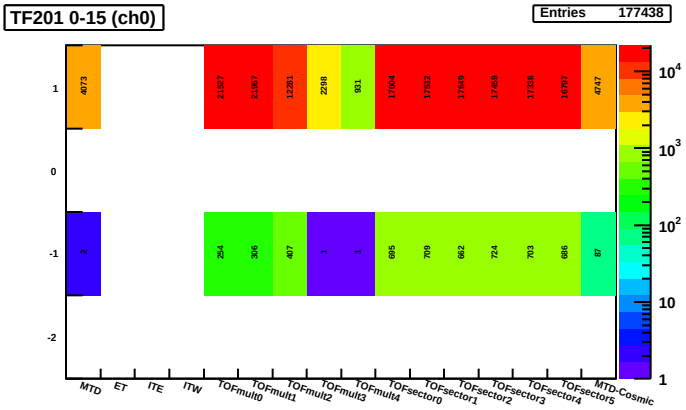

Input to FE002 QT board
Entries 6.4e+07
ADC
channel

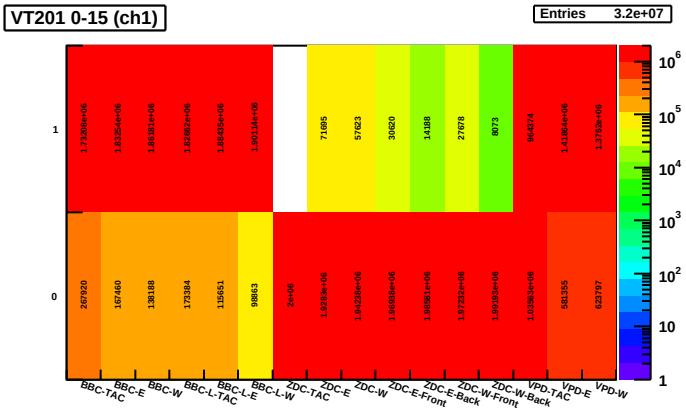

Input to FE003 QT board
Entries 6.4e+07
ADC
channel

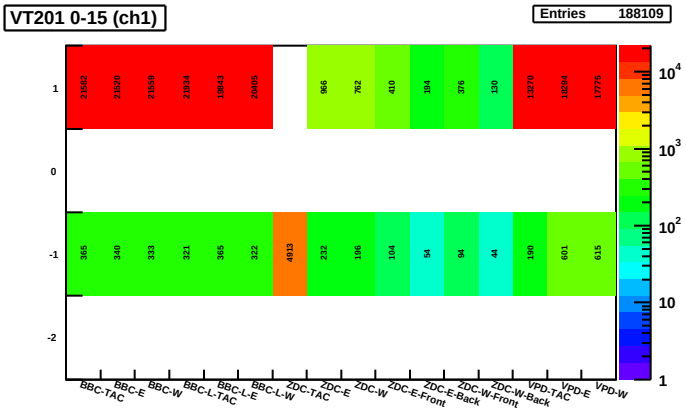

Input to FE004 QT board
Entries 6.4e+07
ADC
channel

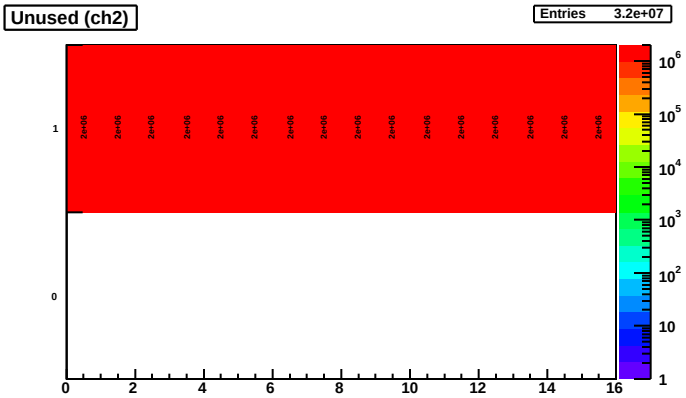

Input to FPE L1 DSM
Entries8000000
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

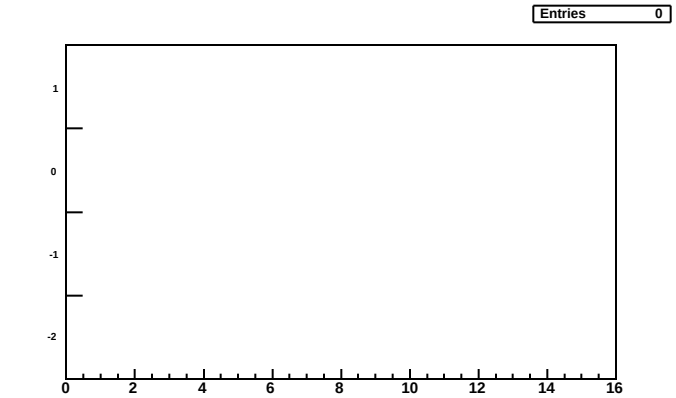

TF201 0-15 (ch0)
Entries 3.2e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

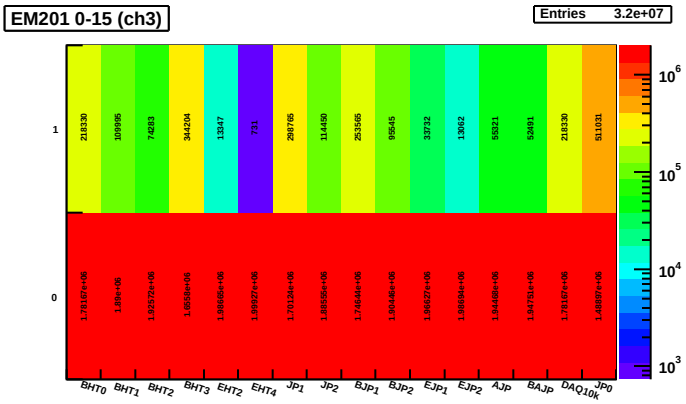

TF201 0-15 (ch0)
Entries 177438
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 3.2e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 188109
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3.2e+07


Entries 0


EM201 0-15 (ch3)
Entries 3.2e+07
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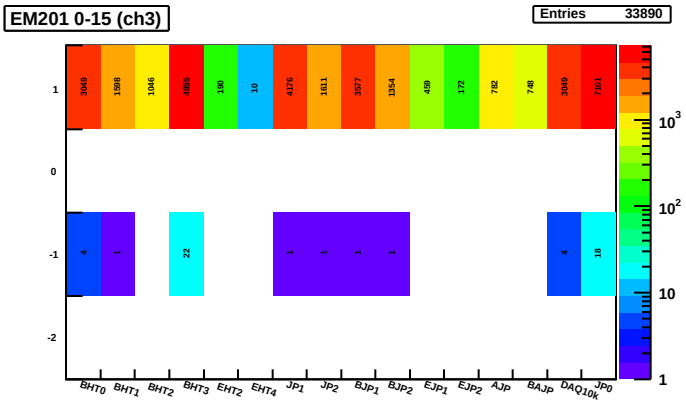
EM201 0-15 (ch3)
Entries 33890
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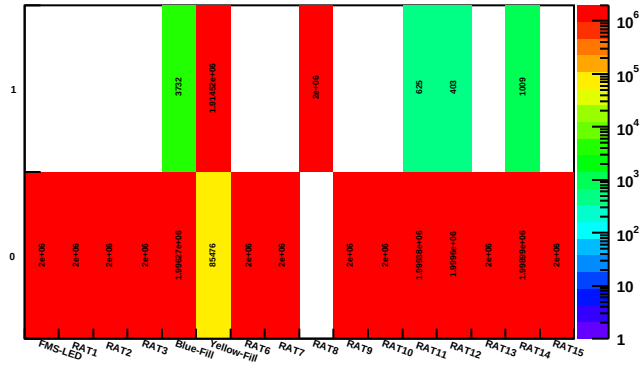
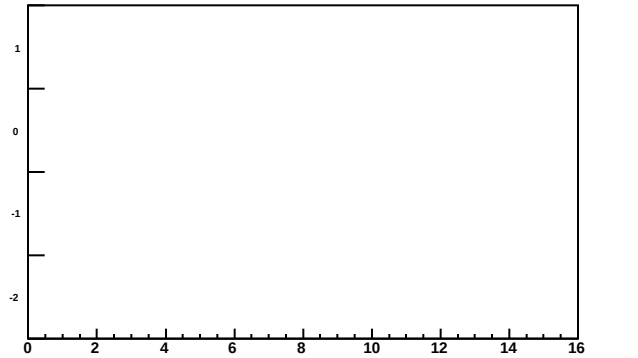
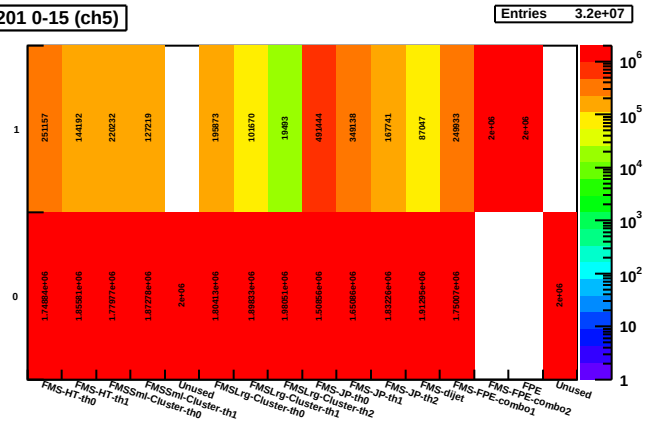
RAT board (ch4)





FP201 0-15 (ch5)



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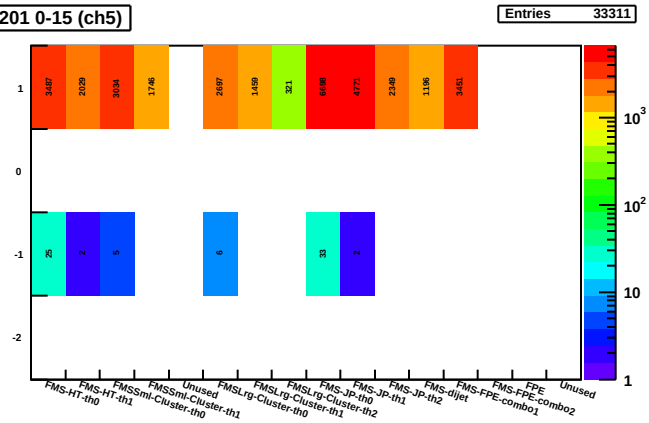
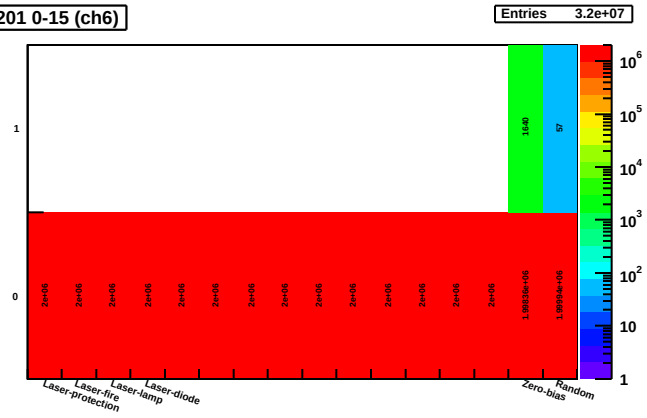
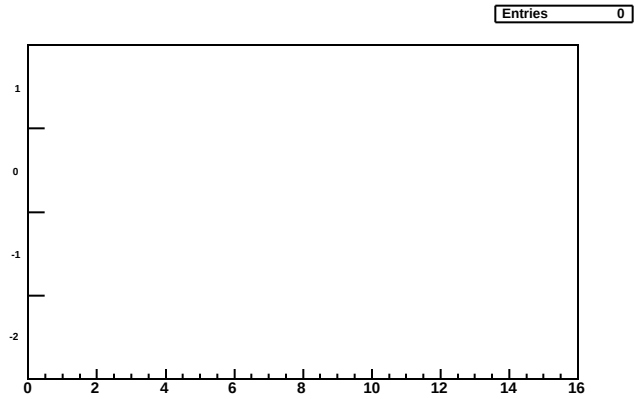
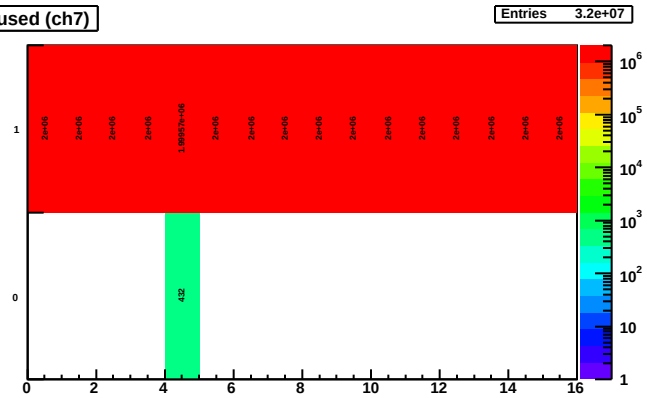
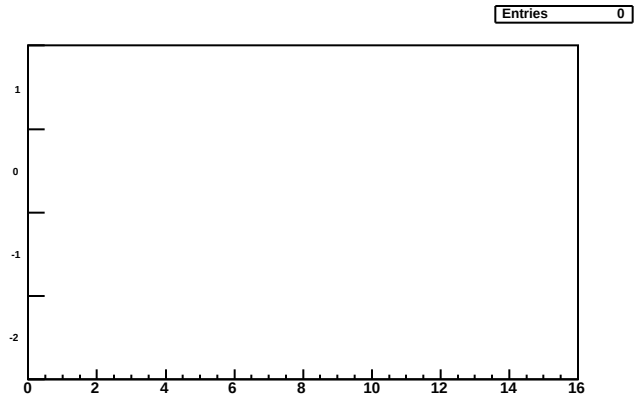


ST201 0-15 (ch6)





Unused (ch7)