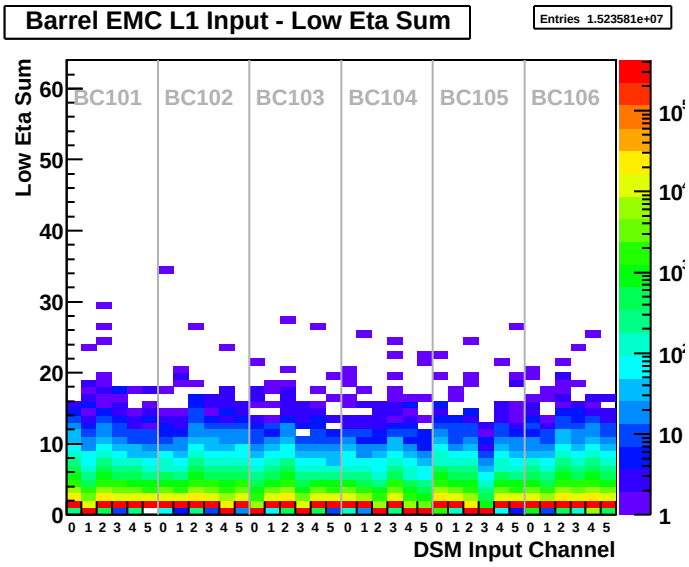
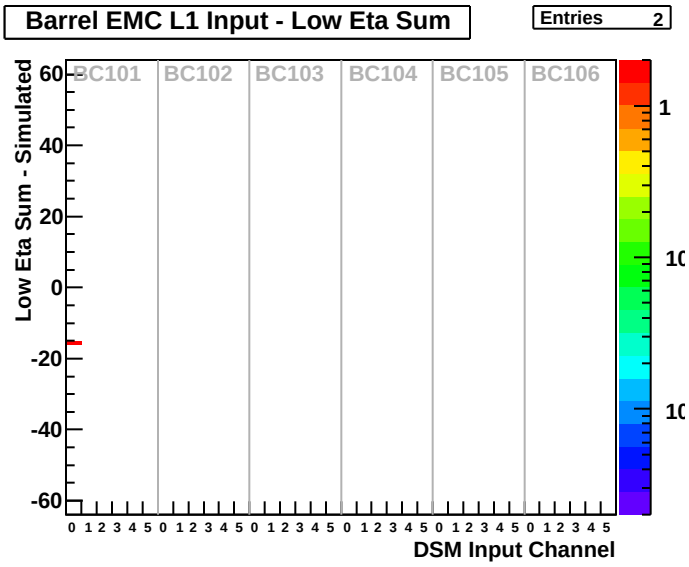
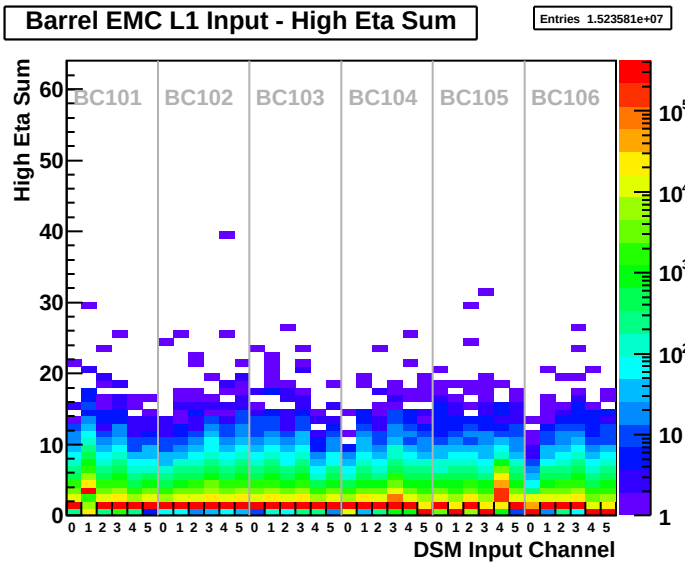
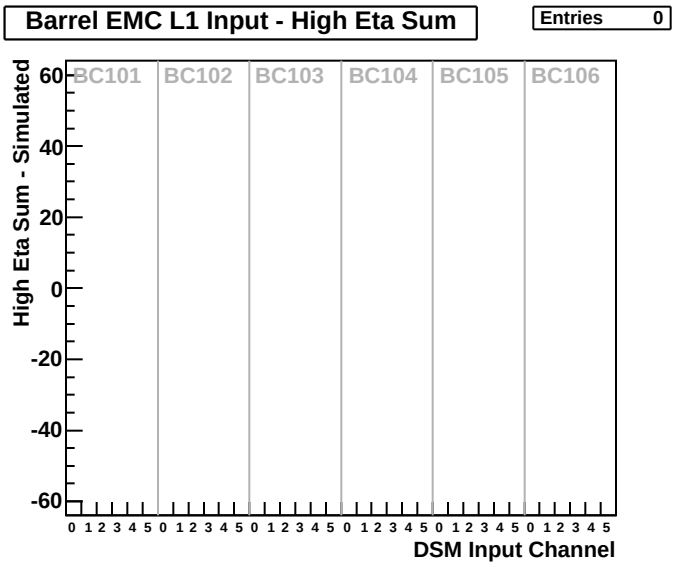
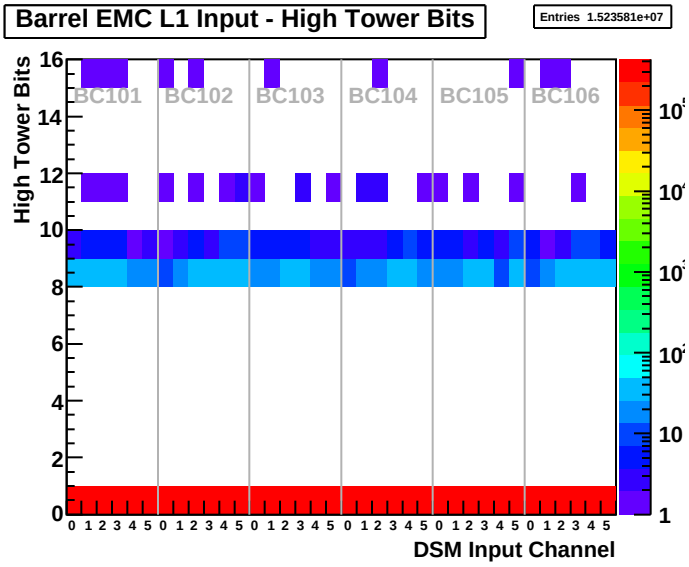
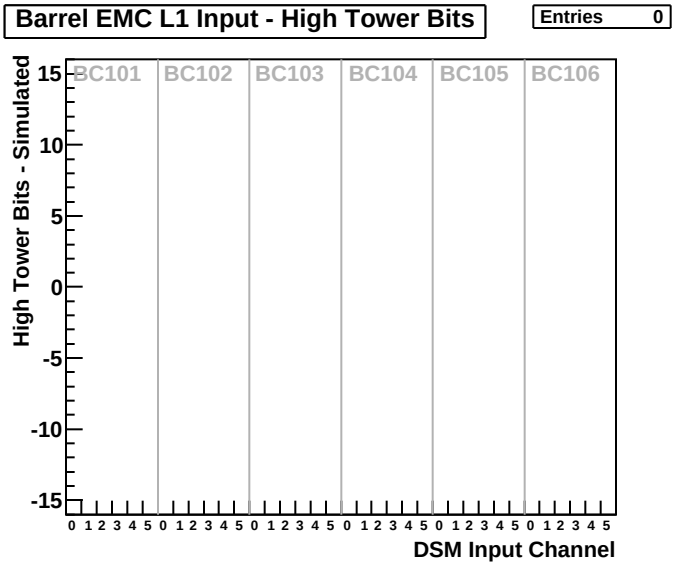

Barrel EMC L1 Input - Low Eta Sum
Entries 1.523581e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

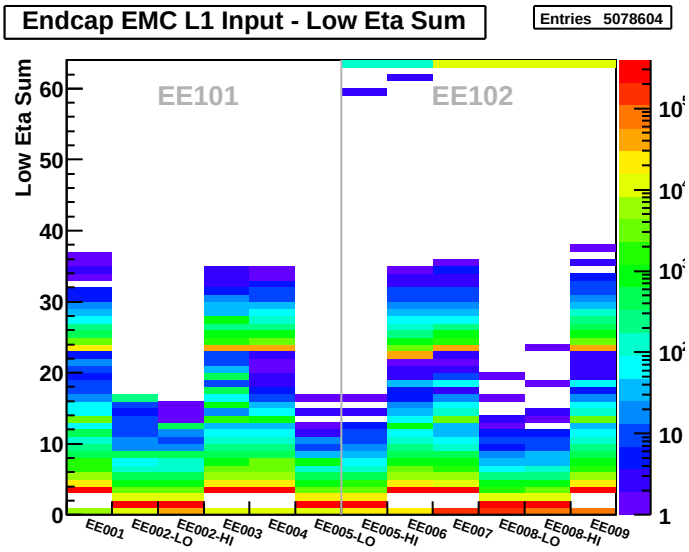

Barrel EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

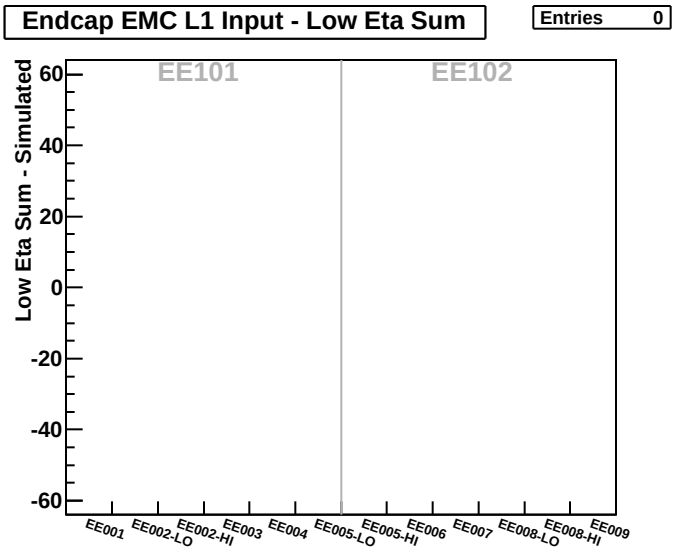

Barrel EMC L1 Input - High Eta Sum
Entries 1.523581e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

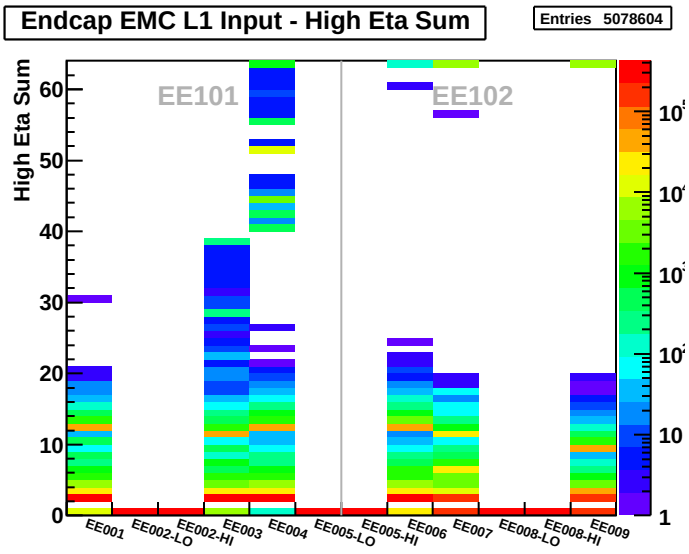

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

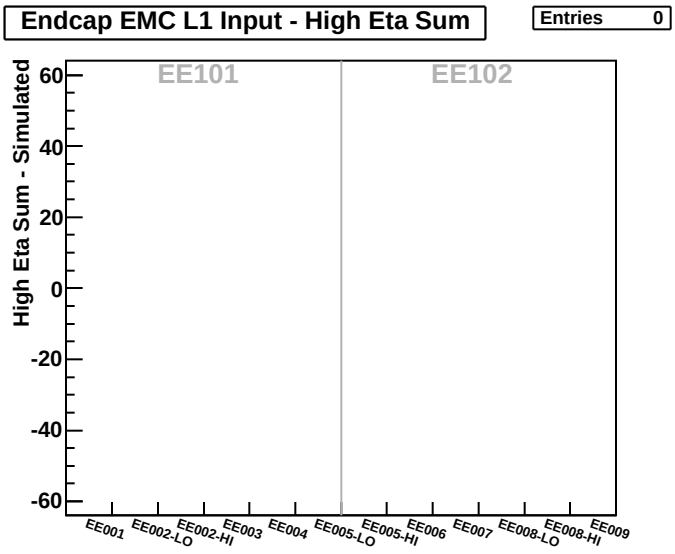

Barrel EMC L1 Input - High Tower Bits
Entries 1.523581e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

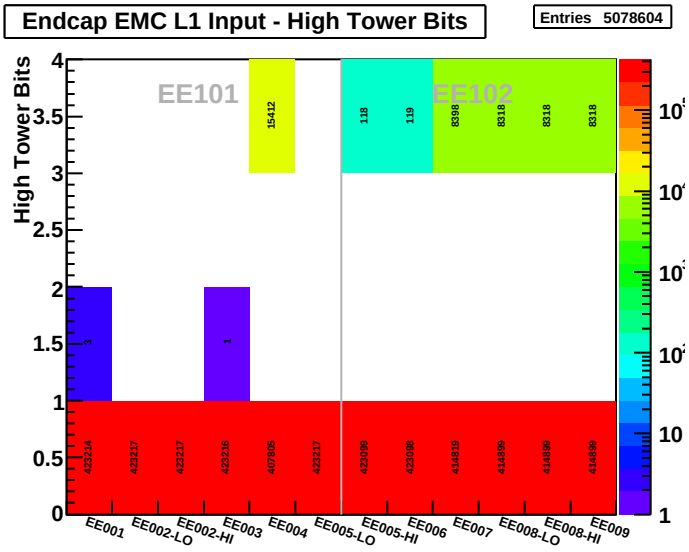

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

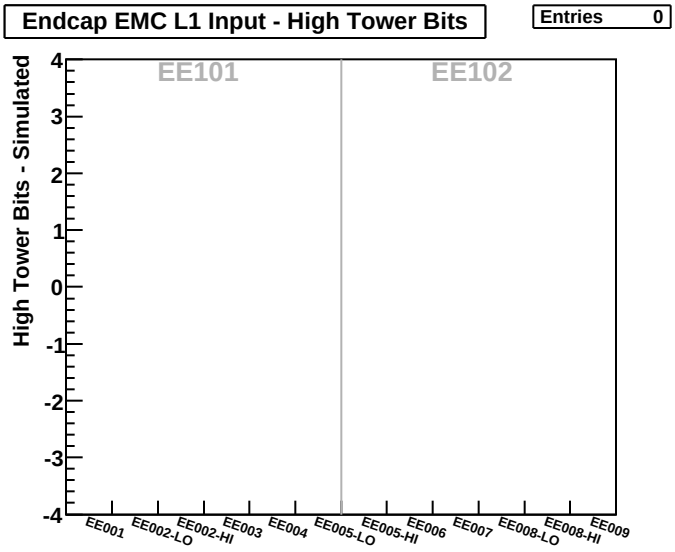

Endcap EMC L1 Input - Low Eta Sum
Entries 5078604
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

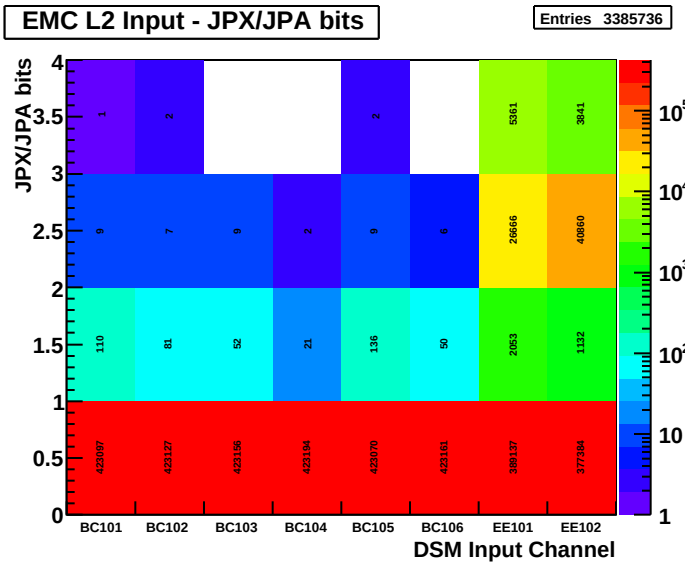

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

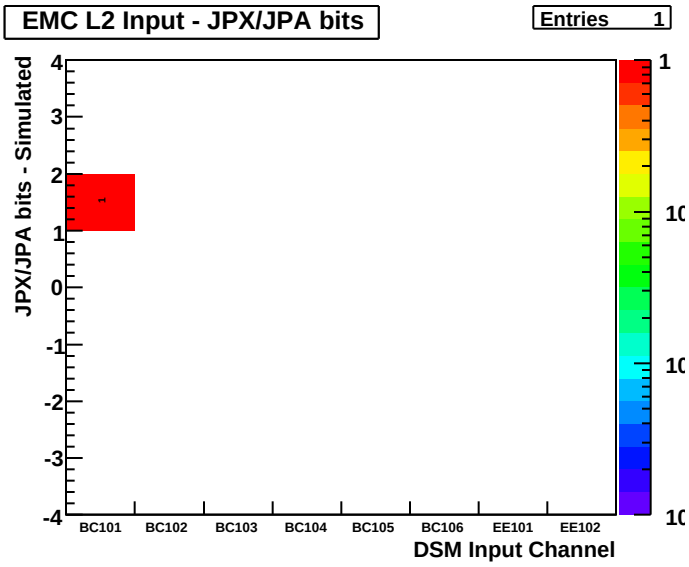

Endcap EMC L1 Input - High Eta Sum
Entries 5078604
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

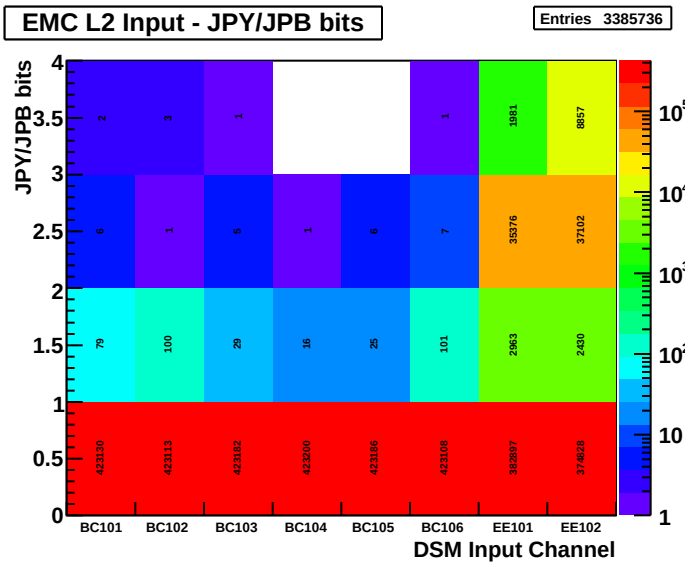

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

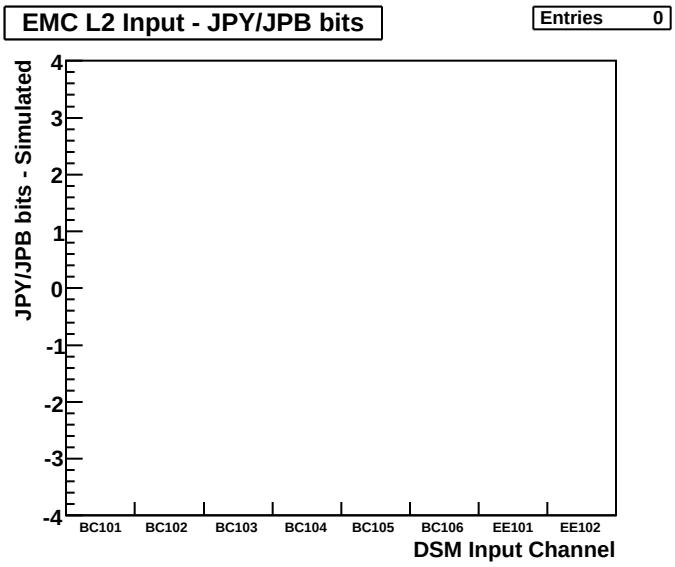

Endcap EMC L1 Input - High Tower Bits
Entries 5078604
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

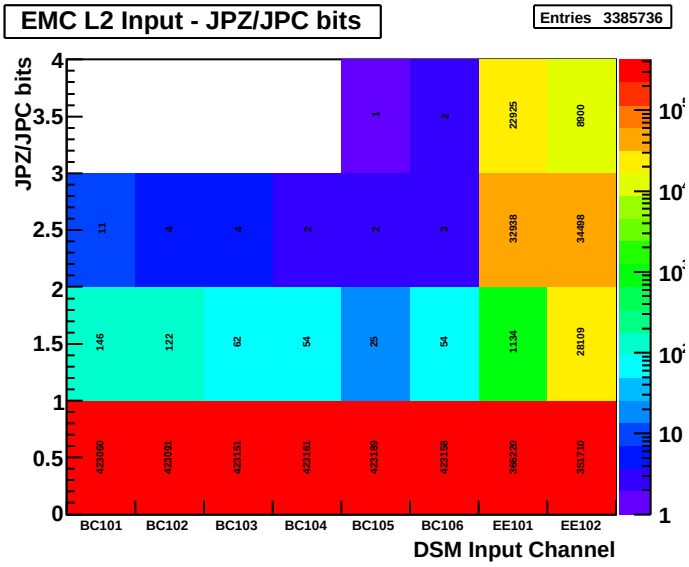

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

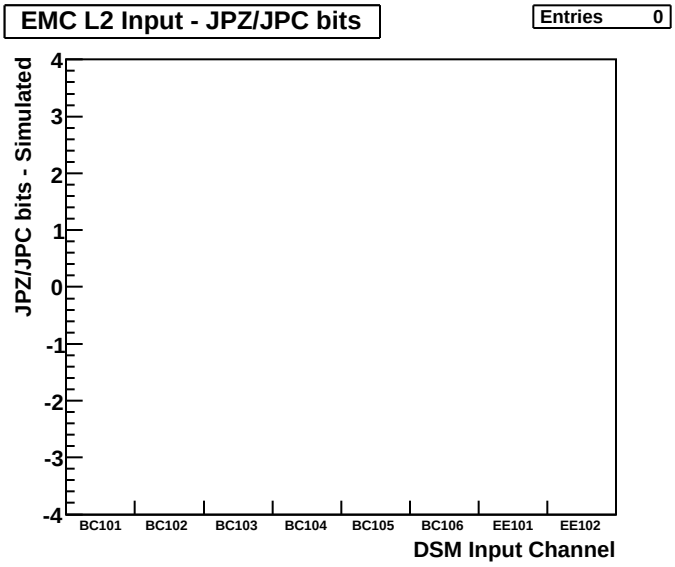

EMC L2 Input - JPX/JPA bits
Entries 3385736
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 2 5361 3841
9 7 9 2 9 6 26666 40860
110 81 52 21 136 50 2053 1132
423097 423127 423156 423194 423070 423161 389137 377384

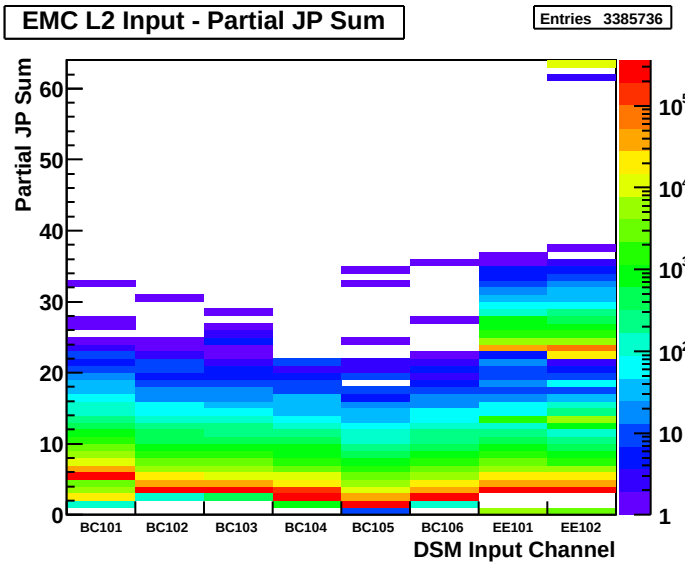

EMC L2 Input - JPX/JPA bits
Entries 1
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1

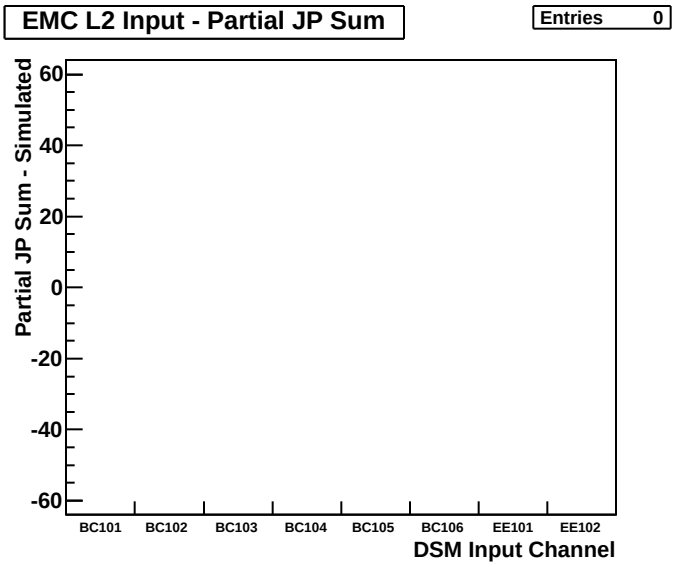

EMC L2 Input - JPY/JPB bits
Entries 3385736
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 3 1 1 1981 8857
6 1 5 1 6 7 35376 37102
79 100 29 16 25 101 2963 2430
423130 423113 423182 423200 423186 423108 382897 374828

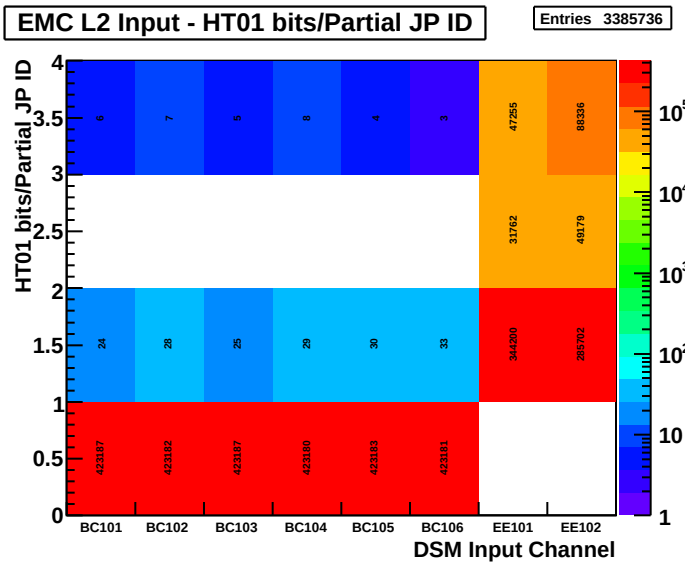

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

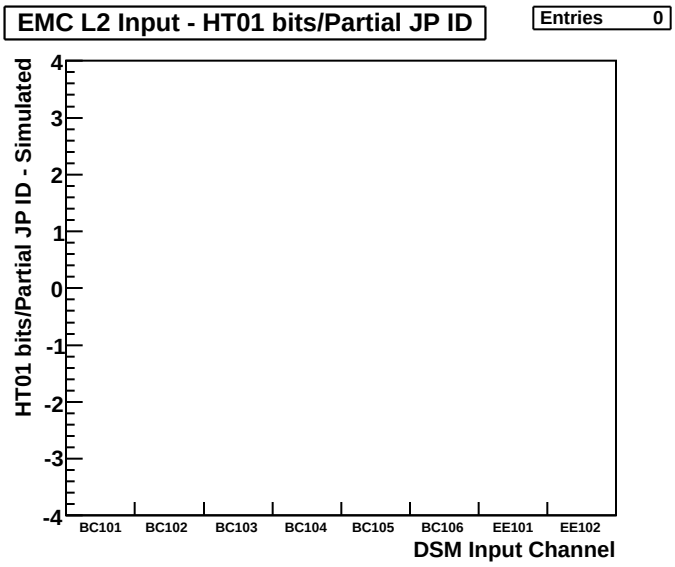

EMC L2 Input - JPZ/JPC bits
Entries 3385736
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 22925 8900
11 4 4 2 2 3 32938 34498
146 122 62 54 25 54 1134 28109
423060 423091 423151 423161 423189 423158 366220 351710

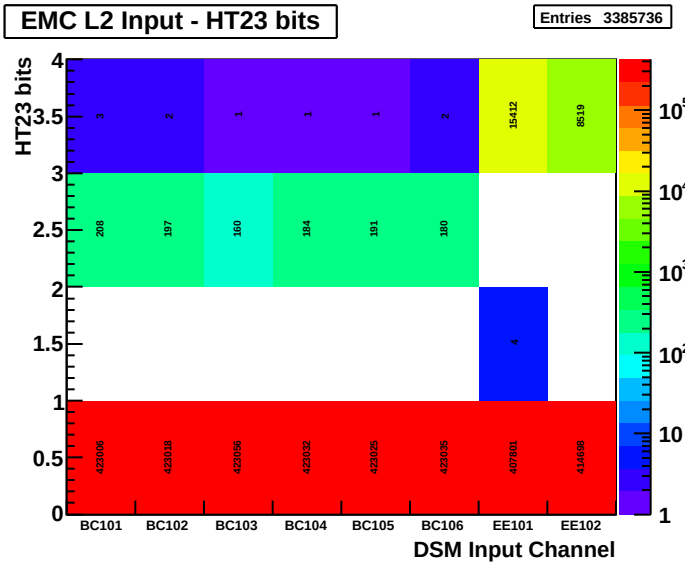

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

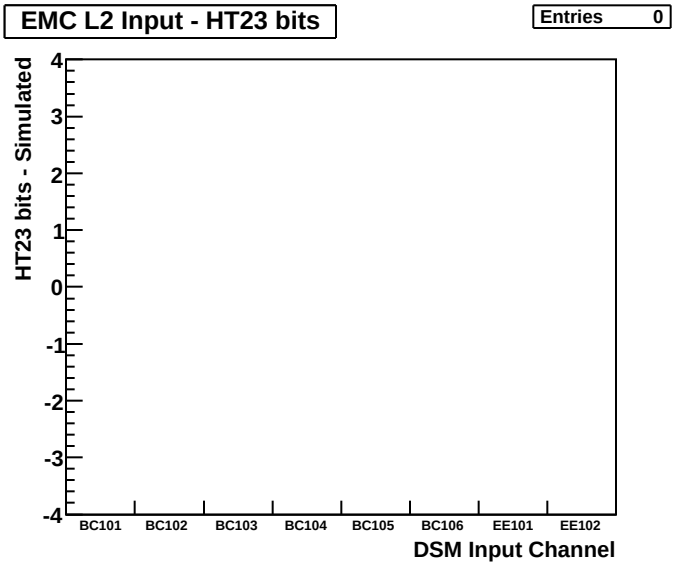

EMC L2 Input - Partial JP Sum
Entries 3385736
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

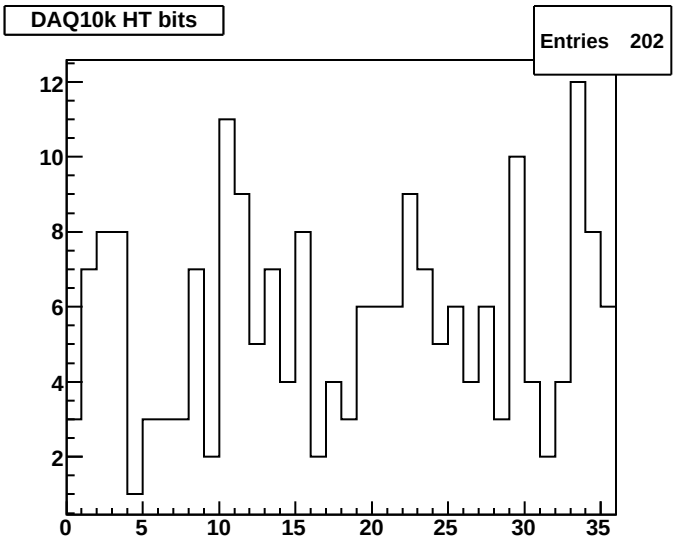

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

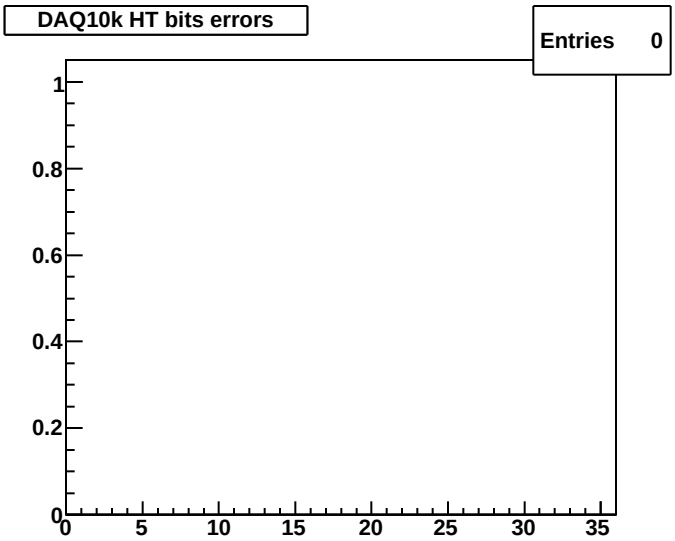

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3385736
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

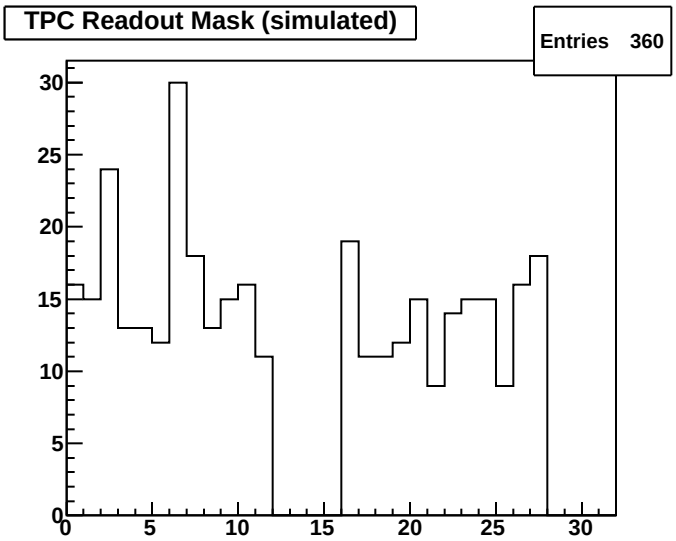

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

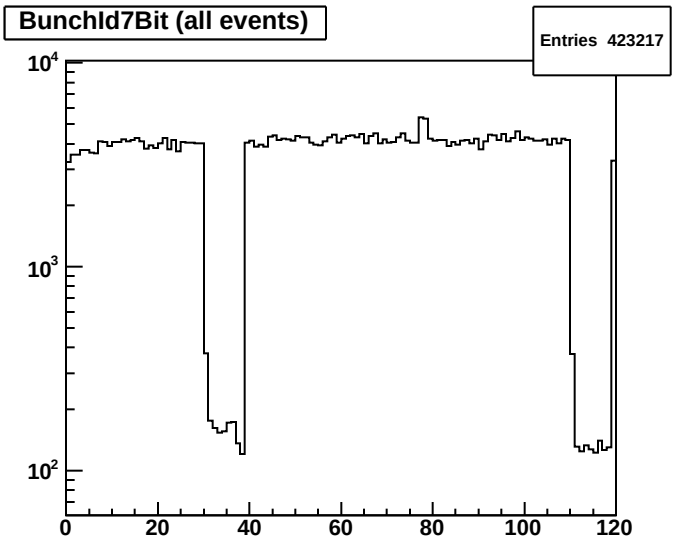

EMC L2 Input - HT23 bits
Entries 3385736
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

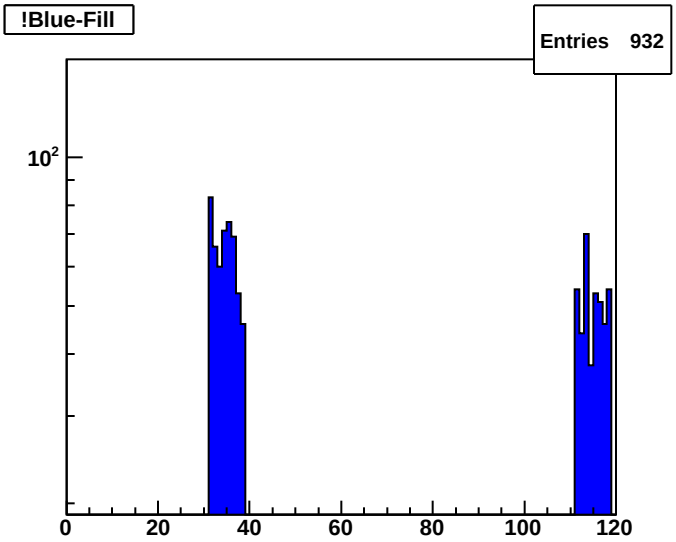

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

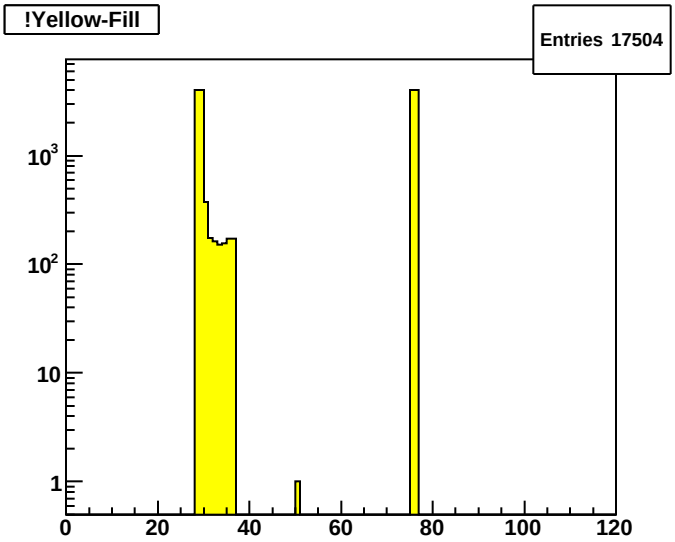

DAQ10k HT bits
Entries 202

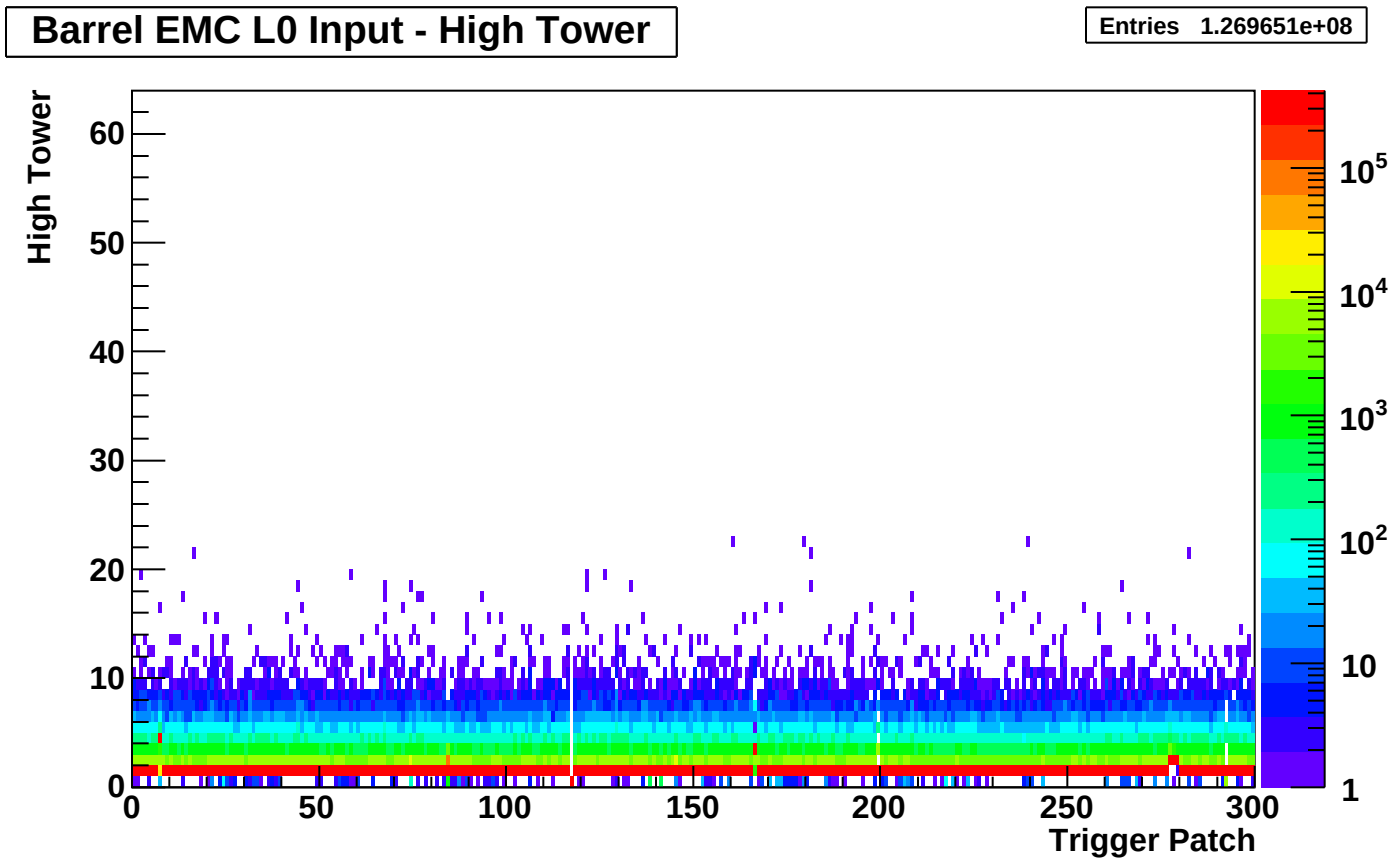

DAQ10k HT bits errors
Entries 0

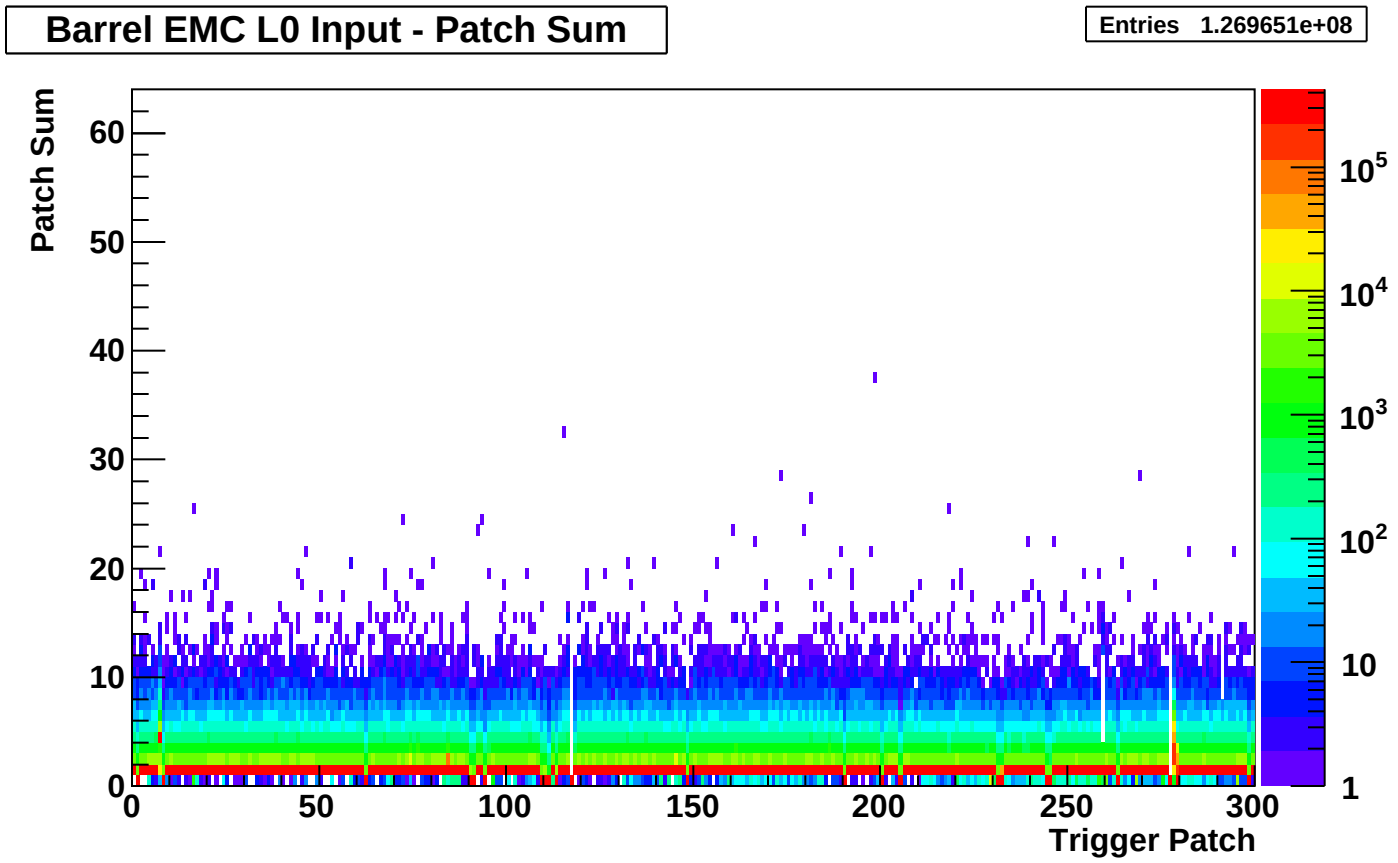

TPC Readout Mask (simulated)
Entries 360

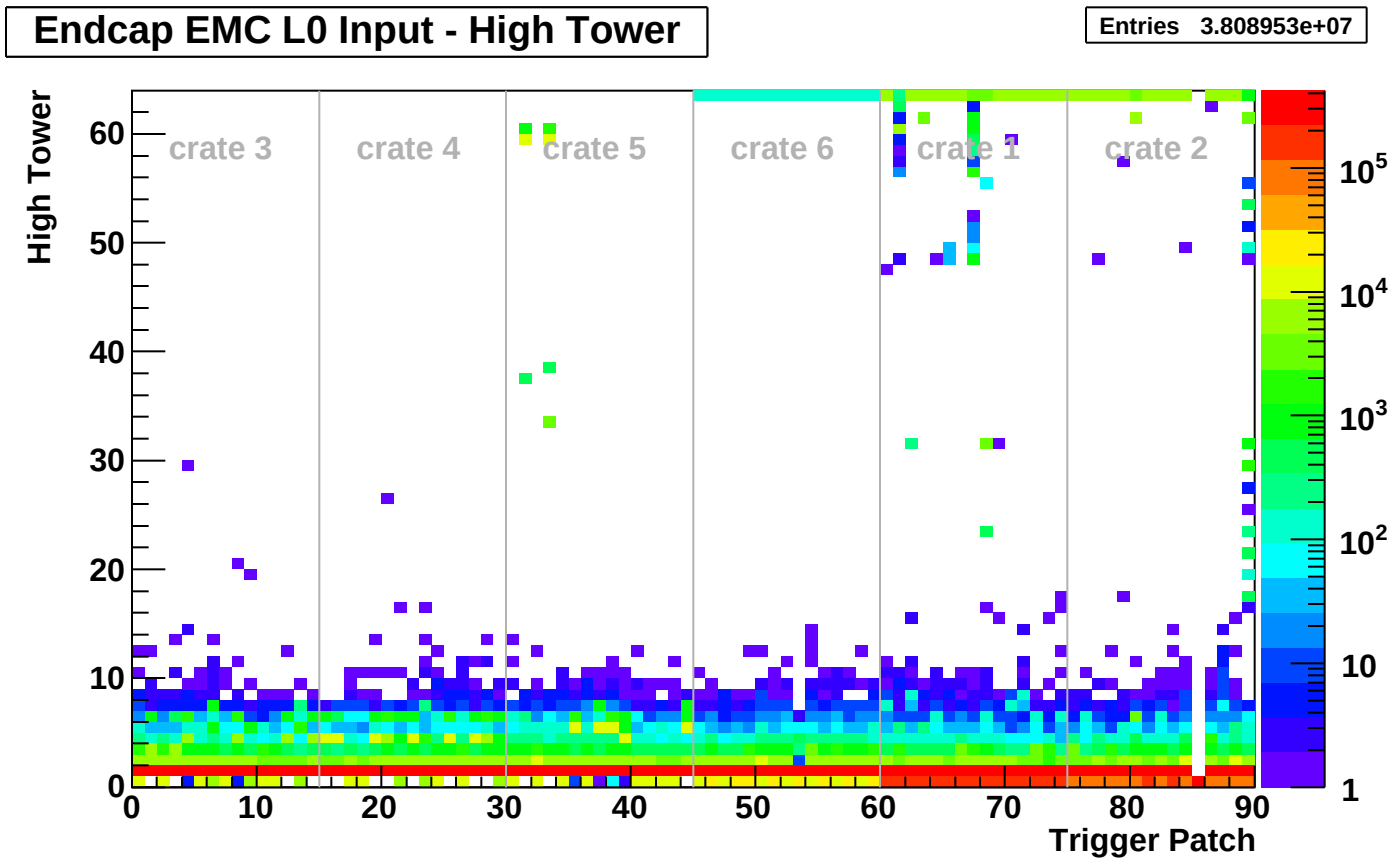

BunchId7Bit (all events)
Entries 423217

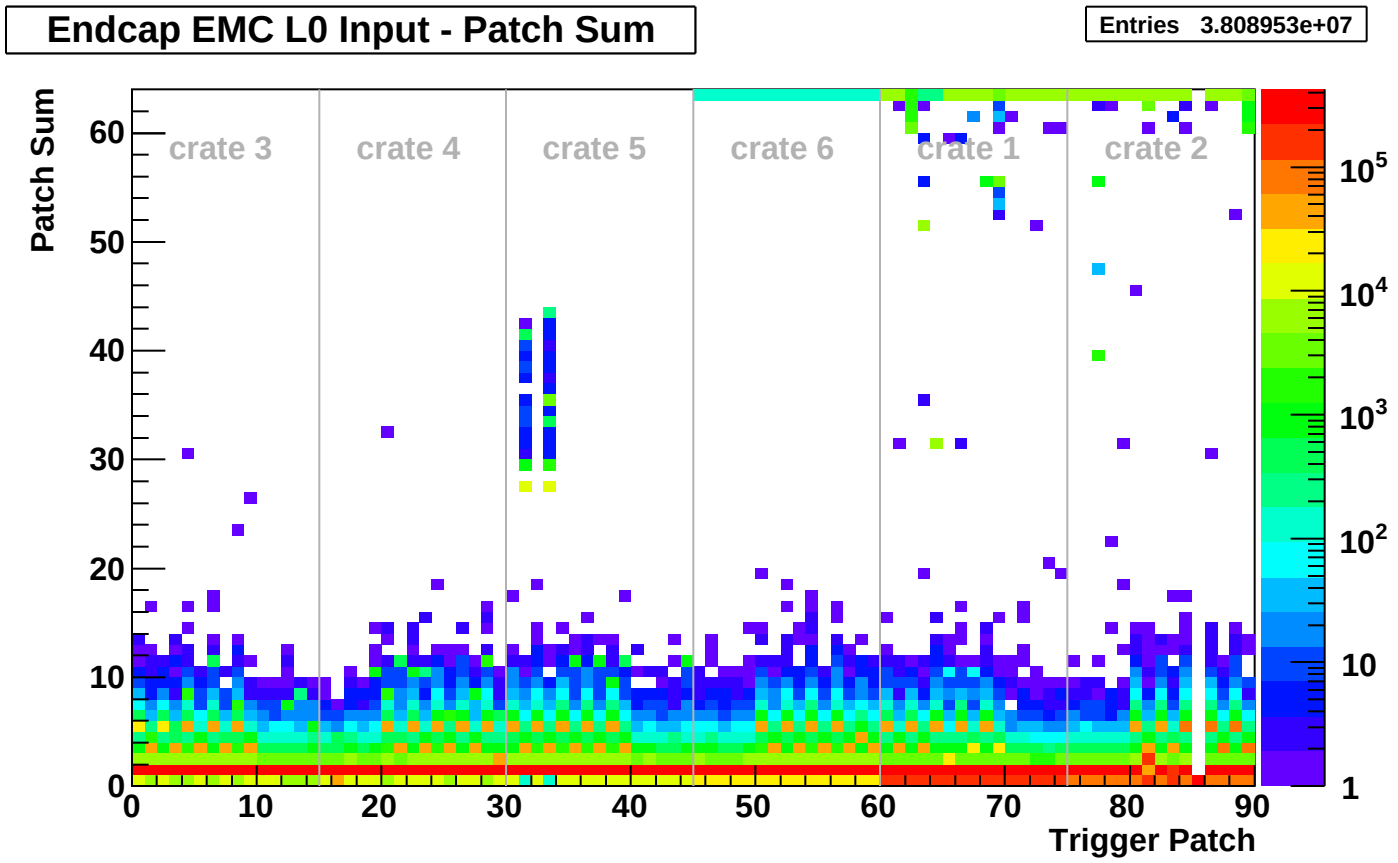

!Blue-Fill
Entries 932

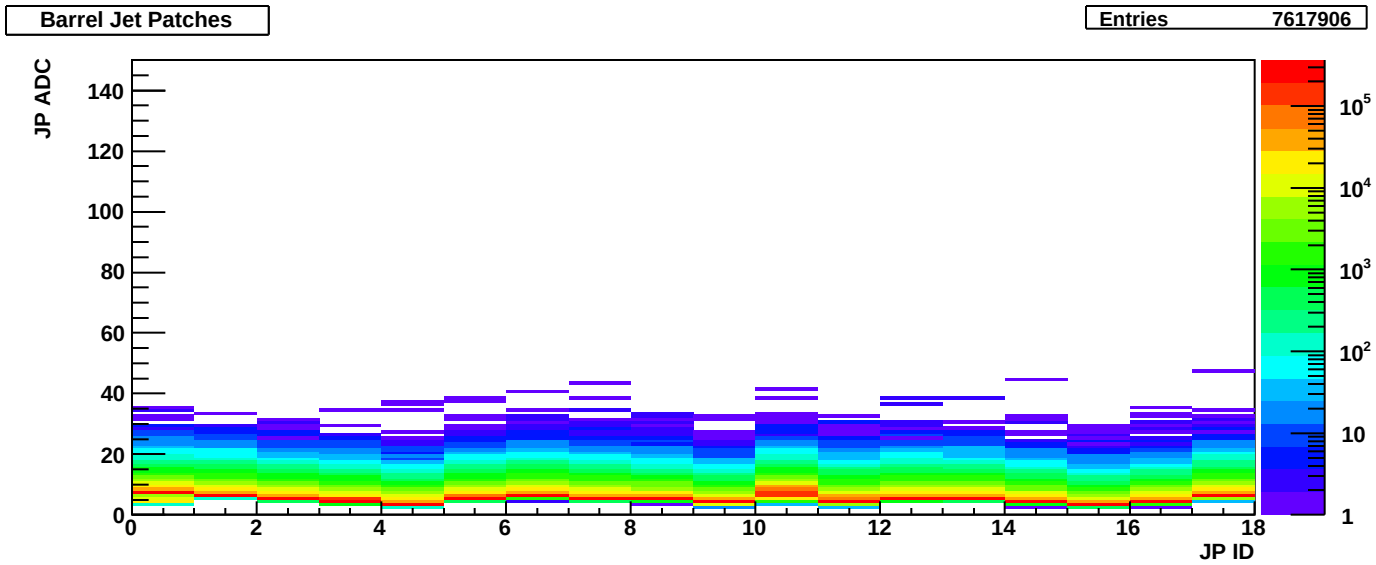

!Yellow-Fill
Entries 17504

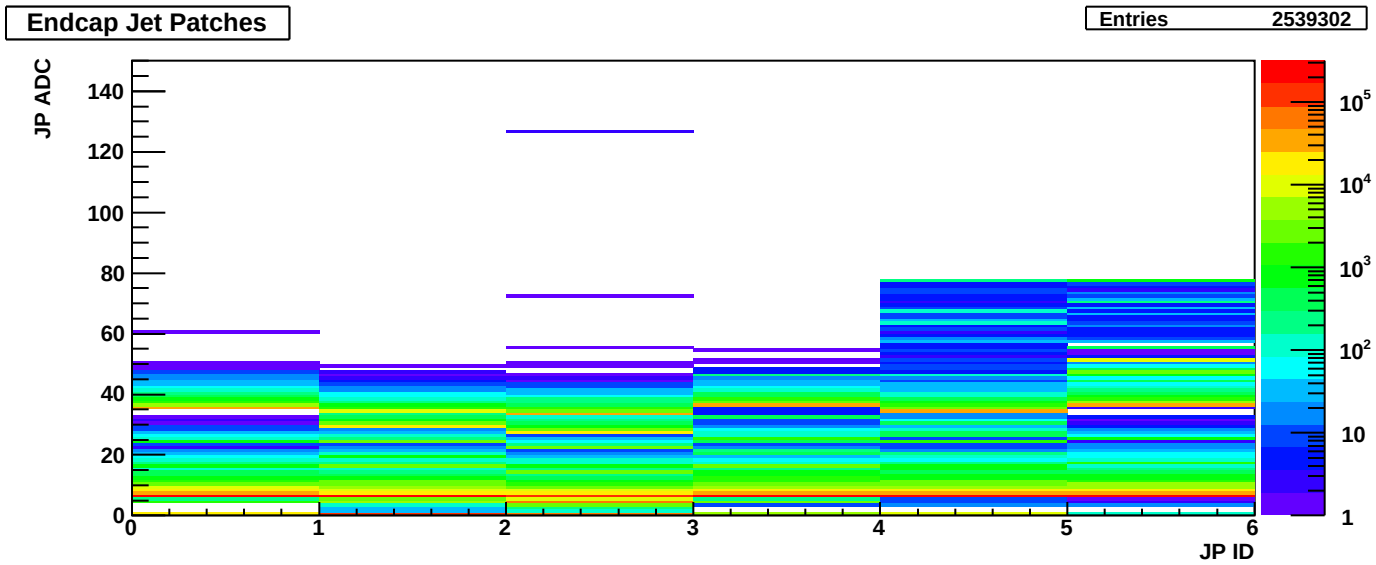

Barrel EMC L0 Input - High Tower
Entries 1.269651e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

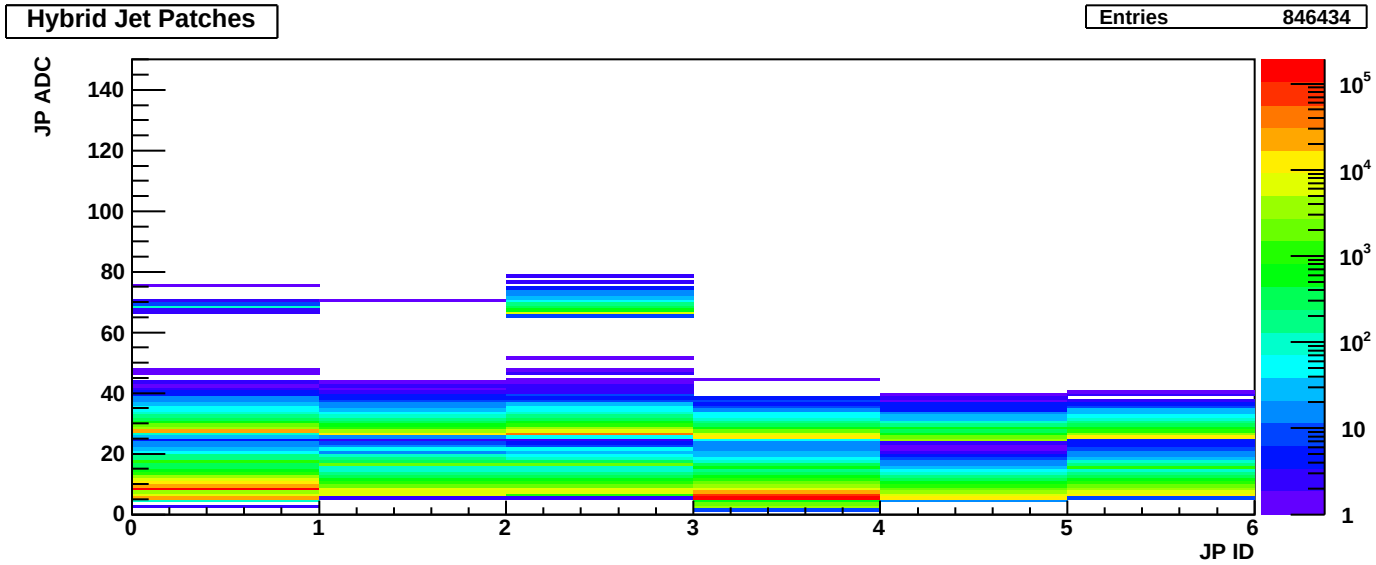

Barrel EMC L0 Input - Patch Sum
Entries 1.269651e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

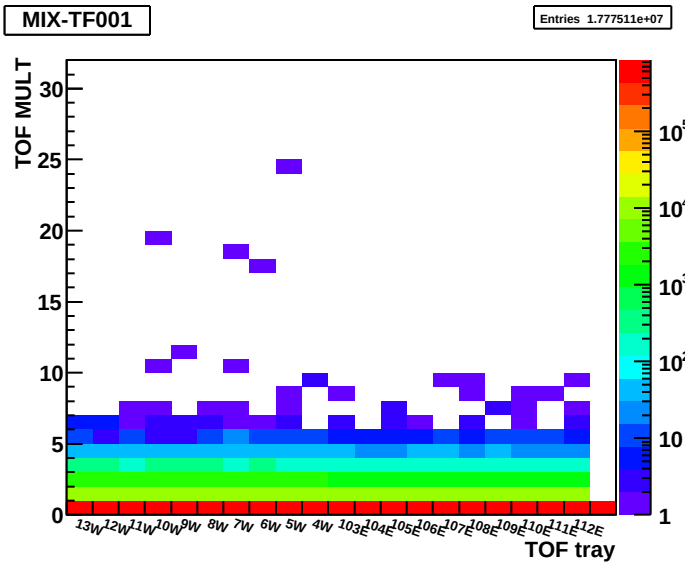

Endcap EMC L0 Input - High Tower
Entries 3.808953e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

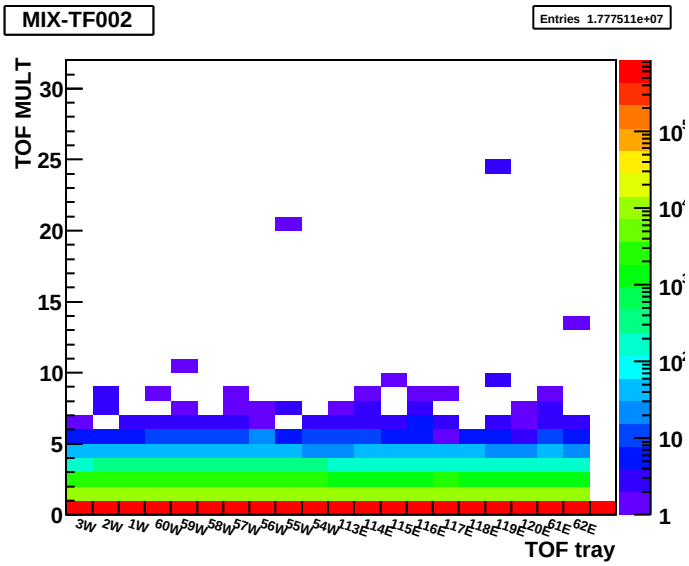

Endcap EMC L0 Input - Patch Sum
Entries 3.808953e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

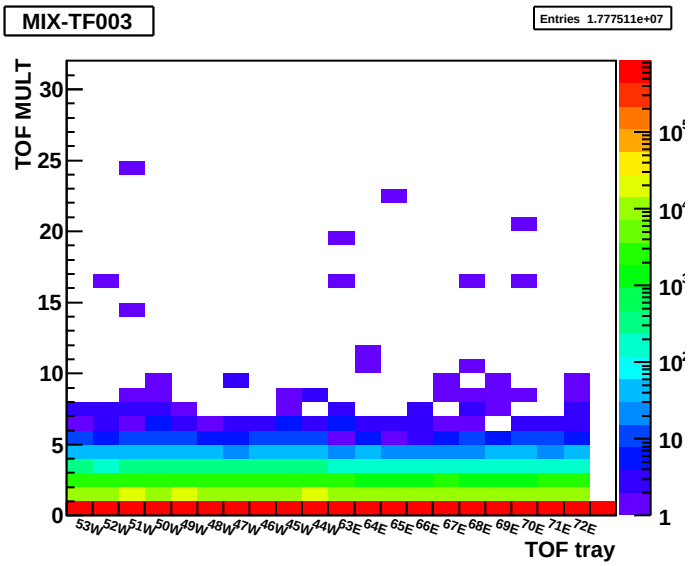

Barrel Jet Patches
Entries 7617906
JP ADC
JP ID

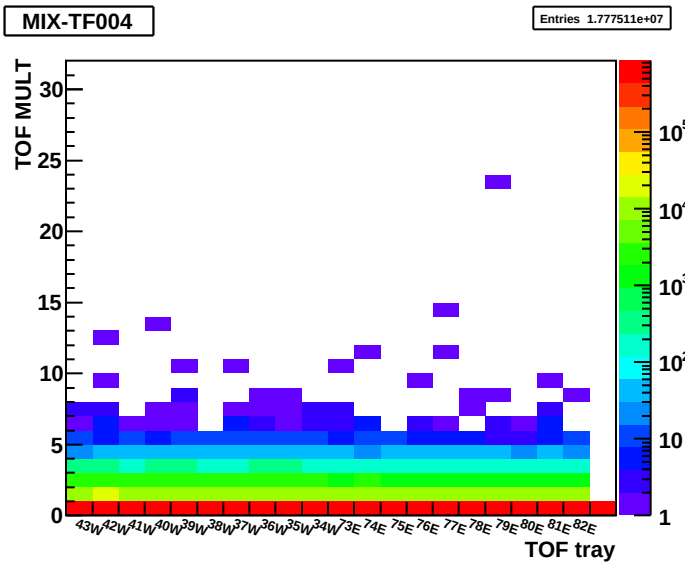

Endcap Jet Patches
Entries 2539302
JP ADC
JP ID

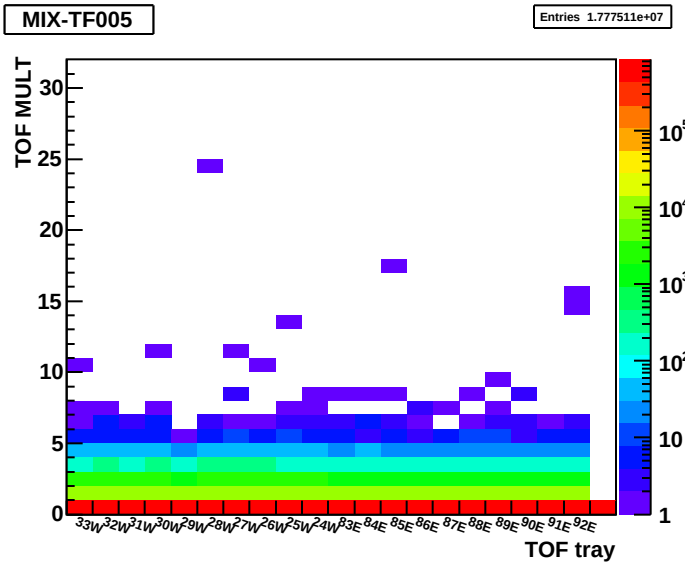

Hybrid Jet Patches
Entries 846434
JP ADC
JP ID

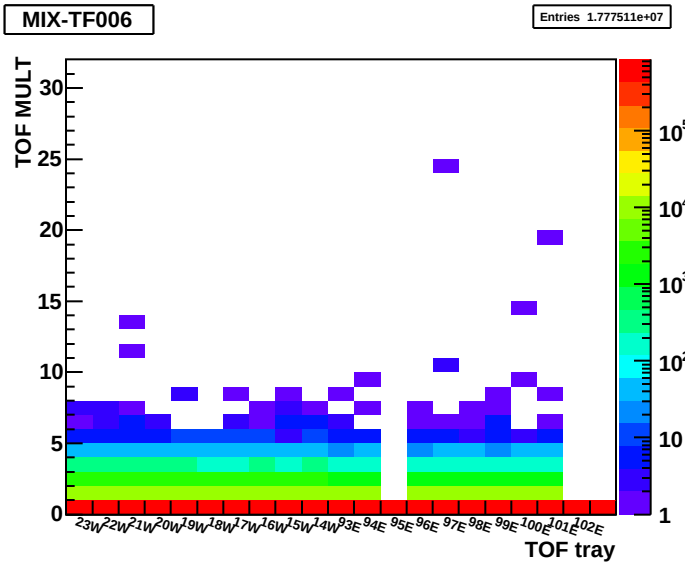

MIX-TF001
Entries 1.777511e+07
TOF MULT
TOF tray

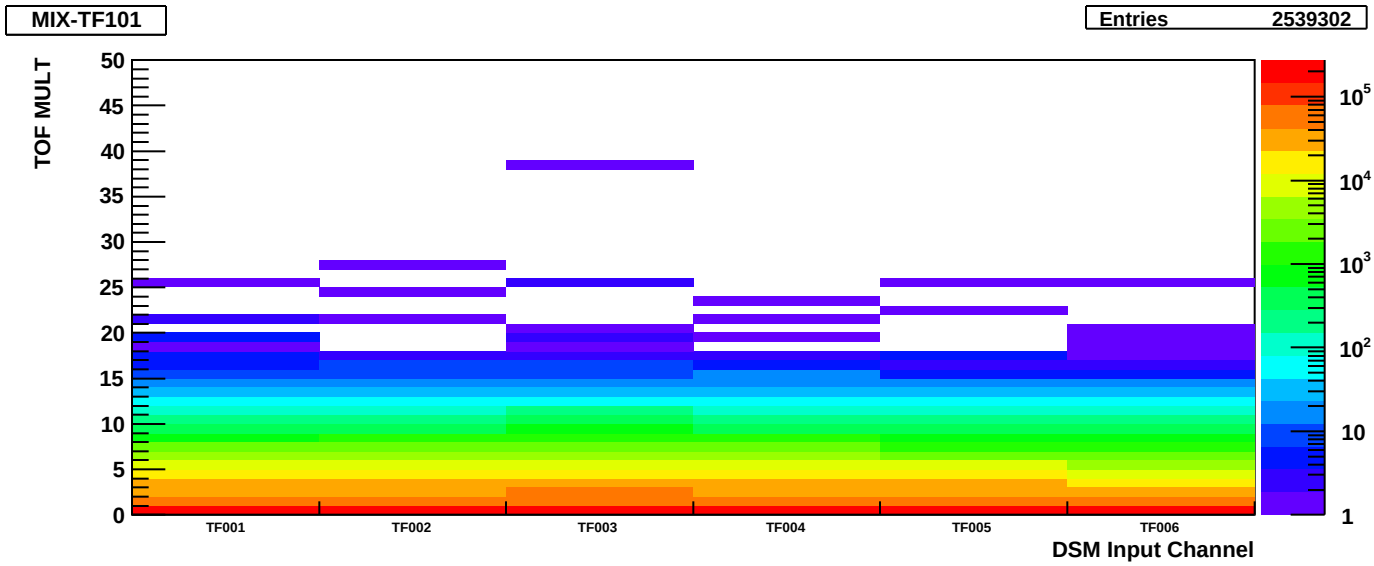

MIX-TF002
Entries 1.777511e+07
TOF MULT
TOF tray

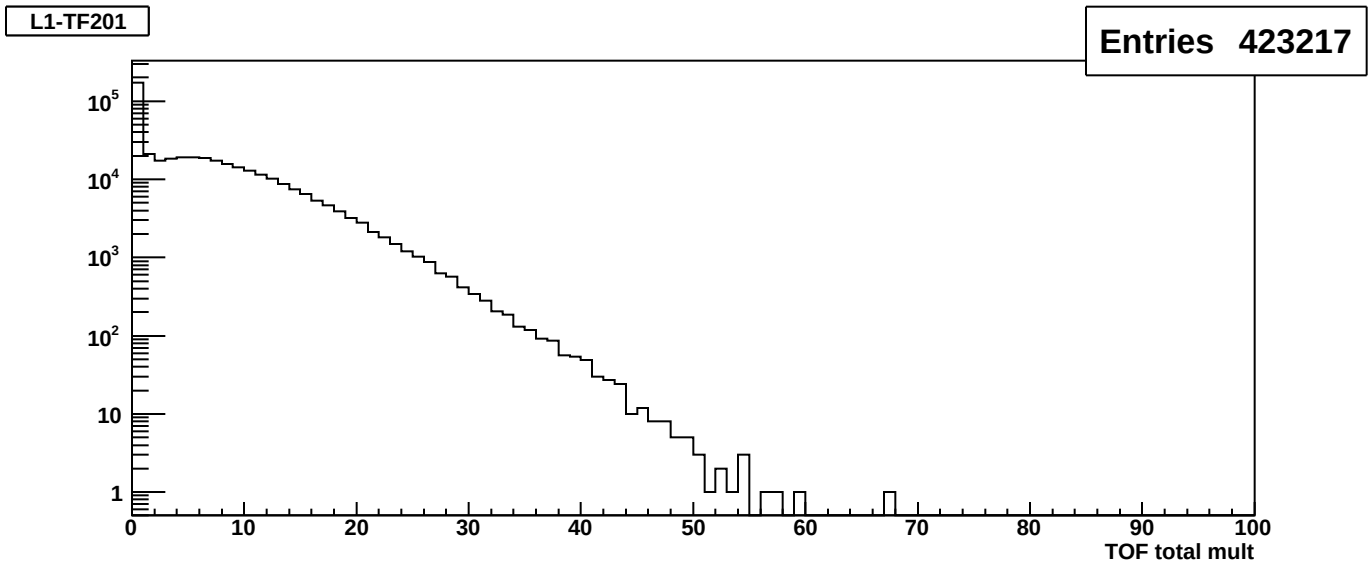

MIX-TF003
Entries 1.777511e+07
TOF MULT
TOF tray

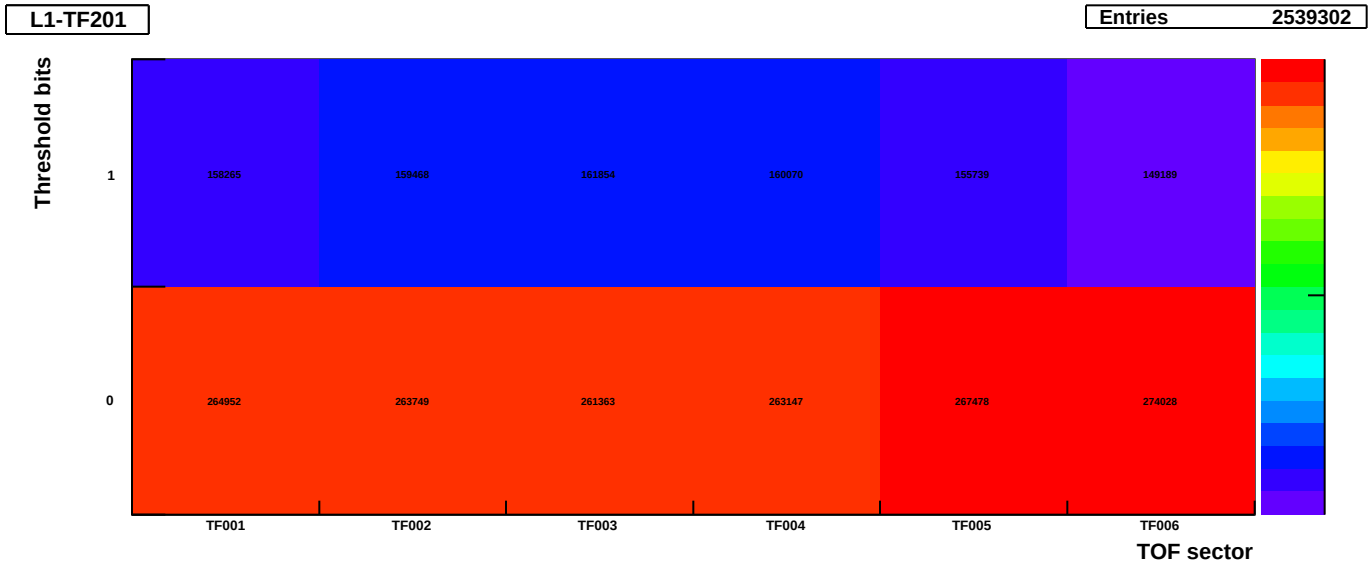

MIX-TF004
Entries 1.777511e+07
TOF MULT
TOF tray

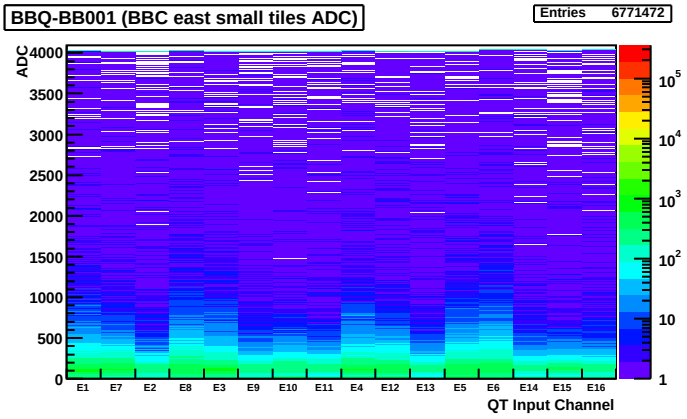

MIX-TF005
Entries 1.777511e+07
TOF MULT
TOF tray

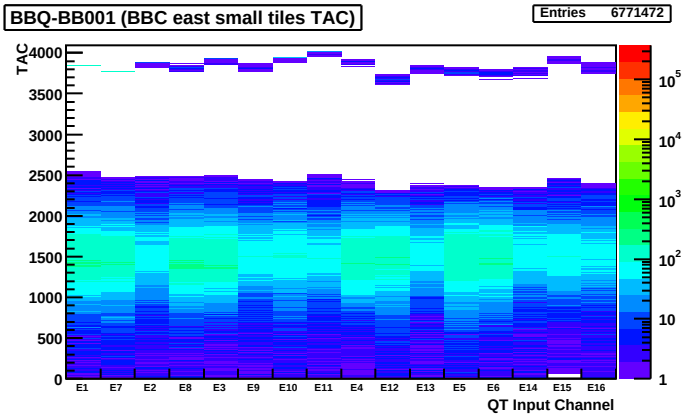

MIX-TF006
Entries 1.777511e+07
TOF MULT
TOF tray

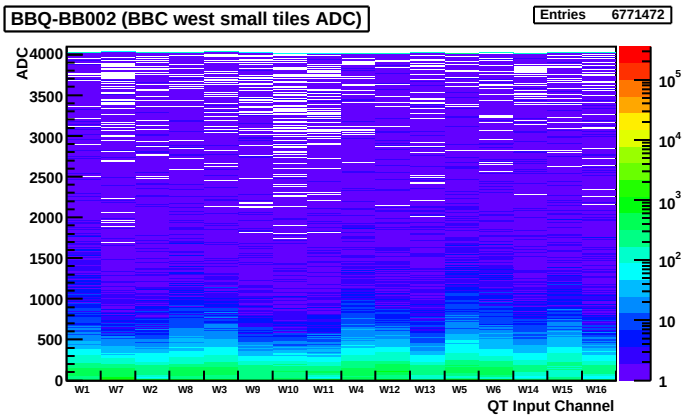

MIX-TF101
Entries 2539302
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

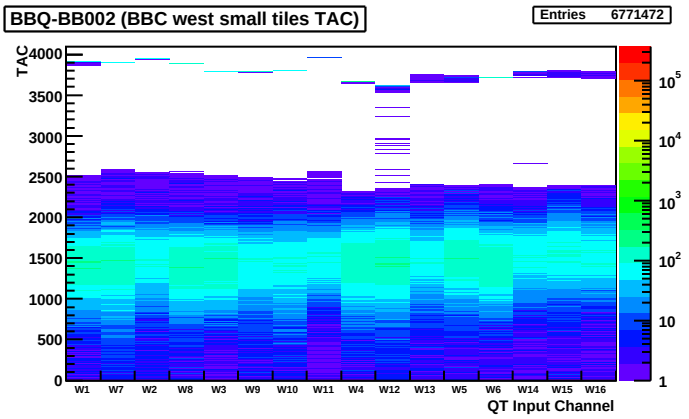

L1-TF201
Entries 423217
TOF total mult

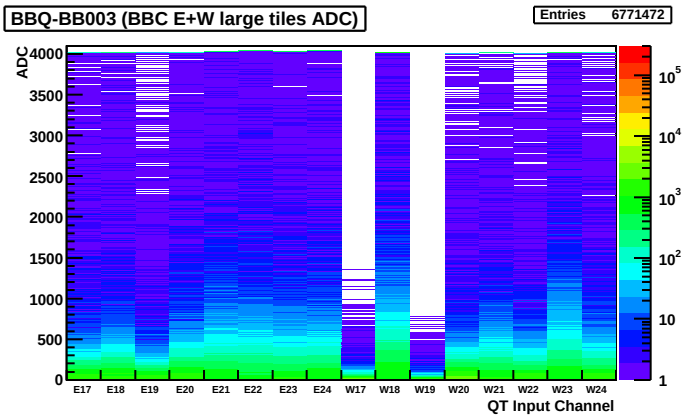

L1-TF201
Entries 2539302
Threshold bits
158265
159468
161854
160070
155739
149189
264952
263749
261363
263147
267478
274028
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

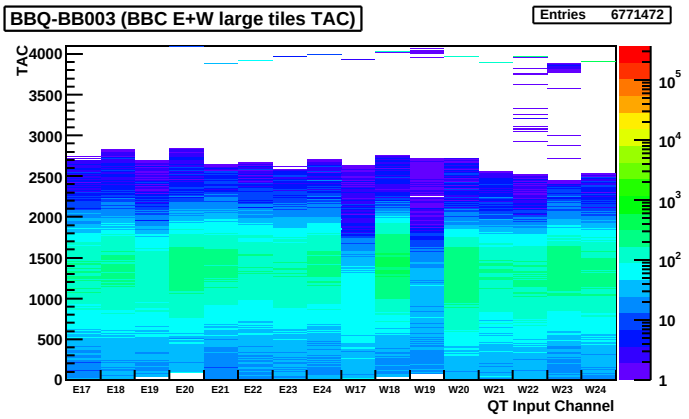

BBQ-BB001 (BBC east small tiles ADC)
Entries 6771472
ADC
QT Input Channel

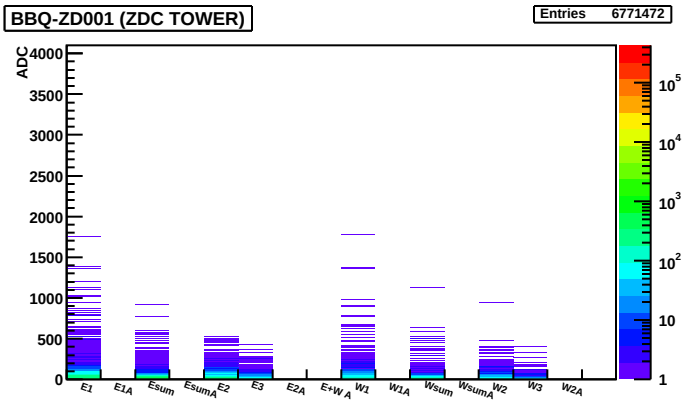

BBQ-BB001 (BBC east small tiles TAC)
Entries 6771472
TAC
QT Input Channel

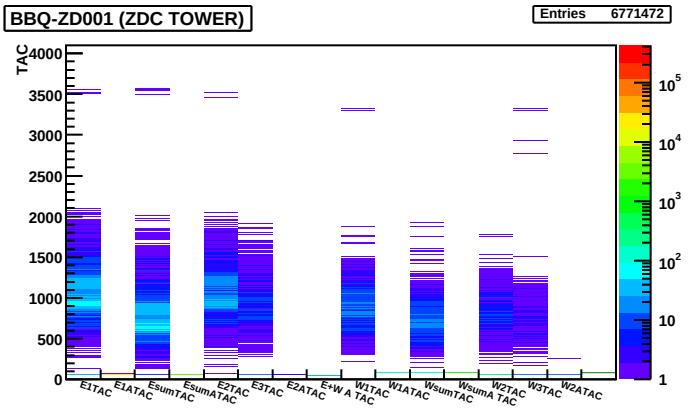

BBQ-BB002 (BBC west small tiles ADC)
Entries 6771472
ADC
QT Input Channel

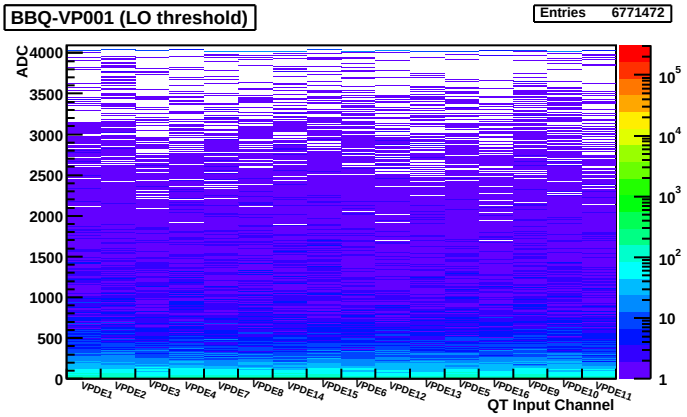

BBQ-BB002 (BBC west small tiles TAC)
Entries 6771472
TAC
QT Input Channel

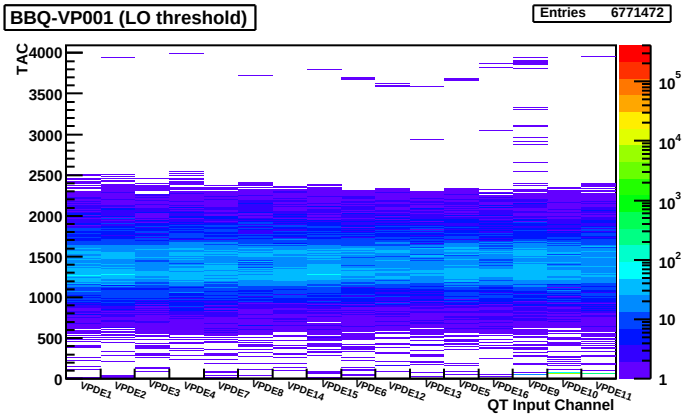

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6771472
ADC
QT Input Channel

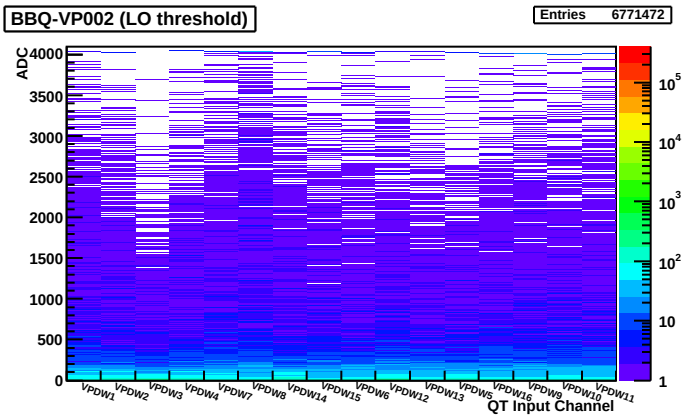

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6771472
TAC
QT Input Channel

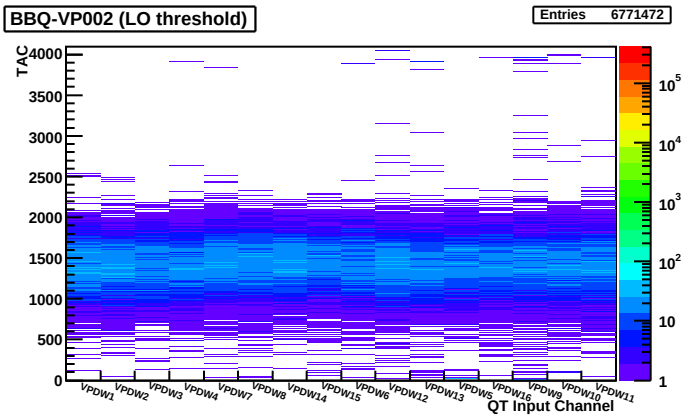

BBQ-ZD001 (ZDC TOWER)
Entries 6771472
ADC

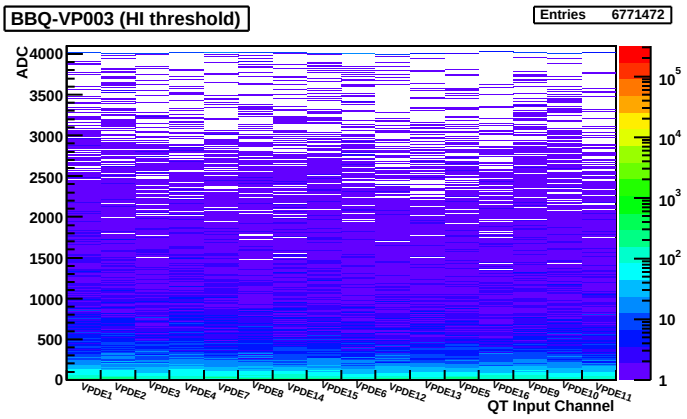

BBQ-ZD001 (ZDC TOWER)
Entries 6771472
TAC

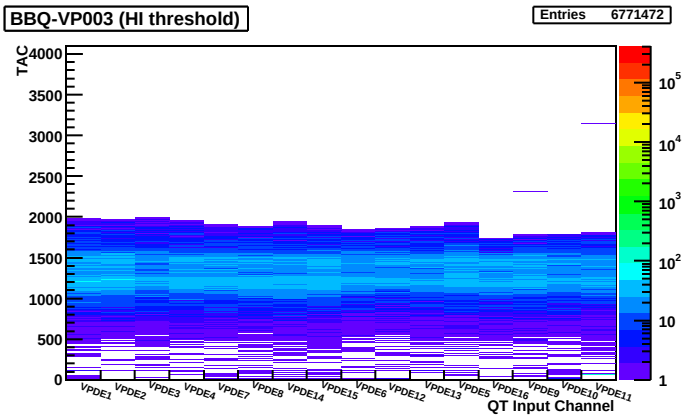

BBQ-VP001 (LO threshold)
Entries 6771472
ADC
QT Input Channel

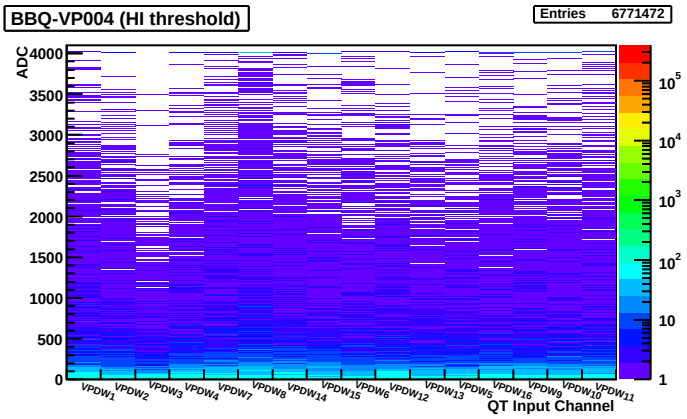

BBQ-VP001 (LO threshold)
Entries 6771472
TAC
QT Input Channel

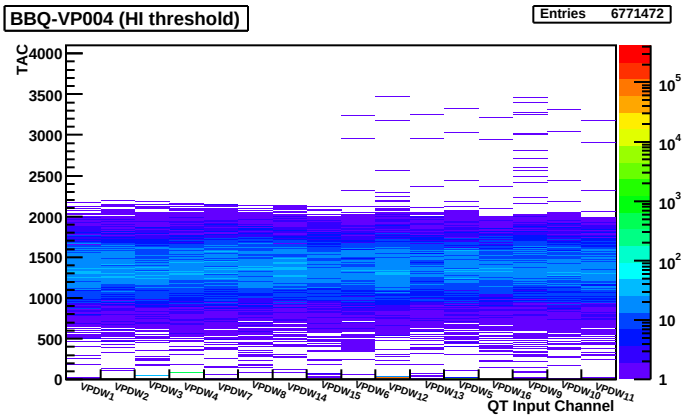

BBQ-VP002 (LO threshold)
Entries 6771472
ADC
QT Input Channel

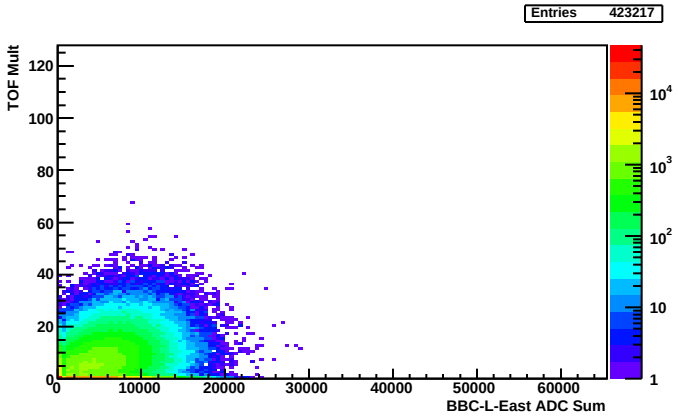

BBQ-VP002 (LO threshold)
Entries 6771472
TAC
QT Input Channel

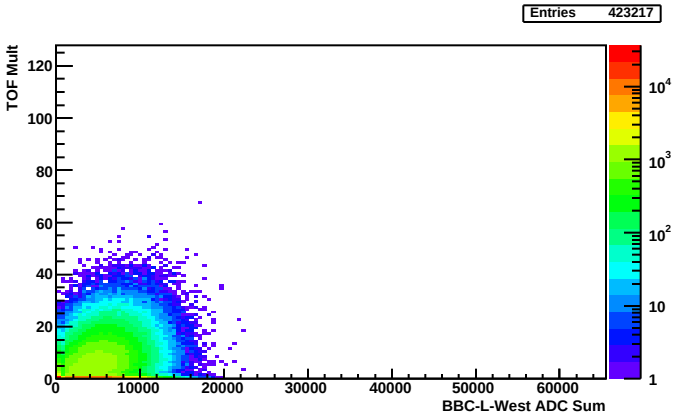

BBQ-VP003 (HI threshold)
Entries 6771472
ADC
QT Input Channel

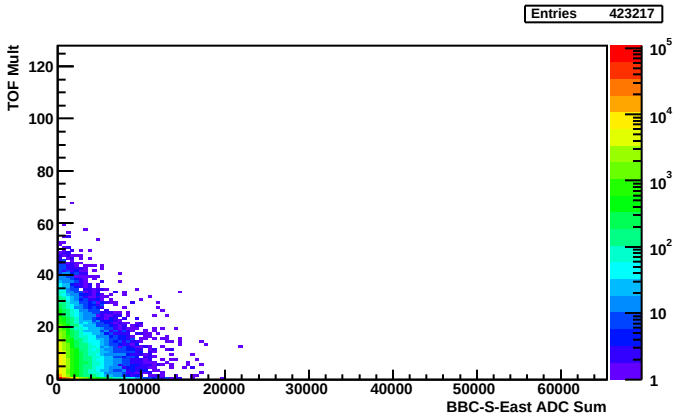

BBQ-VP003 (HI threshold)
Entries 6771472
TAC
QT Input Channel

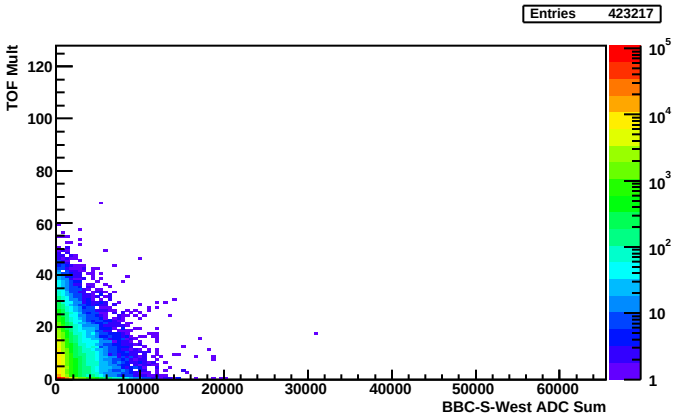

BBQ-VP004 (HI threshold)
Entries 6771472
ADC
QT Input Channel

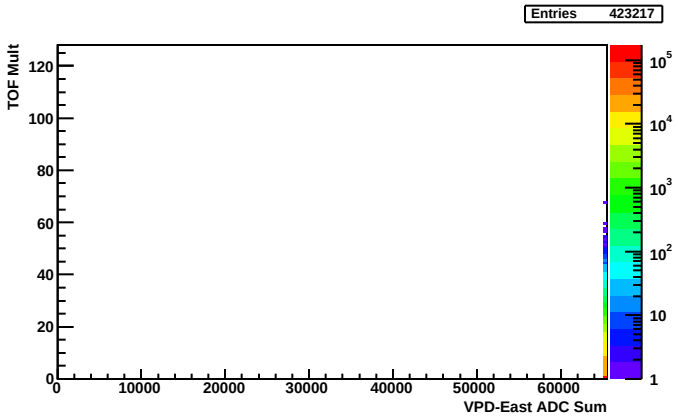

BBQ-VP004 (HI threshold)
Entries 6771472
TAC
QT Input Channel

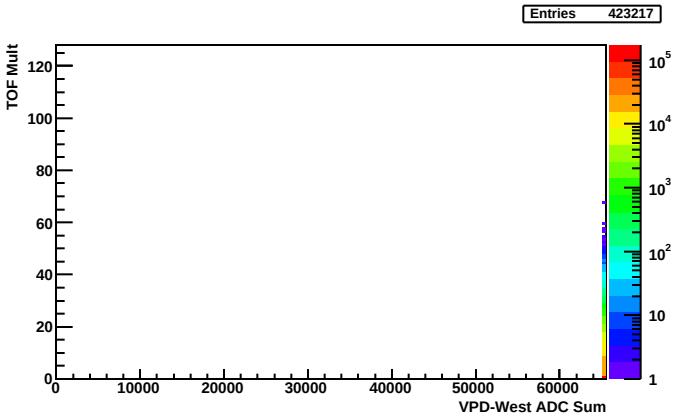

Entries 423217
TOF Mult
BBC-L-East ADC Sum

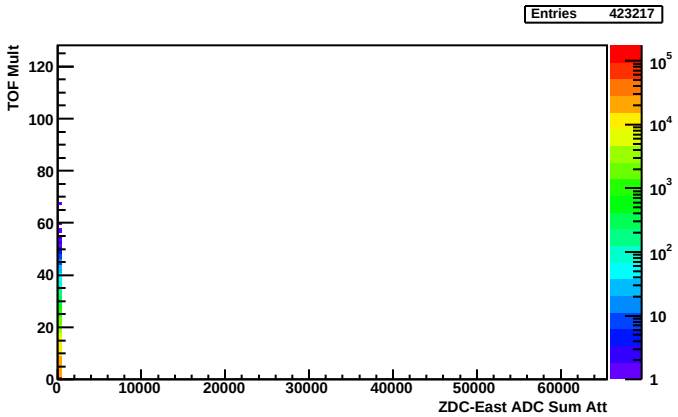

Entries 423217
TOF Mult
BBC-L-West ADC Sum

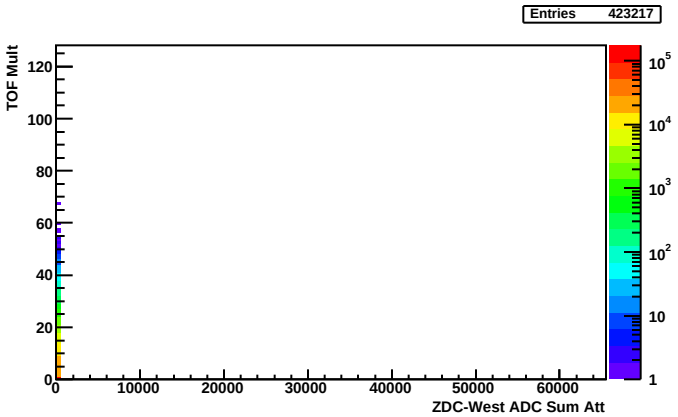

Entries 423217
TOF Mult
BBC-S-East ADC Sum

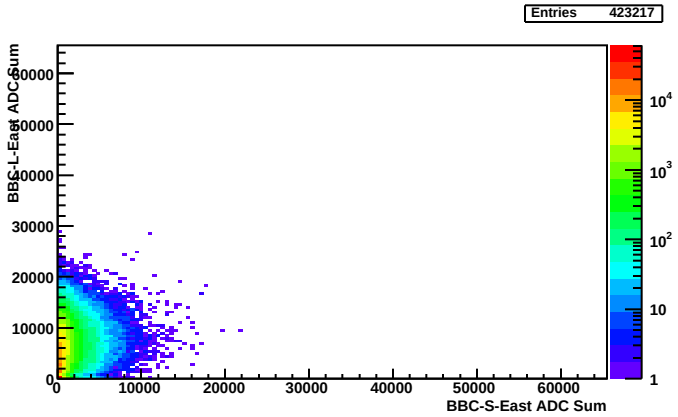

Entries 423217
TOF Mult
BBC-S-West ADC Sum

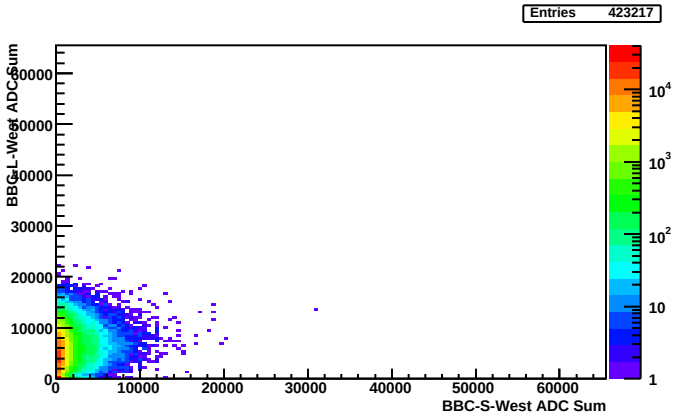

Entries 423217
TOF Mult
VPD-East ADC Sum

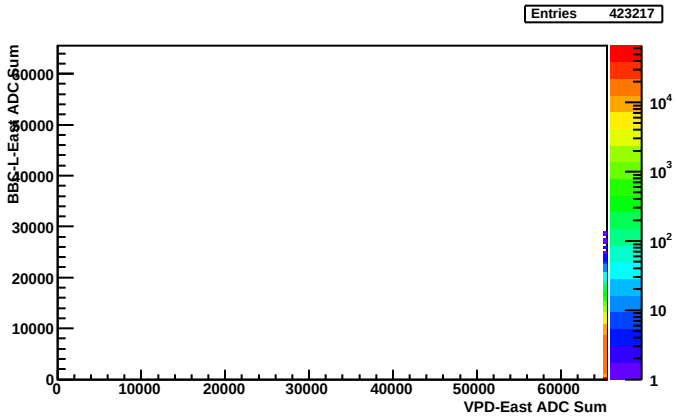

Entries 423217
TOF Mult
VPD-West ADC Sum

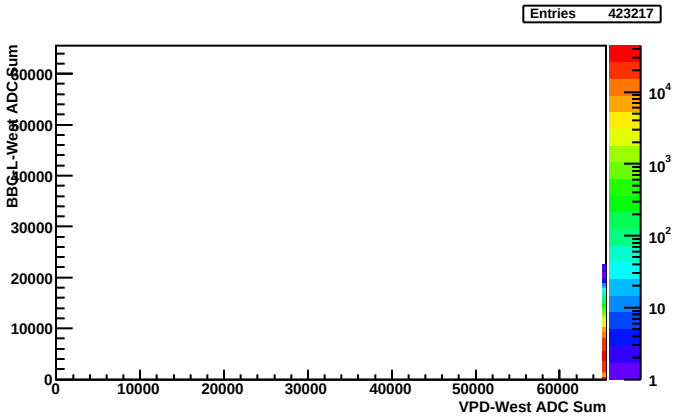

Entries 423217
TOF Mult
ZDC-East ADC Sum Att

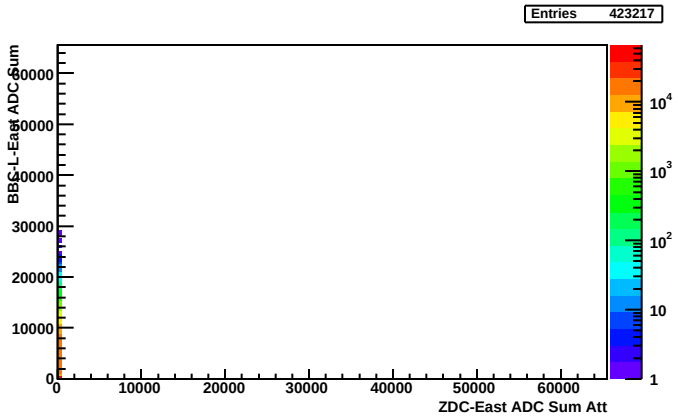

Entries 423217
TOF Mult
ZDC-West ADC Sum Att

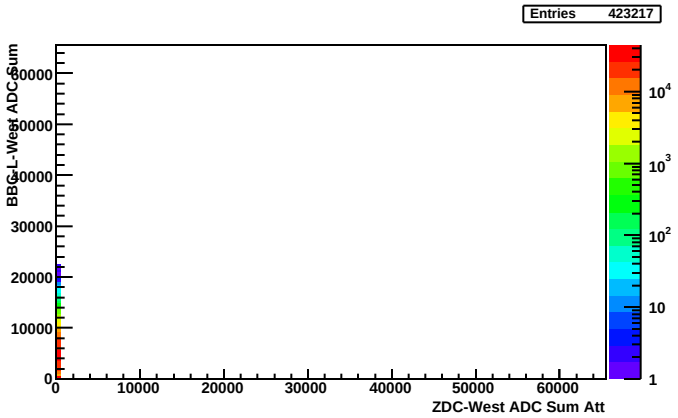

Entries 423217
BBC-L-East ADC Sum
BBC-S-East ADC Sum

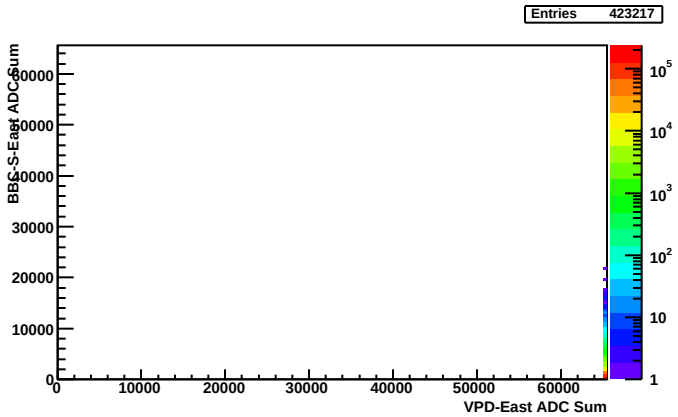

Entries 423217
BBC-L-West ADC Sum
BBC-S-West ADC Sum

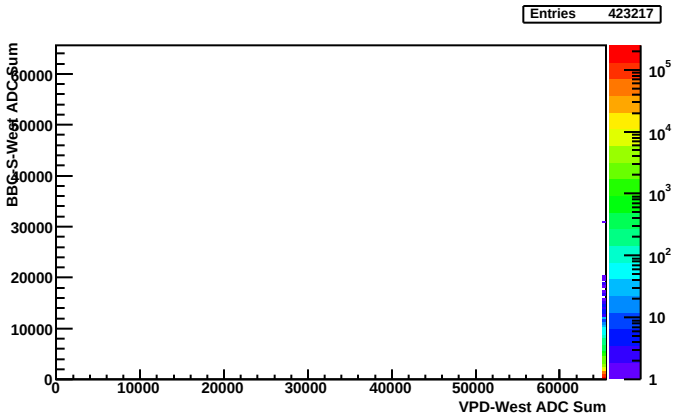

Entries 423217
BBC-L-East ADC Sum
VPD-East ADC Sum

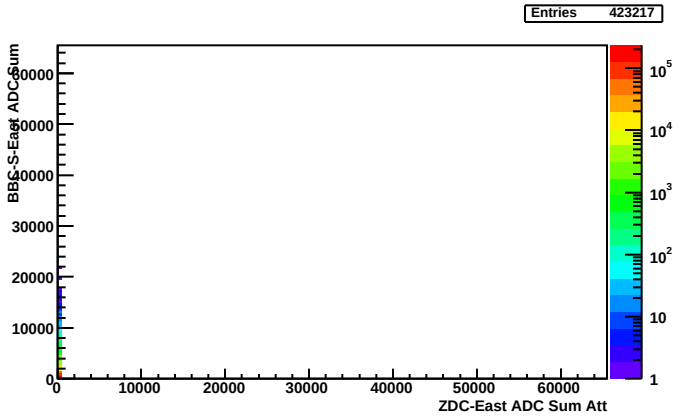

Entries 423217
BBC-L-West ADC Sum
VPD-West ADC Sum

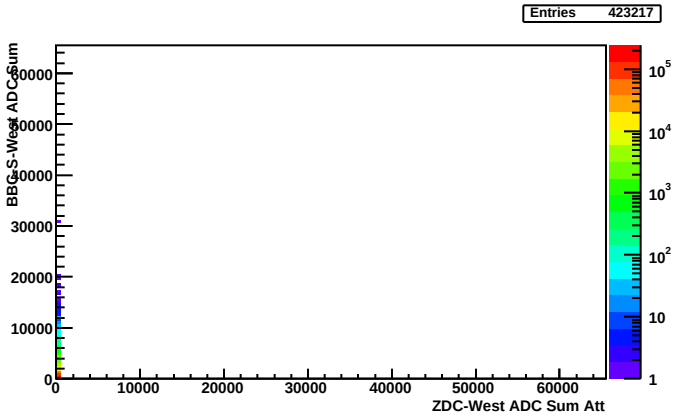

Entries 423217
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

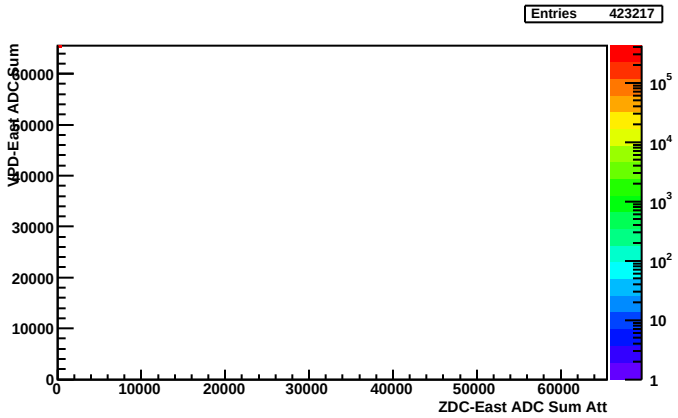

Entries 423217
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

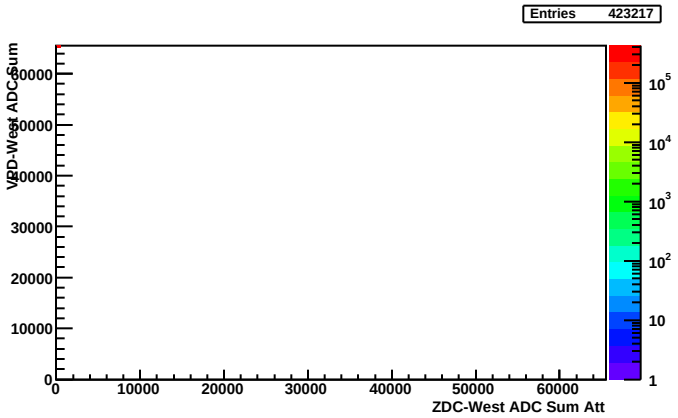

Entries 423217
BBC-S-East ADC Sum
VPD-East ADC Sum

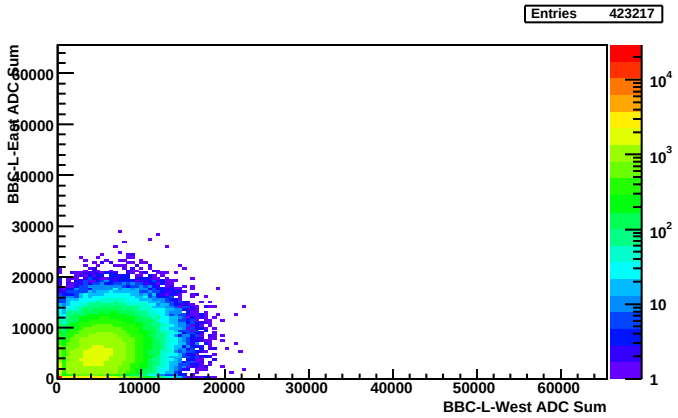

Entries 423217
BBC-S-West ADC Sum
VPD-West ADC Sum

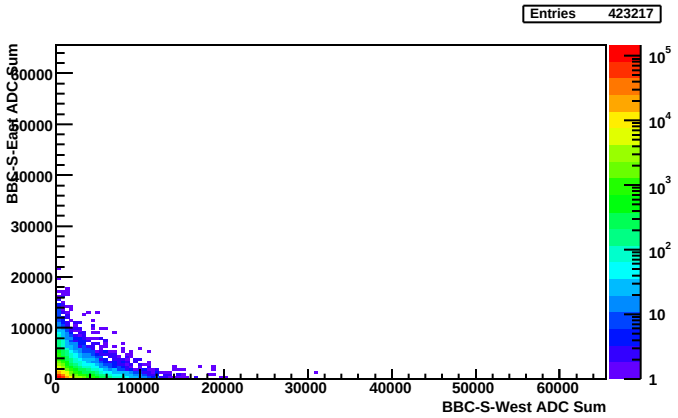

Entries 423217
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

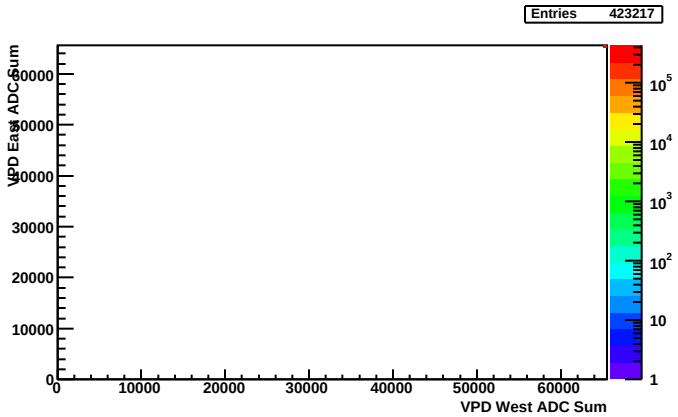

Entries 423217
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

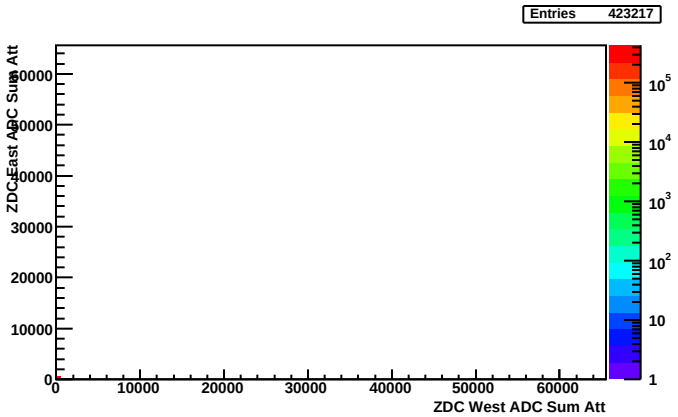

Entries 423217
VPD-East ADC Sum
ZDC-East ADC Sum Att

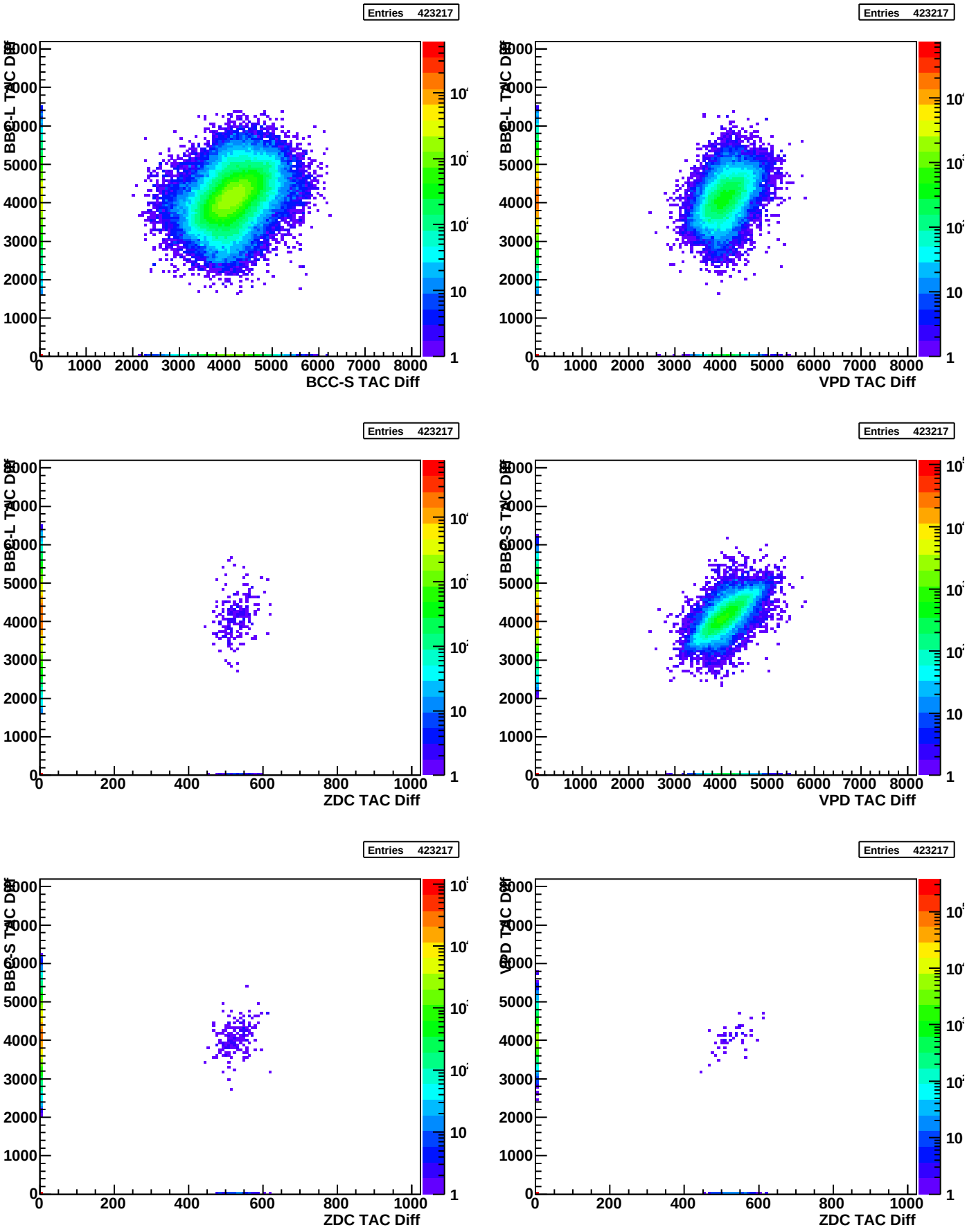

Entries 423217
VPD-West ADC Sum
ZDC-West ADC Sum Att

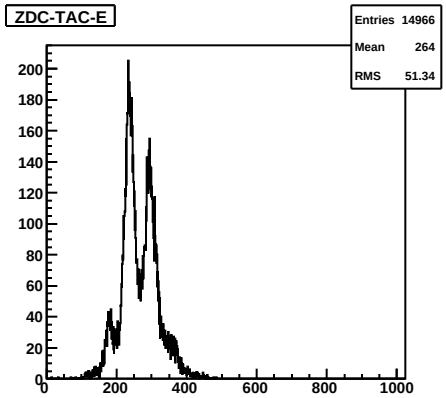

Entries 423217
BBC-L-East ADC Sum
BBC-L-West ADC Sum

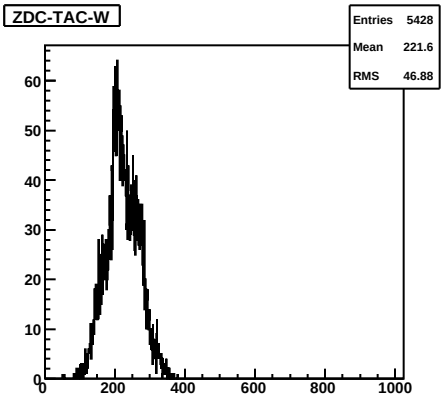

Entries 423217
BBC-S-East ADC Sum
BBC-S-West ADC Sum

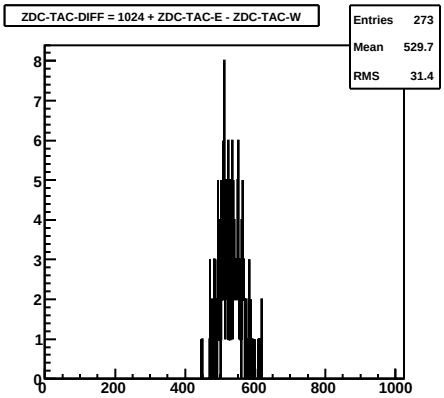

Entries 423217
VPD East ADC Sum
VPD West ADC Sum

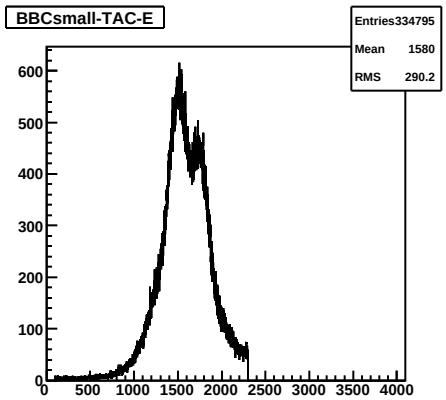

Entries 423217
ZDC East ADC Sum Att
ZDC West ADC Sum Att

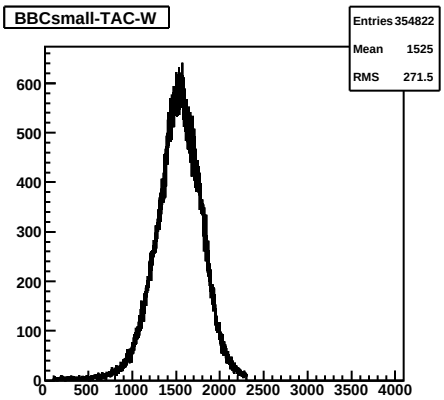

Entries 423217
BBC-L TAC Diff
BCC-S TAC Diff
Entries 423217
BBC-L TAC Diff
VPD TAC Diff
Entries 423217
BBC-L TAC Diff
ZDC TAC Diff
Entries 423217
BBC-S TAC Diff
VPD TAC Diff
Entries 423217
BBC-S TAC Diff
ZDC TAC Diff
Entries 423217
VPD TAC Diff
ZDC TAC Diff

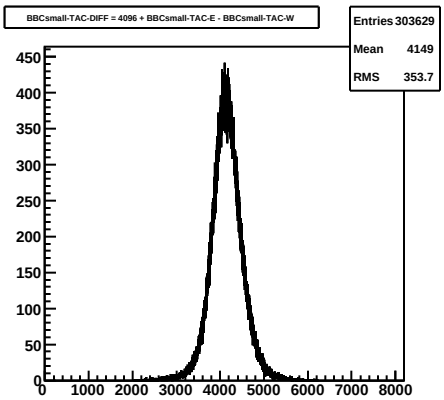

ZDC-TAC-E
Entries 14966
Mean 264
RMS 51.34

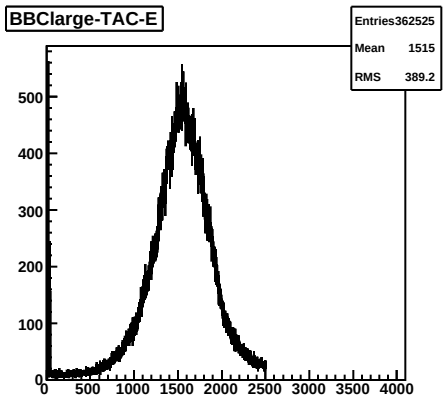

ZDC-TAC-W
Entries 5428
Mean 221.6
RMS 46.88

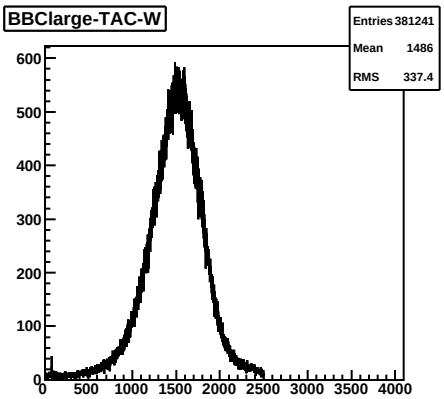

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 273
Mean 529.7
RMS 31.4

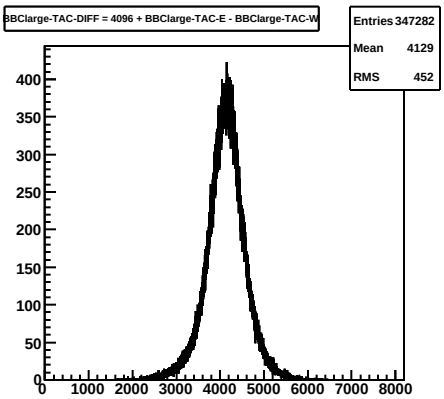

BBCsmall-TAC-E
Entries 334795
Mean 1580
RMS 290.2

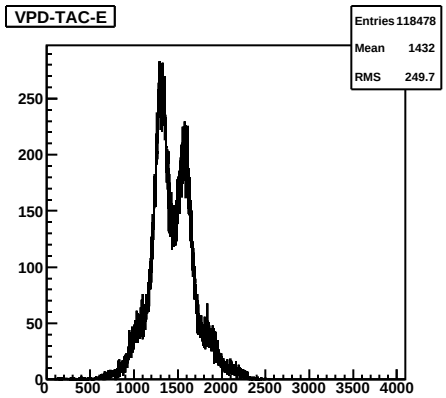

BBCsmall-TAC-W
Entries 354822
Mean 1525
RMS 271.5

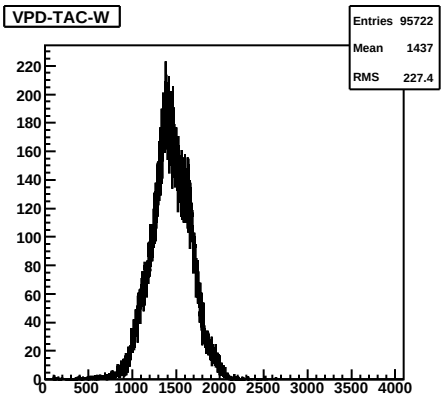

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 303629
Mean 4149
RMS 353.7

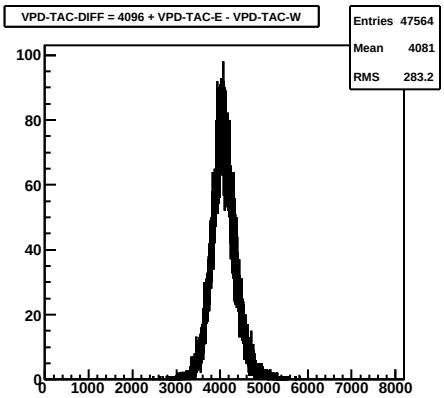

BBClarge-TAC-E
Entries 362525
Mean 1515
RMS 389.2

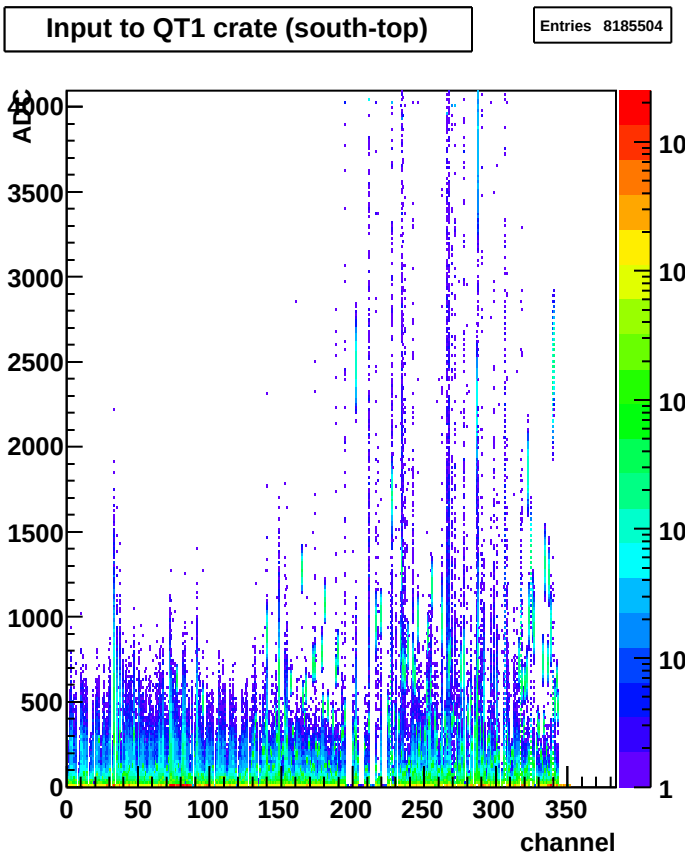

BBClarge-TAC-W
Entries 381241
Mean 1486
RMS 337.4

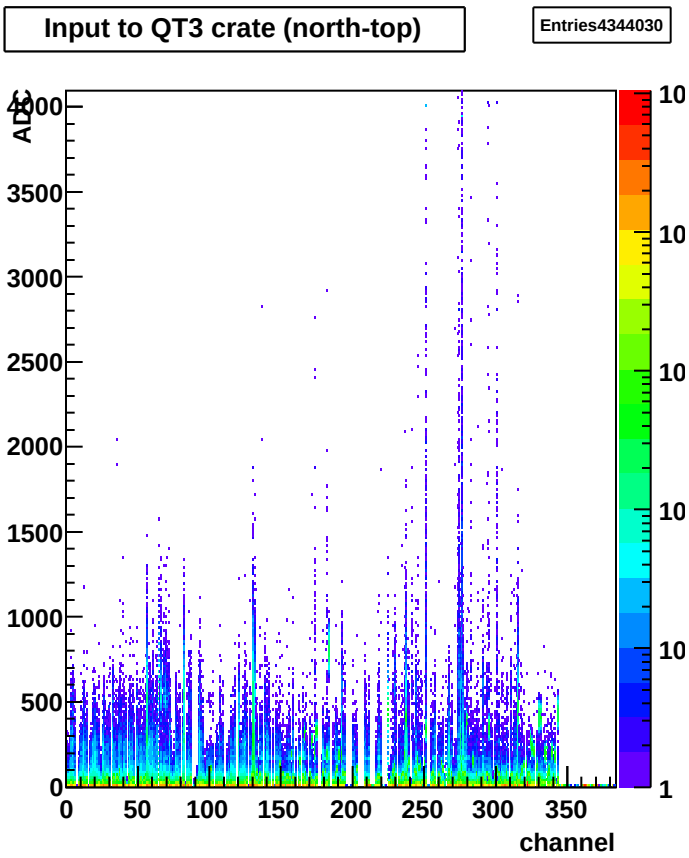

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 347282
Mean 4129
RMS 452

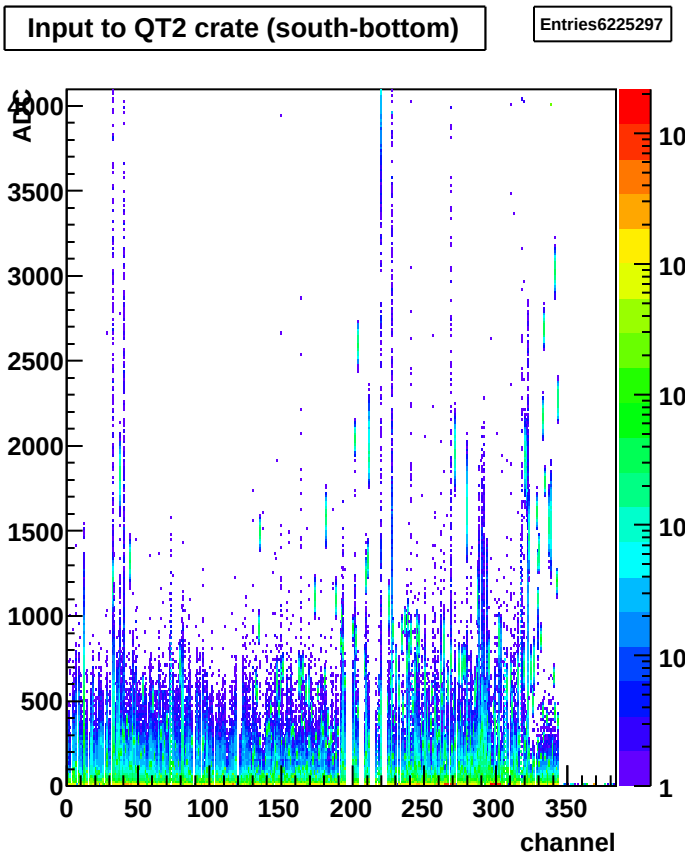

VPD-TAC-E
Entries 118478
Mean 1432
RMS 249.7

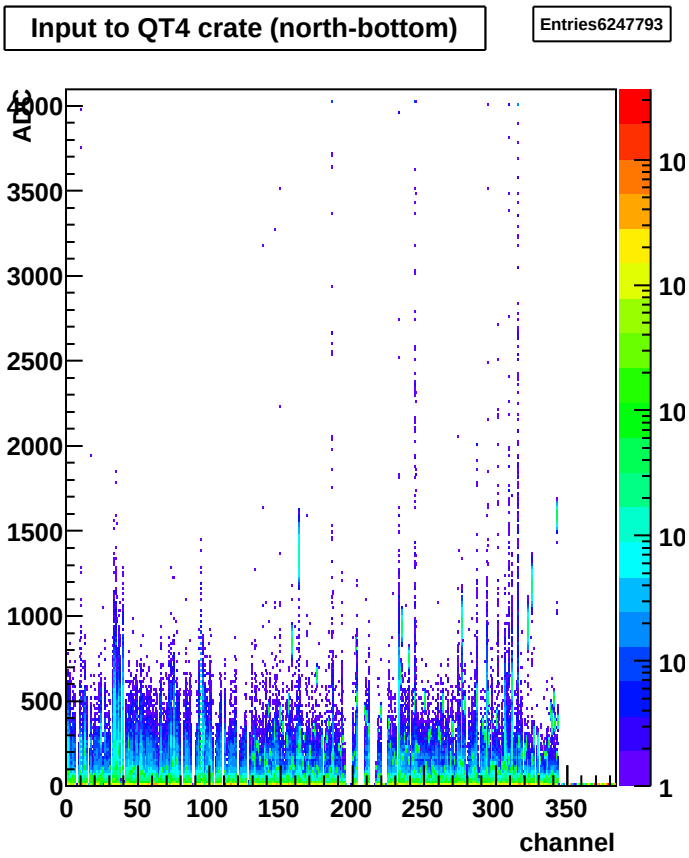

VPD-TAC-W
Entries 95722
Mean 1437
RMS 227.4

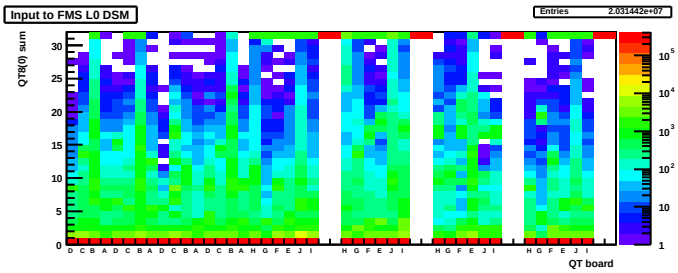

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 47564
Mean 4081
RMS 283.2

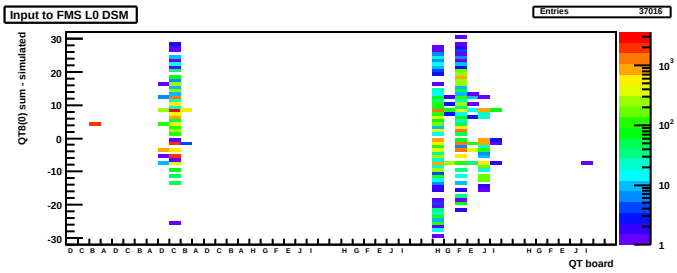

Input to QT1 crate (south-top)
Entries 8185504
ADC
channel

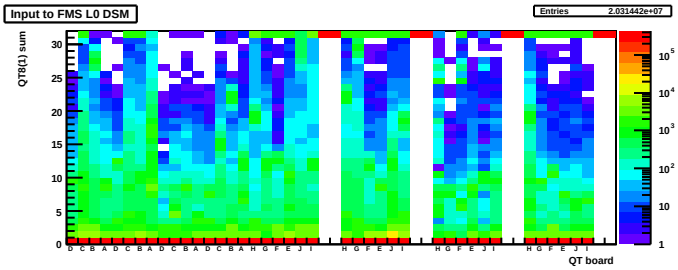

Input to QT3 crate (north-top)
Entries4344030
ADC
channel

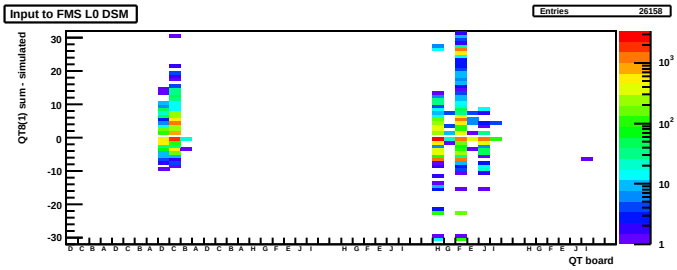

Input to QT2 crate (south-bottom)
Entries6225297
ADC
channel

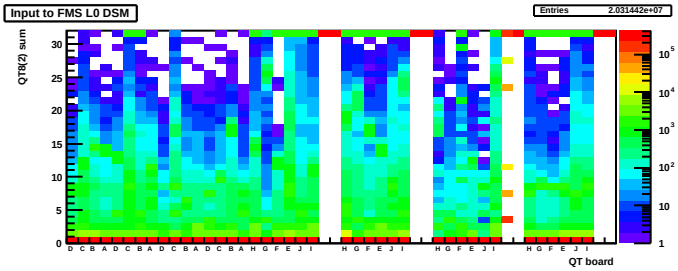

Input to QT4 crate (north-bottom)
Entries6247793
ADC
channel

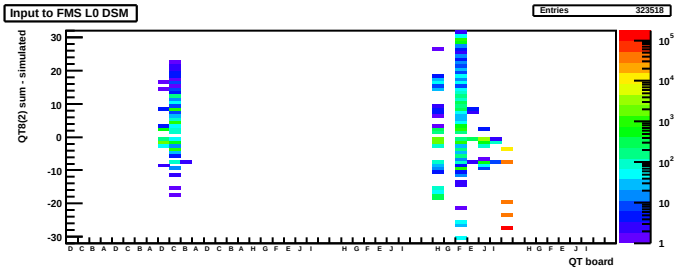

Input to FMS L0 DSM
Entries 2.031442e+07
QT8(0) sum
QT board

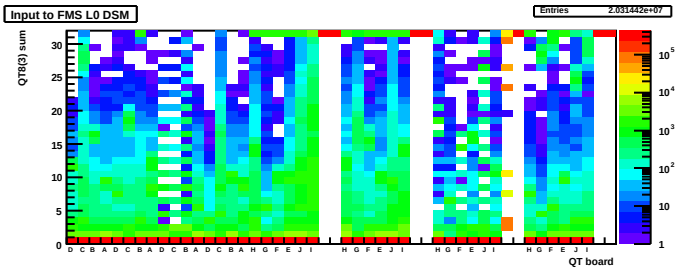

Input to FMS L0 DSM
Entries 37016
QT8(0) sum - simulated
QT board

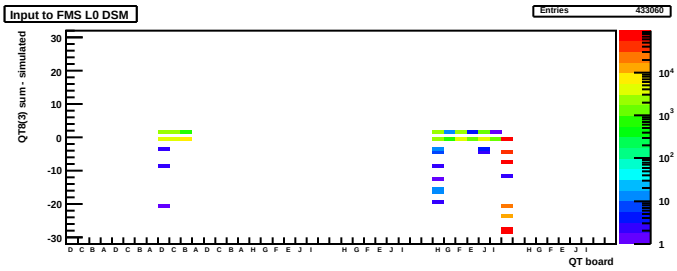

Input to FMS L0 DSM
Entries 2.031442e+07
QT8(1) sum
QT board

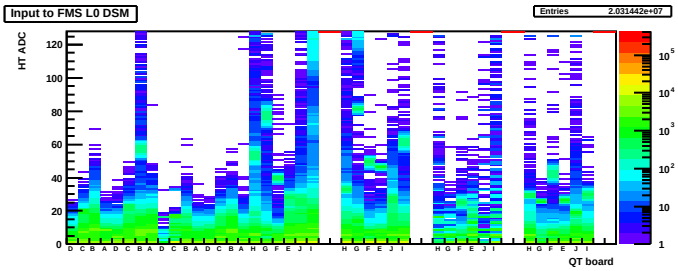

Input to FMS L0 DSM
Entries 26158
QT8(1) sum - simulated
QT board

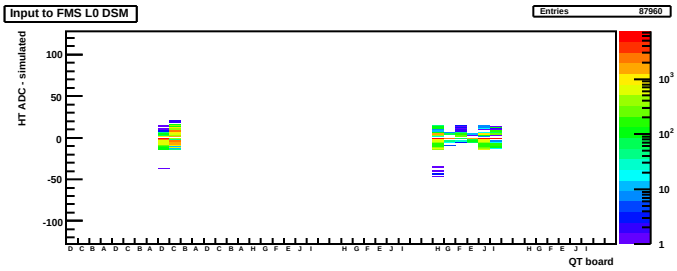

Input to FMS L0 DSM
Entries 2.031442e+07
QT8(2) sum
QT board

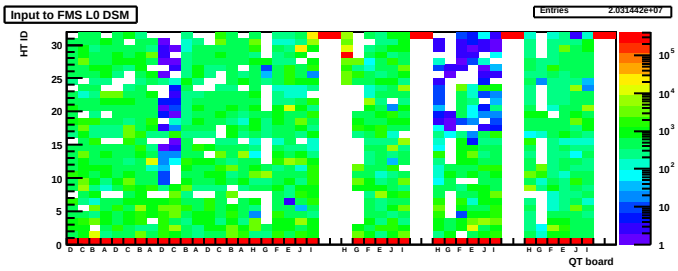

Input to FMS L0 DSM
Entries 323518
QT8(2) sum - simulated
QT board

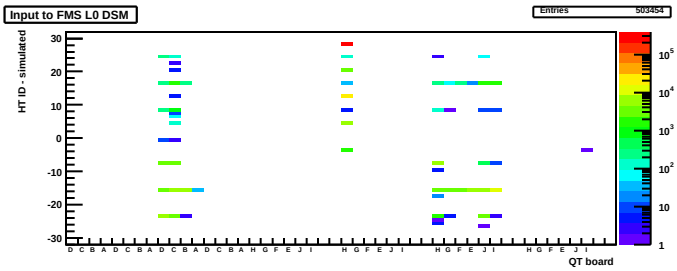

Input to FMS L0 DSM
Entries 2.031442e+07
QT8(3) sum
QT board

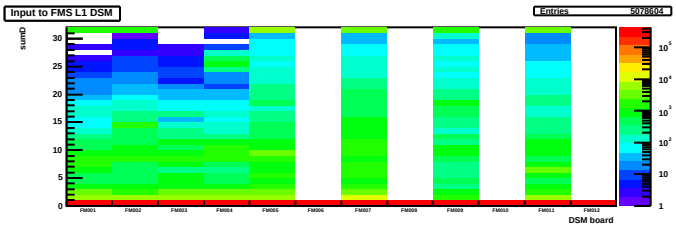

Input to FMS L0 DSM
Entries 433060
QT8(3) sum - simulated
QT board

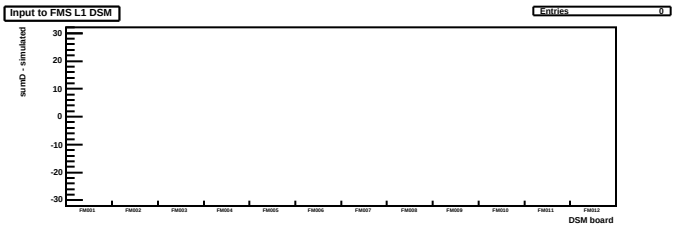

Input to FMS L0 DSM
Entries 2.031442e+07
HT ADC
QT board

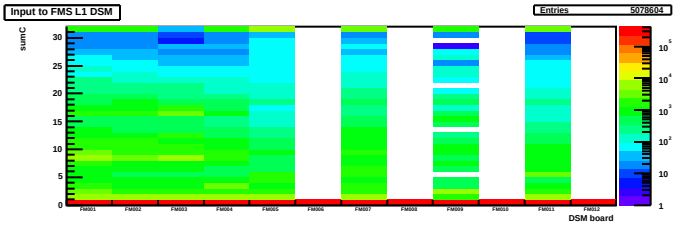

Input to FMS L0 DSM
Entries 87960
HT ADC - simulated
QT board

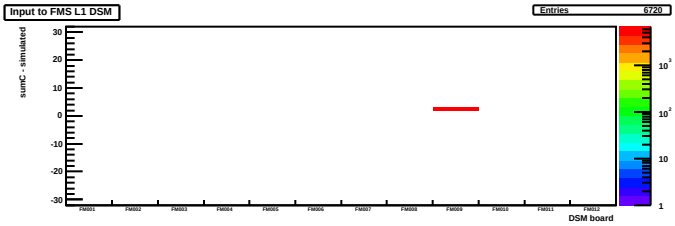

Input to FMS L0 DSM
Entries 2.031442e+07
HT ID
QT board

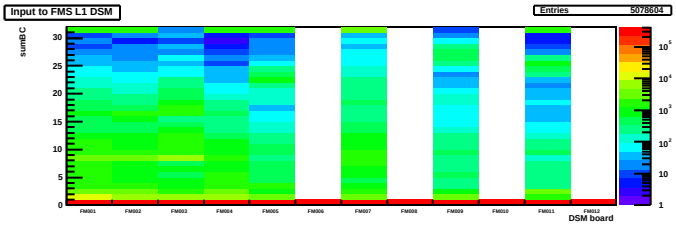

Input to FMS L0 DSM
Entries 503454
HT ID - simulated
QT board

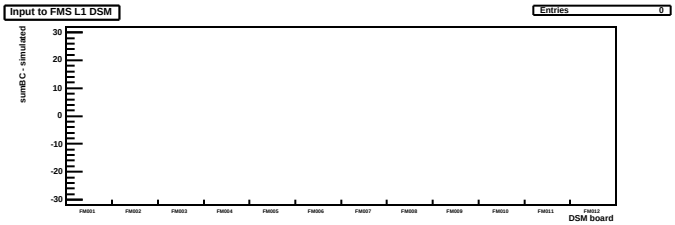

Input to FMS L1 DSM
Entries 5078604
sumD
DSM board

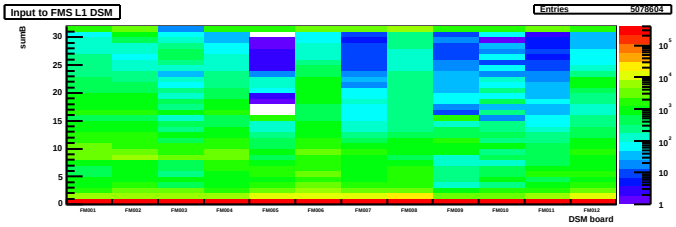

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

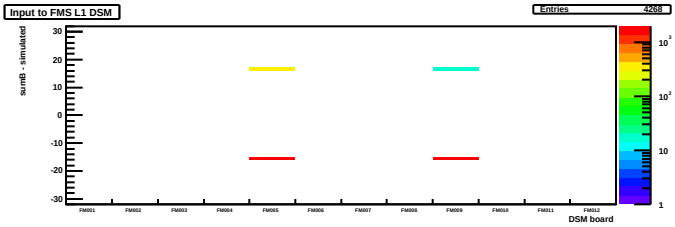

Input to FMS L1 DSM
Entries 5078604
sumC
DSM board

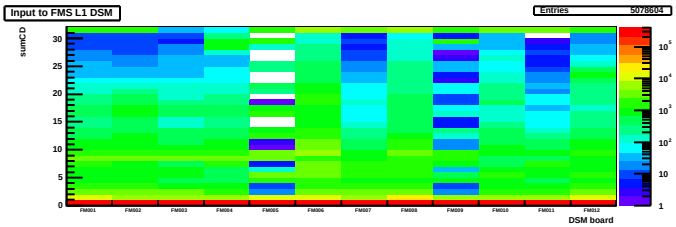

Input to FMS L1 DSM
Entries 6720
sumC - simulated
DSM board

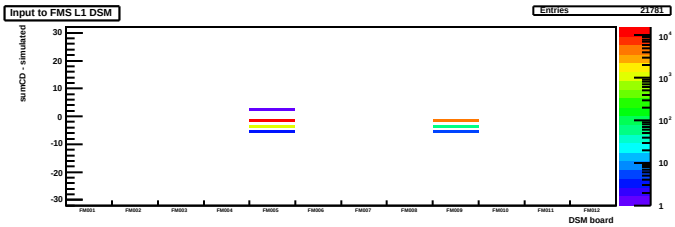

Input to FMS L1 DSM
Entries 5078604
sumBC
DSM board

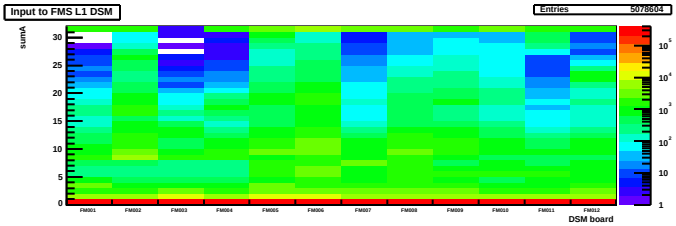

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

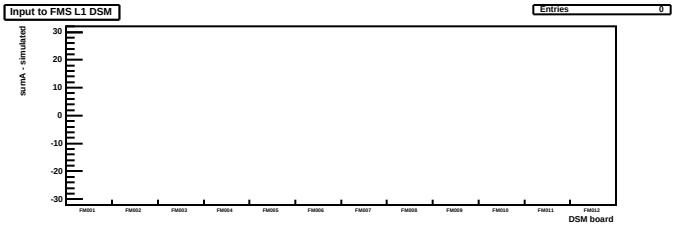

Input to FMS L1 DSM
Entries 5078604
sumB
DSM board

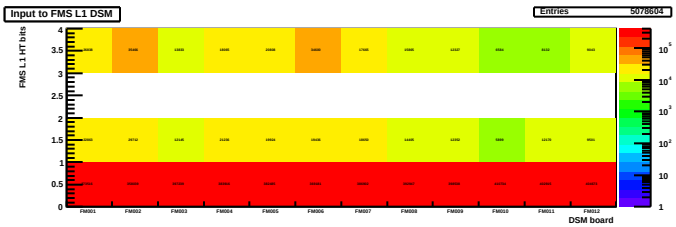

Input to FMS L1 DSM
Entries 4268
sumB - simulated
DSM board

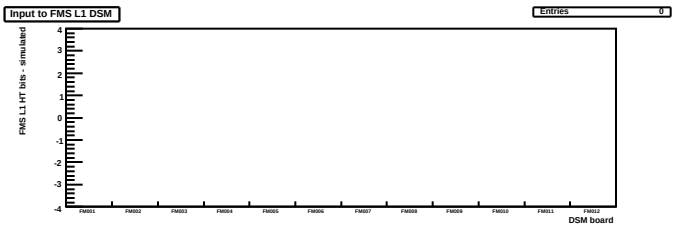

Input to FMS L1 DSM
Entries 5078604
sumCD
DSM board

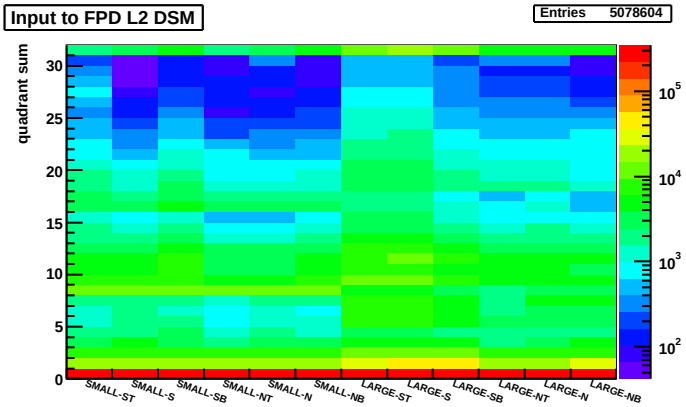

Input to FMS L1 DSM
Entries 21781
sumCD - simulated
DSM board

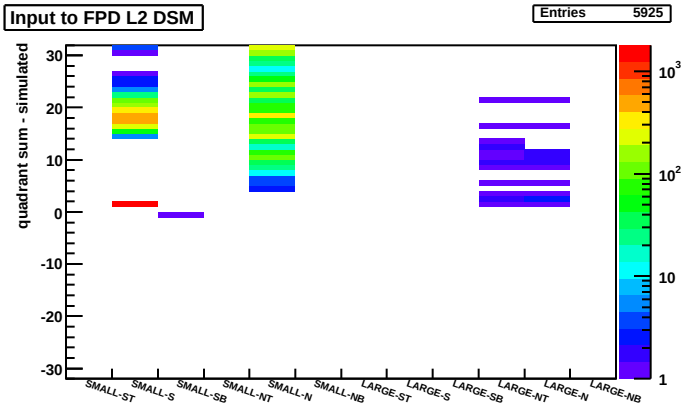

Input to FMS L1 DSM
Entries 5078604
sumA
DSM board

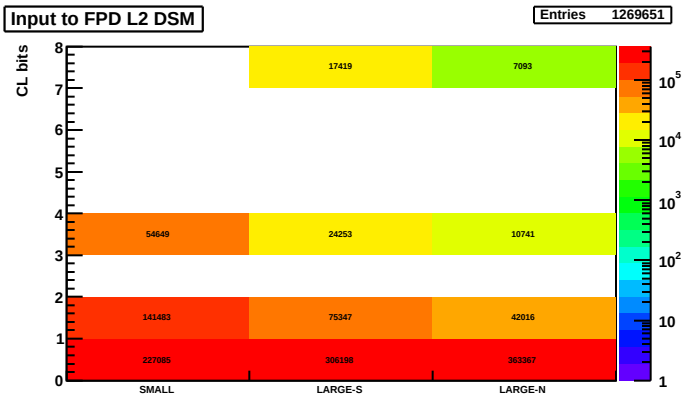

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

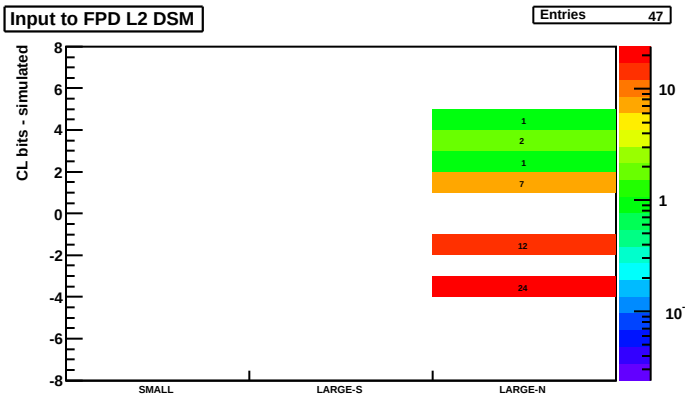

Input to FMS L1 DSM
Entries 5078604
FMS L1 HT bits
DSM board

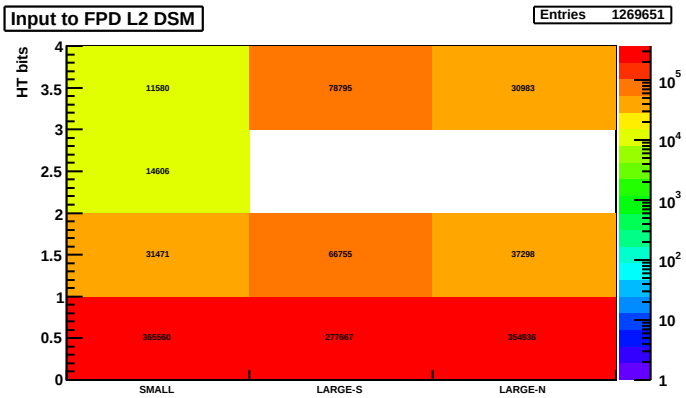

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

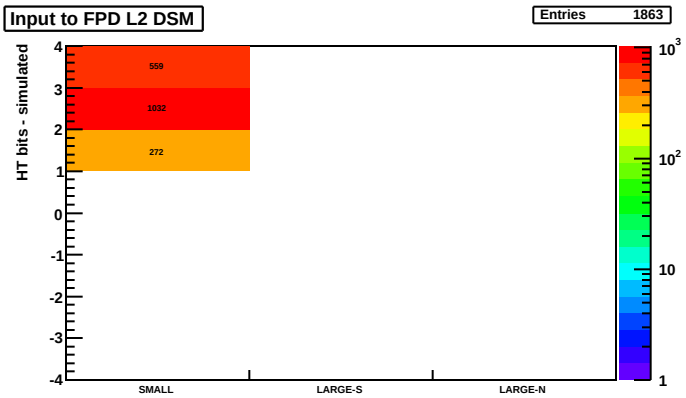

Input to FPD L2 DSM
Entries 5078604
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

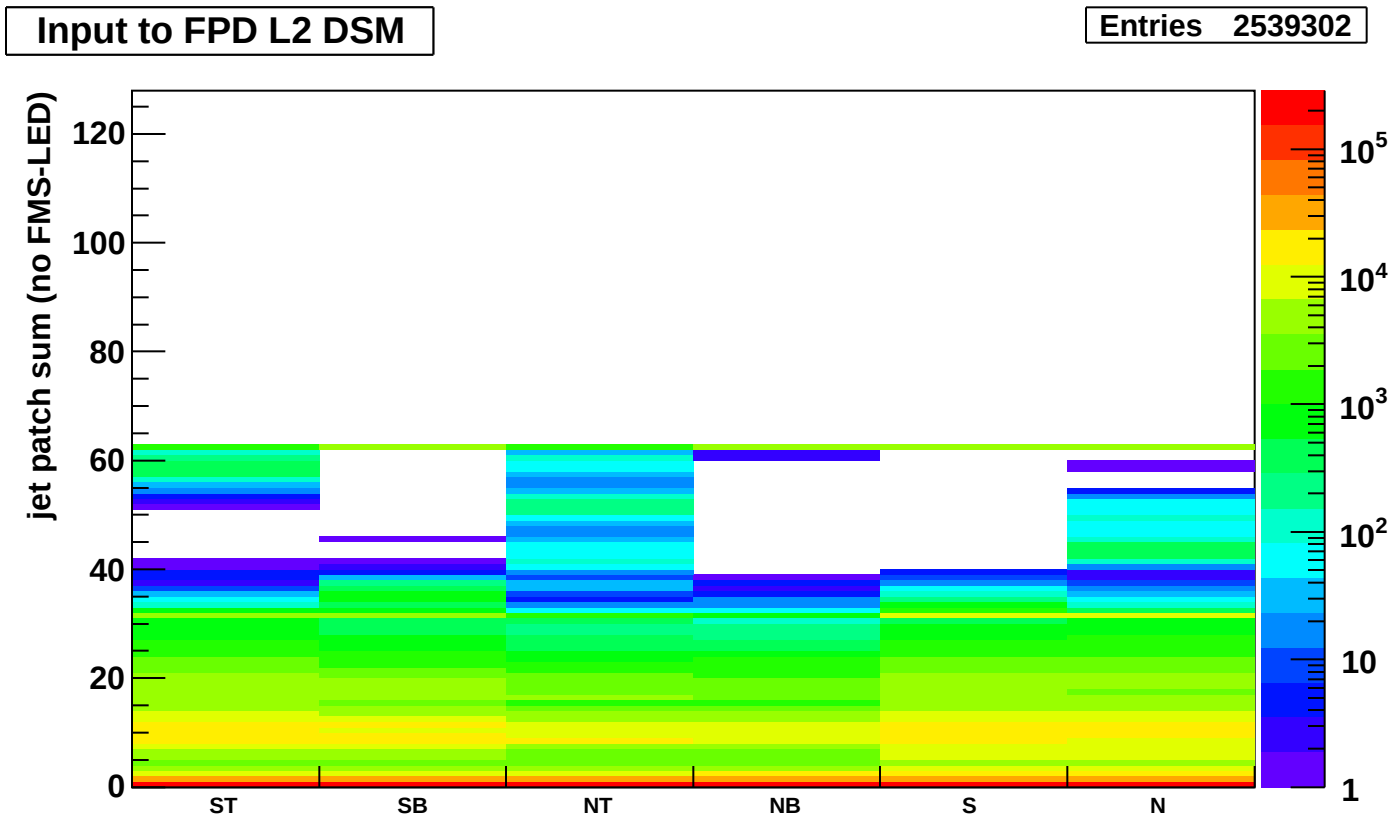

Input to FPD L2 DSM
Entries 5925
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

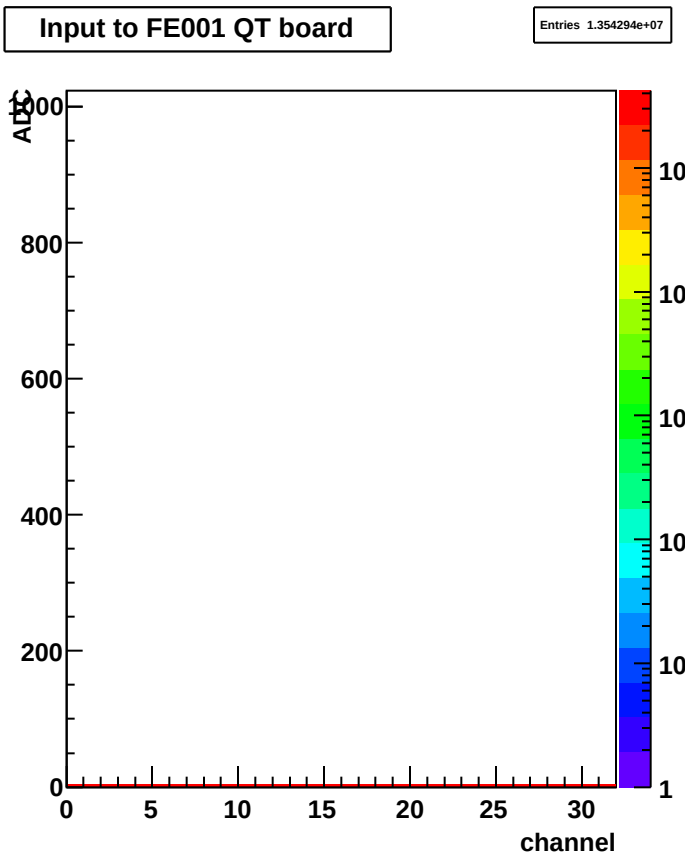

Input to FPD L2 DSM
Entries 1269651
CL bits
17419
7093
54649
24253
10741
141483
75347
42016
227085
306198
363367
SMALL
LARGE-S
LARGE-N

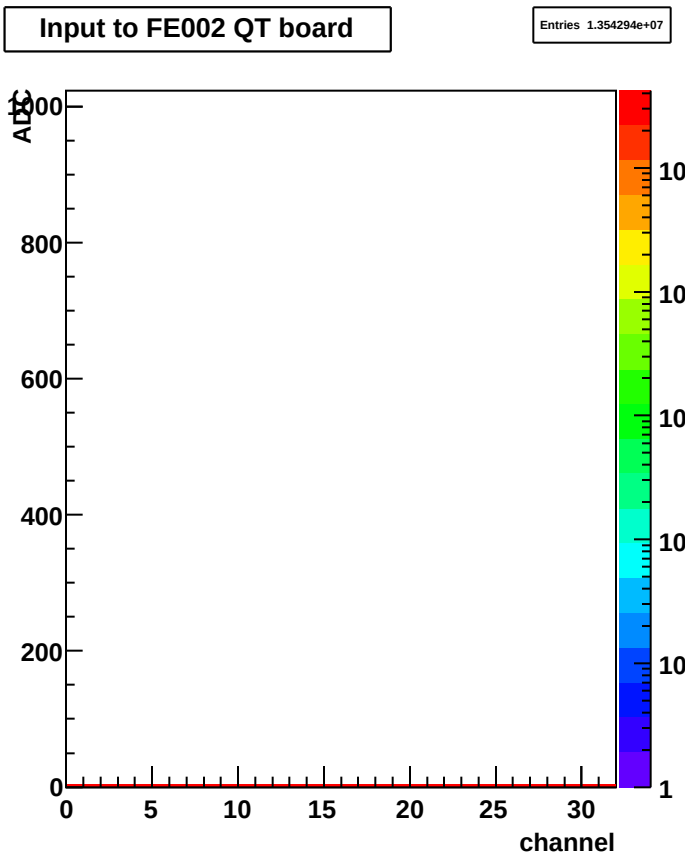

Input to FPD L2 DSM
Entries 47
CL bits - simulated
1
2
1
7
12
24
SMALL
LARGE-S
LARGE-N

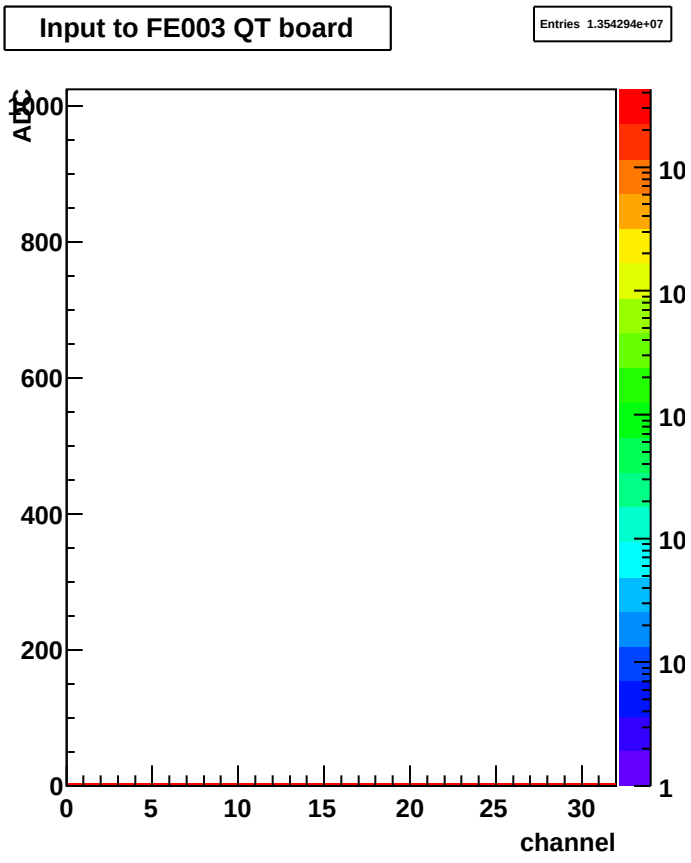

Input to FPD L2 DSM
Entries 1269651
HT bits
11580
78795
30983
14606
31471
66755
37298
365560
277667
354936
SMALL
LARGE-S
LARGE-N

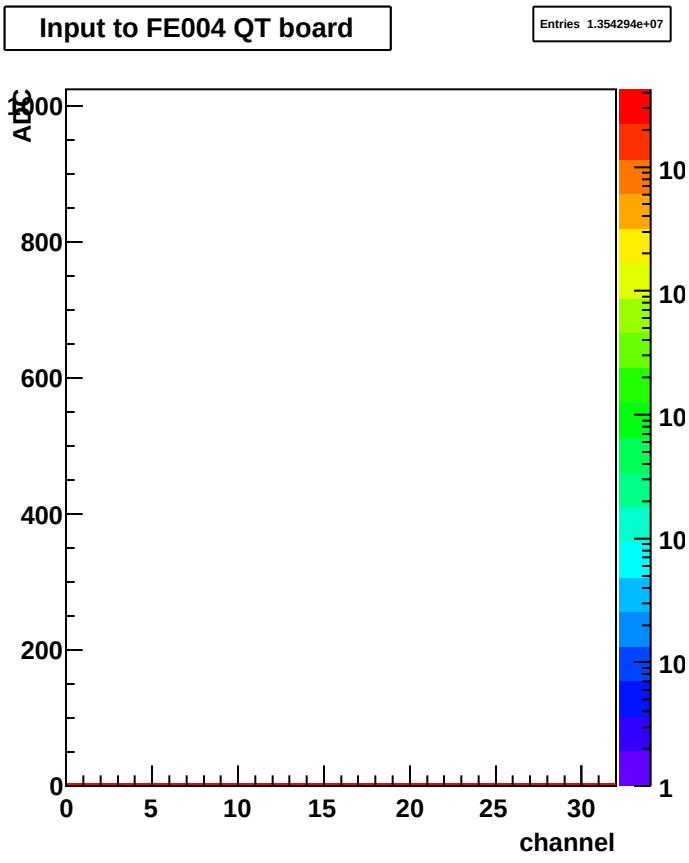

Input to FPD L2 DSM
Entries 1863
HT bits - simulated
559
1032
272
SMALL
LARGE-S
LARGE-N

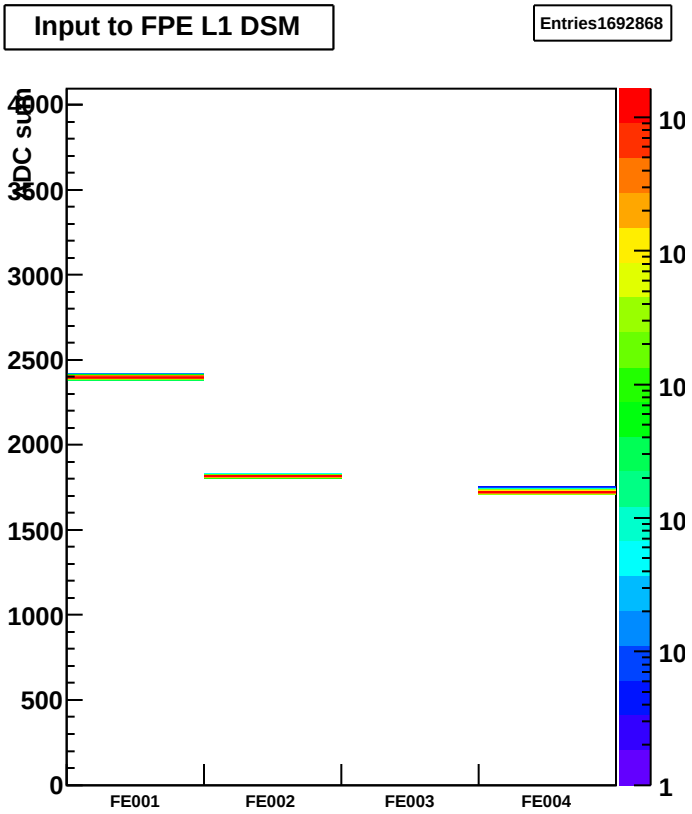

Input to FPD L2 DSM
Entries 2539302
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

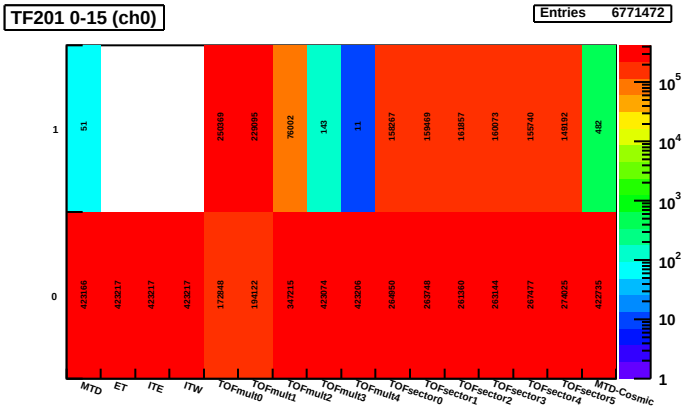

Input to FE001 QT board
Entries 1.354294e+07
ADC
channel

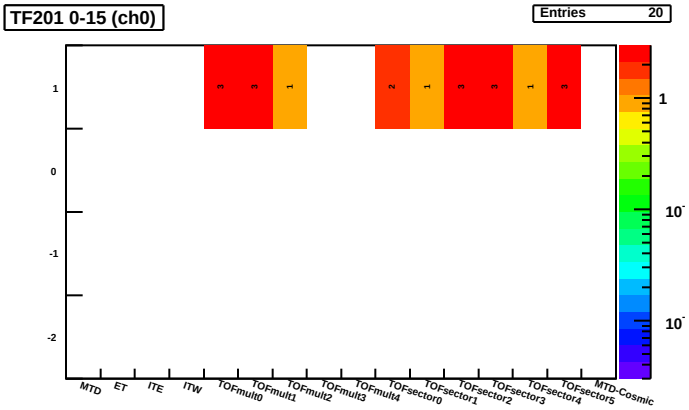

Input to FE002 QT board
Entries 1.354294e+07
ADC
channel

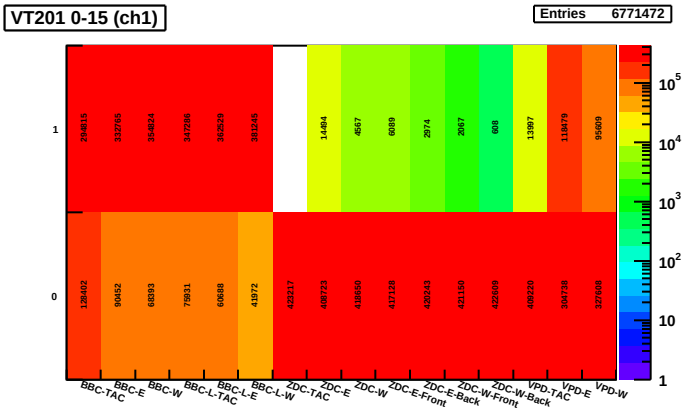

Input to FE003 QT board
Entries 1.354294e+07
ADC
channel

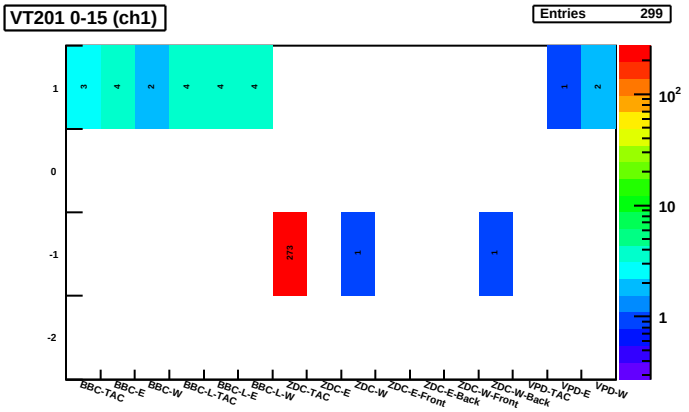

Input to FE004 QT board
Entries 1.354294e+07
ADC
channel


Input to FPE L1 DSM
Entries1692868
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 6771472
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 20
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


VT201 0-15 (ch1)
Entries 6771472
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 299
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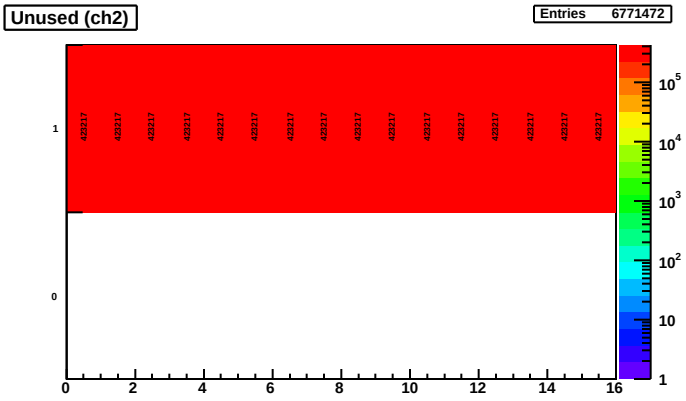
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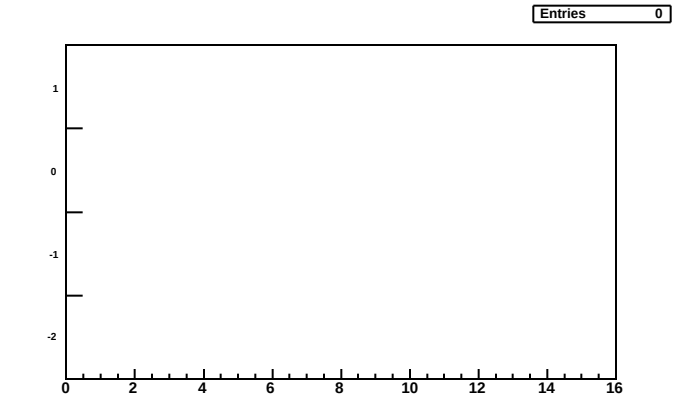
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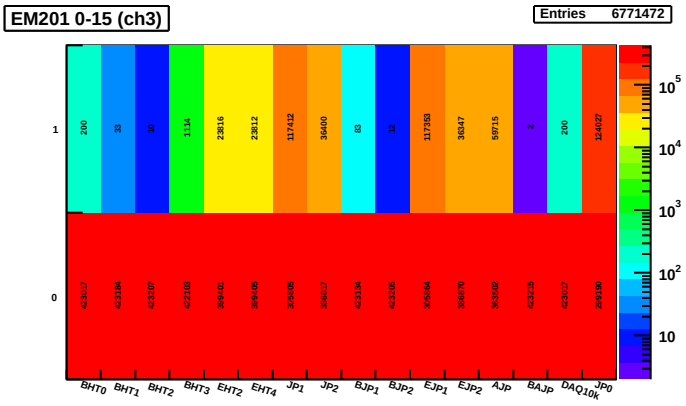
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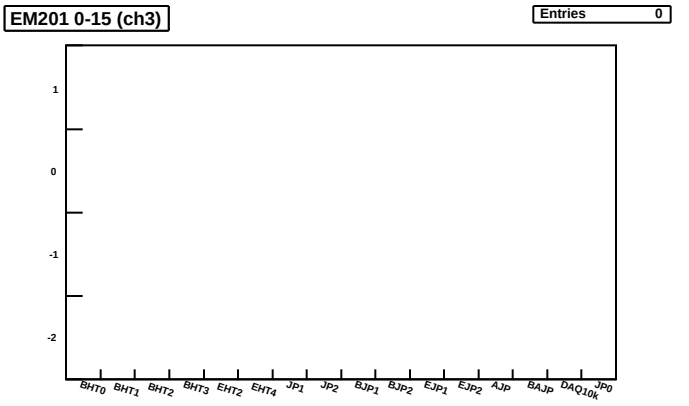
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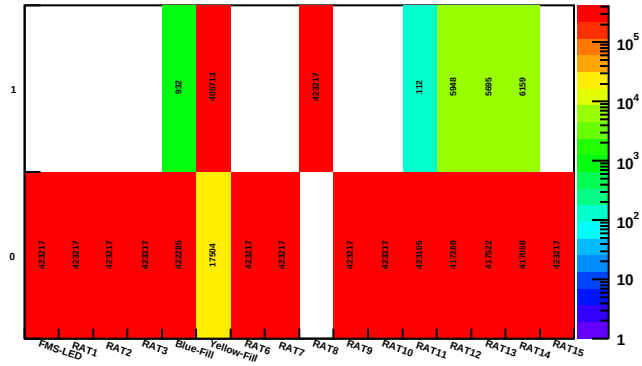
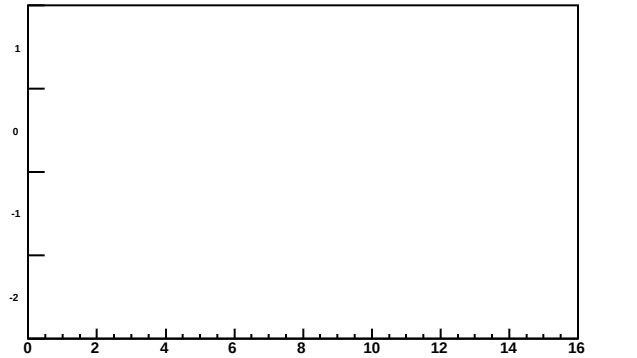
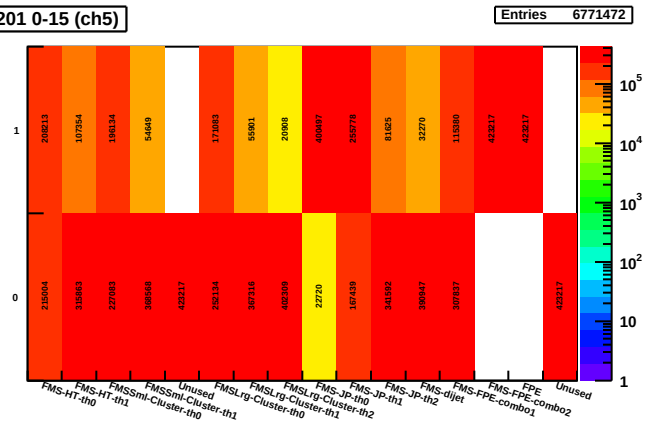
RAT board (ch4)

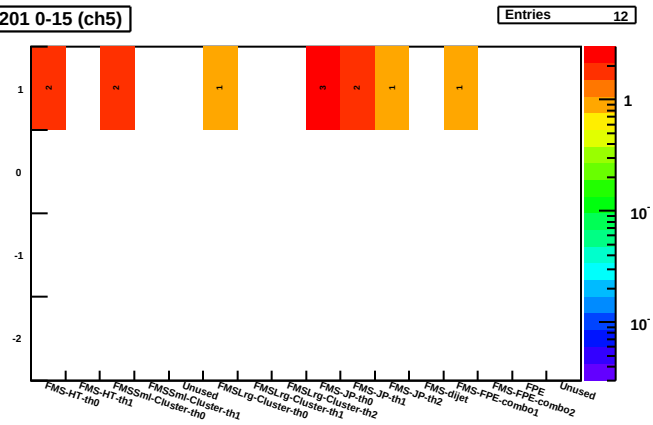




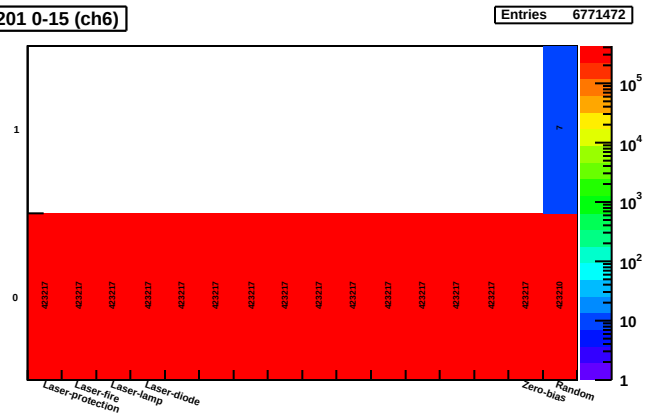
FP201 0-15 (ch5)

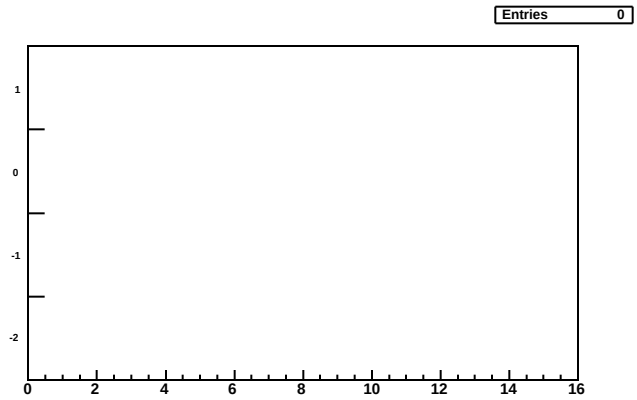
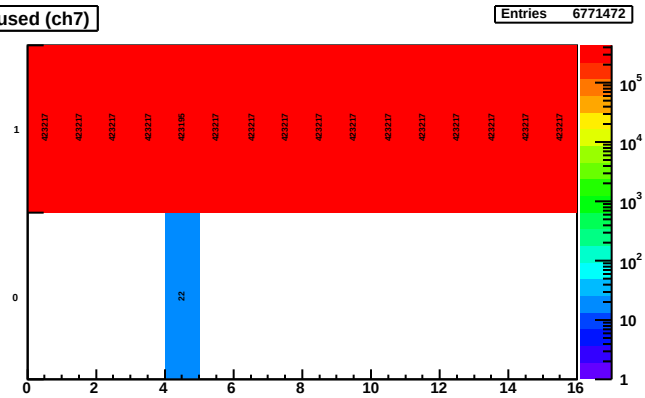
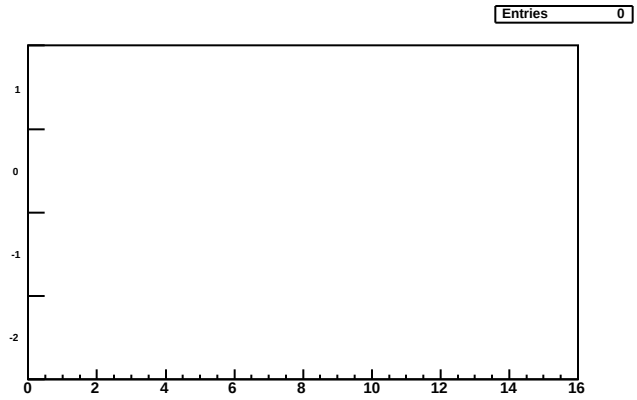


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)