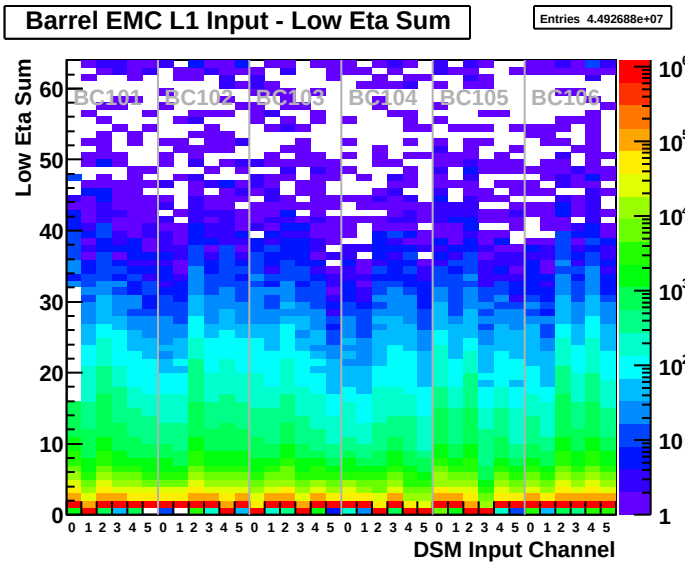
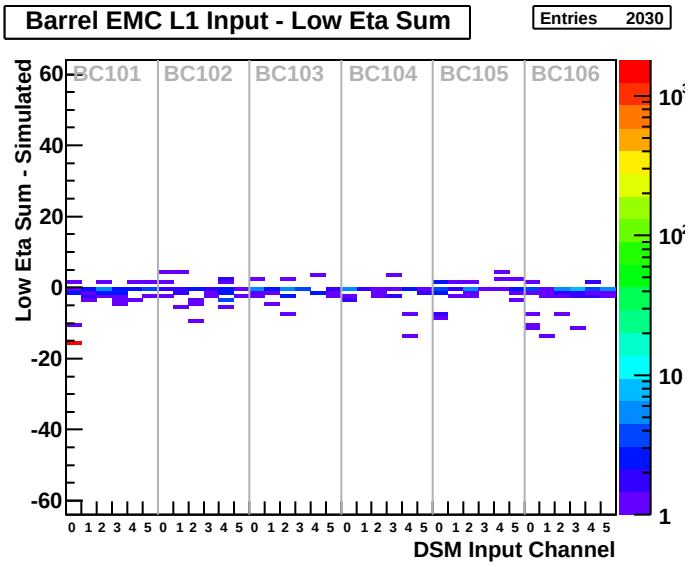
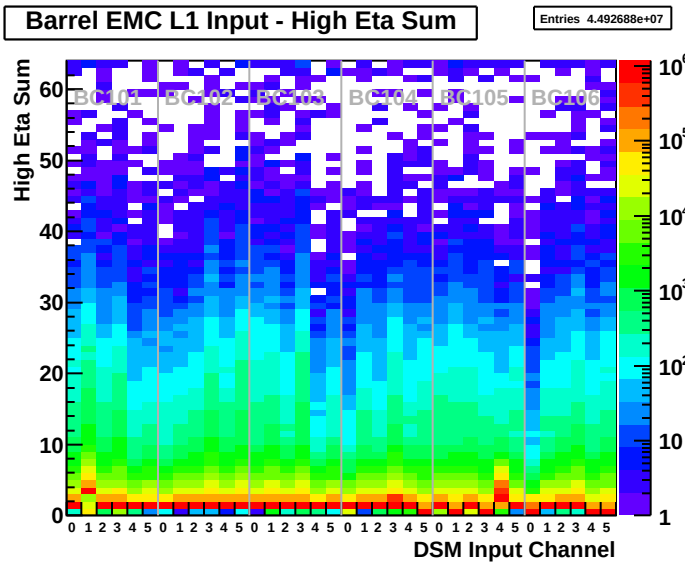
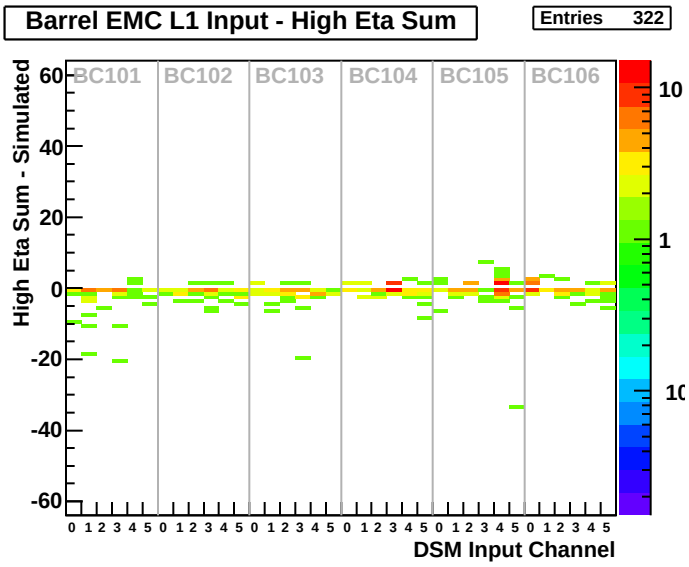
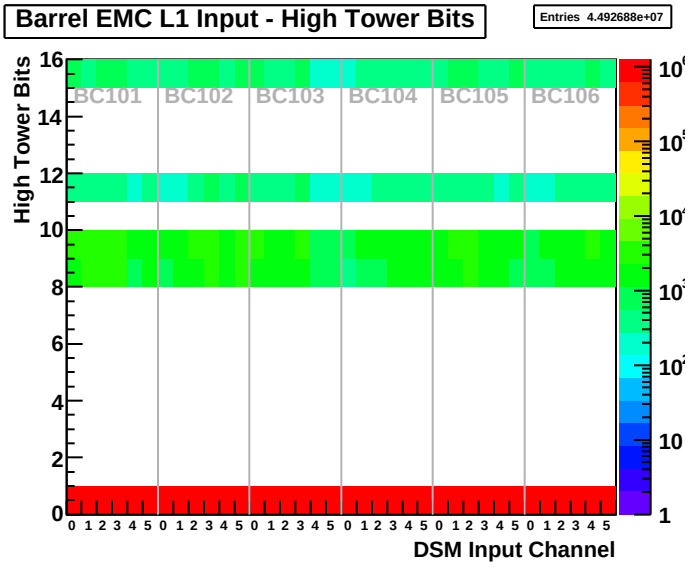
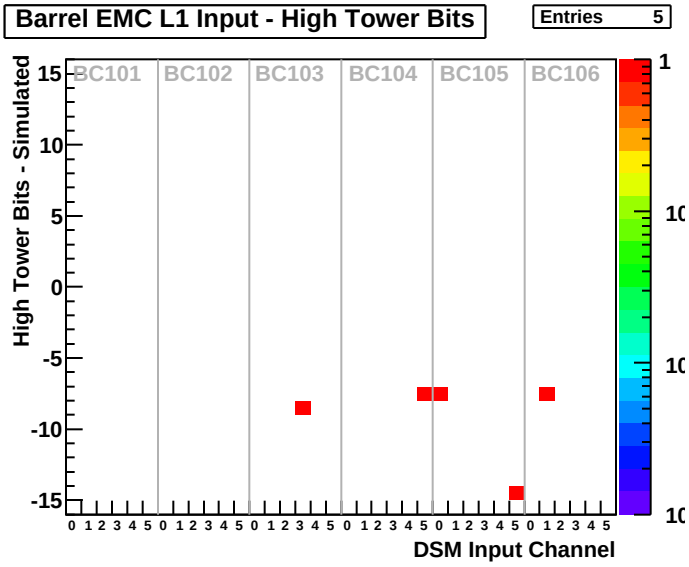

Barrel EMC L1 Input - Low Eta Sum
Entries 4.492688e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

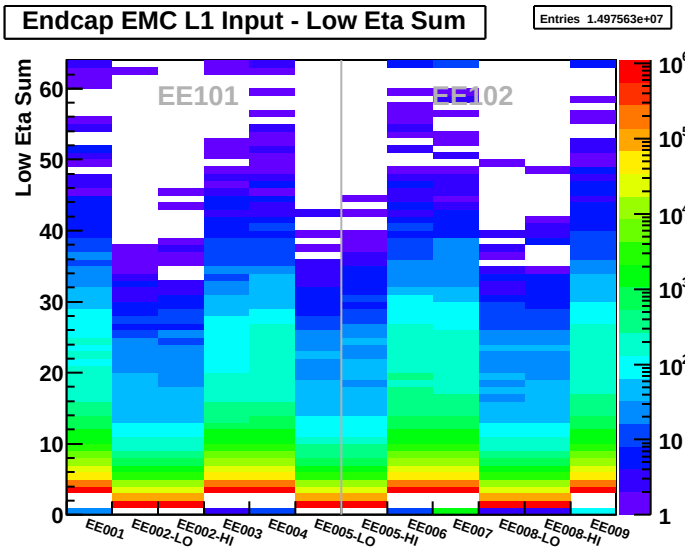

Barrel EMC L1 Input - Low Eta Sum
Entries 2030
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

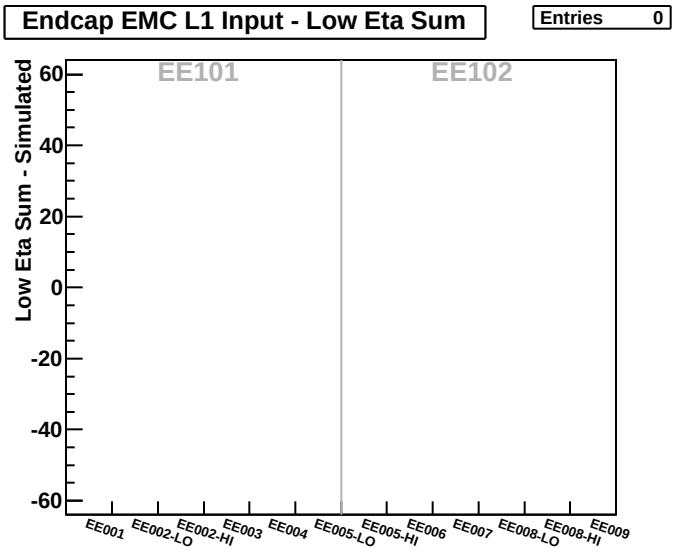

Barrel EMC L1 Input - High Eta Sum
Entries 4.492688e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

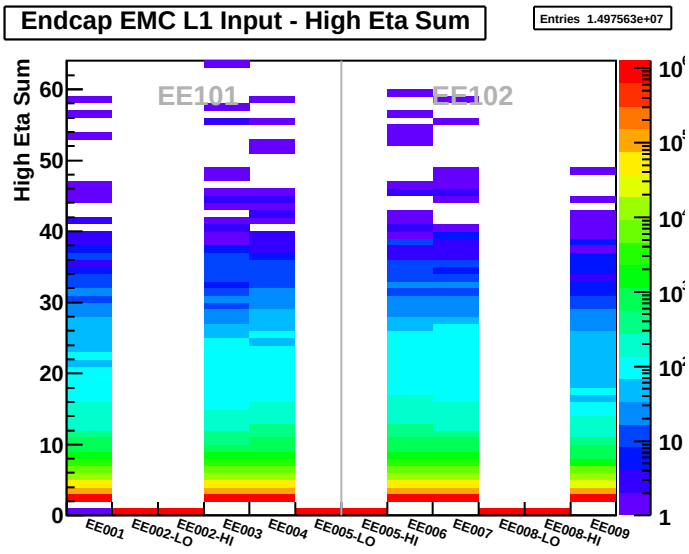

Barrel EMC L1 Input - High Eta Sum
Entries 322
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

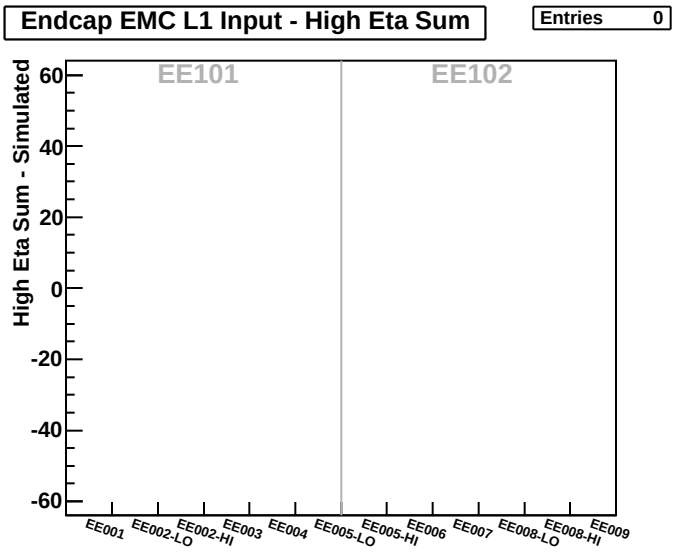

Barrel EMC L1 Input - High Tower Bits
Entries 4.492688e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

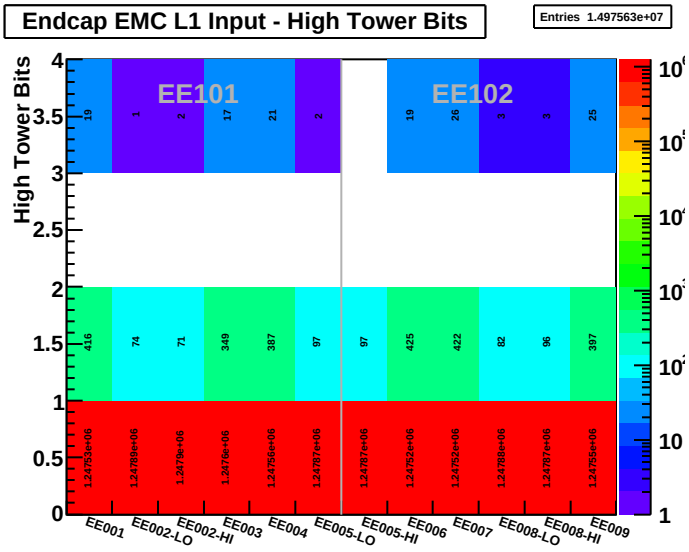

Barrel EMC L1 Input - High Tower Bits
Entries 5
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

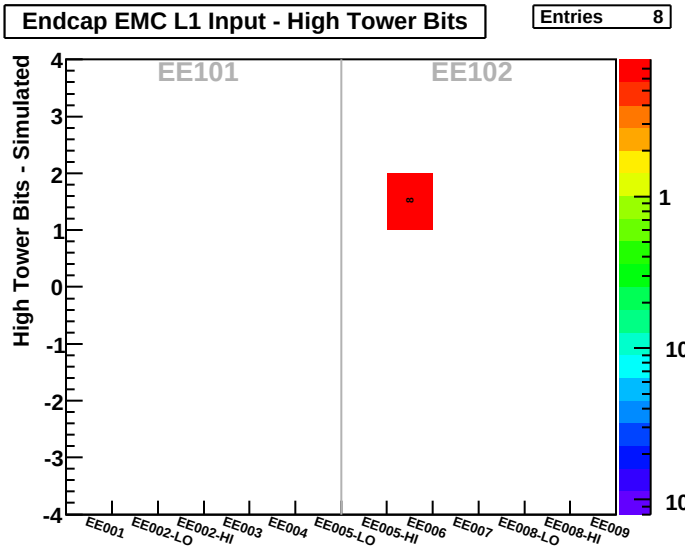

Endcap EMC L1 Input - Low Eta Sum
Entries 1.497563e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

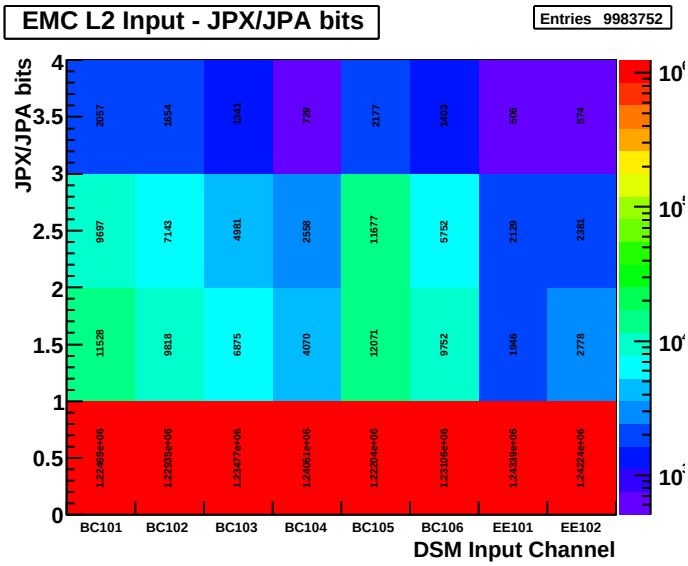

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

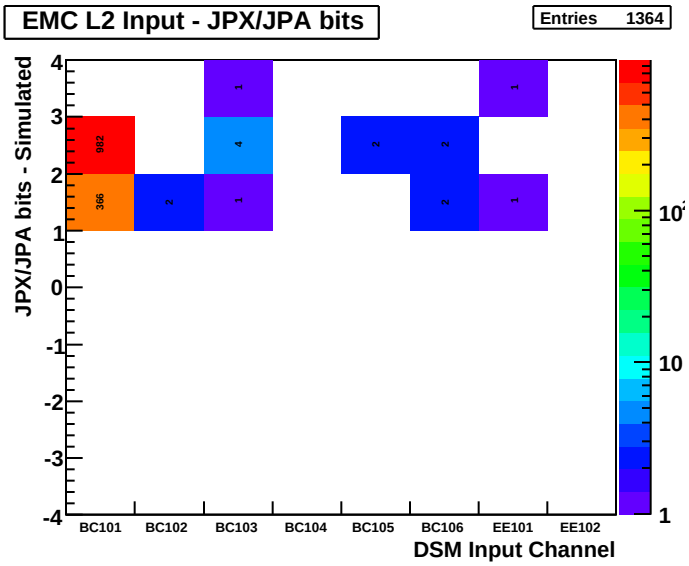

Endcap EMC L1 Input - High Eta Sum
Entries 1.497563e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

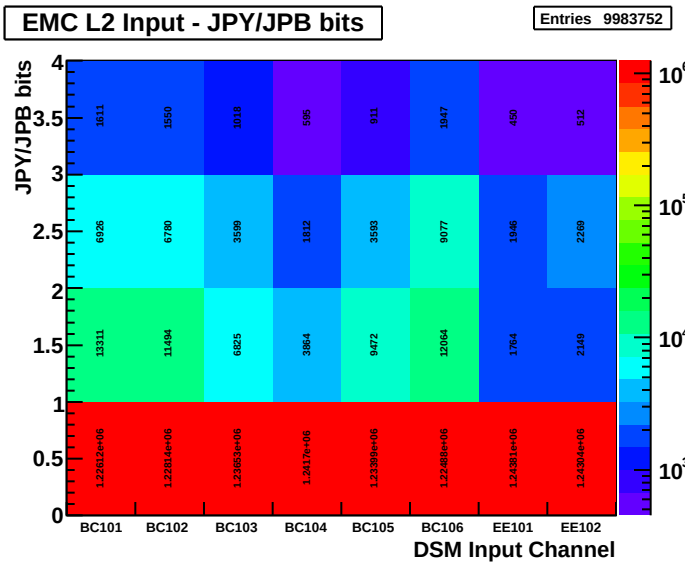

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

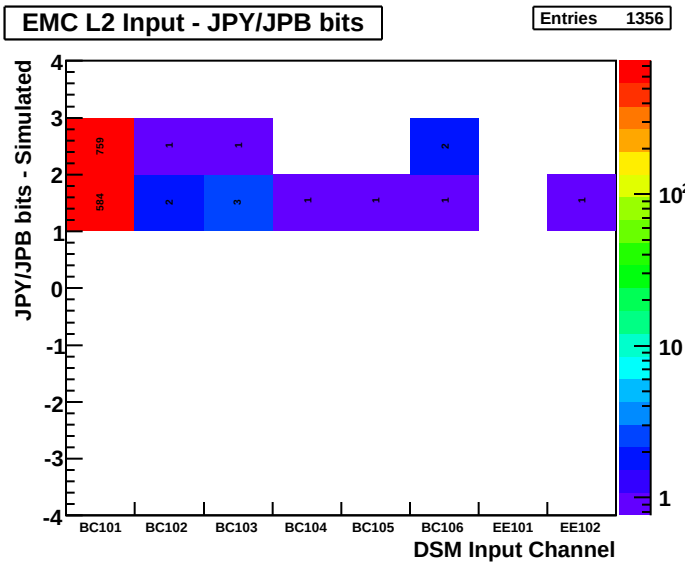

Endcap EMC L1 Input - High Tower Bits
Entries 1.497563e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

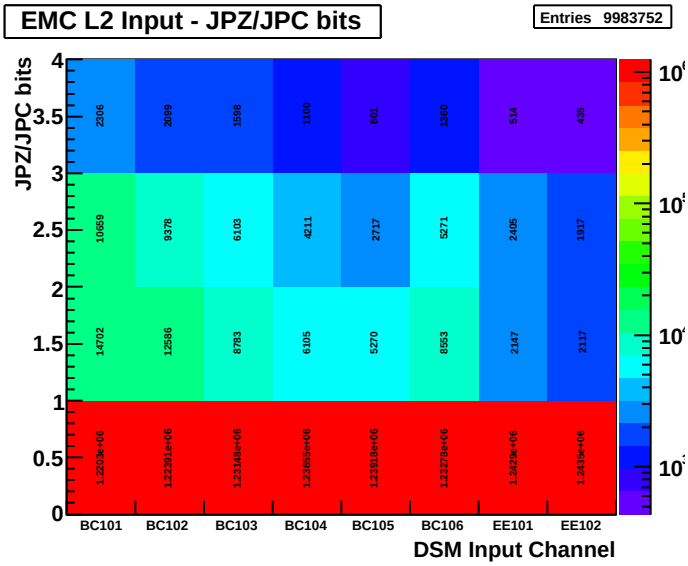

Endcap EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

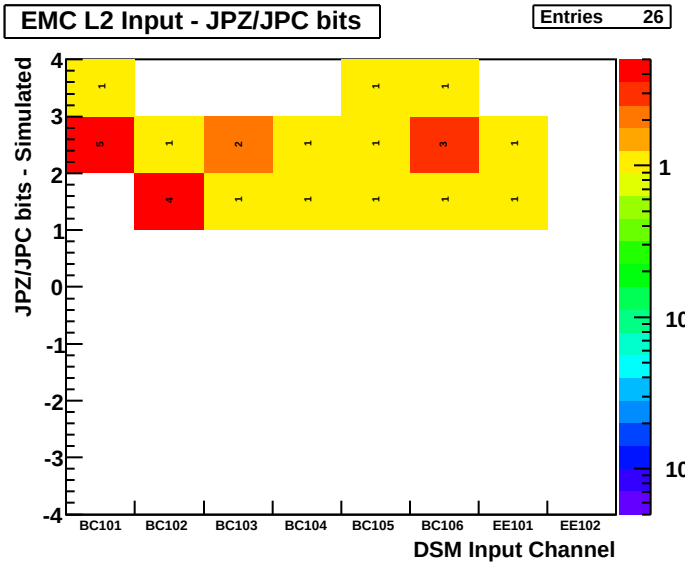

EMC L2 Input - JPX/JPA bits
Entries 9983752
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2057 1654 1341 729 2177 1403 506 574
9697 7143 4981 2558 11677 5752 2129 2381
11528 9818 6875 4070 12071 9752 1946 2778
1.22469e+06 1.22935e+06 1.23477e+06 1.24061e+06 1.22204e+06 1.23106e+06 1.24339e+06 1.24224e+06

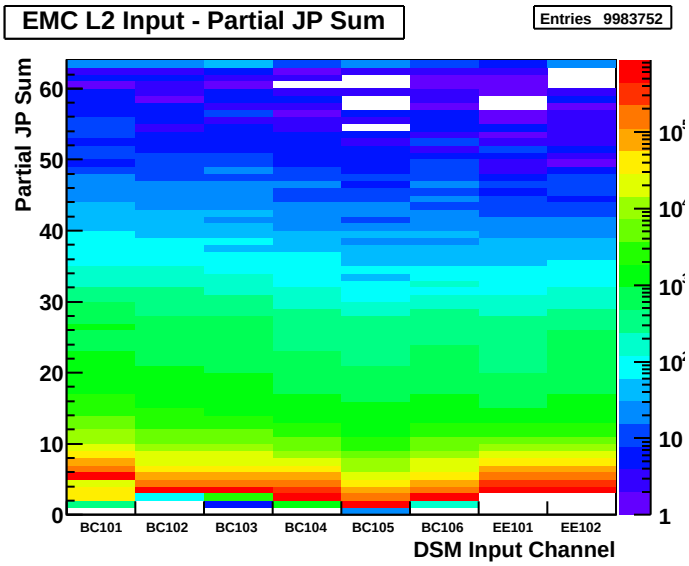

EMC L2 Input - JPX/JPA bits
Entries 1364
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
982 1 1
366 2 1 2 2 1

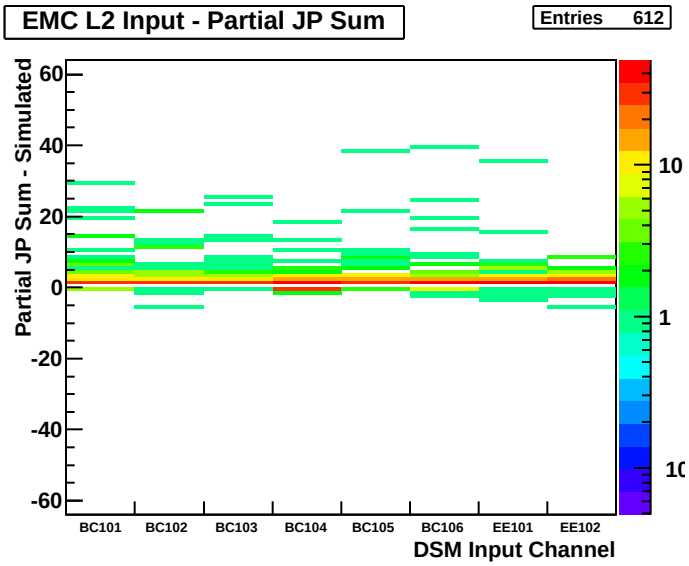

EMC L2 Input - JPY/JPB bits
Entries 9983752
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1611 1550 1018 595 911 1947 450 512
6926 6780 3599 1812 3593 9077 1946 2289
13311 11494 6825 3864 9472 12064 1764 2149
1.22612e+06 1.22814e+06 1.23653e+06 1.24117e+06 1.23399e+06 1.22498e+06 1.24381e+06 1.24304e+06

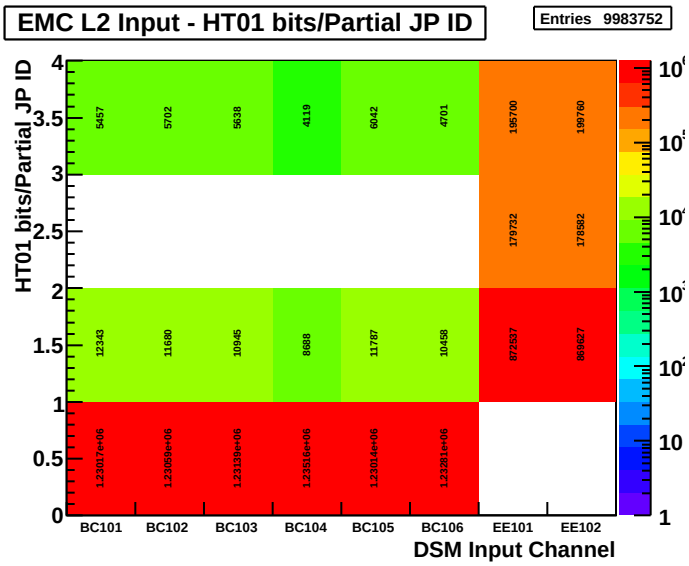

EMC L2 Input - JPY/JPB bits
Entries 1356
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
759 1 1 2
584 2 3 1 1 1 1

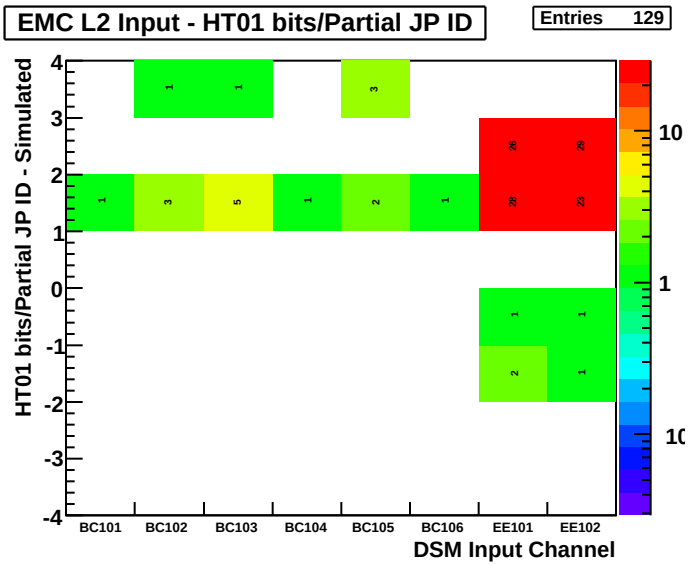

EMC L2 Input - JPZ/JPC bits
Entries 9983752
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2306 2099 1598 1100 801 1360 514 435
10659 9378 6103 4211 2717 5271 2405 1917
14702 12586 8783 6105 5270 8553 2147 2117
1.2203e+06 1.22391e+06 1.23148e+06 1.23655e+06 1.23918e+06 1.23278e+06 1.2429e+06 1.2435e+06

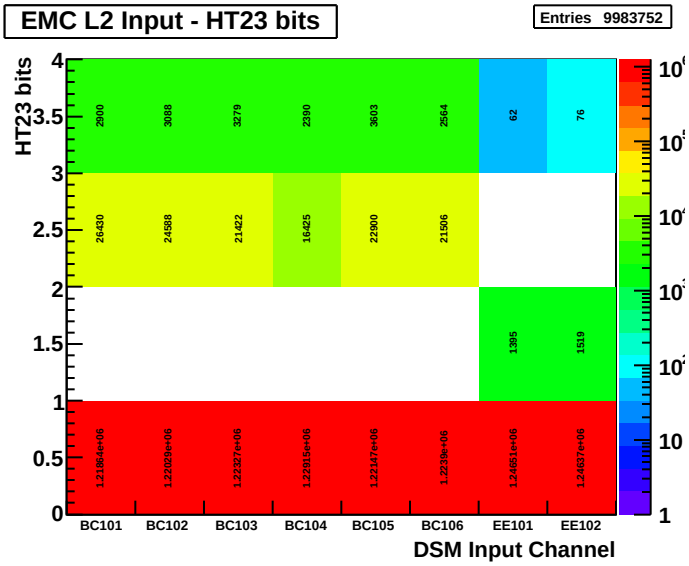

EMC L2 Input - JPZ/JPC bits
Entries 26
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
5 1 2 1 1 3 1
4 1 1 1 1 1

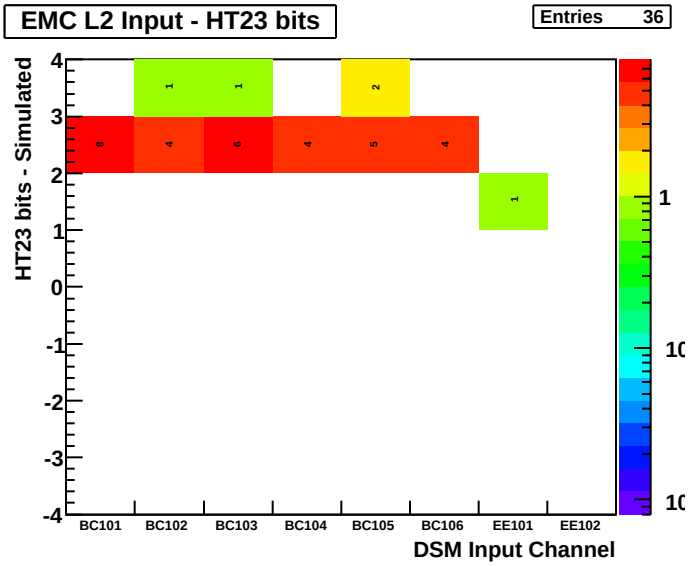

EMC L2 Input - Partial JP Sum
Entries 9983752
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

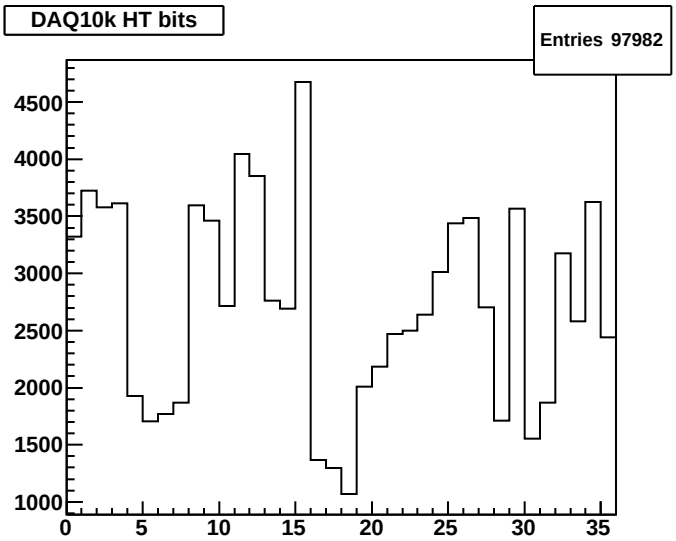

EMC L2 Input - Partial JP Sum
Entries 612
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

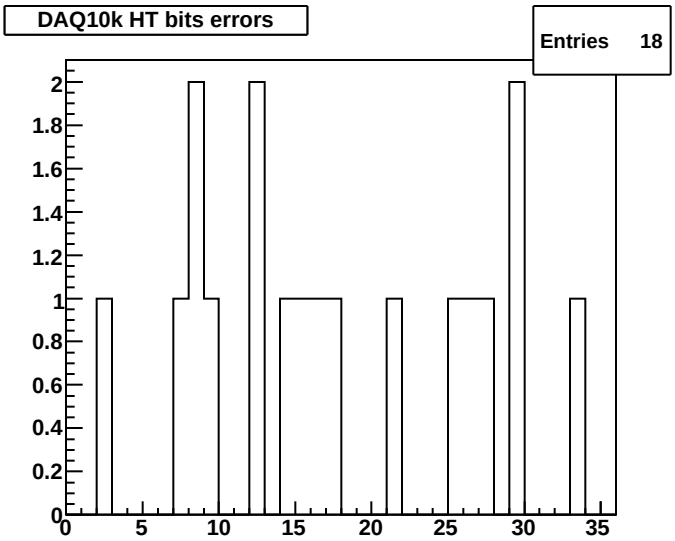

EMC L2 Input - HT01 bits/Partial JP ID
Entries 9983752
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

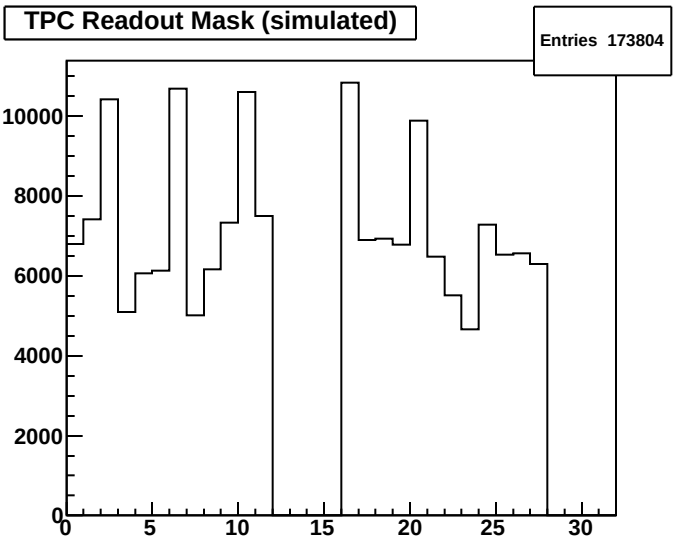

EMC L2 Input - HT01 bits/Partial JP ID
Entries 129
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

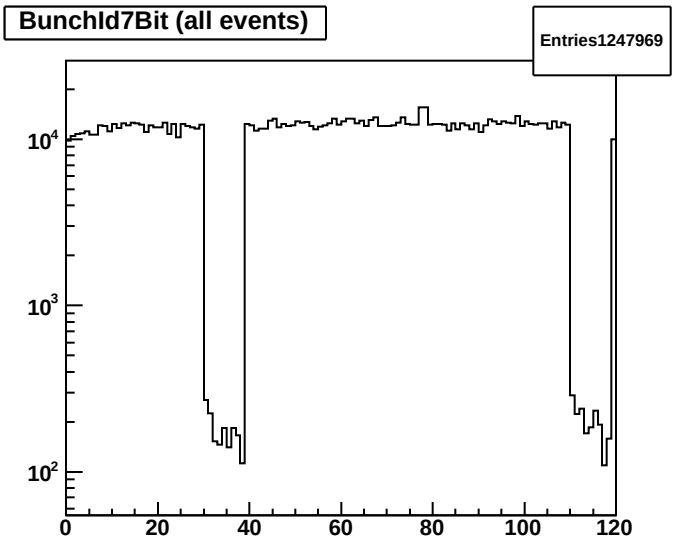

EMC L2 Input - HT23 bits
Entries 9983752
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

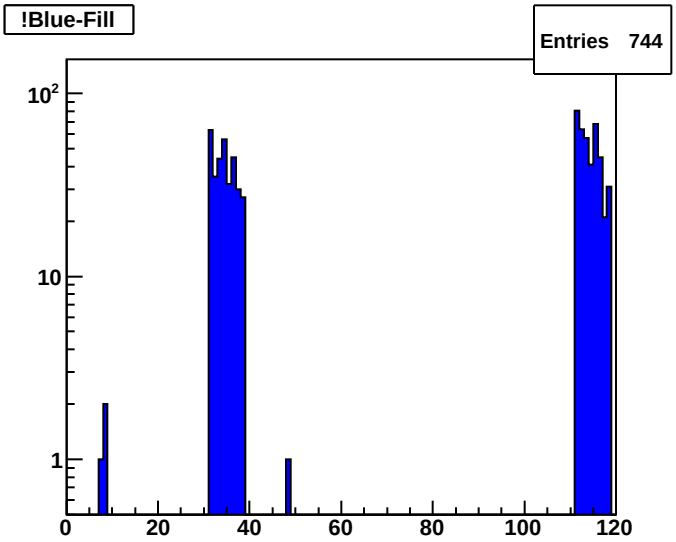

EMC L2 Input - HT23 bits
Entries 36
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

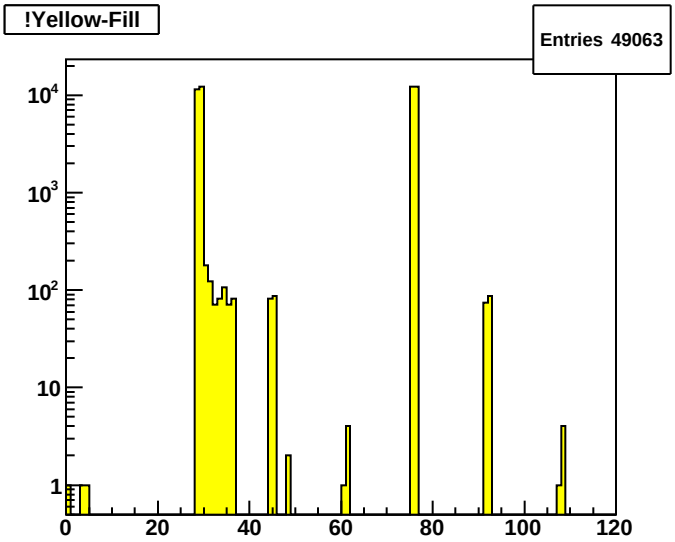

DAQ10k HT bits
Entries 97982

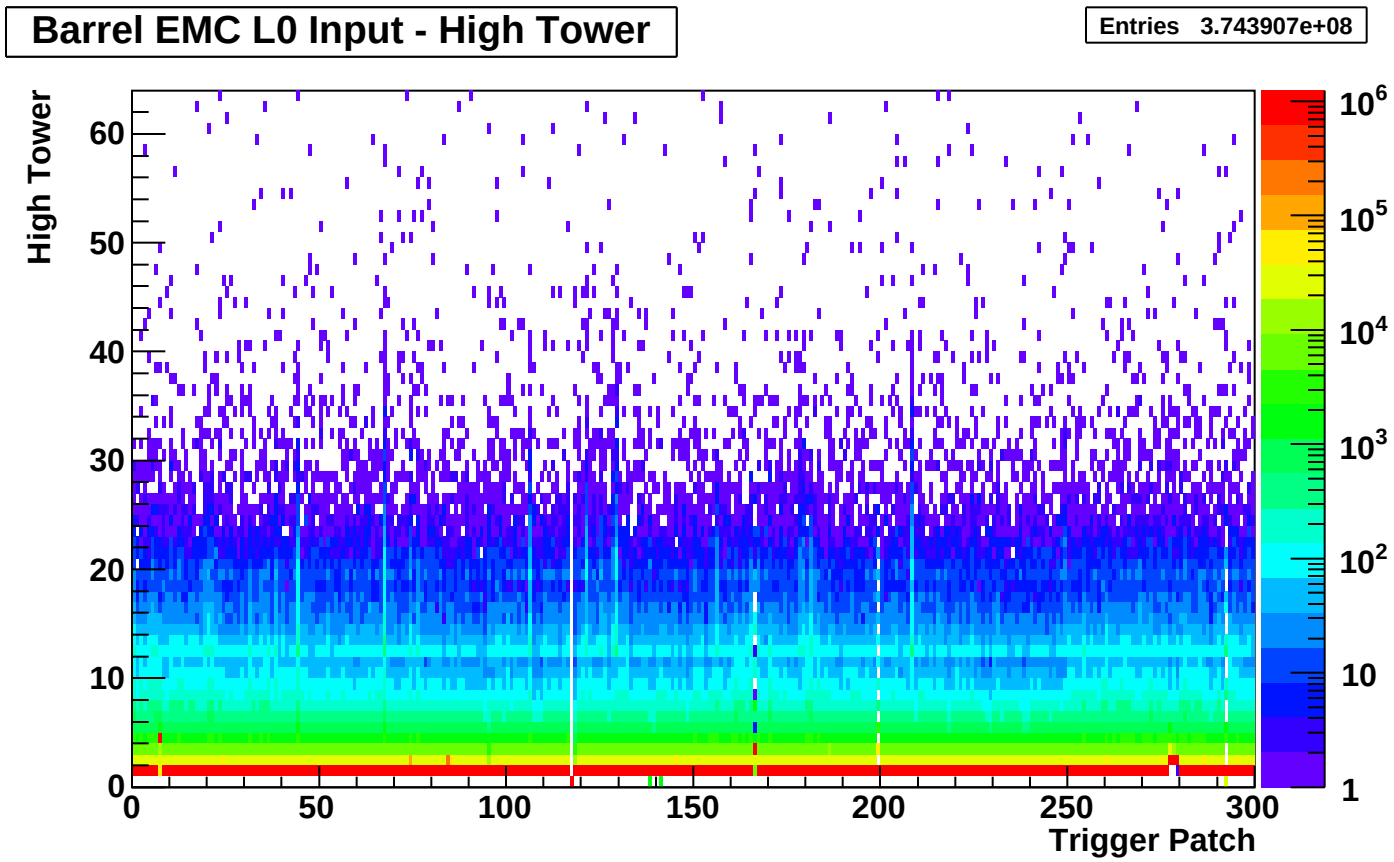

DAQ10k HT bits errors
Entries 18

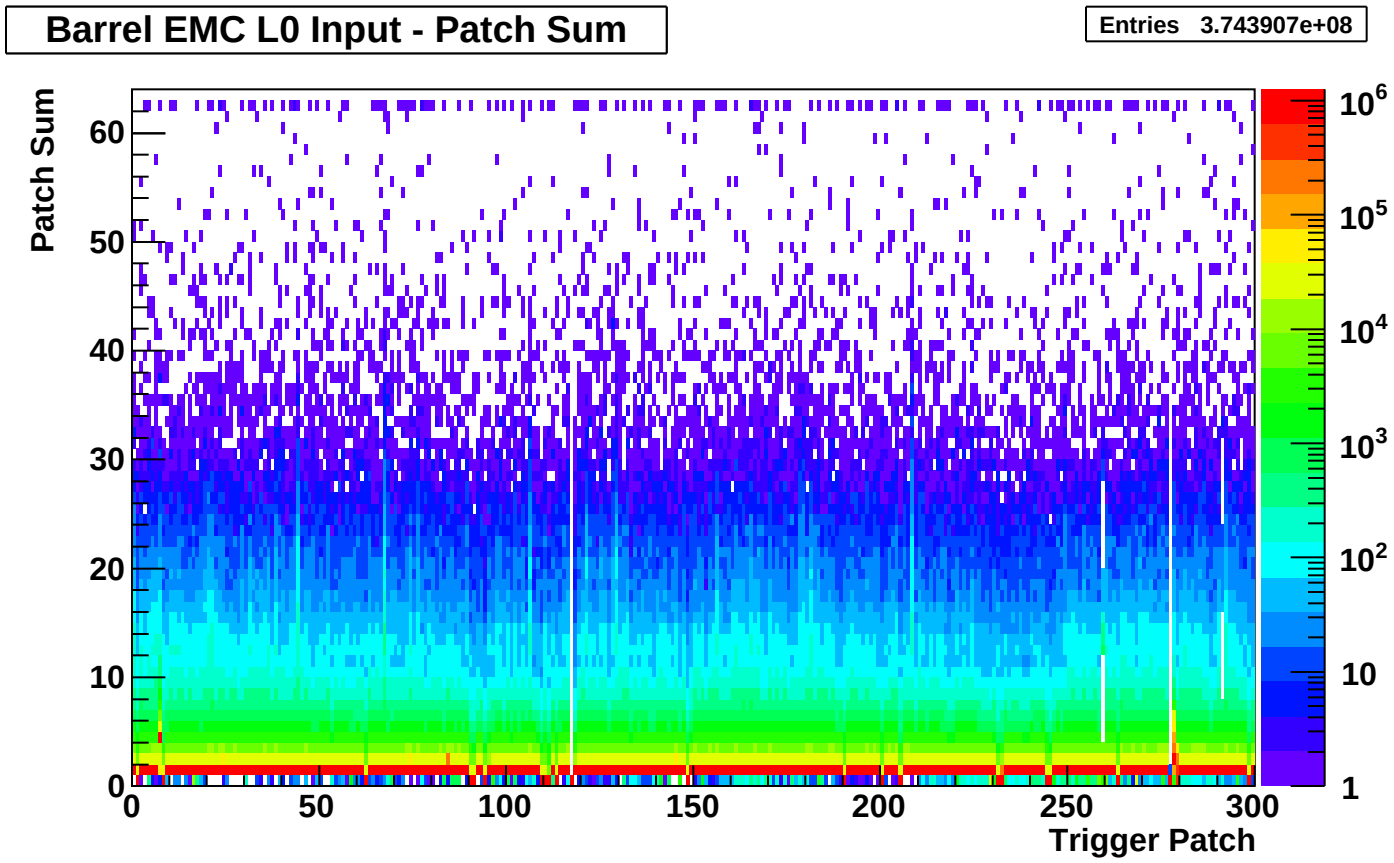

TPC Readout Mask (simulated)
Entries 173804

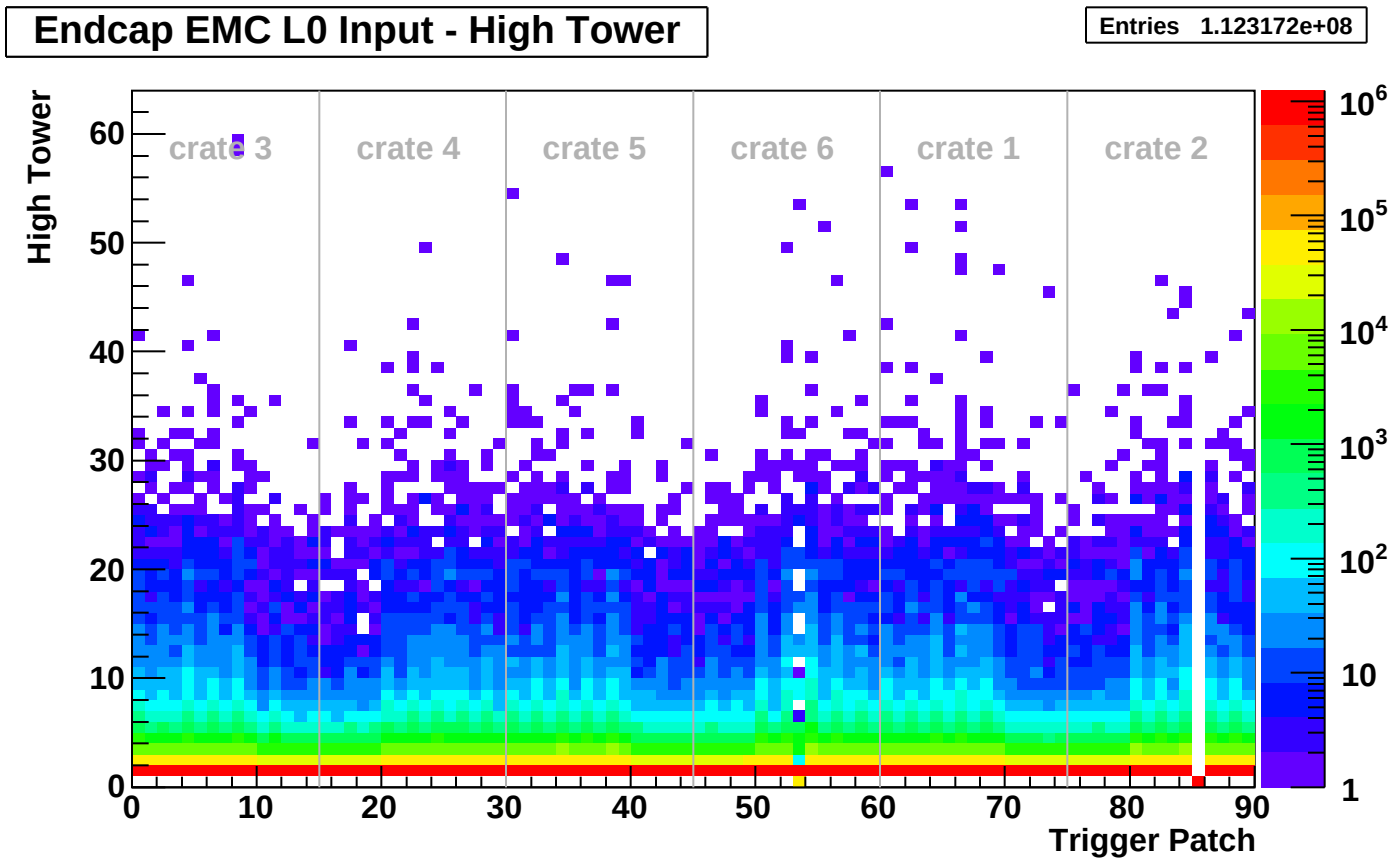

BunchId7Bit (all events)
Entries1247969

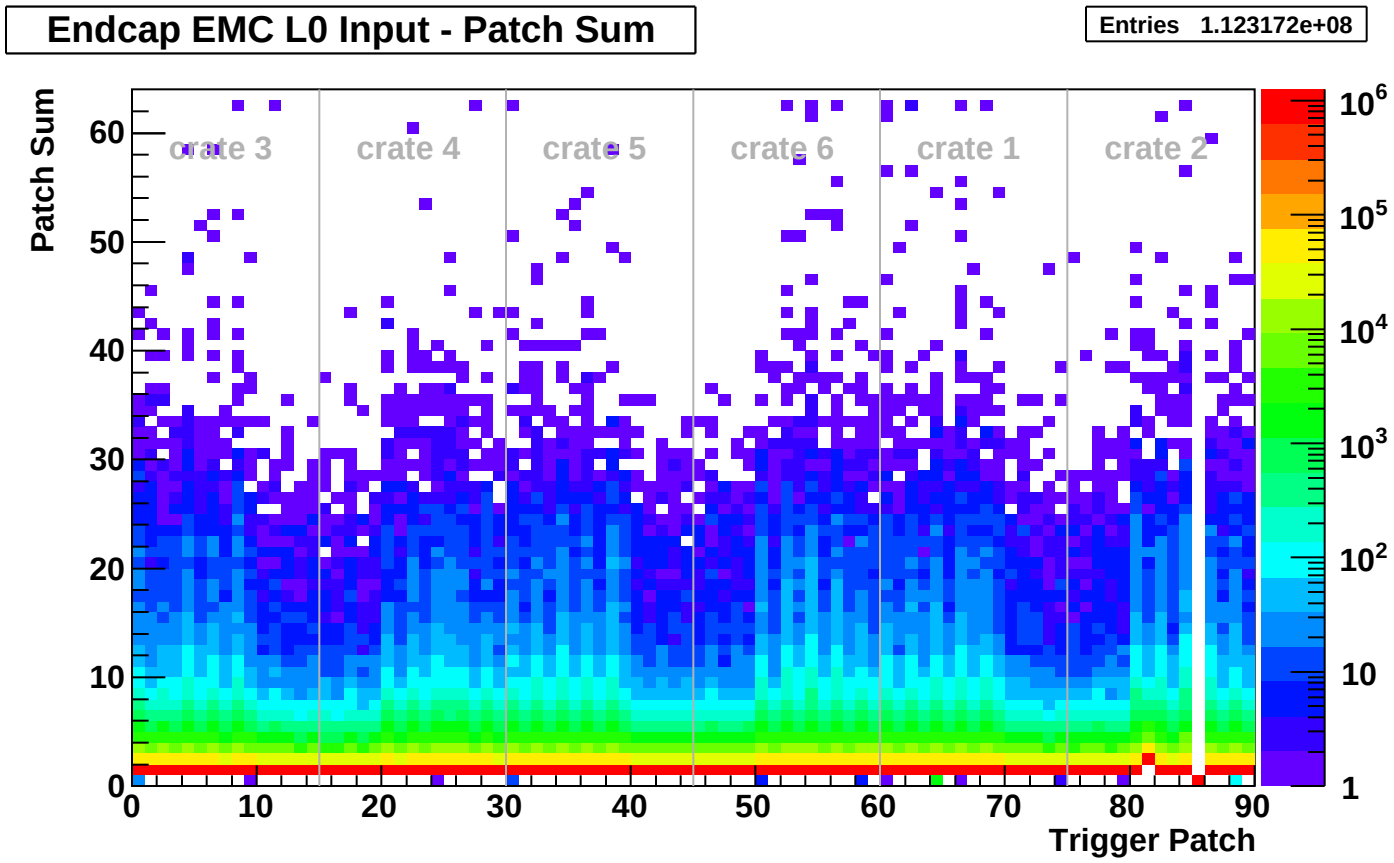

!Blue-Fill
Entries 744

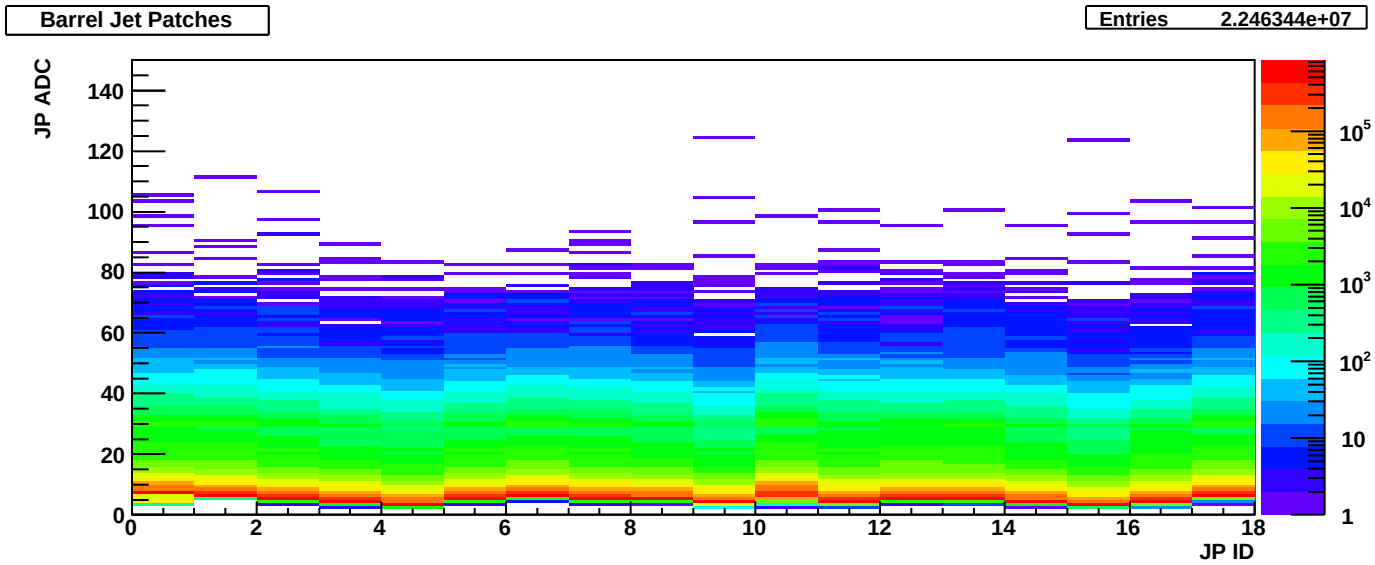

!Yellow-Fill
Entries 49063

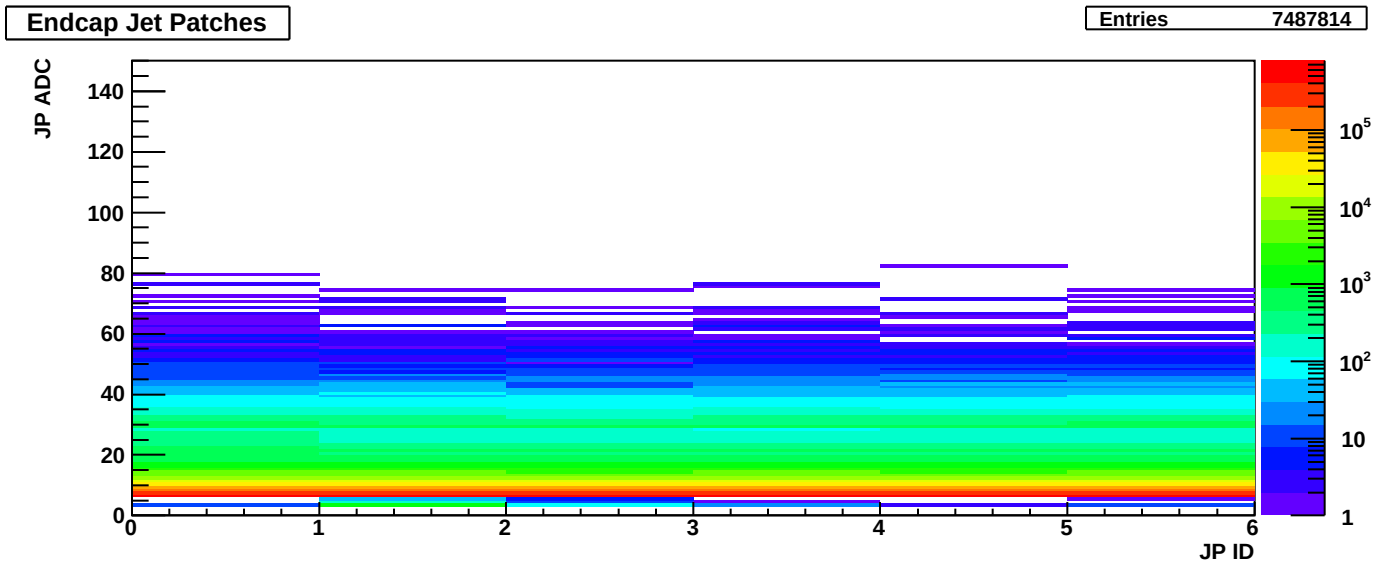

Barrel EMC L0 Input - High Tower
Entries 3.743907e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

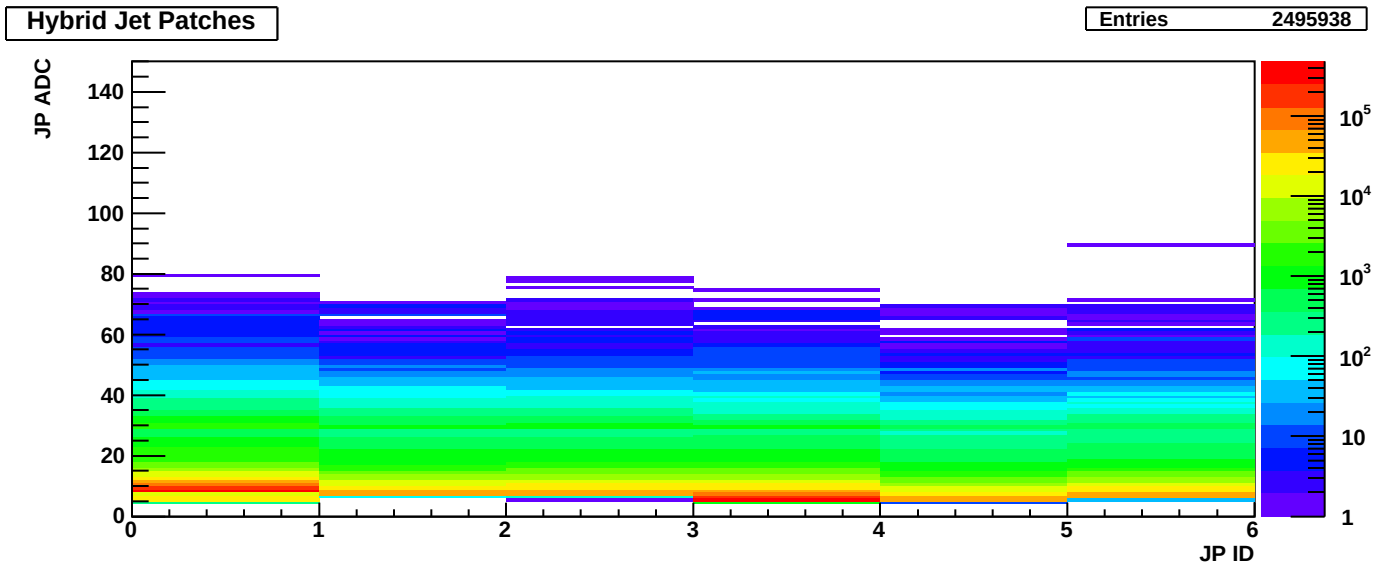

Barrel EMC L0 Input - Patch Sum
Entries 3.743907e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

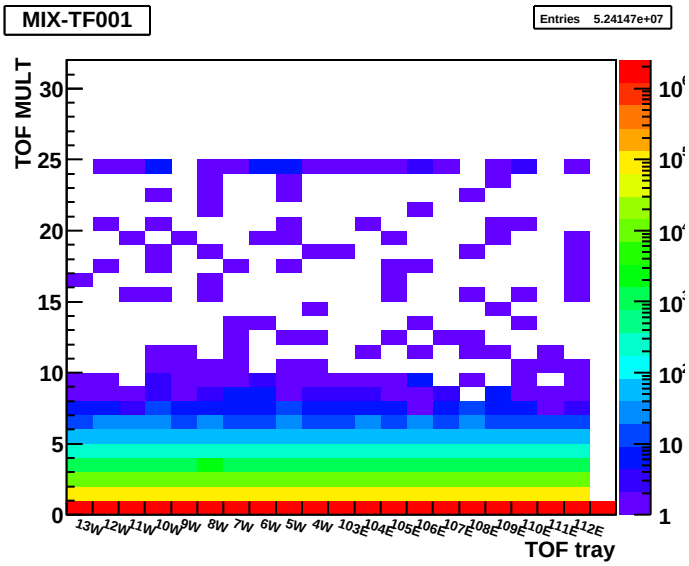

Endcap EMC L0 Input - High Tower
Entries 1.123172e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

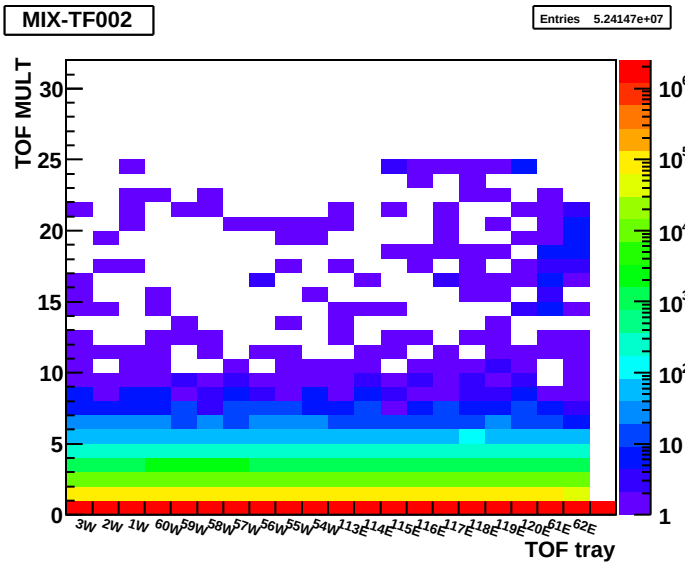

Endcap EMC L0 Input - Patch Sum
Entries 1.123172e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

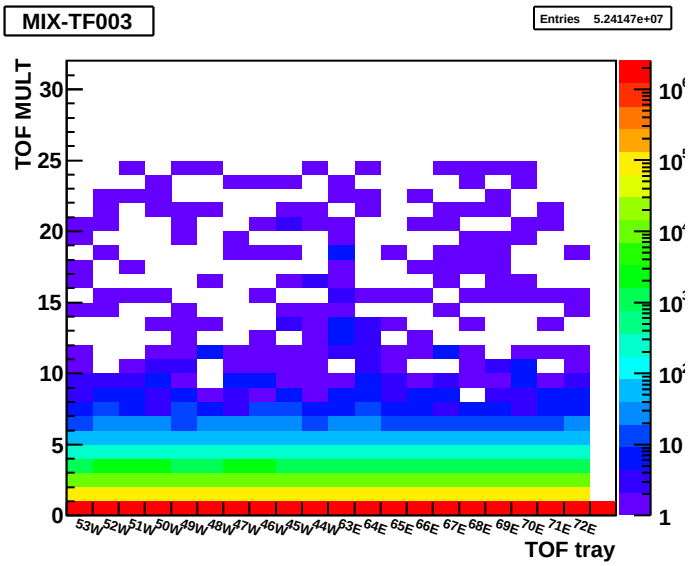

Barrel Jet Patches
Entries 2.246344e+07
JP ADC
JP ID

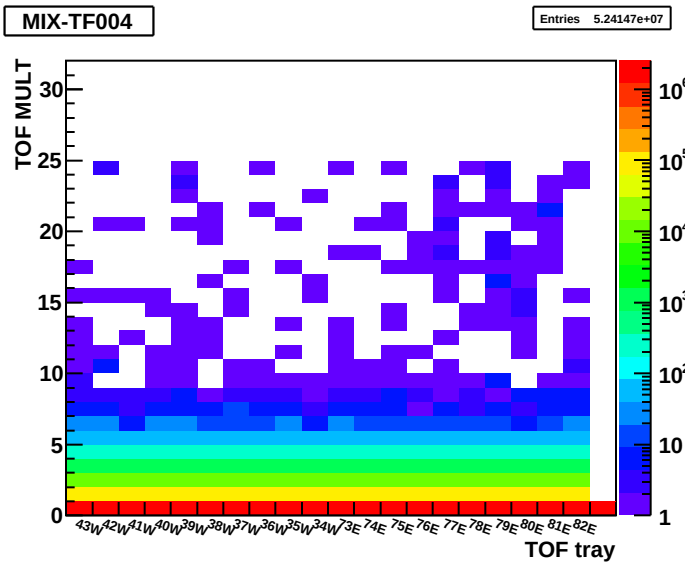

Endcap Jet Patches
Entries 7487814
JP ADC
JP ID

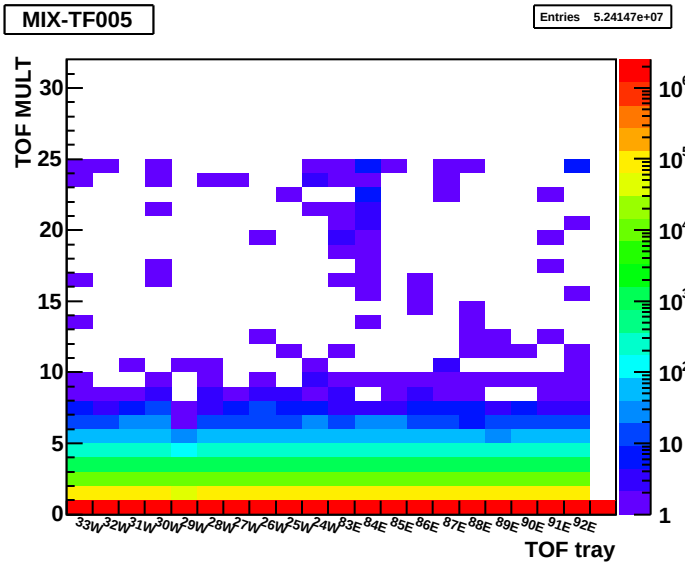

Hybrid Jet Patches
Entries 2495938
JP ADC
JP ID

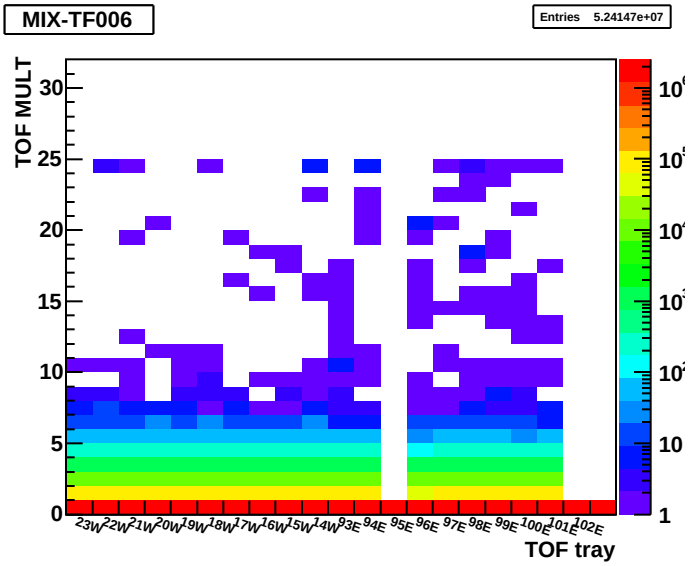

MIX-TF001
Entries 5.24147e+07
TOF MULT
TOF tray

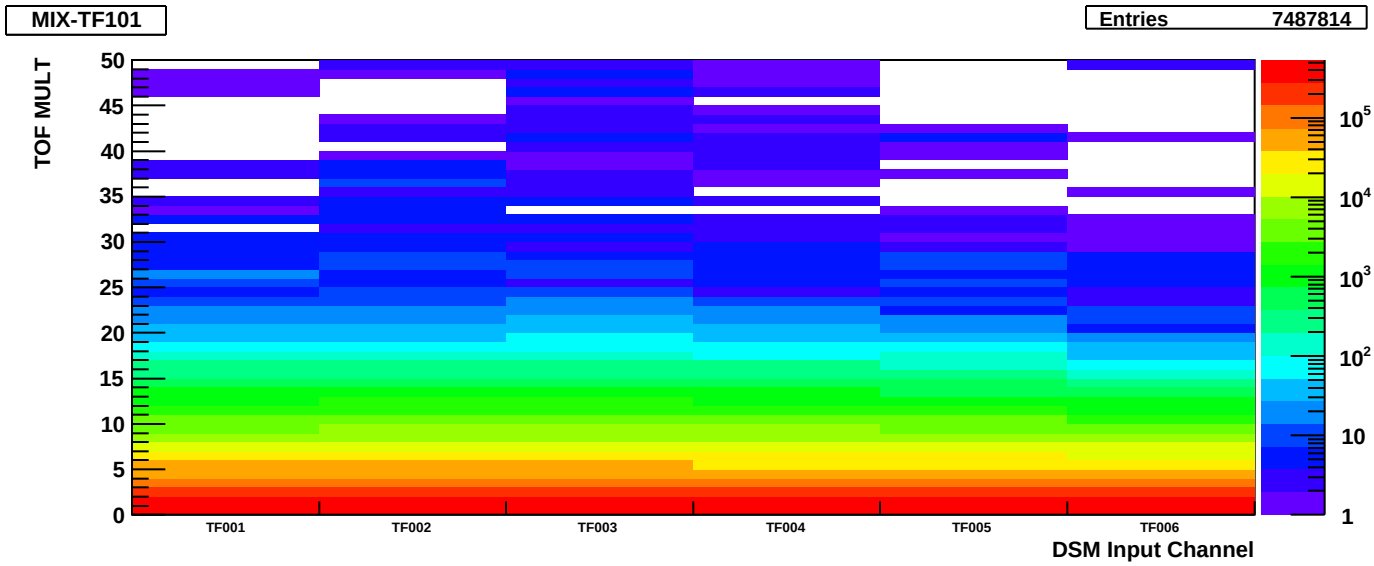

MIX-TF002
Entries 5.24147e+07
TOF MULT
TOF tray

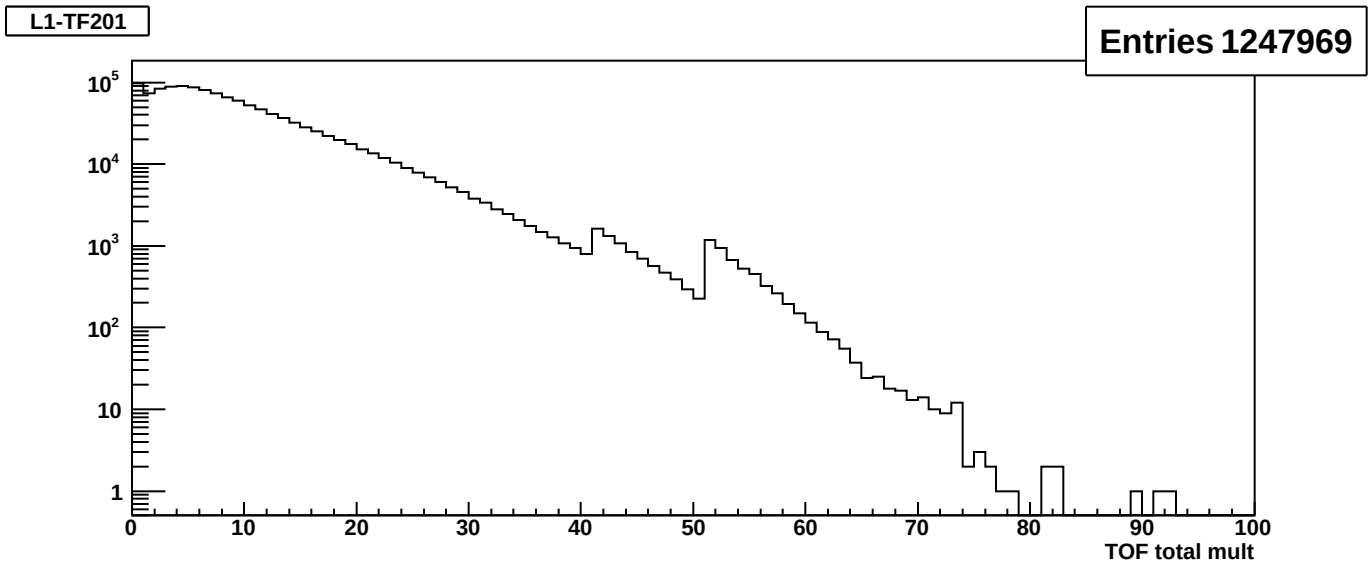

MIX-TF003
Entries 5.24147e+07
TOF MULT
TOF tray

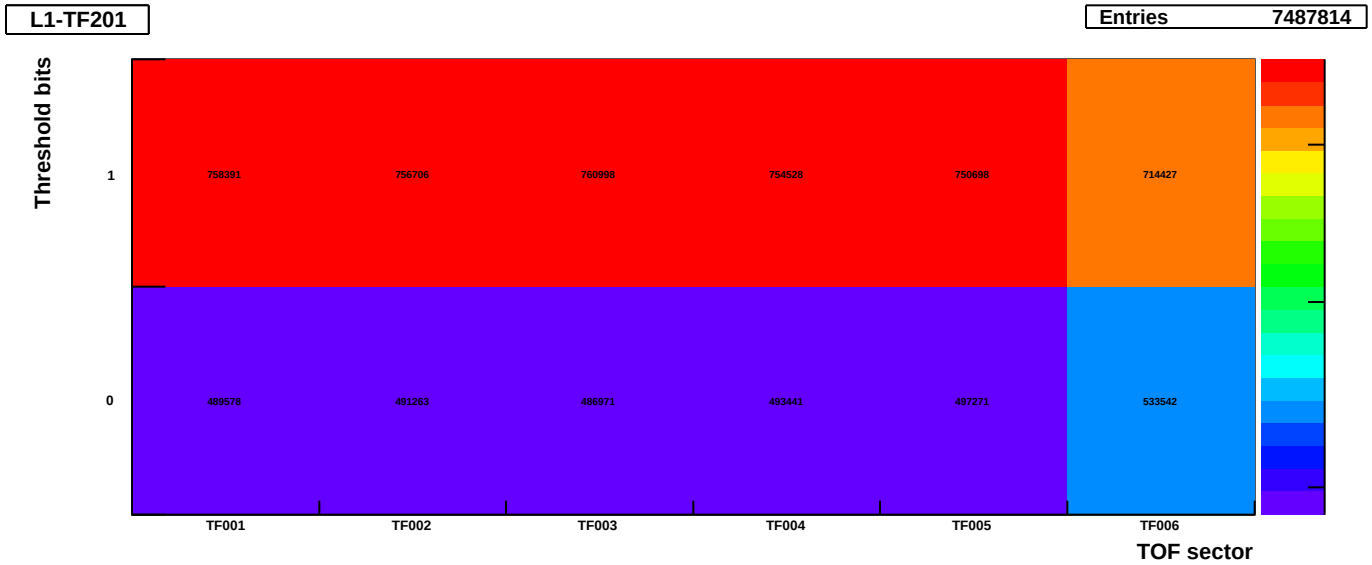

MIX-TF004
Entries 5.24147e+07
TOF MULT
TOF tray

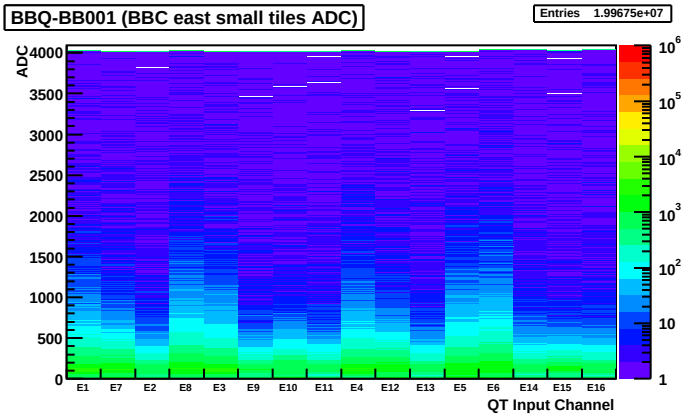

MIX-TF005
Entries 5.24147e+07
TOF MULT
TOF tray

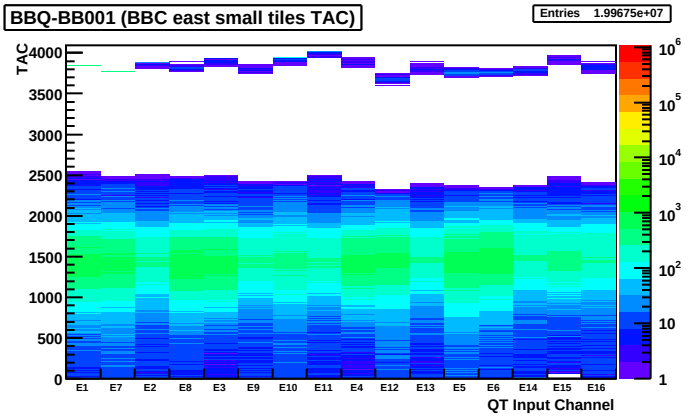

MIX-TF006
Entries 5.24147e+07
TOF MULT
TOF tray

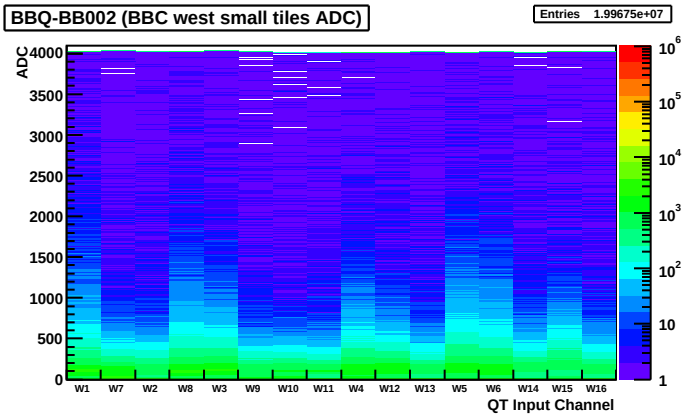

MIX-TF101
Entries 7487814
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

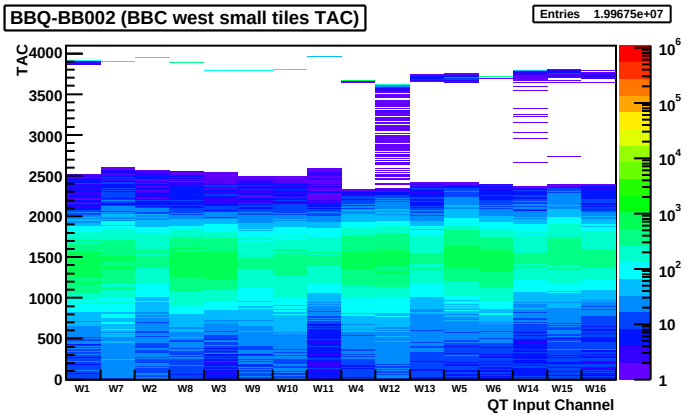

L1-TF201
Entries 1247969
TOF total mult

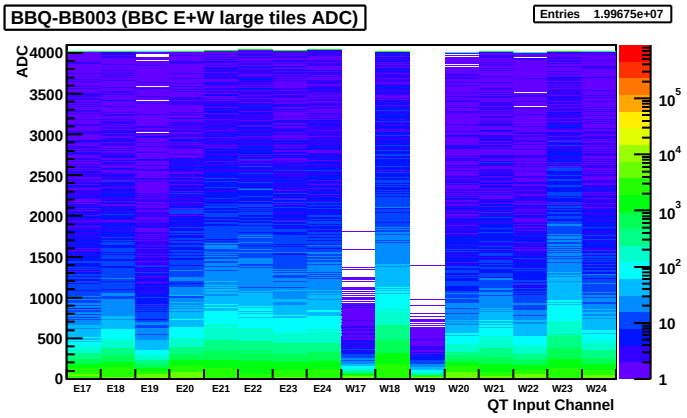

L1-TF201
Entries 7487814
Threshold bits
758391
756706
760998
754528
750698
714427
489578
491263
486971
493441
497271
533542
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

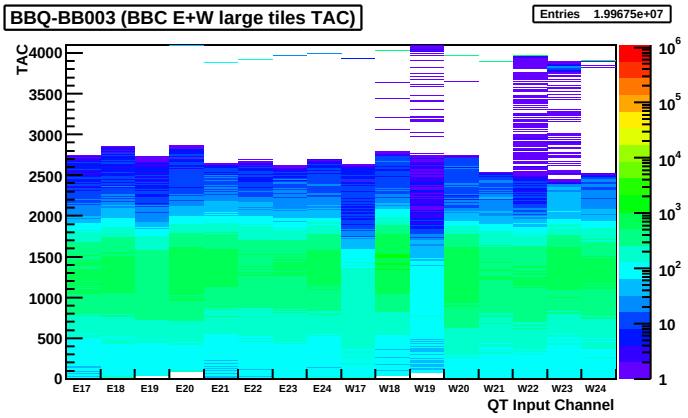

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.99675e+07
ADC
QT Input Channel

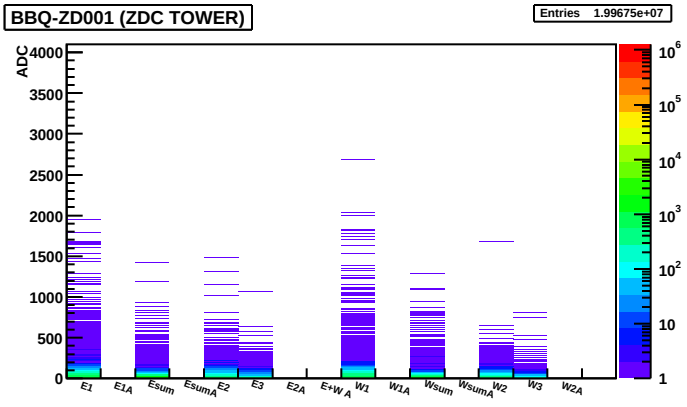

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.99675e+07
TAC
QT Input Channel

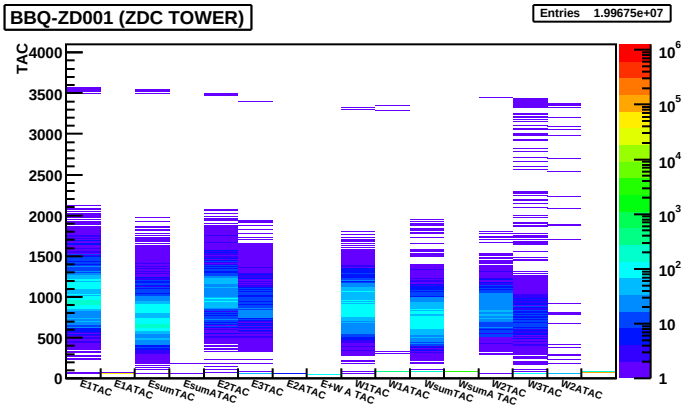

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.99675e+07
ADC
QT Input Channel

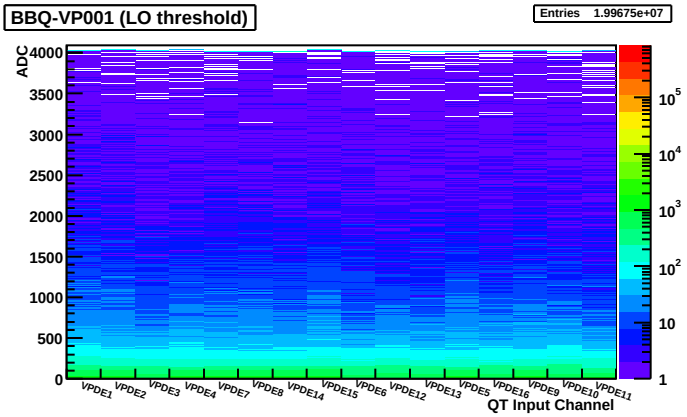

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.99675e+07
TAC
QT Input Channel

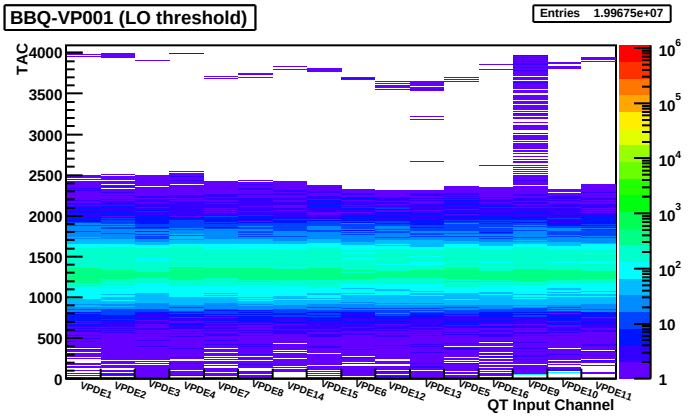

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.99675e+07
ADC
QT Input Channel

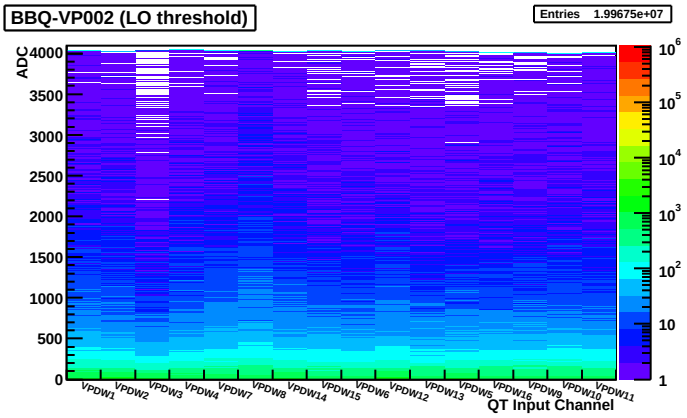

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.99675e+07
TAC
QT Input Channel

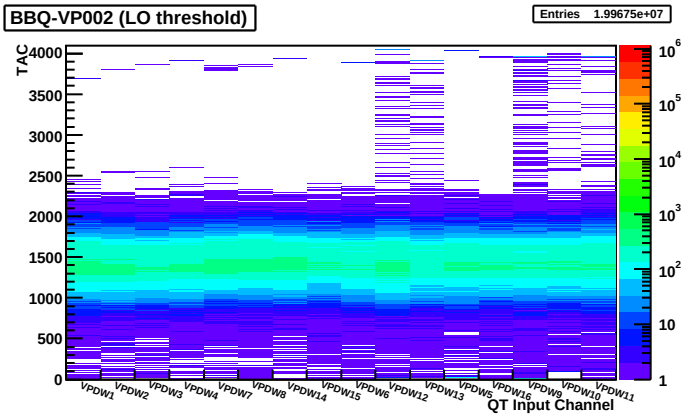

BBQ-ZD001 (ZDC TOWER)
Entries 1.99675e+07
ADC

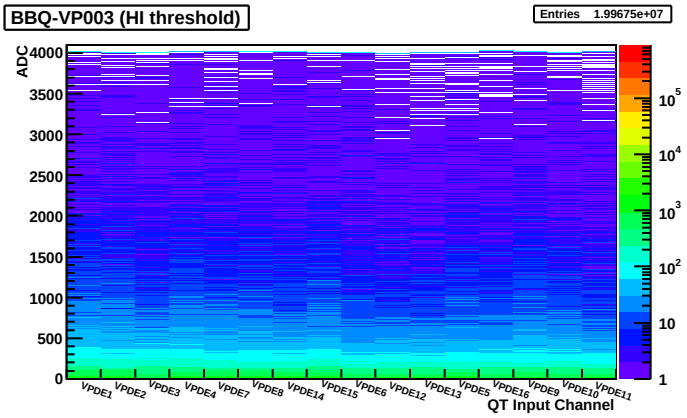

BBQ-ZD001 (ZDC TOWER)
Entries 1.99675e+07
TAC

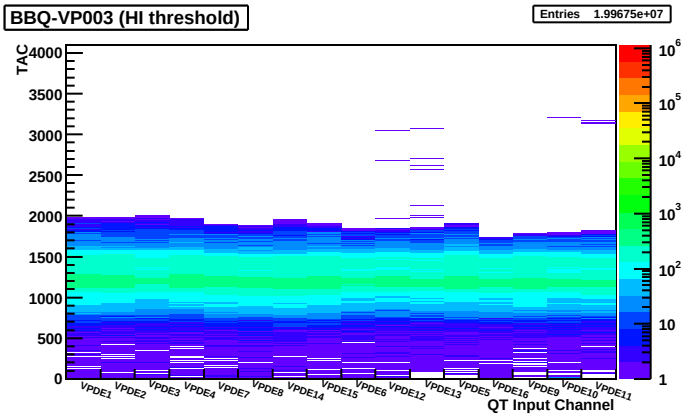

BBQ-VP001 (LO threshold)
Entries 1.99675e+07
ADC
QT Input Channel

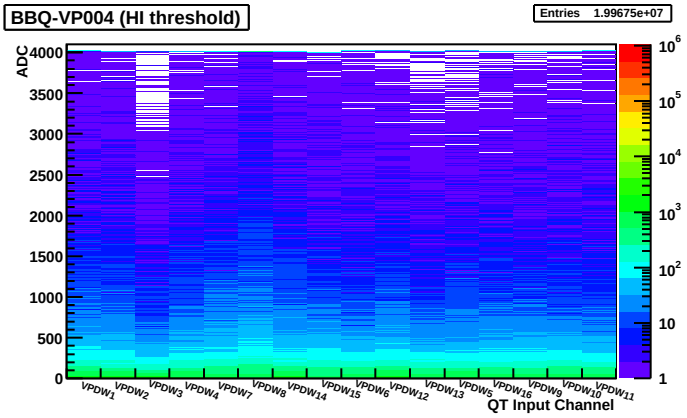

BBQ-VP001 (LO threshold)
Entries 1.99675e+07
TAC
QT Input Channel

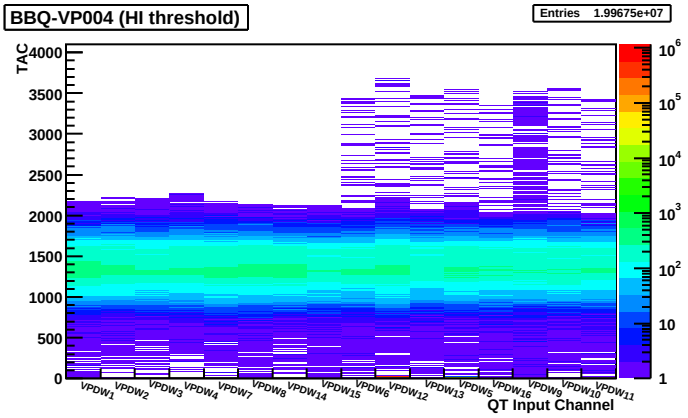

BBQ-VP002 (LO threshold)
Entries 1.99675e+07
ADC
QT Input Channel

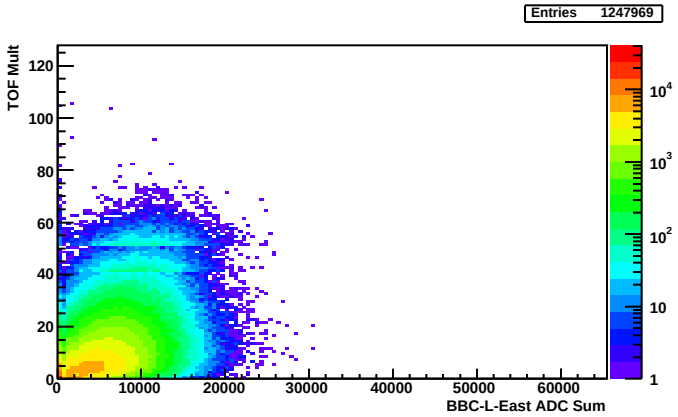

BBQ-VP002 (LO threshold)
Entries 1.99675e+07
TAC
QT Input Channel

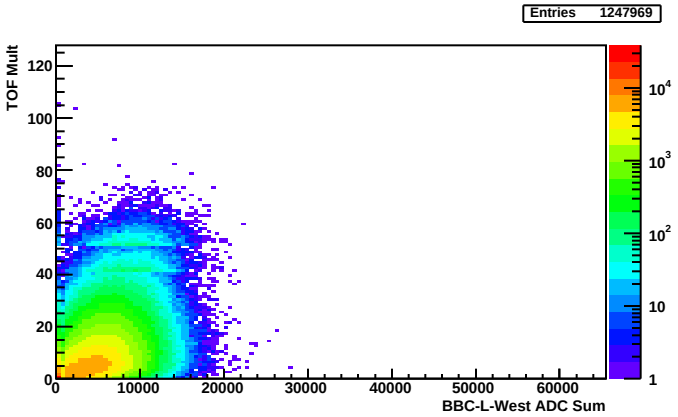

BBQ-VP003 (HI threshold)
Entries 1.99675e+07
ADC
QT Input Channel

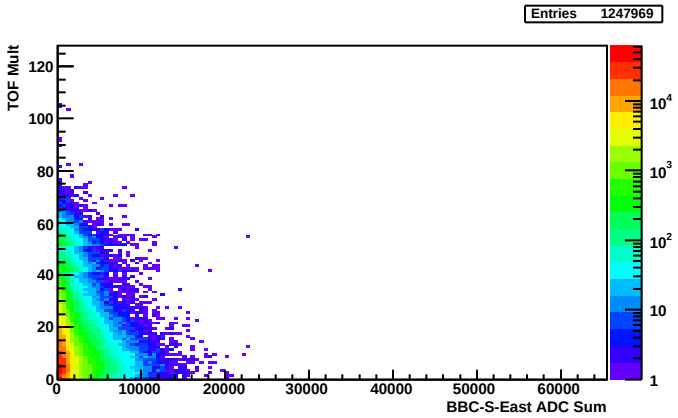

BBQ-VP003 (HI threshold)
Entries 1.99675e+07
TAC
QT Input Channel

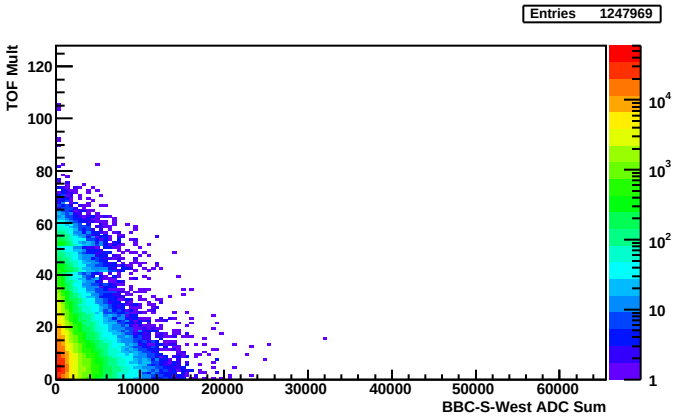

BBQ-VP004 (HI threshold)
Entries 1.99675e+07
ADC
QT Input Channel

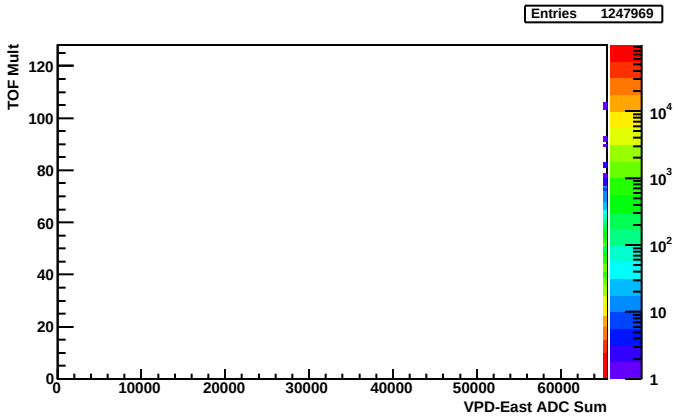

BBQ-VP004 (HI threshold)
Entries 1.99675e+07
TAC
QT Input Channel

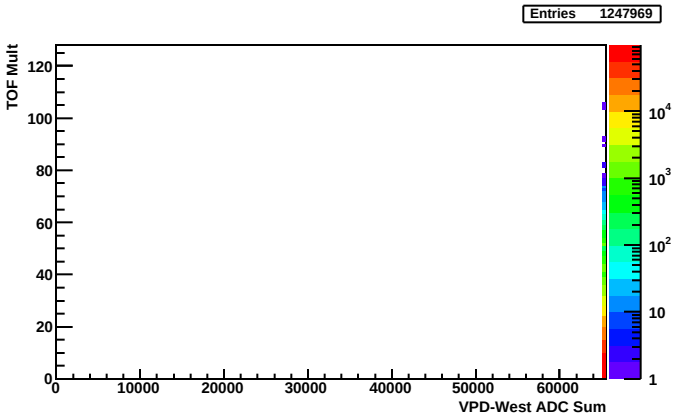

Entries 1247969
TOF Mult
BBC-L-East ADC Sum

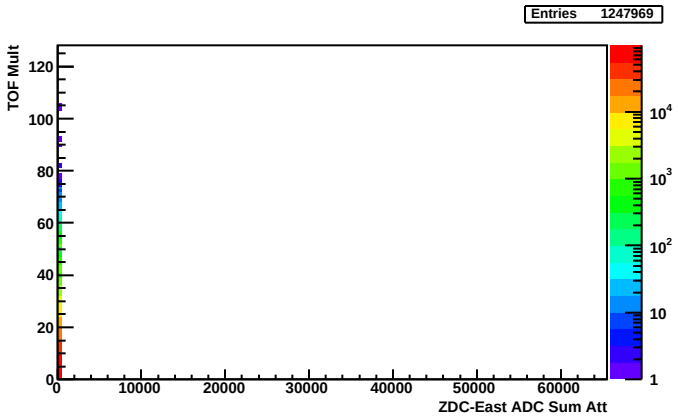

Entries 1247969
TOF Mult
BBC-L-West ADC Sum

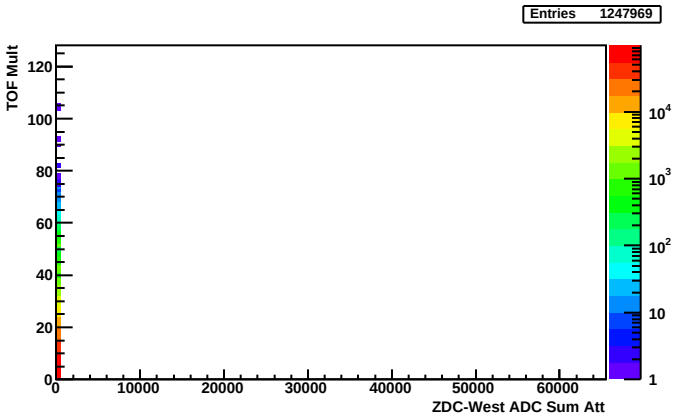

Entries 1247969
TOF Mult
BBC-S-East ADC Sum

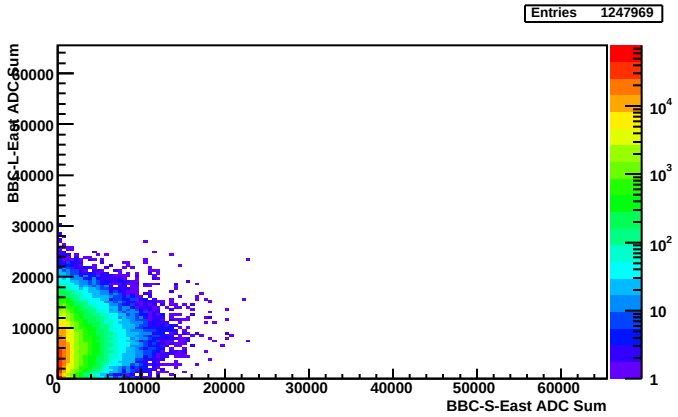

Entries 1247969
TOF Mult
BBC-S-West ADC Sum

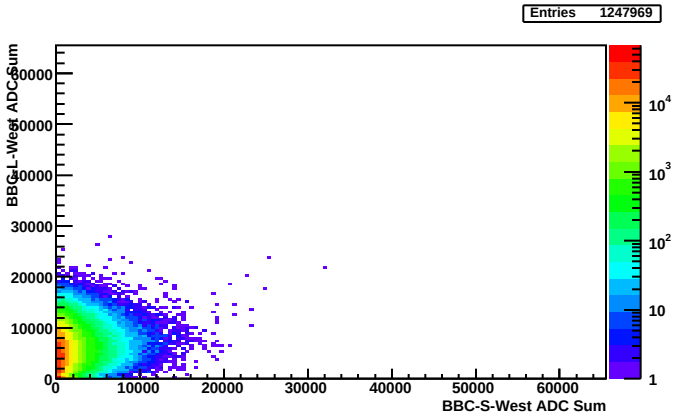

Entries 1247969
TOF Mult
VPD-East ADC Sum

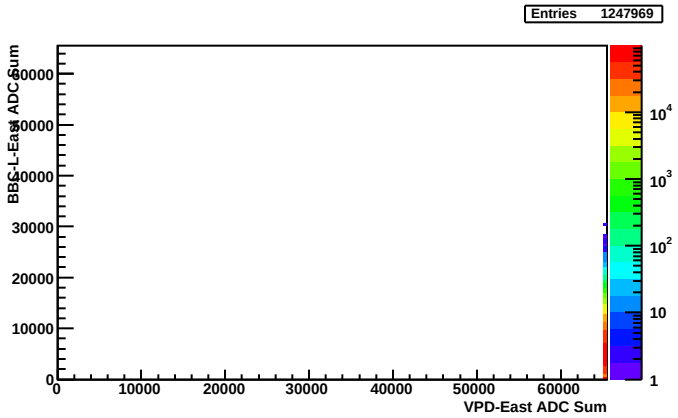

Entries 1247969
TOF Mult
VPD-West ADC Sum

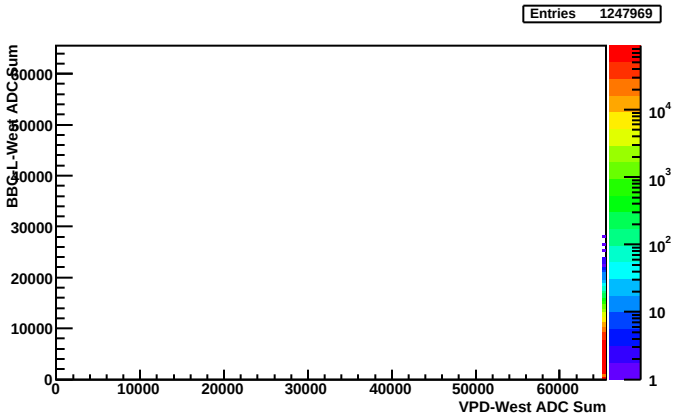

Entries 1247969
TOF Mult
ZDC-East ADC Sum Att

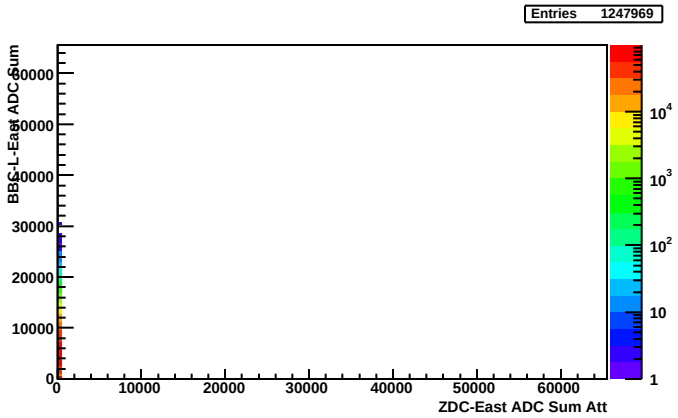

Entries 1247969
TOF Mult
ZDC-West ADC Sum Att

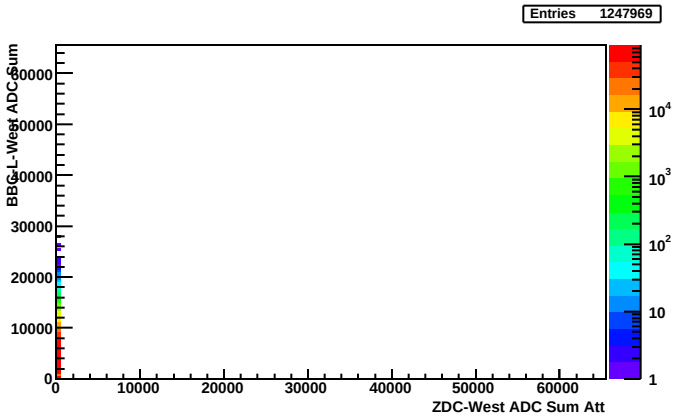

Entries 1247969
BBC-L-East ADC Sum
BBC-S-East ADC Sum

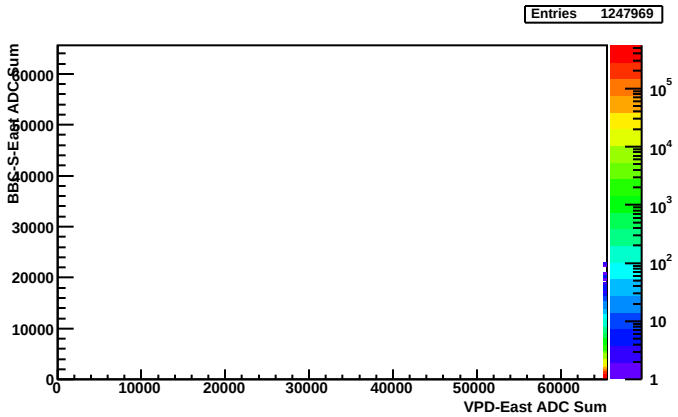

Entries 1247969
BBC-L-West ADC Sum
BBC-S-West ADC Sum

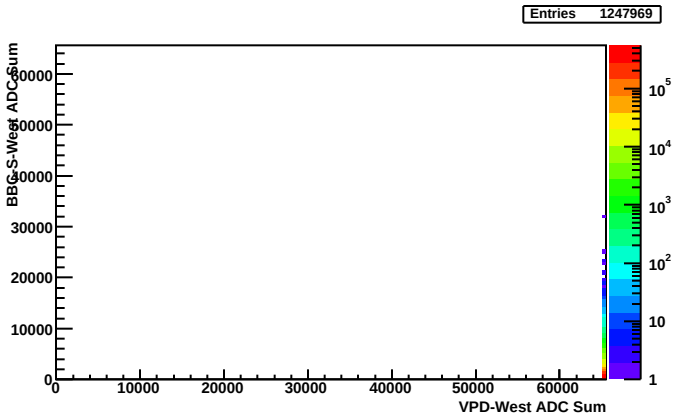

Entries 1247969
BBC-L-East ADC Sum
VPD-East ADC Sum

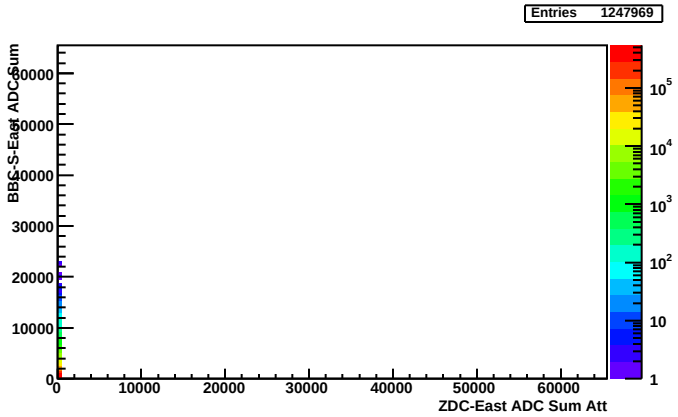

Entries 1247969
BBC-L-West ADC Sum
VPD-West ADC Sum

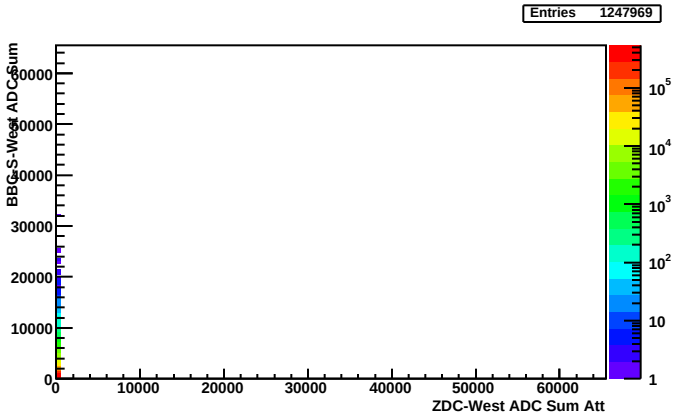

Entries 1247969
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

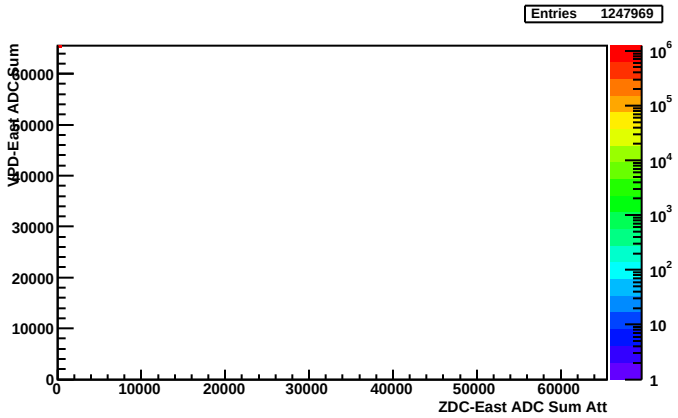

Entries 1247969
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

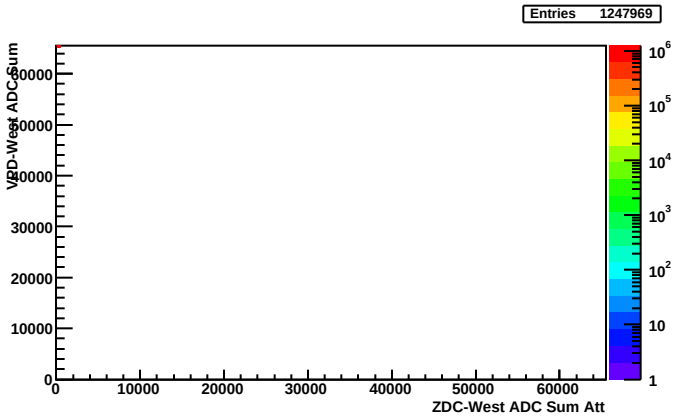

Entries 1247969
BBC-S-East ADC Sum
VPD-East ADC Sum

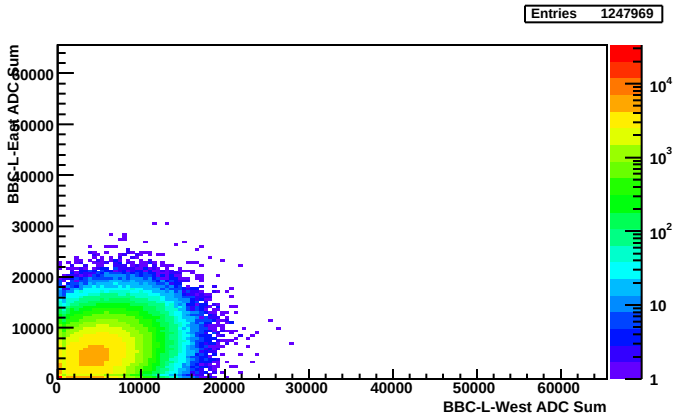

Entries 1247969
BBC-S-West ADC Sum
VPD-West ADC Sum

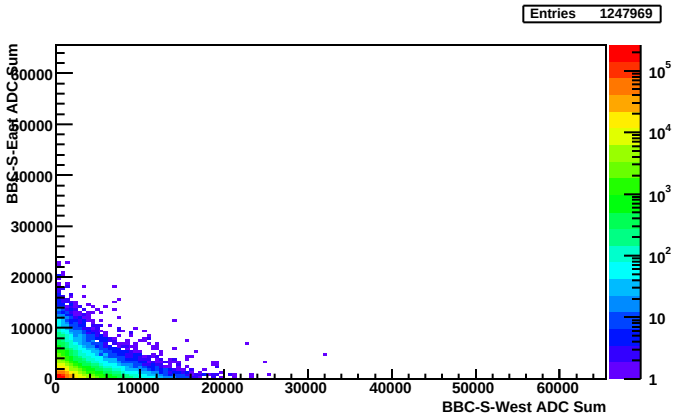

Entries 1247969
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

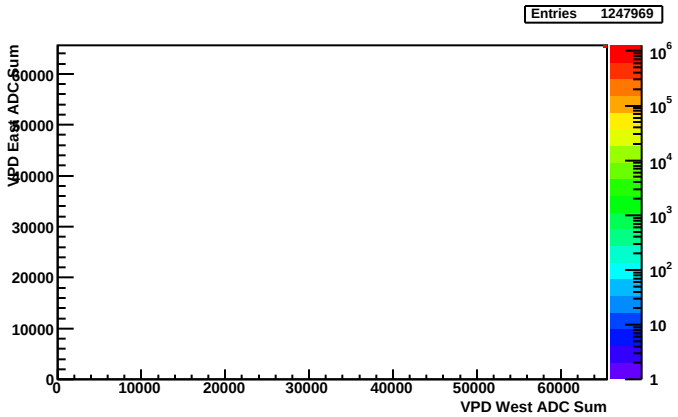

Entries 1247969
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

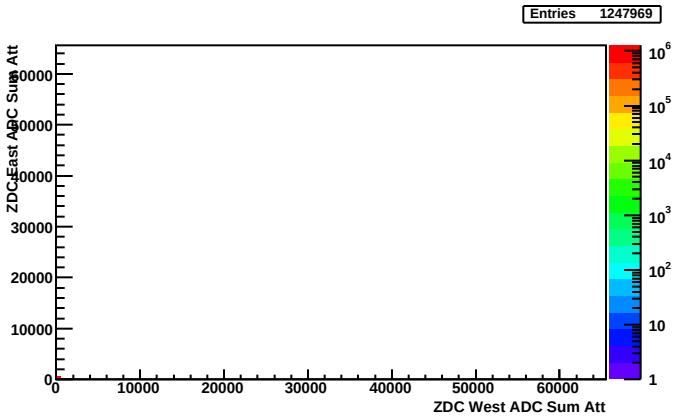

Entries 1247969
VPD-East ADC Sum
ZDC-East ADC Sum Att

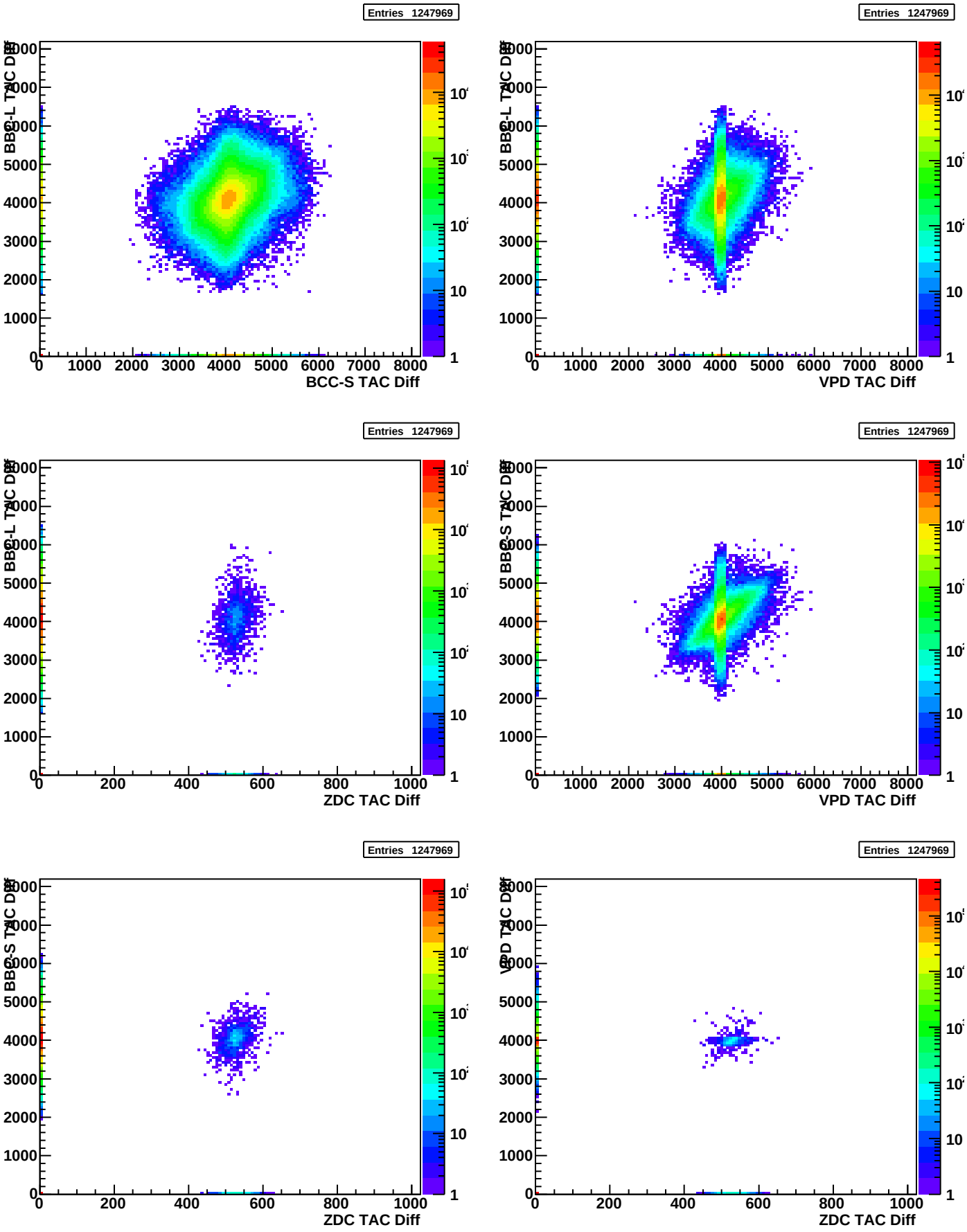

Entries 1247969
VPD-West ADC Sum
ZDC-West ADC Sum Att

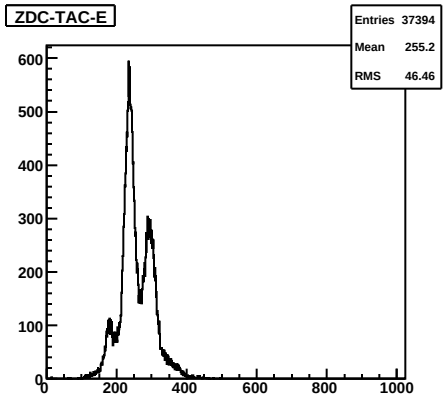

Entries 1247969
BBC-L-East ADC Sum
BBC-L-West ADC Sum

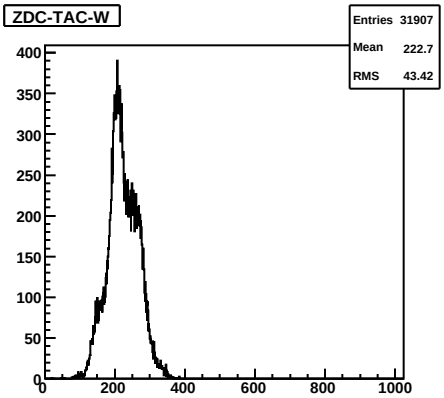

Entries 1247969
BBC-S-East ADC Sum
BBC-S-West ADC Sum

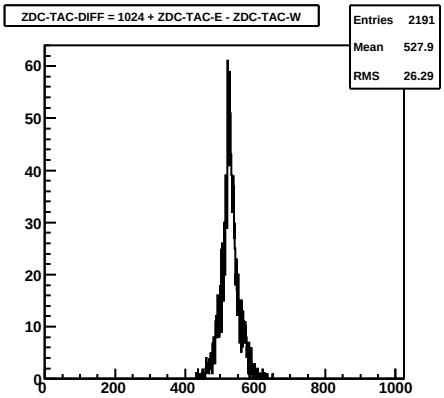

Entries 1247969
VPD East ADC Sum
VPD West ADC Sum

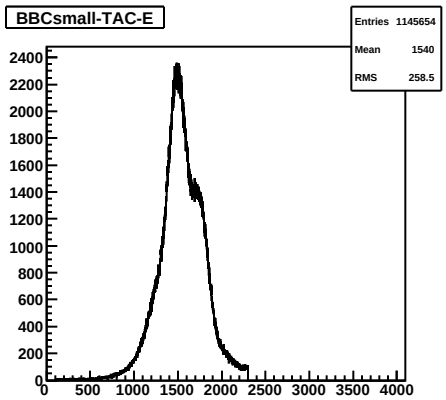

Entries 1247969
ZDC East ADC Sum Att
ZDC West ADC Sum Att

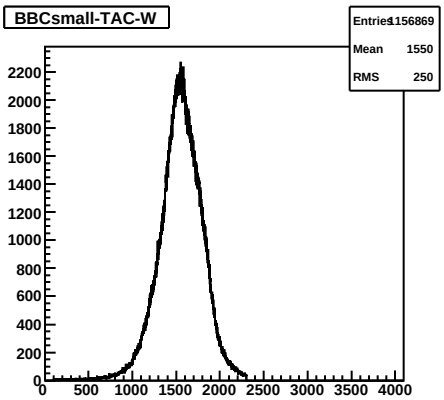

Entries 1247969
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1247969
BBC-L TAC Diff
VPD TAC Diff
Entries 1247969
BBC-L TAC Diff
ZDC TAC Diff
Entries 1247969
BBC-S TAC Diff
VPD TAC Diff
Entries 1247969
BBC-S TAC Diff
ZDC TAC Diff
Entries 1247969
VPD TAC Diff
ZDC TAC Diff

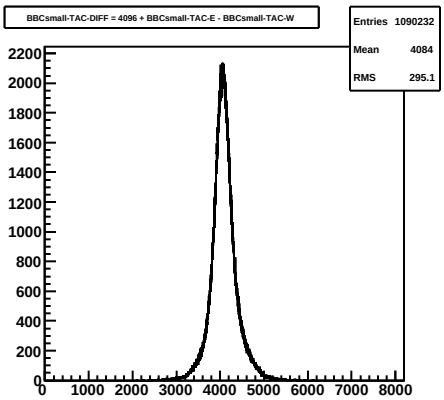

ZDC-TAC-E
Entries 37394
Mean 255.2
RMS 46.46

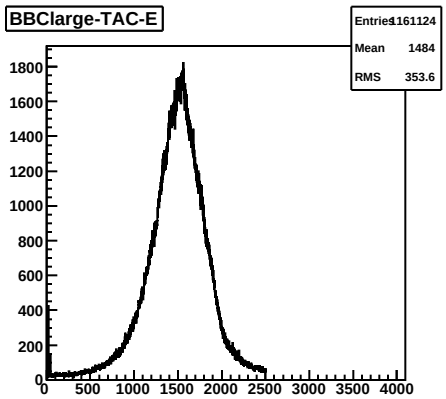

ZDC-TAC-W
Entries 31907
Mean 222.7
RMS 43.42

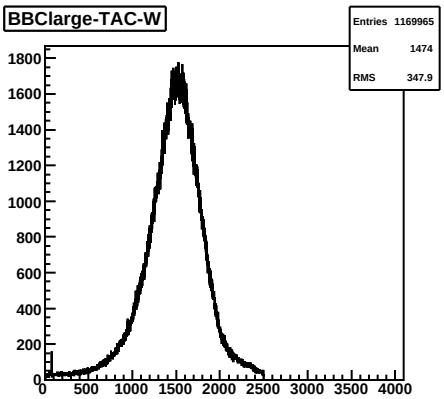

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2191
Mean 527.9
RMS 26.29

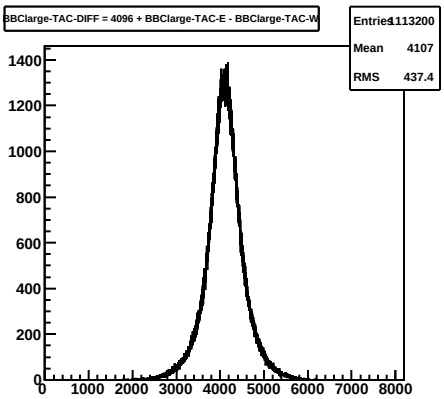

BBCsmall-TAC-E
Entries 1145654
Mean 1540
RMS 258.5

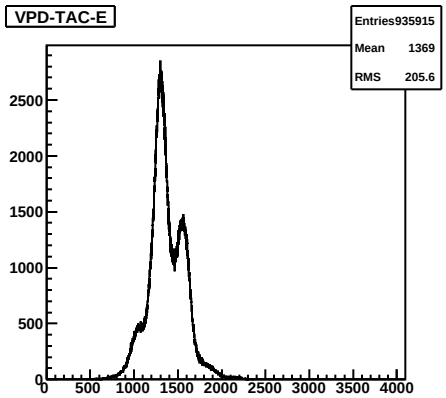

BBCsmall-TAC-W
Entries 1156869
Mean 1550
RMS 250

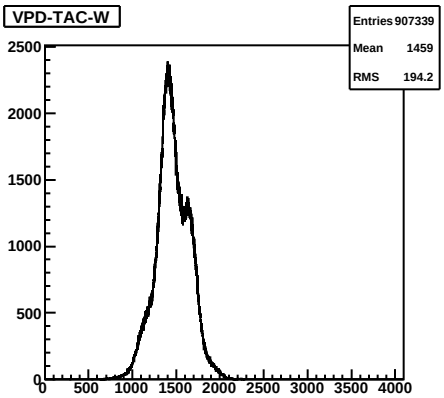

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1090232
Mean 4084
RMS 295.1

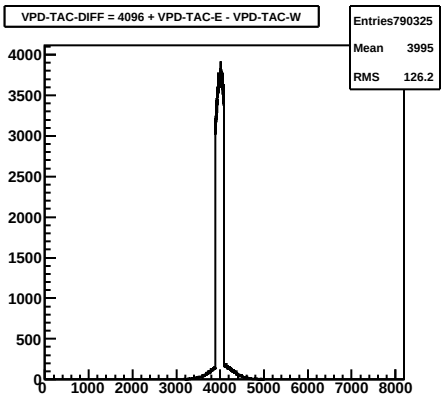

BBClarge-TAC-E
Entries 1161124
Mean 1484
RMS 353.6

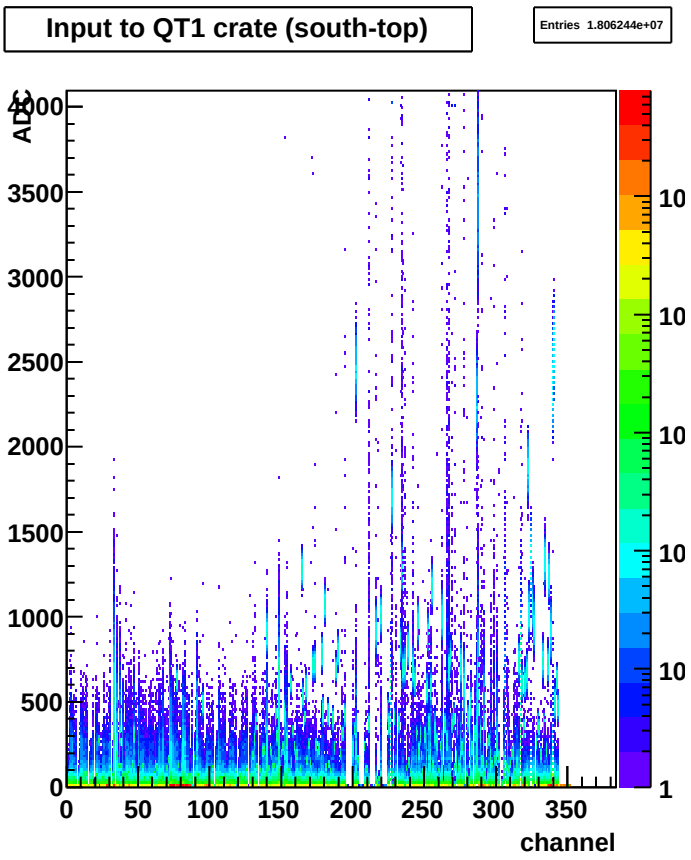

BBClarge-TAC-W
Entries 1169965
Mean 1474
RMS 347.9

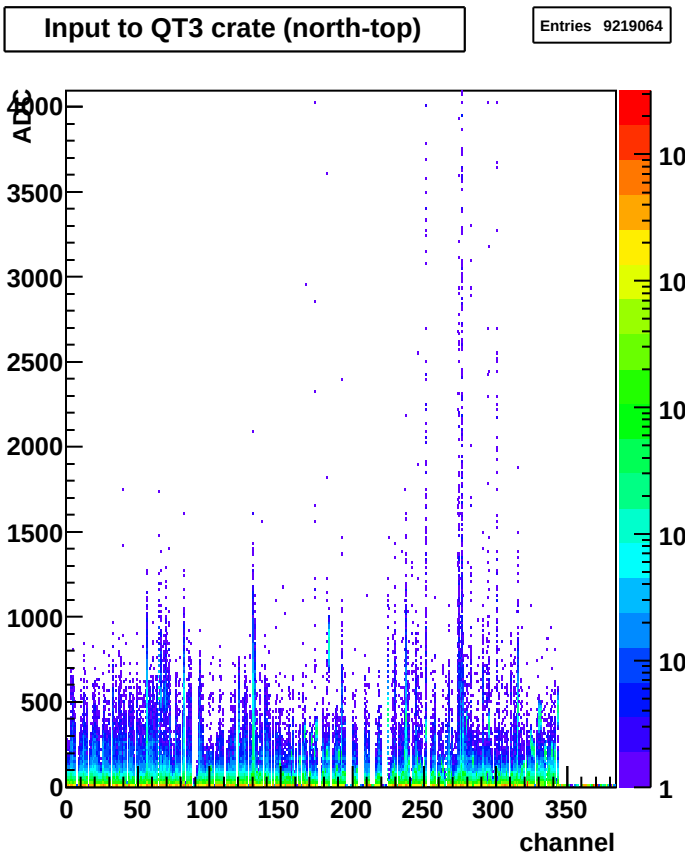

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1113200
Mean 4107
RMS 437.4

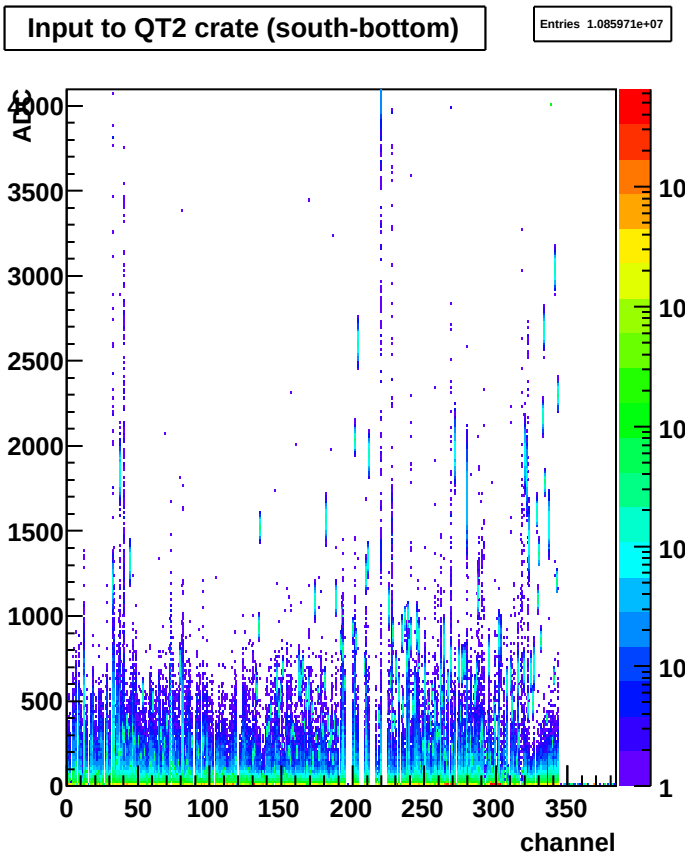

VPD-TAC-E
Entries 935915
Mean 1369
RMS 205.6

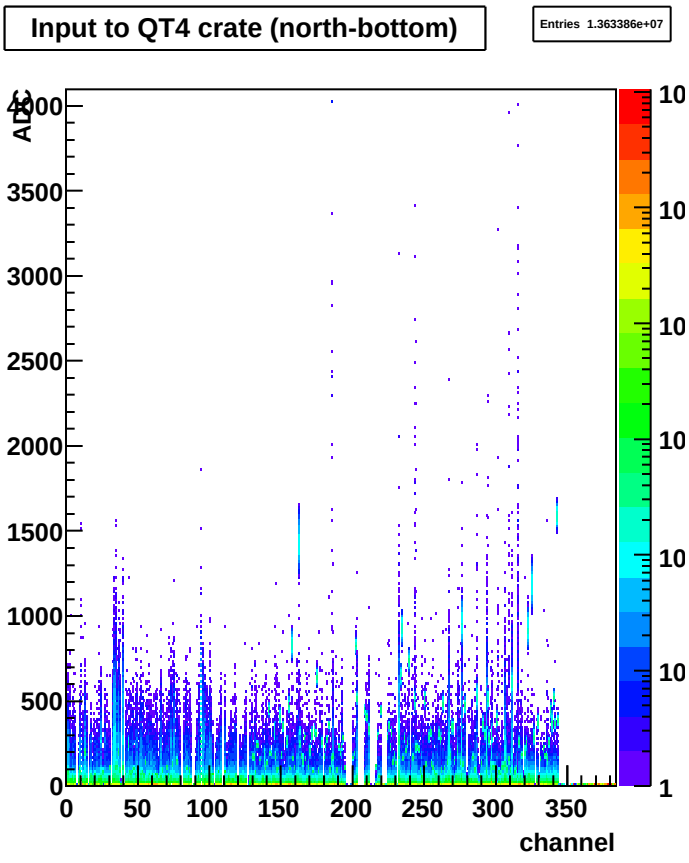

VPD-TAC-W
Entries 907339
Mean 1459
RMS 194.2

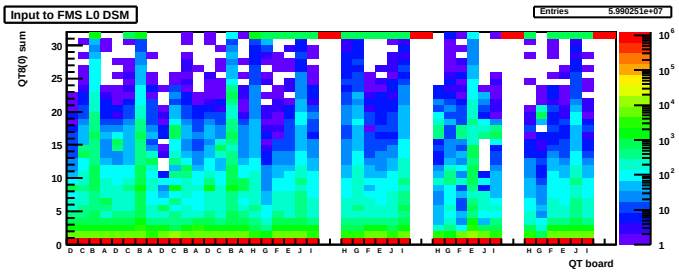

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 790325
Mean 3995
RMS 126.2

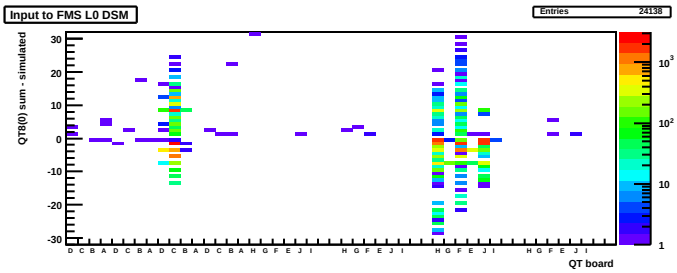

Input to QT1 crate (south-top)
Entries 1.806244e+07
ADC
channel

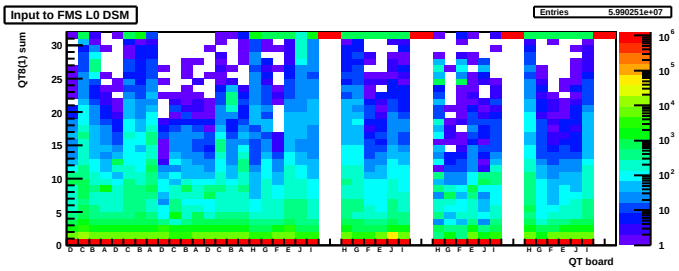

Input to QT3 crate (north-top)
Entries 9219064
ADC
channel

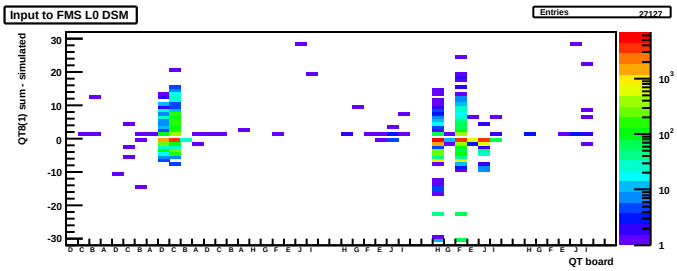

Input to QT2 crate (south-bottom)
Entries 1.085971e+07
ADC
channel

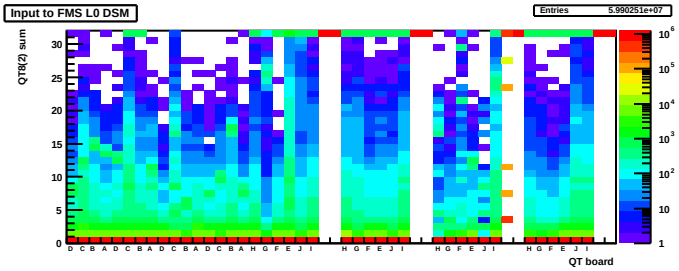

Input to QT4 crate (north-bottom)
Entries 1.363386e+07
ADC
channel

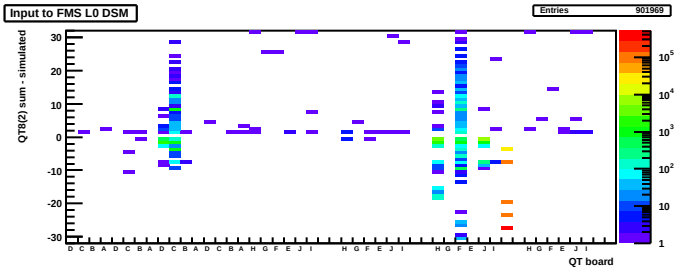

Input to FMS L0 DSM
Entries 5.990251e+07
QT8(0) sum
QT board

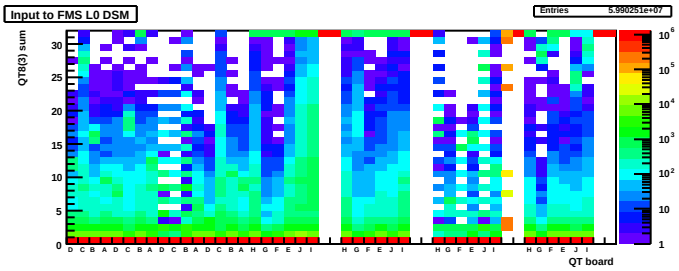

Input to FMS L0 DSM
Entries 24138
QT8(0) sum - simulated
QT board

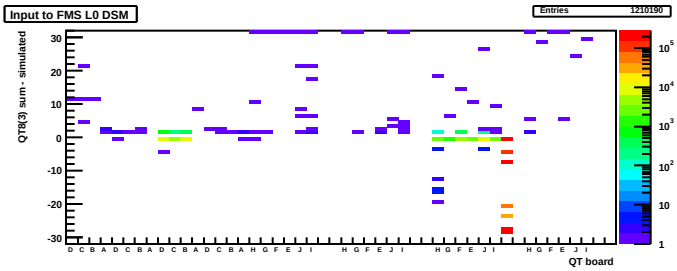

Input to FMS L0 DSM
Entries 5.990251e+07
QT8(1) sum
QT board

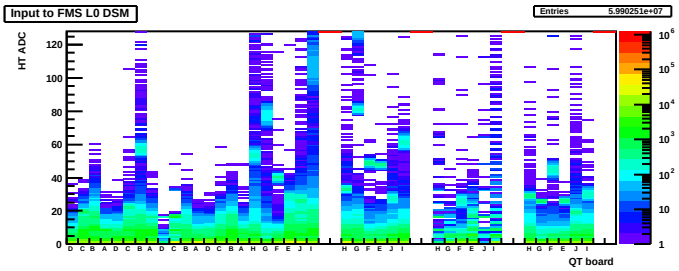

Input to FMS L0 DSM
Entries 27127
QT8(1) sum - simulated
QT board

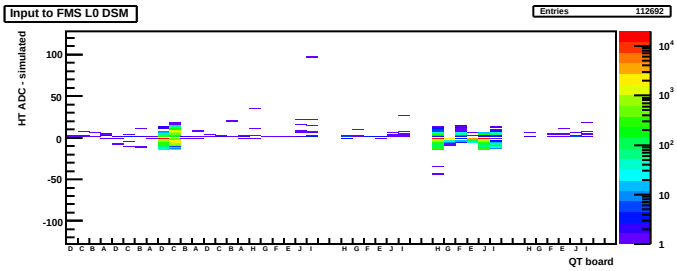

Input to FMS L0 DSM
Entries 5.990251e+07
QT8(2) sum
QT board

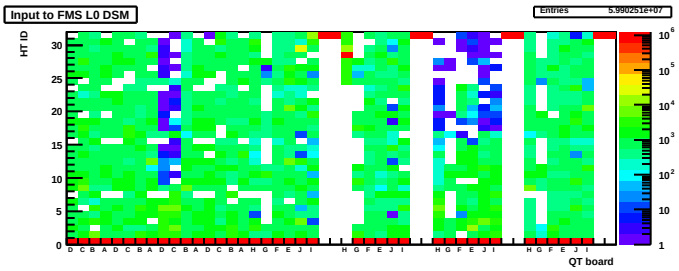

Input to FMS L0 DSM
Entries 901969
QT8(2) sum - simulated
QT board

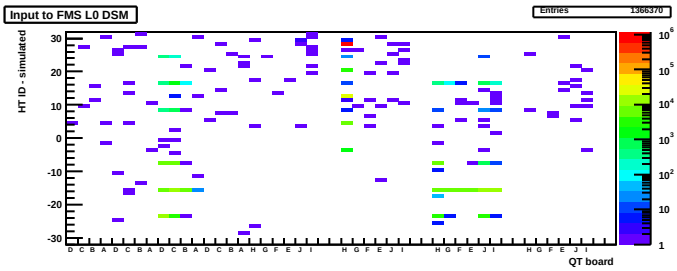

Input to FMS L0 DSM
Entries 5.990251e+07
QT8(3) sum
QT board

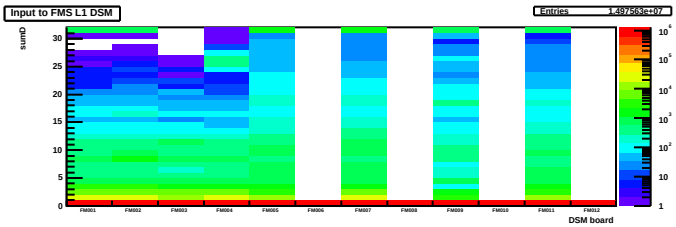

Input to FMS L0 DSM
Entries 1210190
QT8(3) sum - simulated
QT board

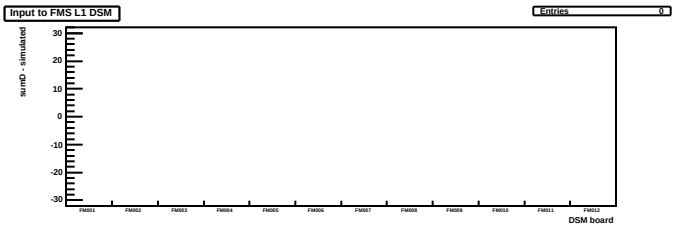

Input to FMS L0 DSM
Entries 5.990251e+07
HT ADC
QT board

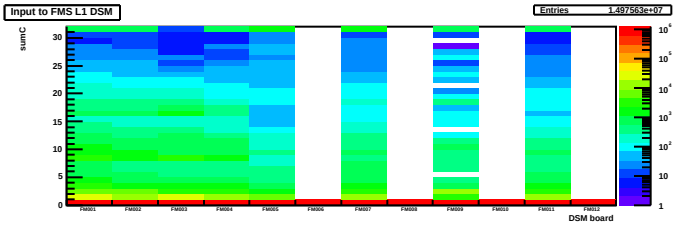

Input to FMS L0 DSM
Entries 112692
HT ADC - simulated
QT board

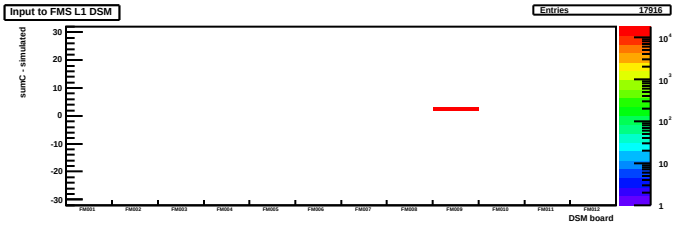

Input to FMS L0 DSM
Entries 5.990251e+07
HT ID
QT board

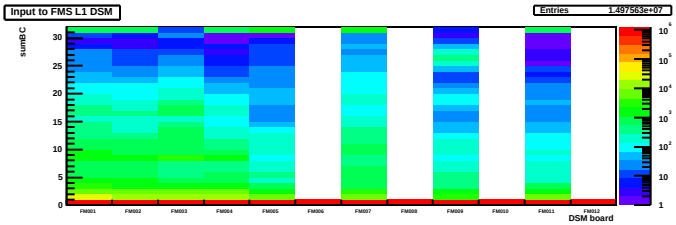

Input to FMS L0 DSM
Entries 1366370
HT ID - simulated
QT board

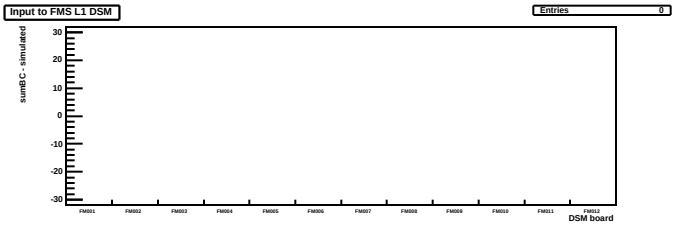

Input to FMS L1 DSM
Entries 1.497563e+07
sumD
DSM board

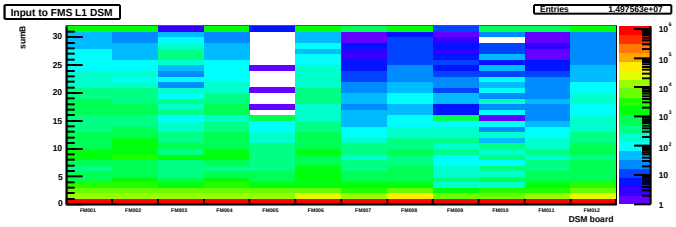

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

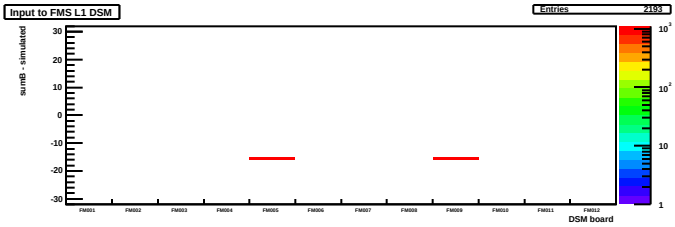

Input to FMS L1 DSM
Entries 1.497563e+07
sumC
DSM board

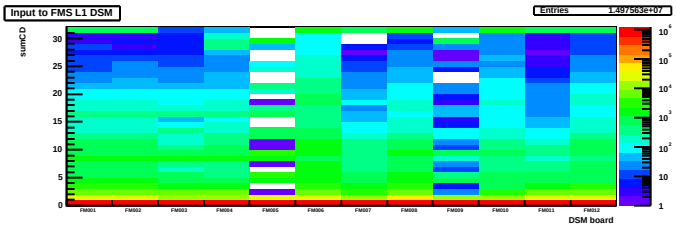

Input to FMS L1 DSM
Entries 17916
sumC - simulated
DSM board

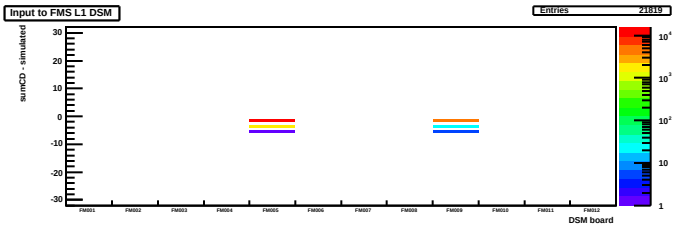

Input to FMS L1 DSM
Entries 1.497563e+07
sumBC
DSM board

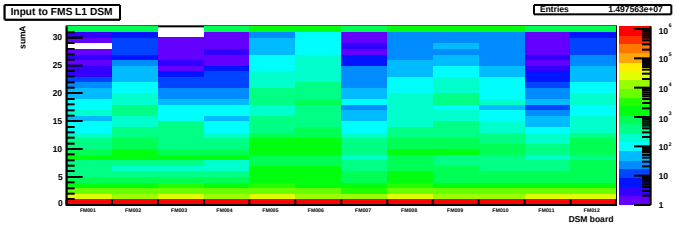

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

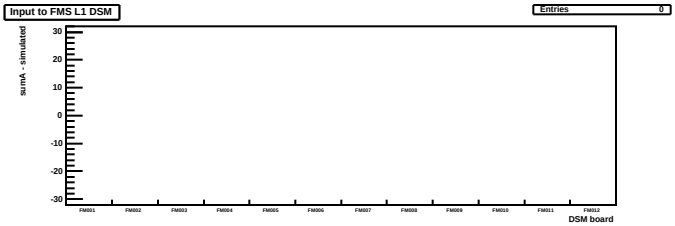

Input to FMS L1 DSM
Entries 1.497563e+07
sumB
DSM board

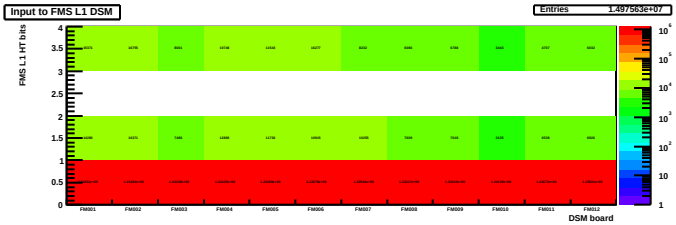

Input to FMS L1 DSM
Entries 2193
sumB - simulated
DSM board

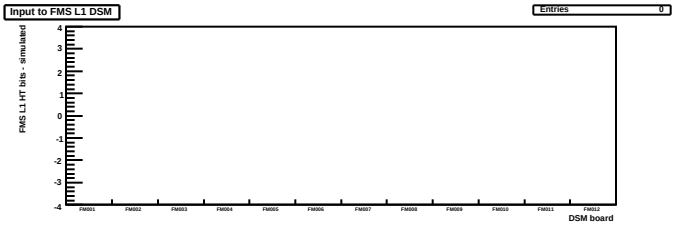

Input to FMS L1 DSM
Entries 1.497563e+07
sumCD
DSM board

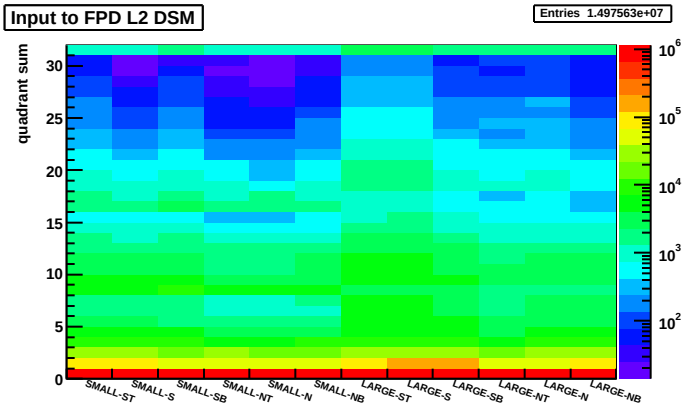

Input to FMS L1 DSM
Entries 21819
sumCD - simulated
DSM board

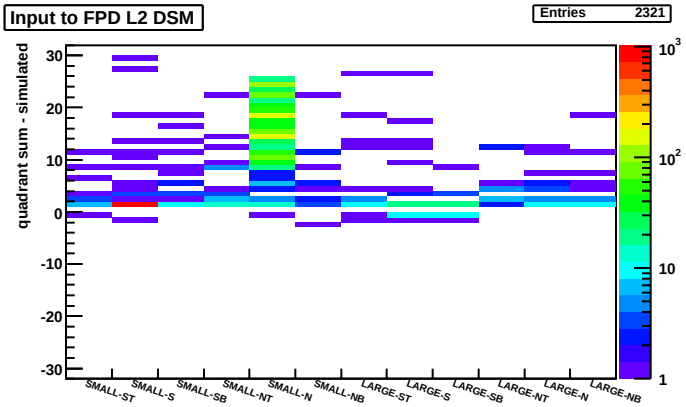

Input to FMS L1 DSM
Entries 1.497563e+07
sumA
DSM board

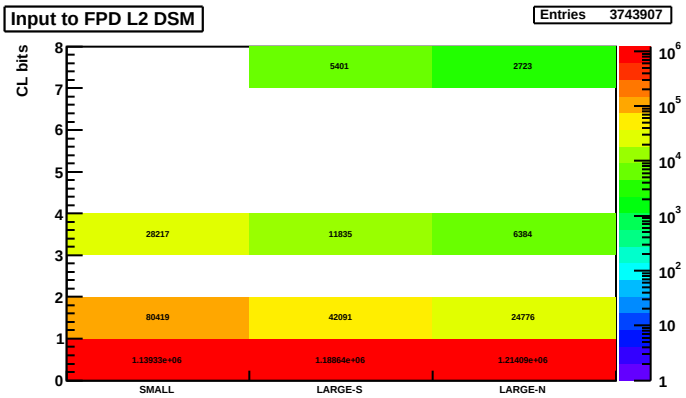

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

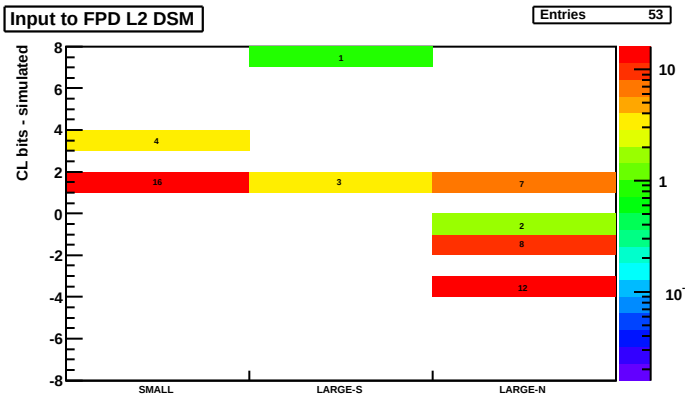

Input to FMS L1 DSM
Entries 1.497563e+07
FMS L1 HT bits
DSM board

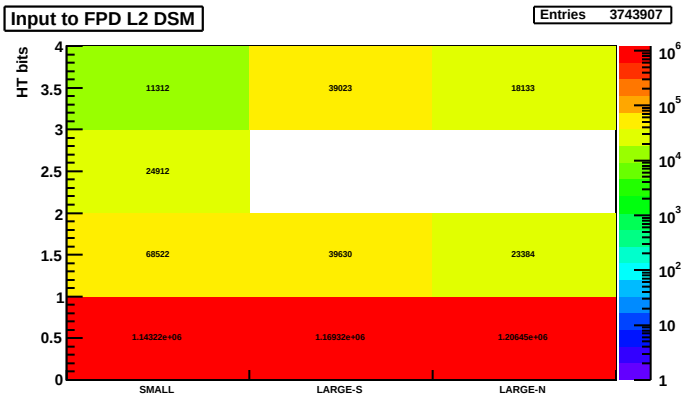

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

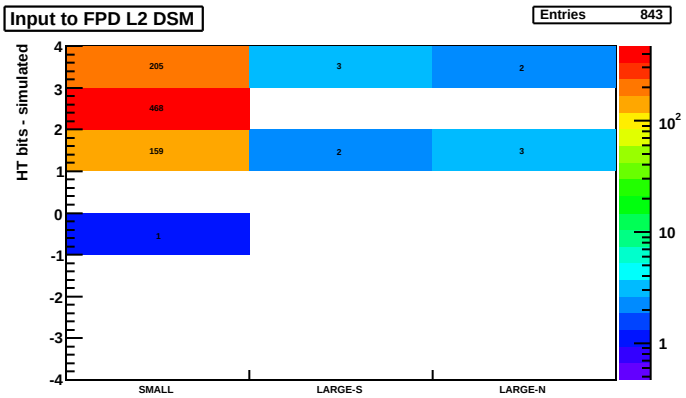

Input to FPD L2 DSM
Entries 1.497563e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

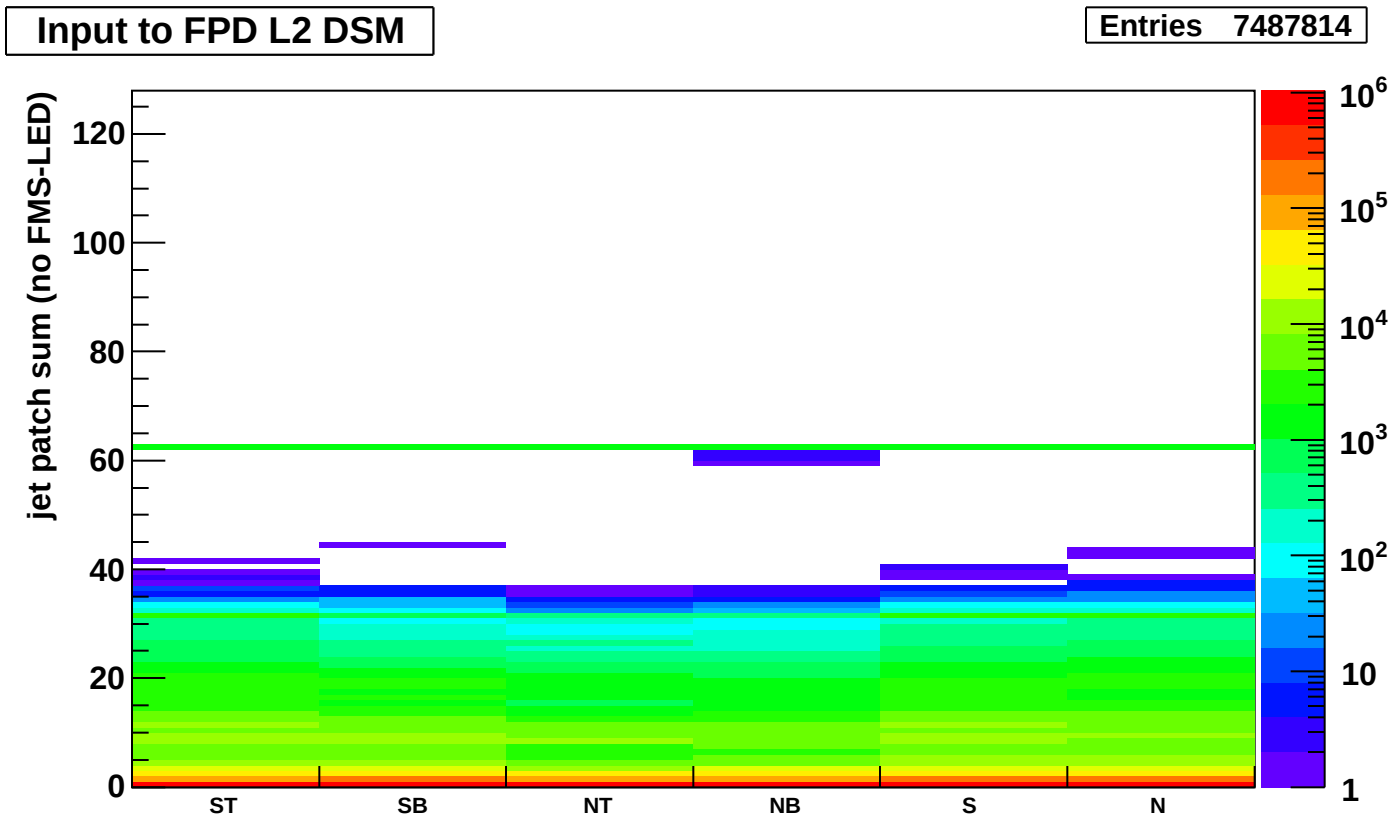

Input to FPD L2 DSM
Entries 2321
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

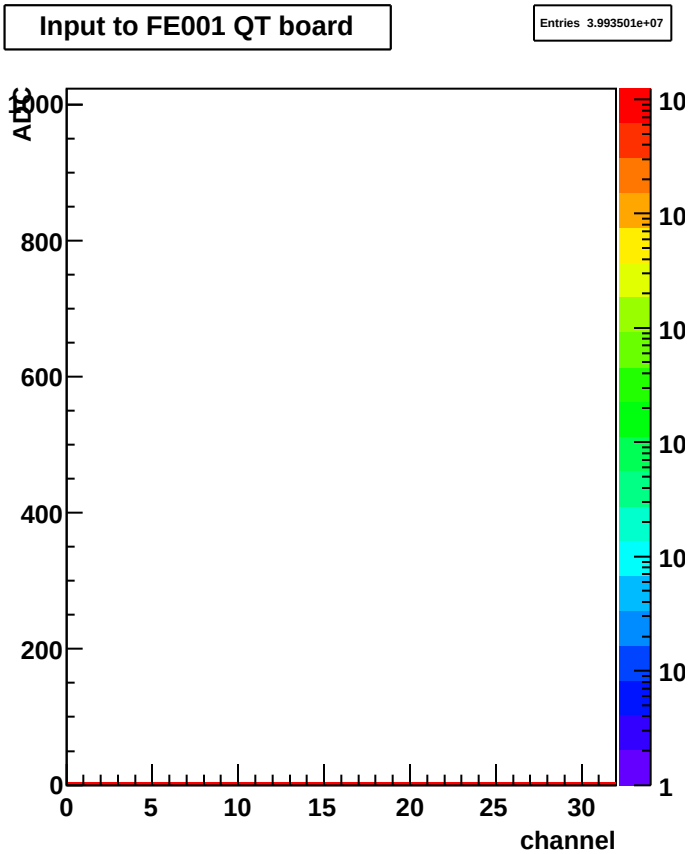

Input to FPD L2 DSM
Entries 3743907
CL bits
5401
2723
28217
11835
6384
80419
42091
24776
1.13933e+06
1.18864e+06
1.21409e+06
SMALL
LARGE-S
LARGE-N

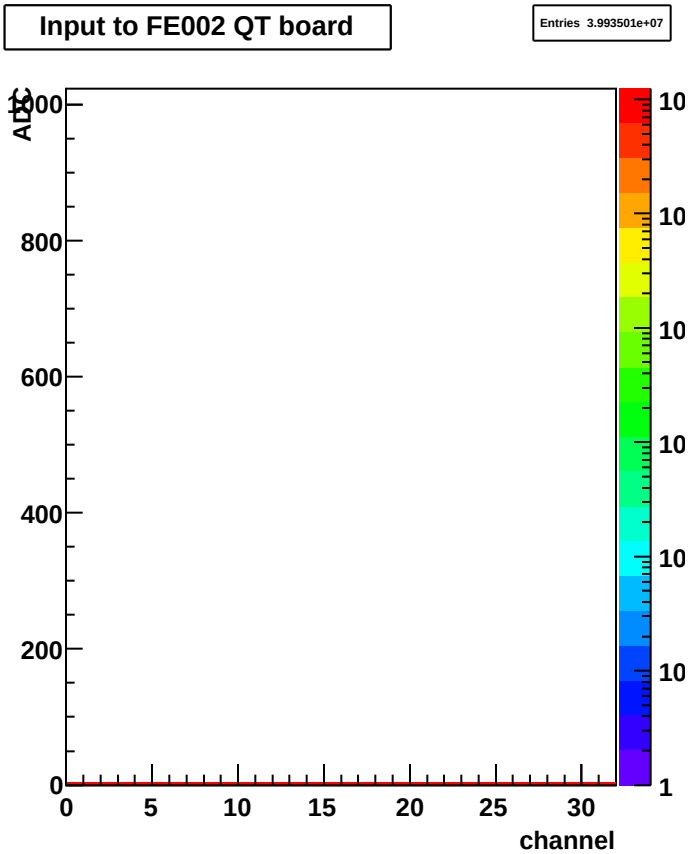

Input to FPD L2 DSM
Entries 53
CL bits - simulated
1
4
16
3
7
2
8
12
SMALL
LARGE-S
LARGE-N

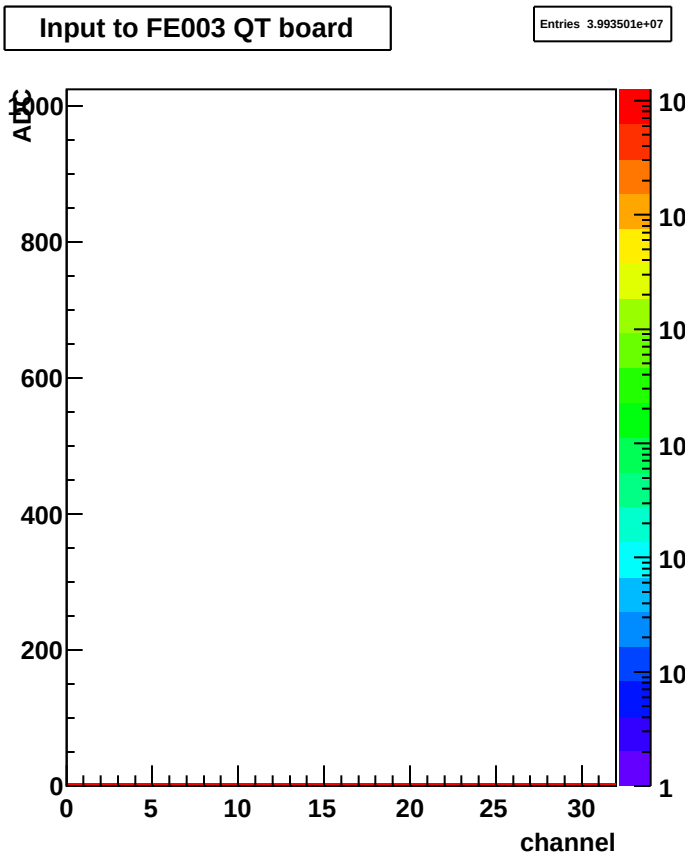

Input to FPD L2 DSM
Entries 3743907
HT bits
11312
39023
18133
24912
68522
39630
23384
1.14322e+06
1.16932e+06
1.20645e+06
SMALL
LARGE-S
LARGE-N

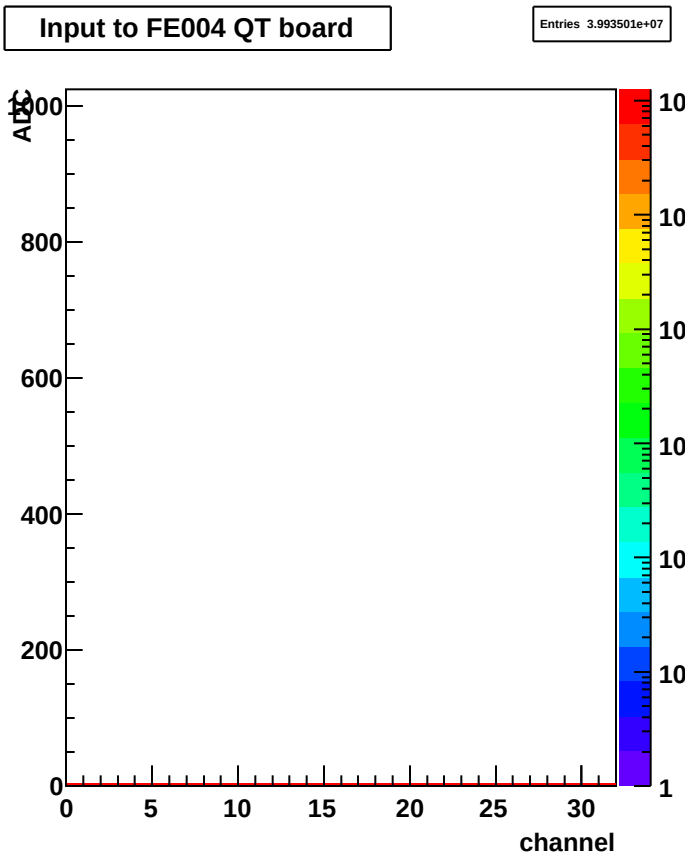

Input to FPD L2 DSM
Entries 843
HT bits - simulated
205
3
2
468
159
2
3
1
SMALL
LARGE-S
LARGE-N

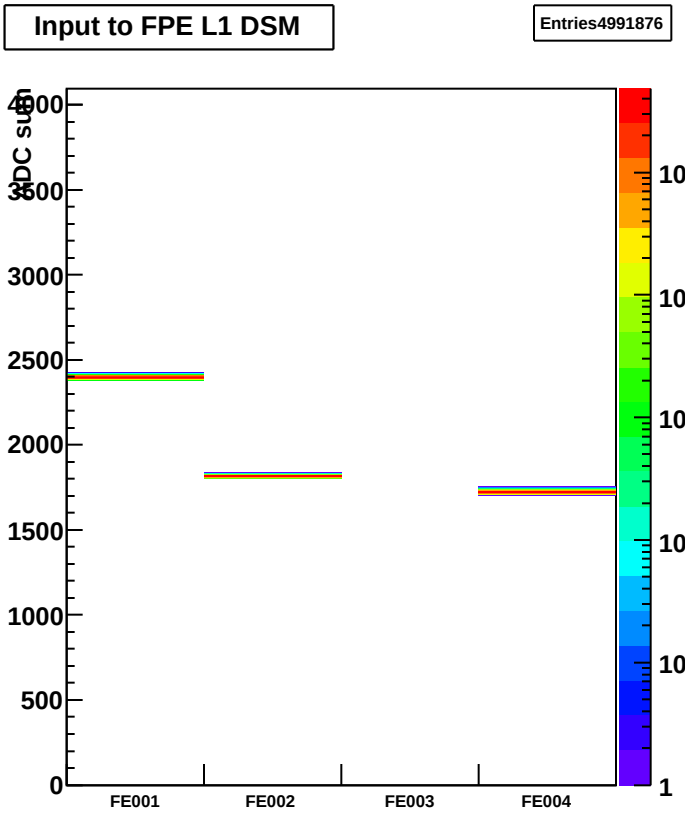

Input to FPD L2 DSM
Entries 7487814
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 3.993501e+07
ADC
channel

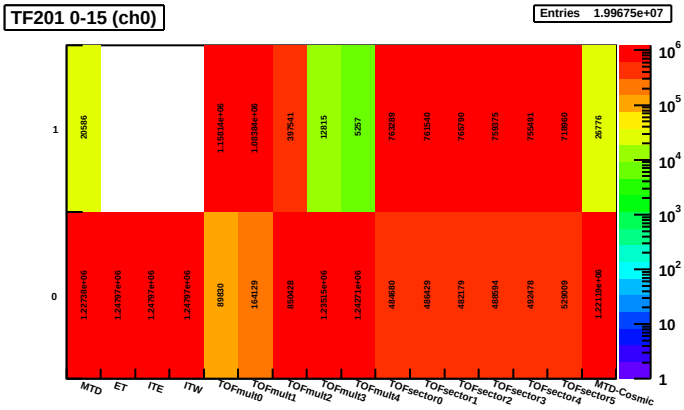

Input to FE002 QT board
Entries 3.993501e+07
ADC
channel


Input to FE003 QT board
Entries 3.993501e+07
ADC
channel

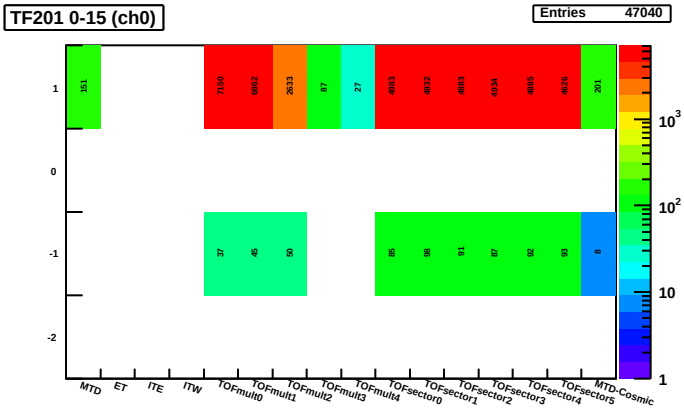

Input to FE004 QT board
Entries 3.993501e+07
ADC
channel


Input to FPE L1 DSM
Entries4991876
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

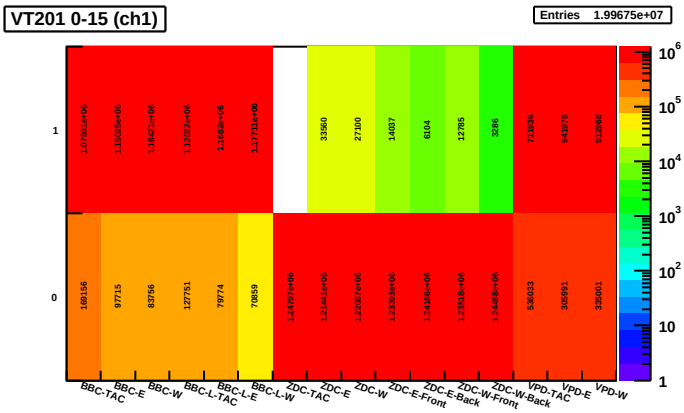
TF201 0-15 (ch0)



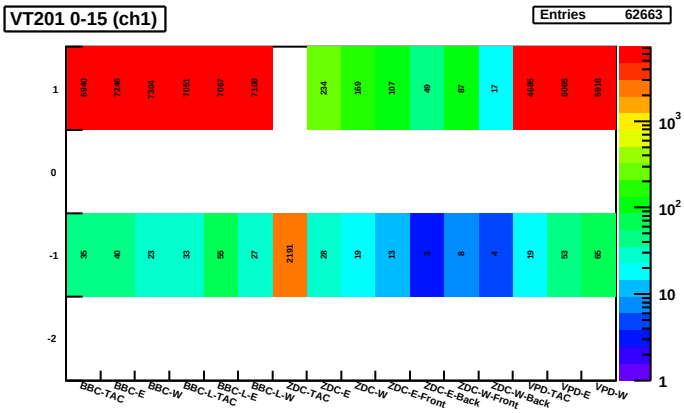
TF201 0-15 (ch0)



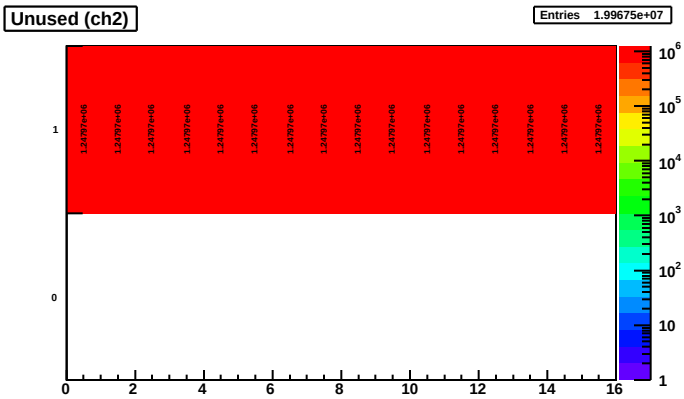
VT201 0-15 (ch1)

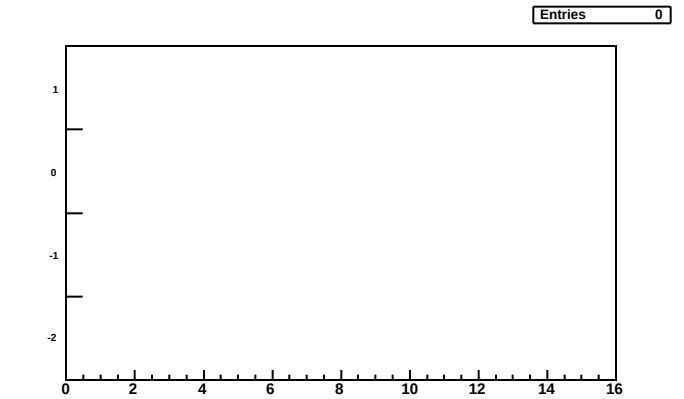


VT201 0-15 (ch1)

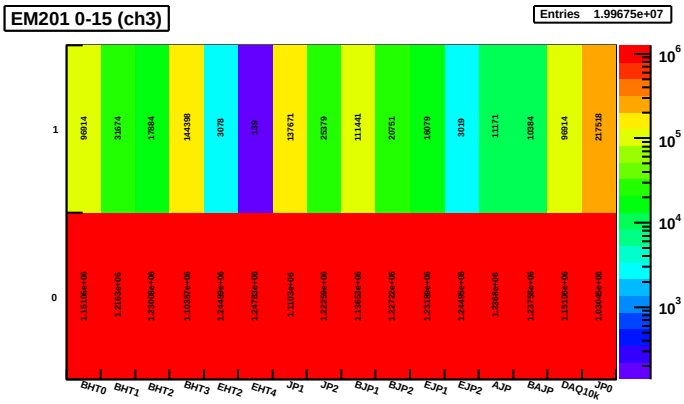


Unused (ch2)

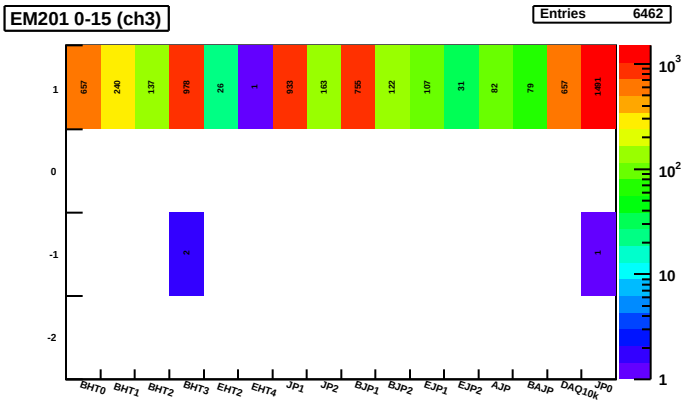




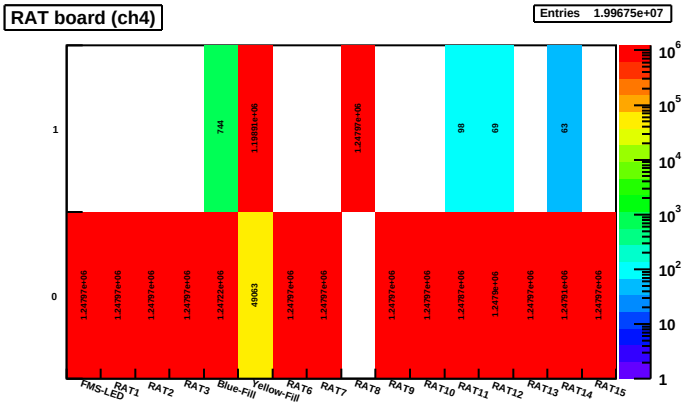
EM201 0-15 (ch3)

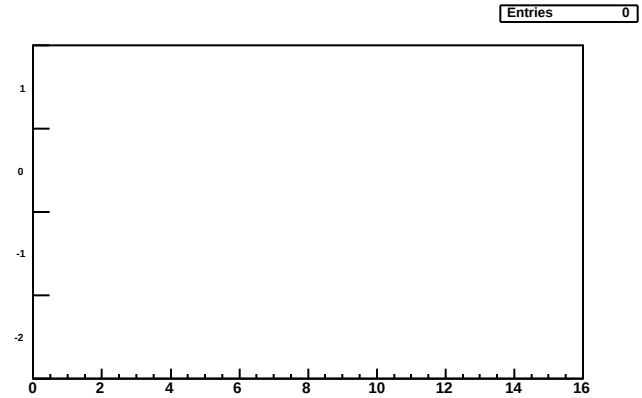


EM201 0-15 (ch3)

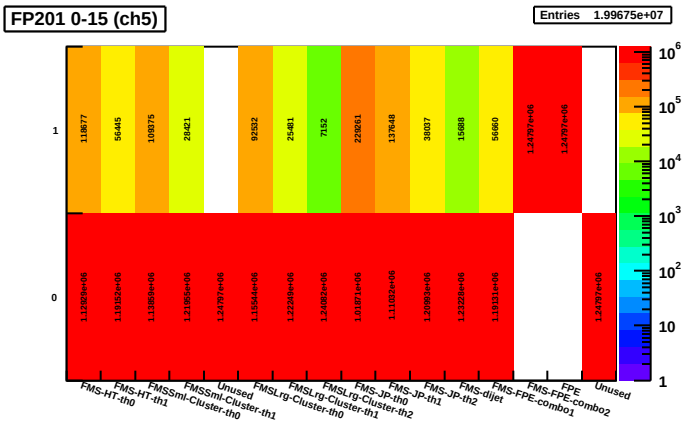


RAT board (ch4)

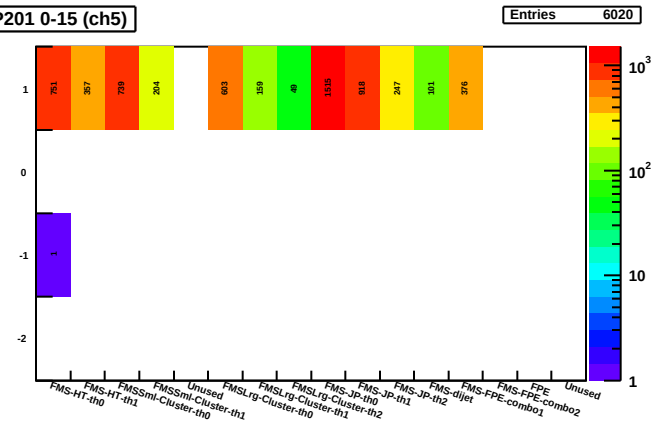




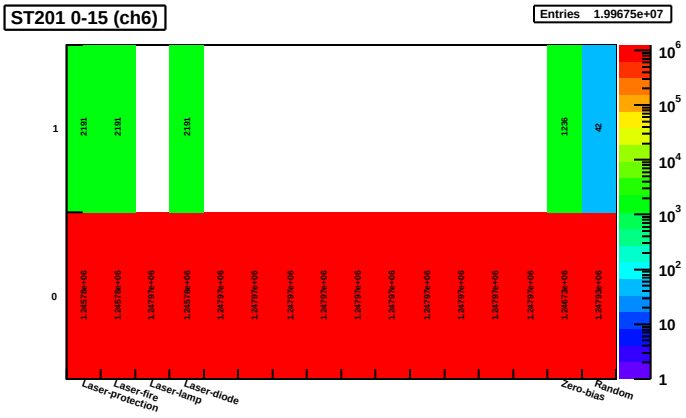
FP201 0-15 (ch5)

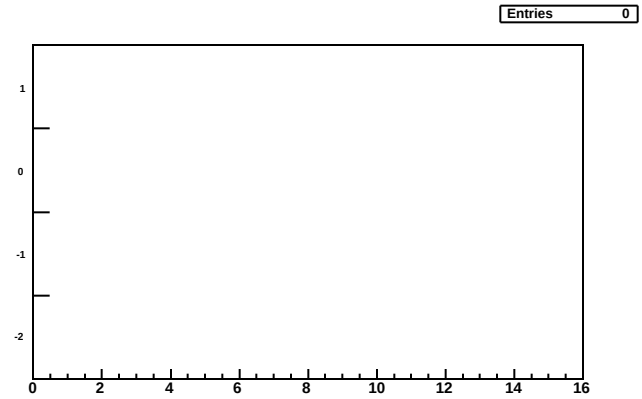
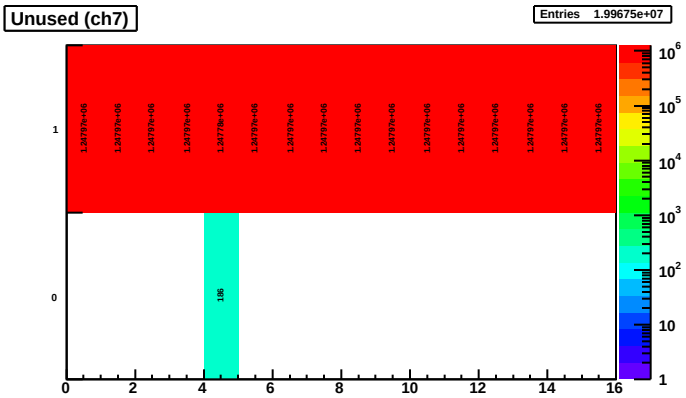
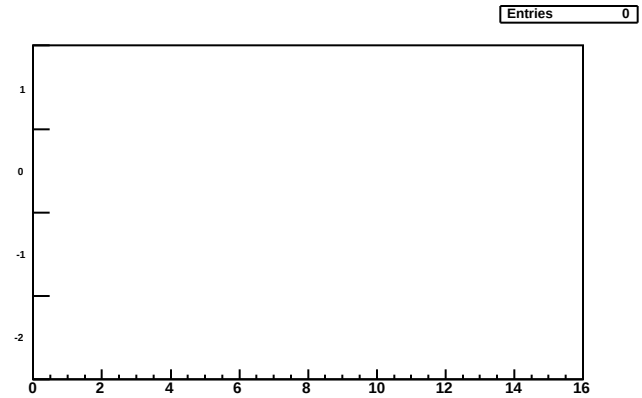


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)