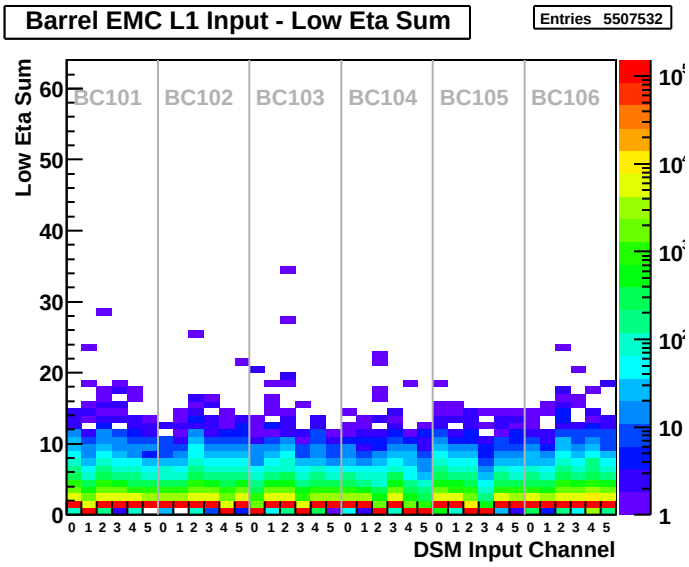
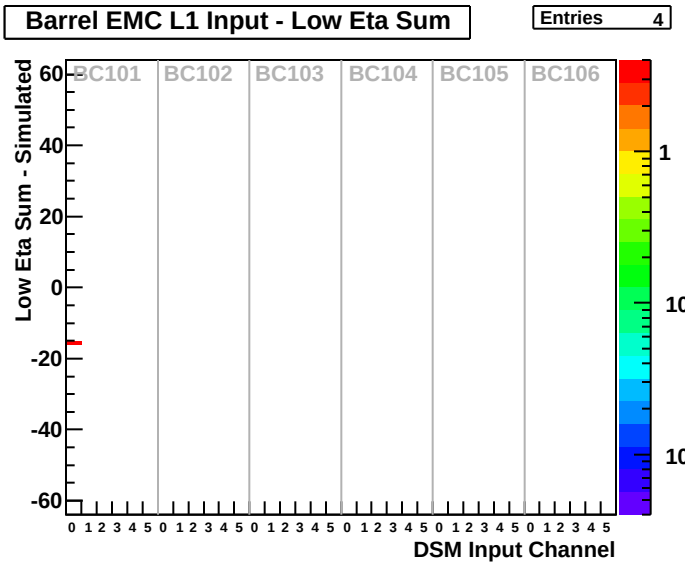
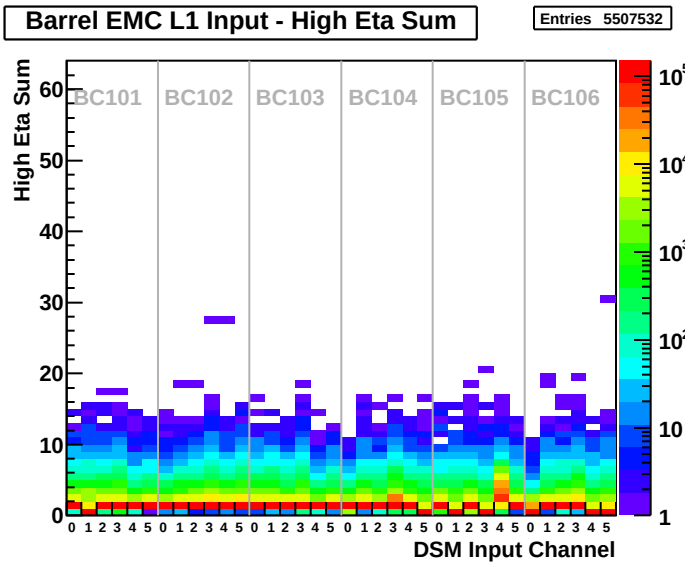
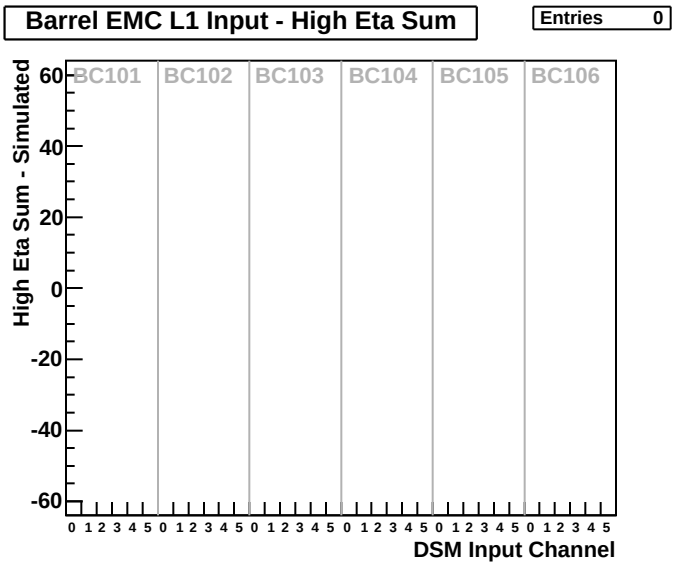
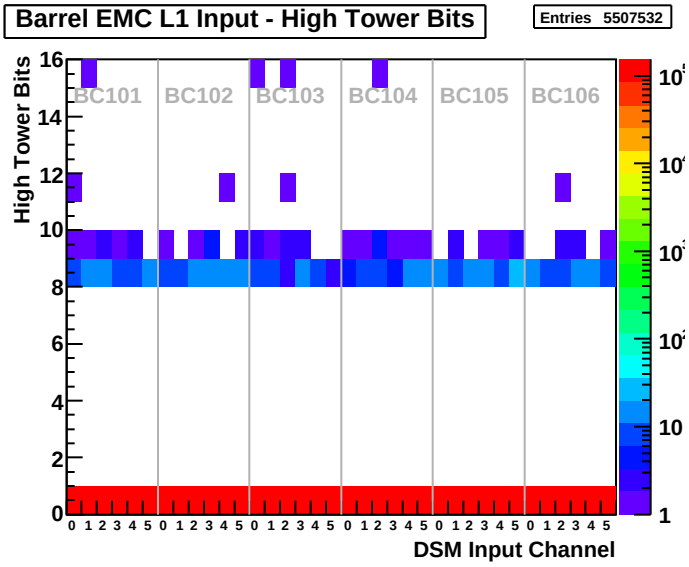
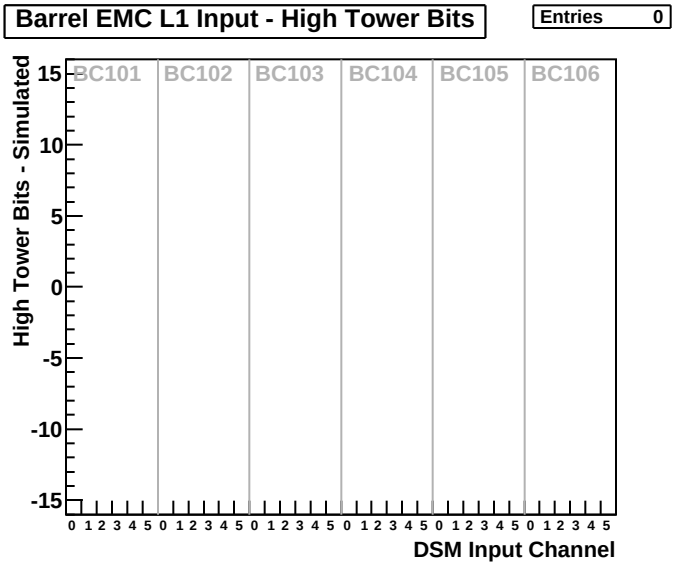

Barrel EMC L1 Input - Low Eta Sum
Entries 5507532
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

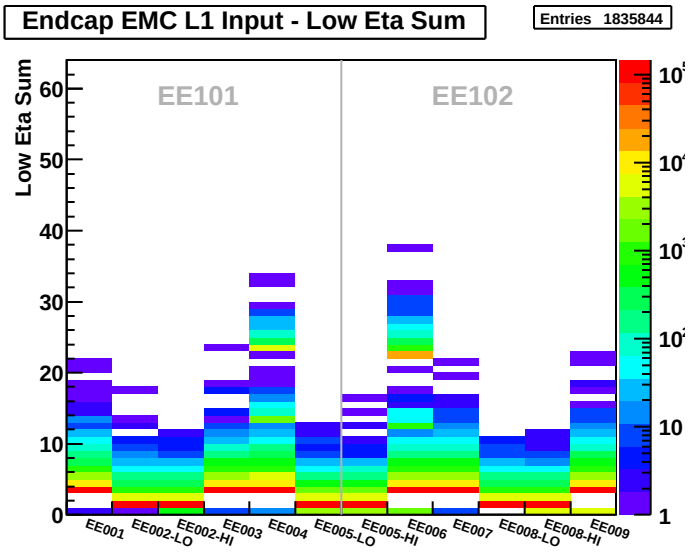

Barrel EMC L1 Input - Low Eta Sum
Entries 4
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

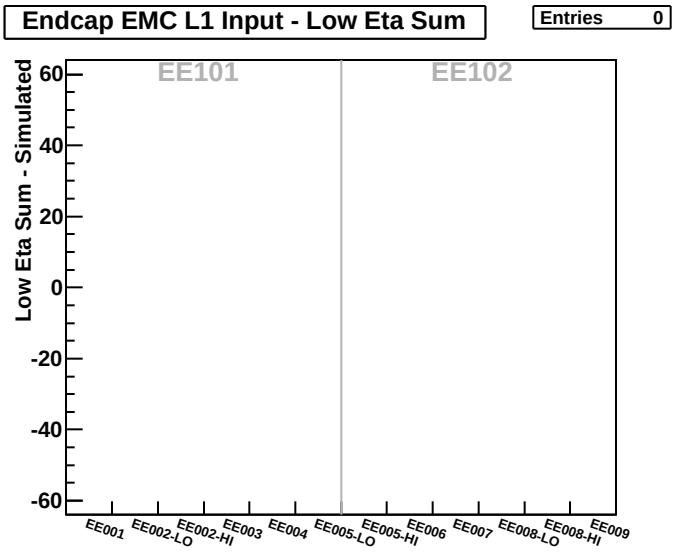

Barrel EMC L1 Input - High Eta Sum
Entries 5507532
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

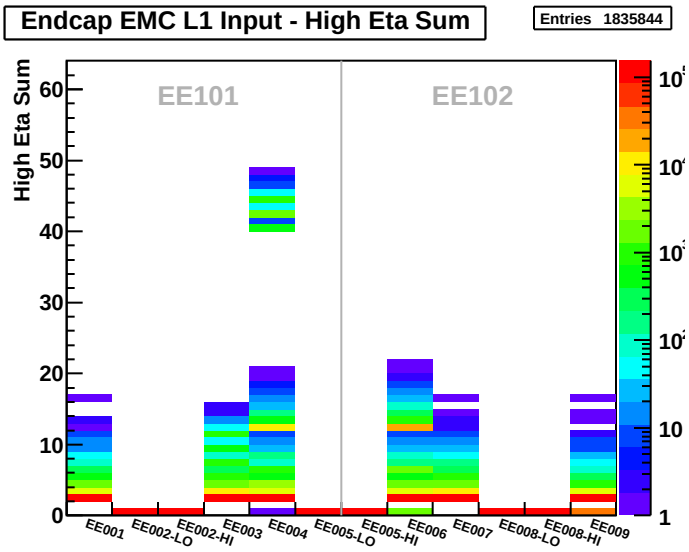

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

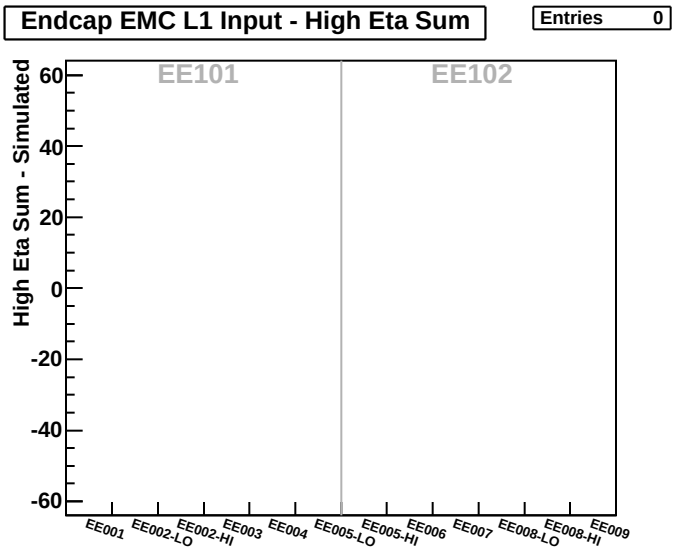

Barrel EMC L1 Input - High Tower Bits
Entries 5507532
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

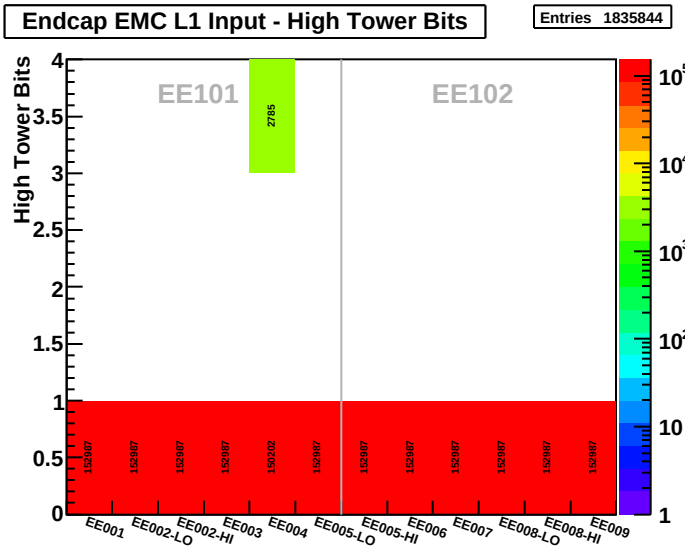

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

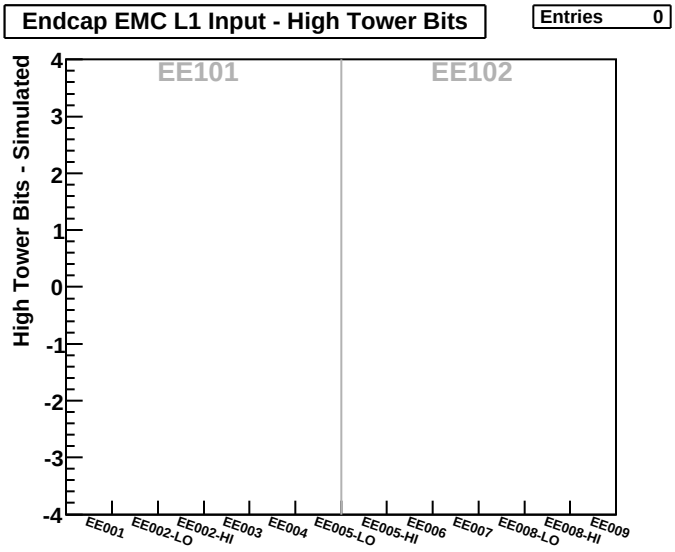

Endcap EMC L1 Input - Low Eta Sum
Entries 1835844
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

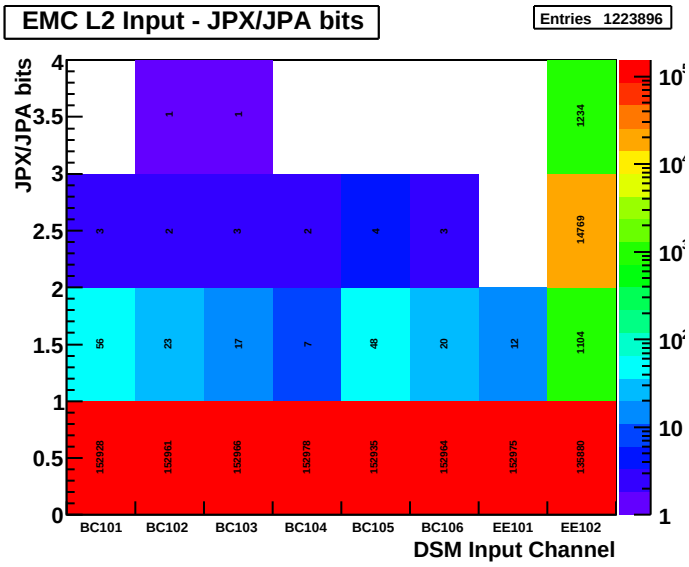

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

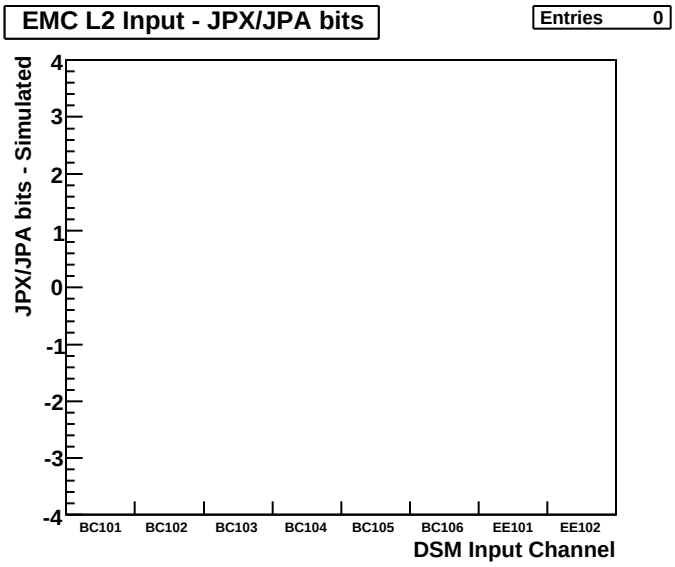

Endcap EMC L1 Input - High Eta Sum
Entries 1835844
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

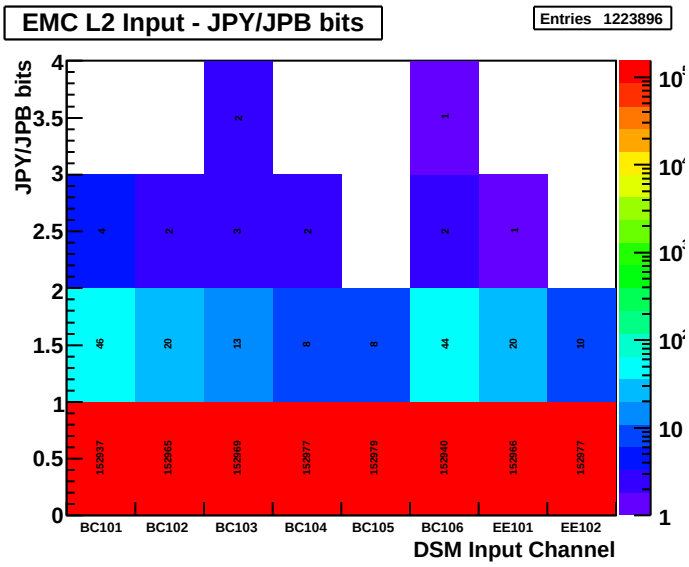

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

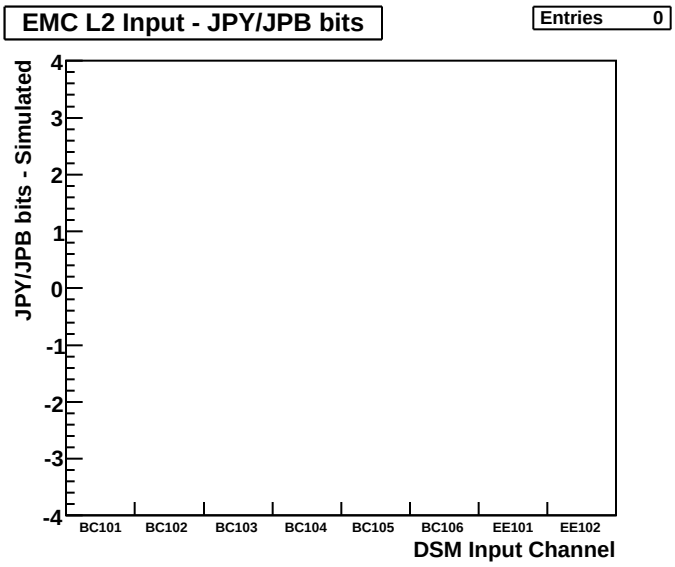

Endcap EMC L1 Input - High Tower Bits
Entries 1835844
High Tower Bits
EE101
EE102
2785
152987
150202
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

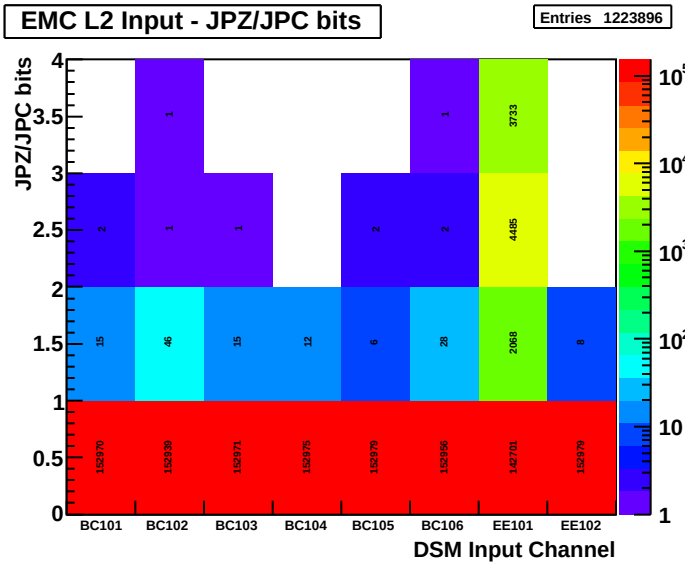

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

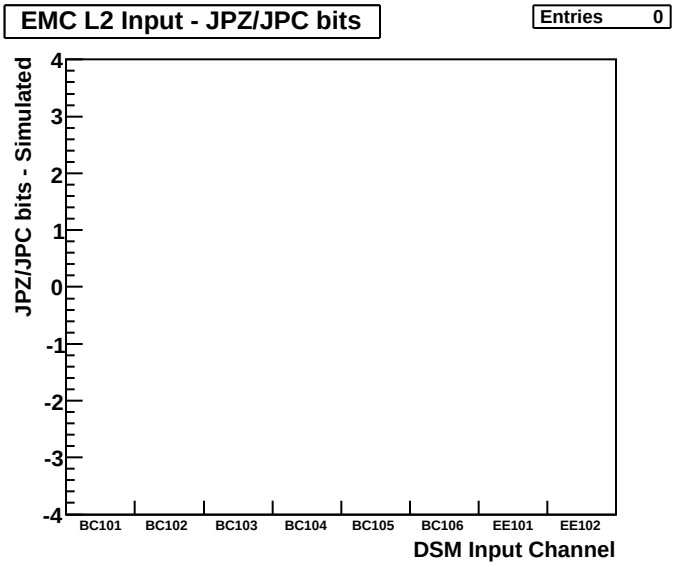

EMC L2 Input - JPX/JPA bits
Entries 1223896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

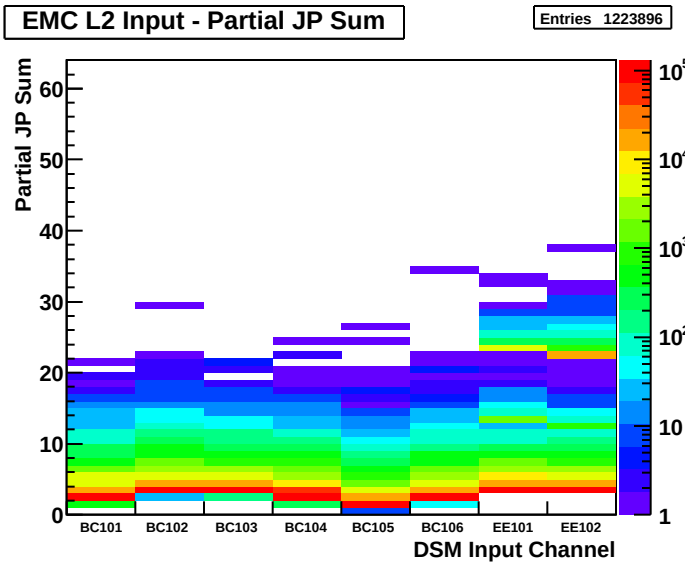

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

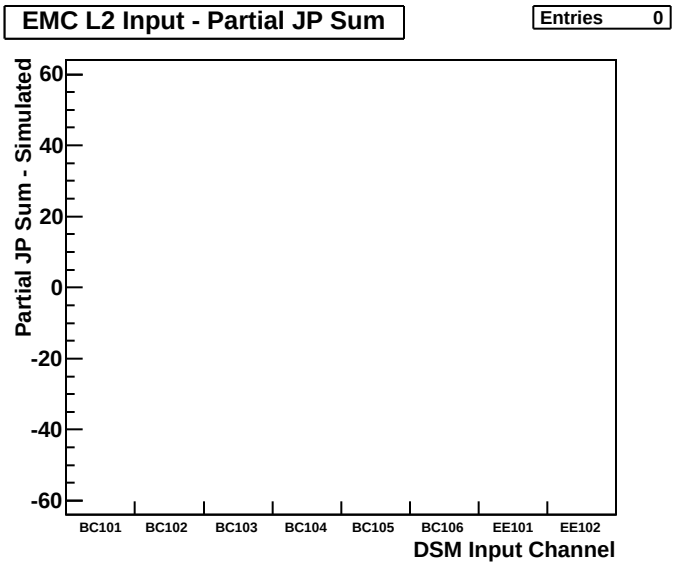

EMC L2 Input - JPY/JPB bits
Entries 1223896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

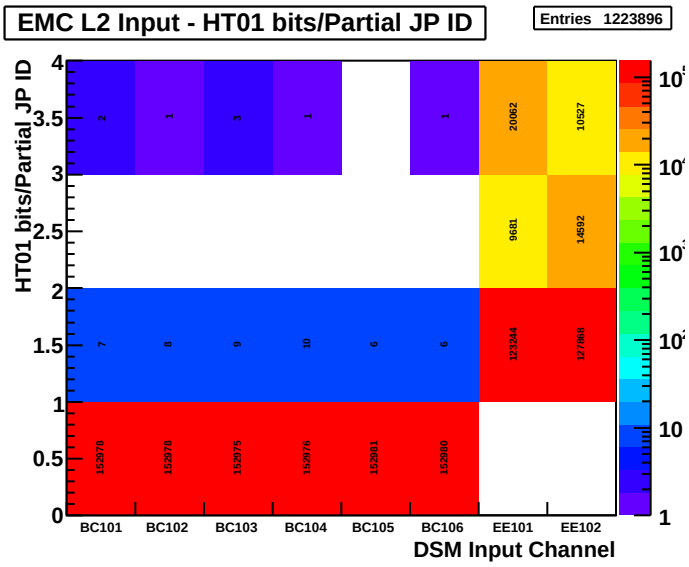

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

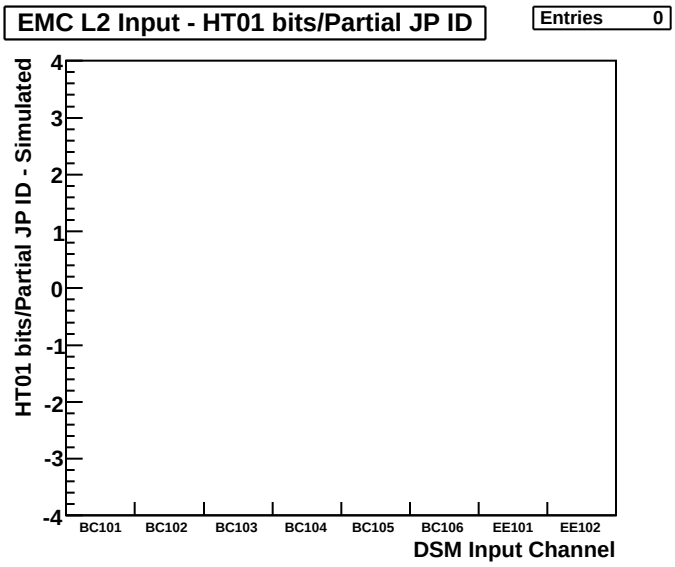

EMC L2 Input - JPZ/JPC bits
Entries 1223896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

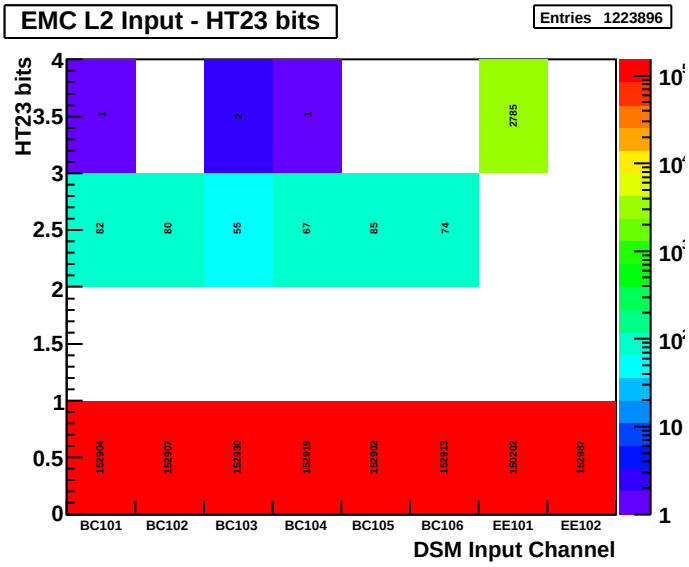

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

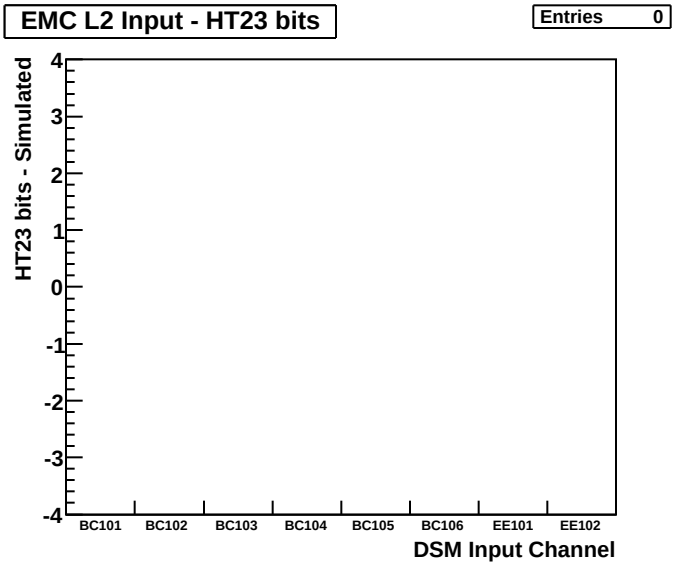

EMC L2 Input - Partial JP Sum
Entries 1223896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

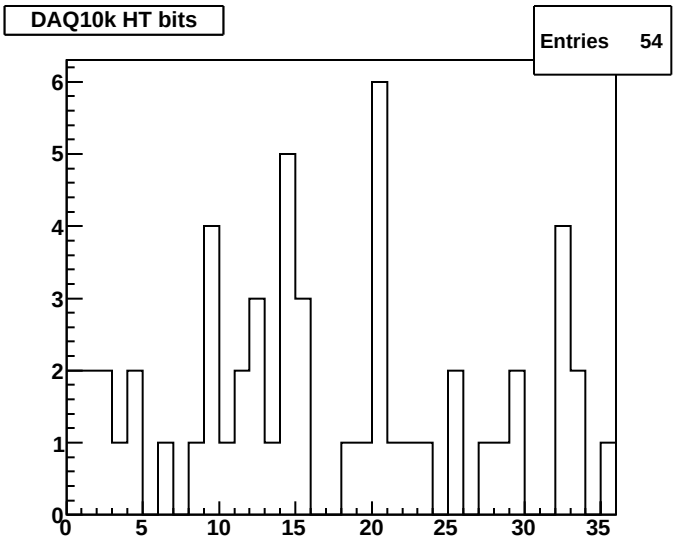

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

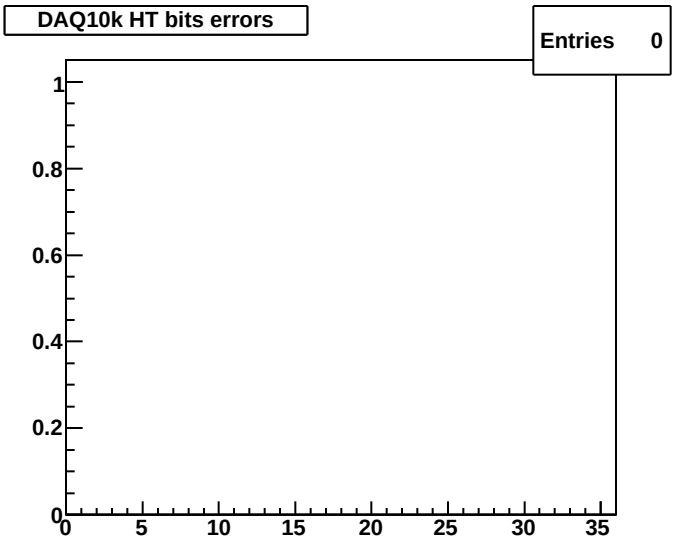

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1223896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

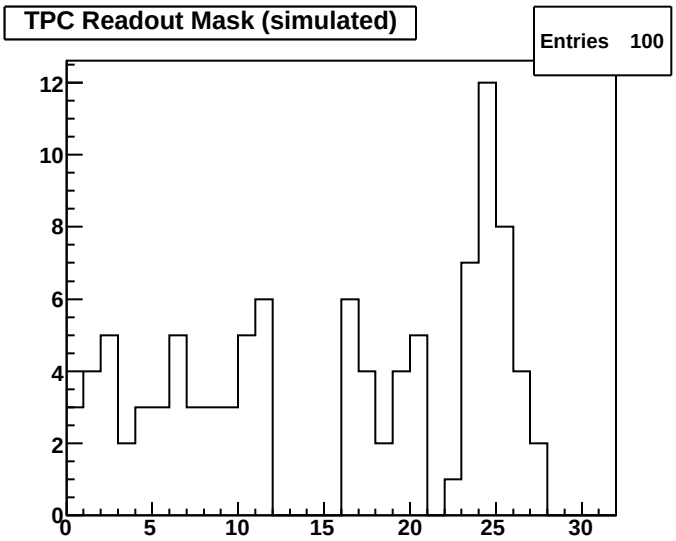

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

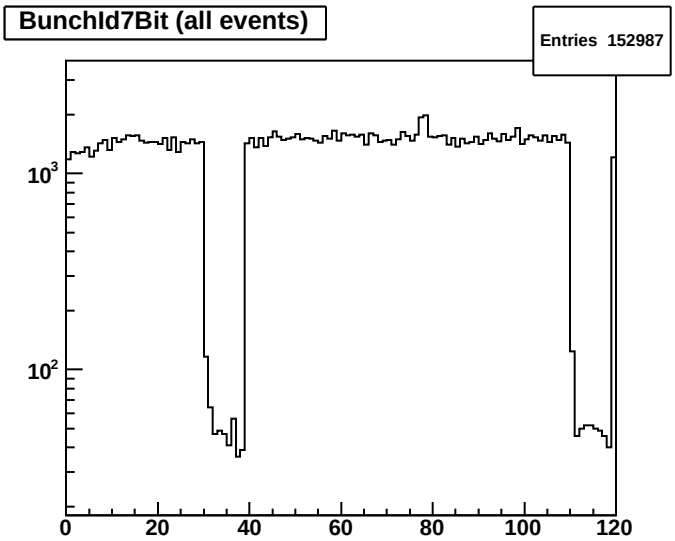

EMC L2 Input - HT23 bits
Entries 1223896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

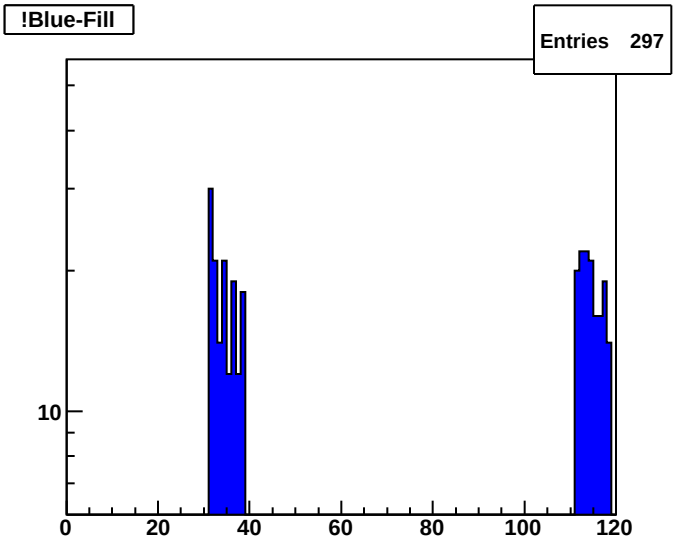

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

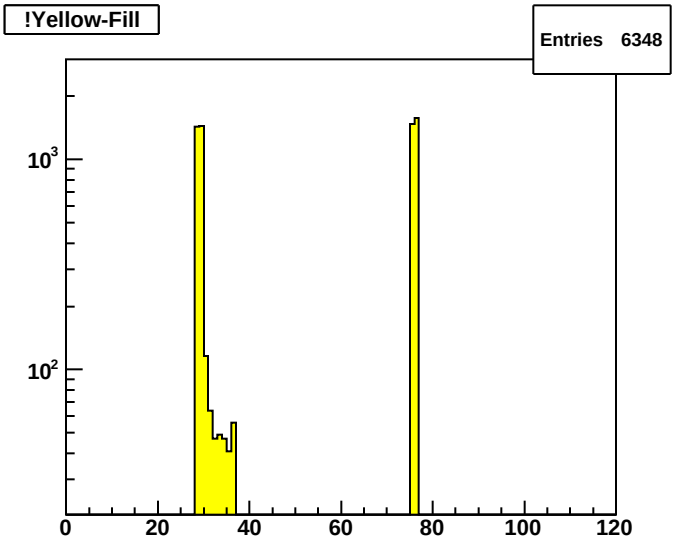

DAQ10k HT bits
Entries 54

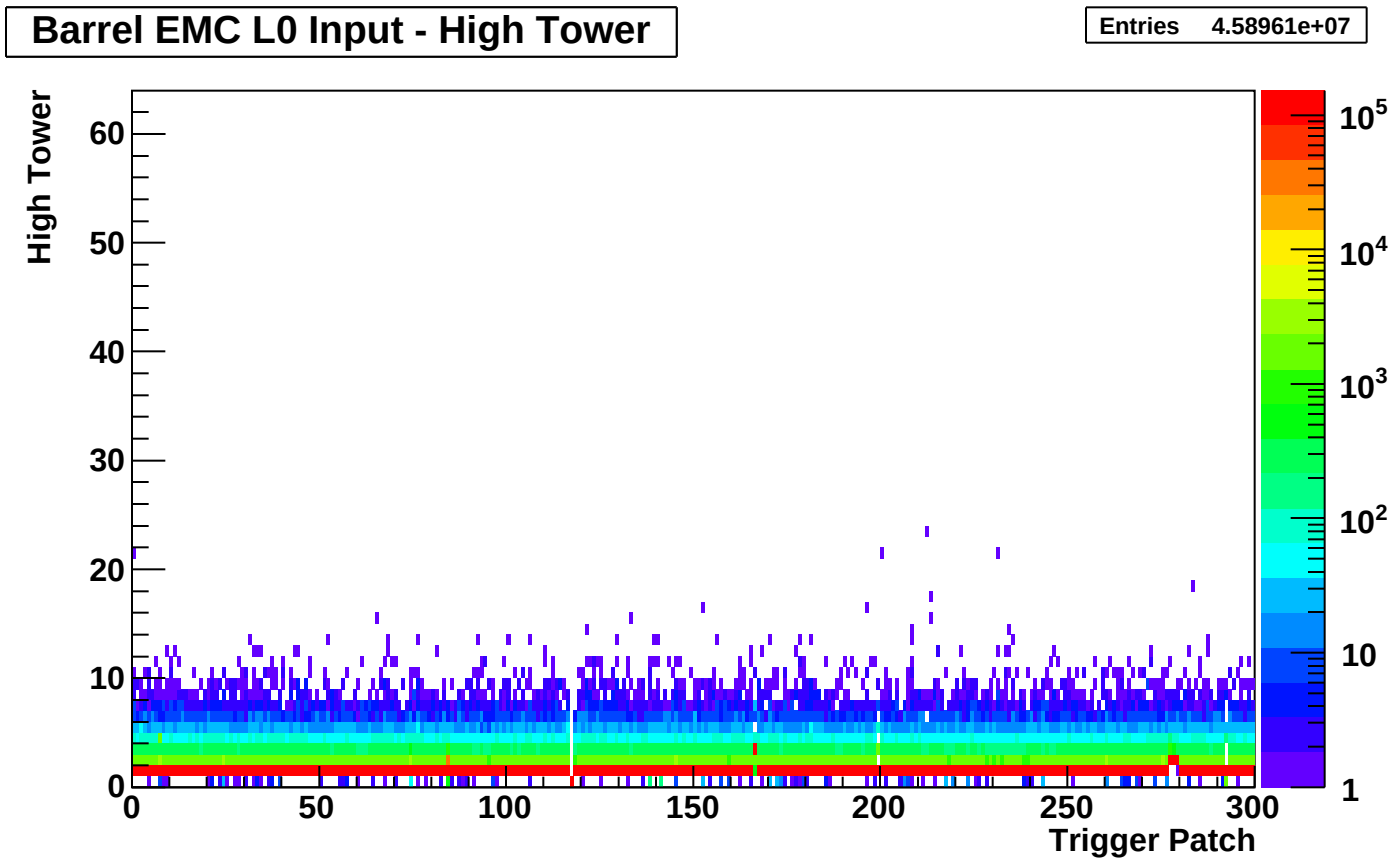

DAQ10k HT bits errors
Entries 0

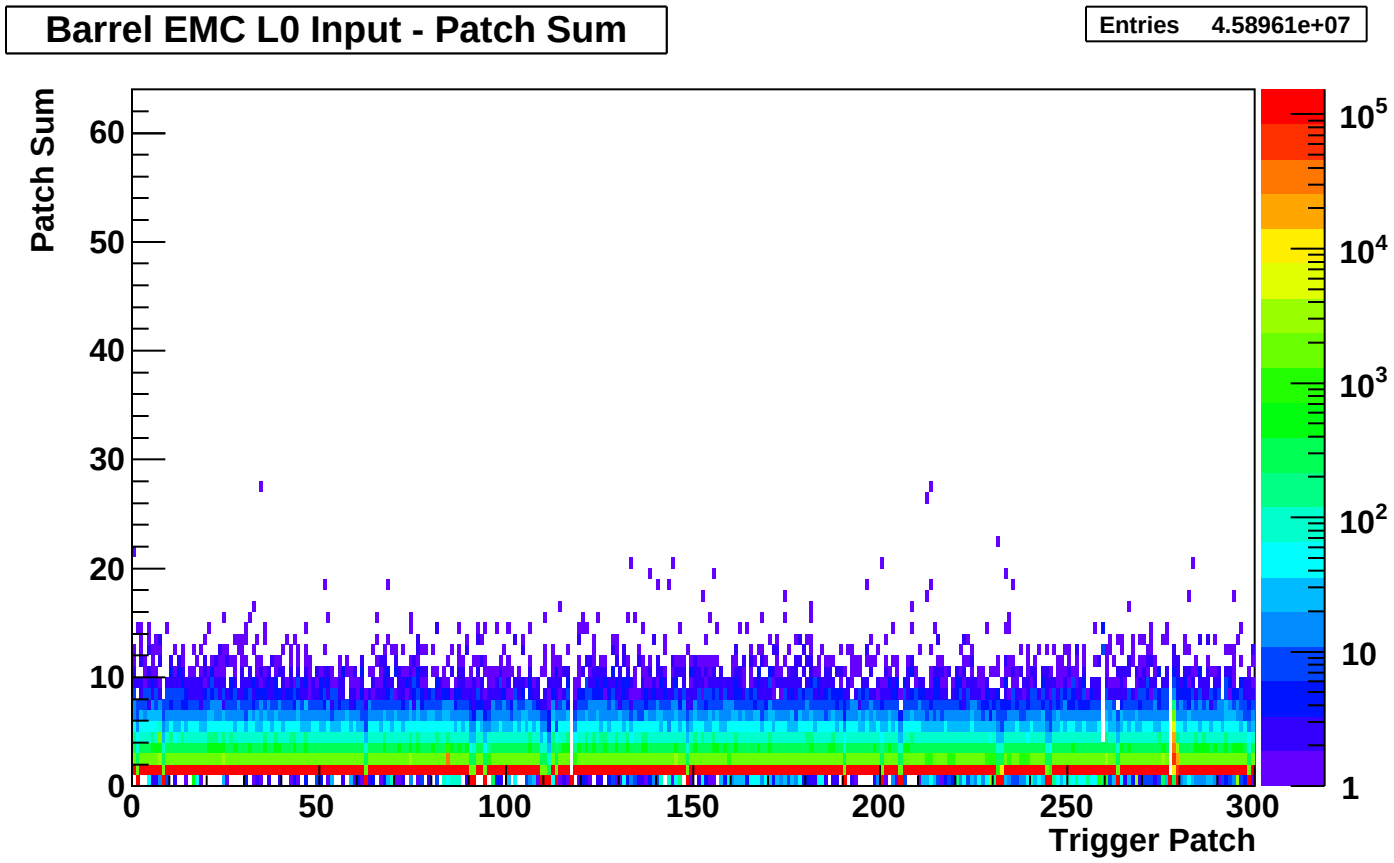

TPC Readout Mask (simulated)
Entries 100

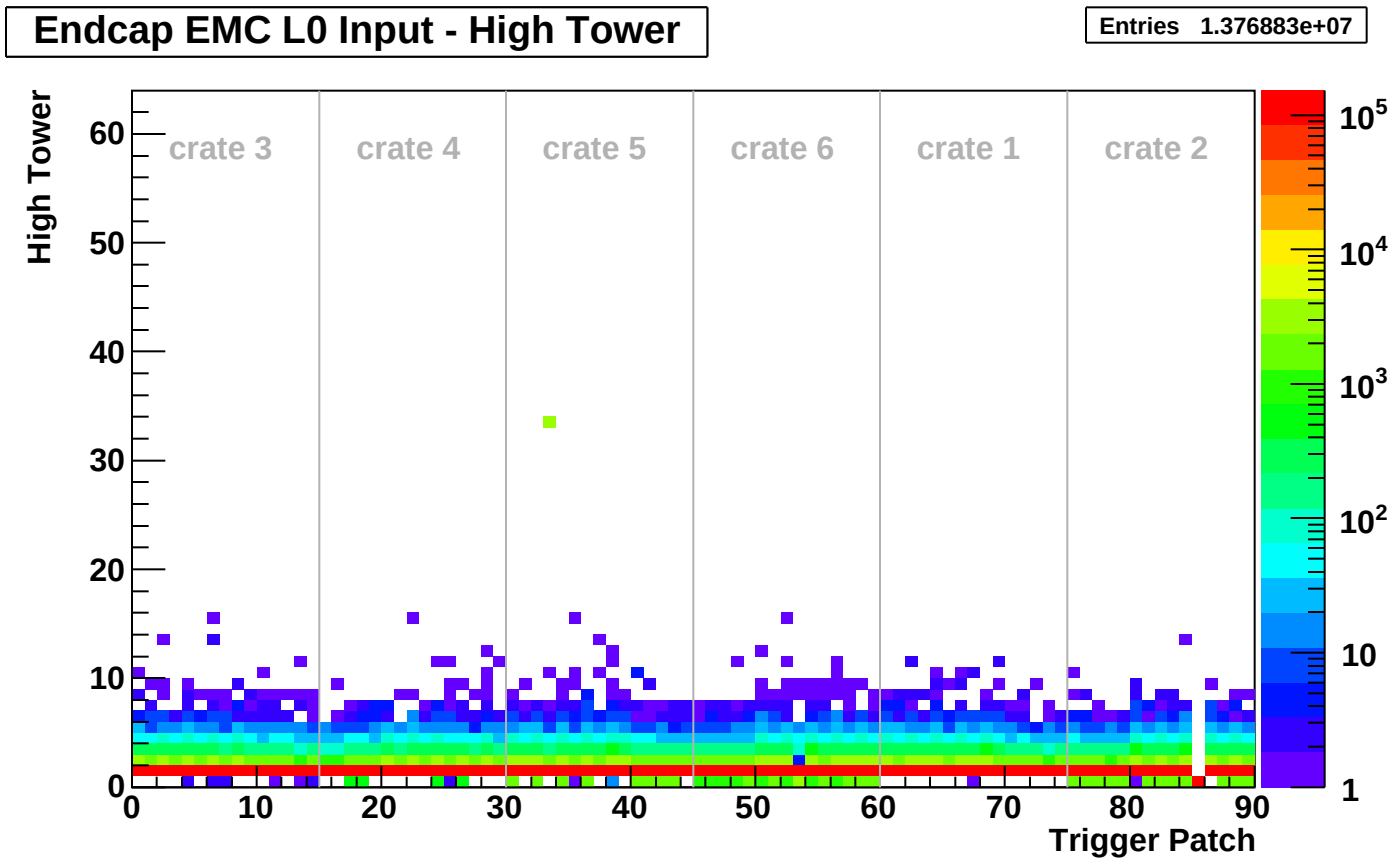

BunchId7Bit (all events)
Entries 152987

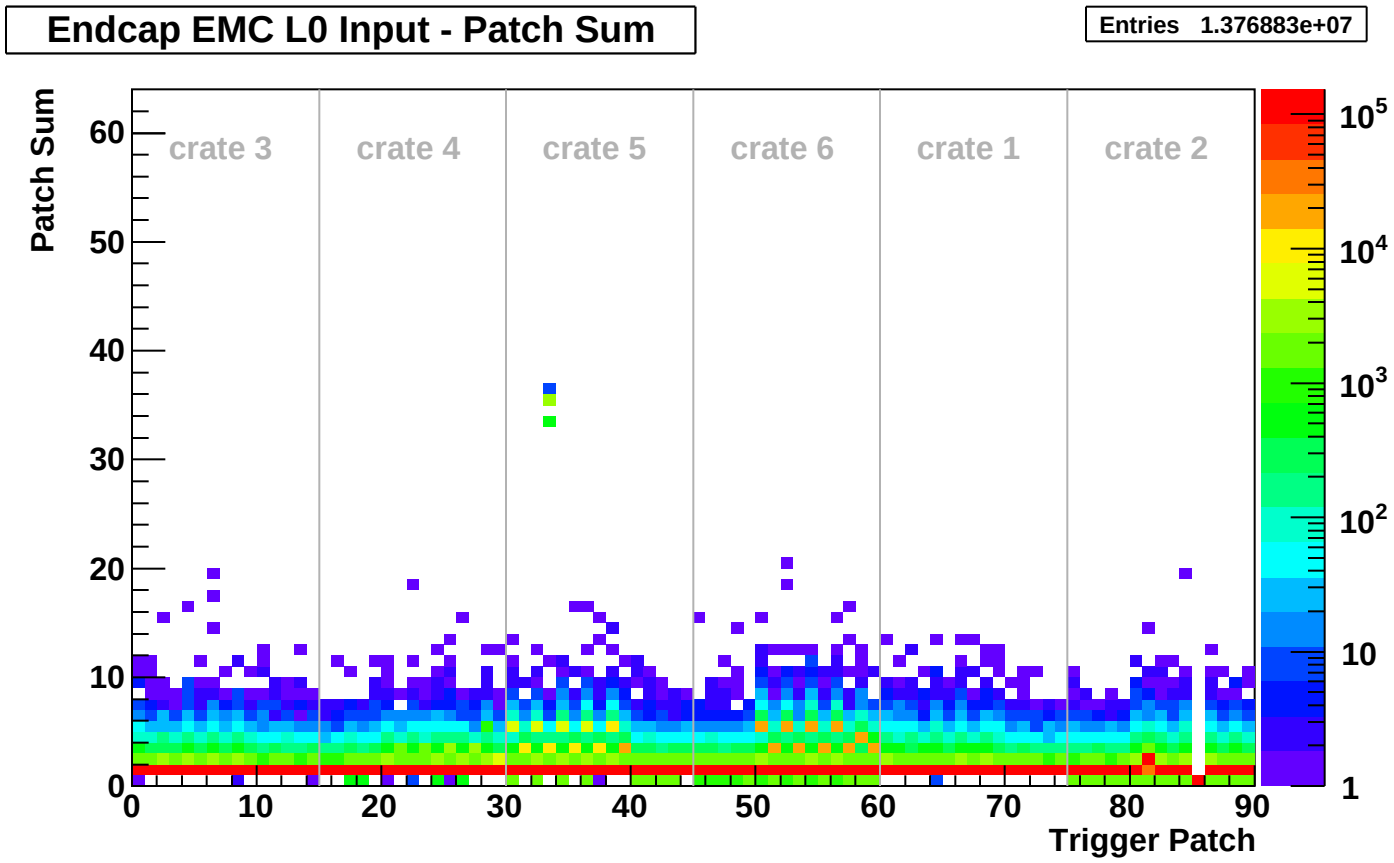

!Blue-Fill
Entries 297

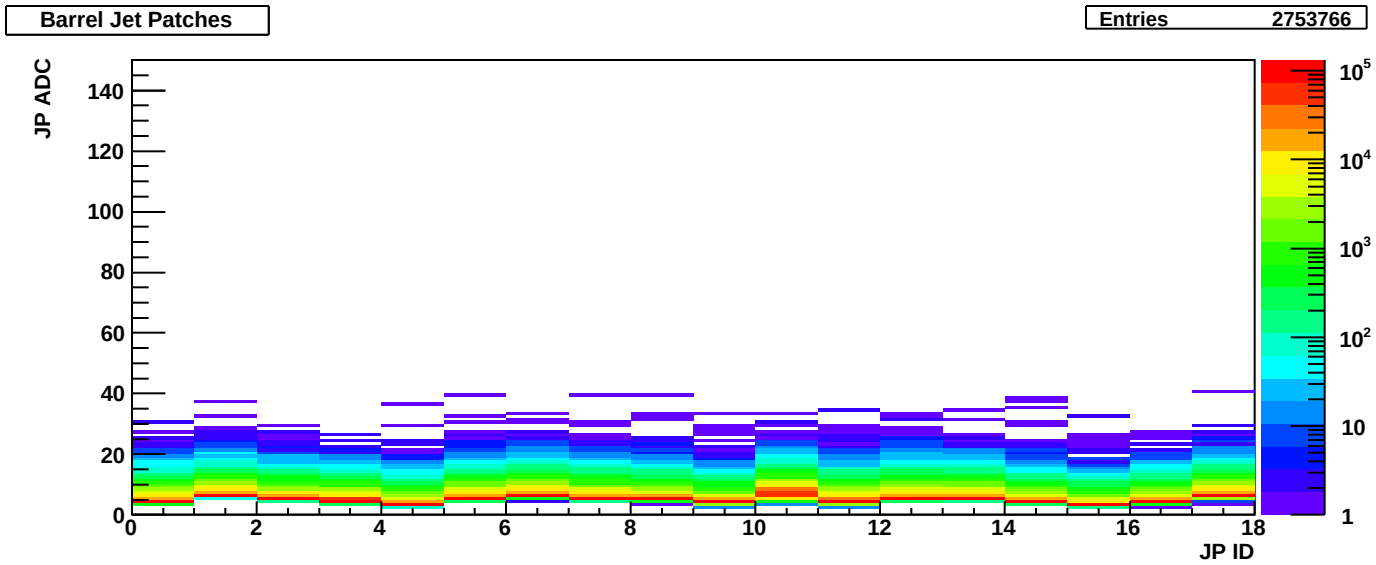

!Yellow-Fill
Entries 6348

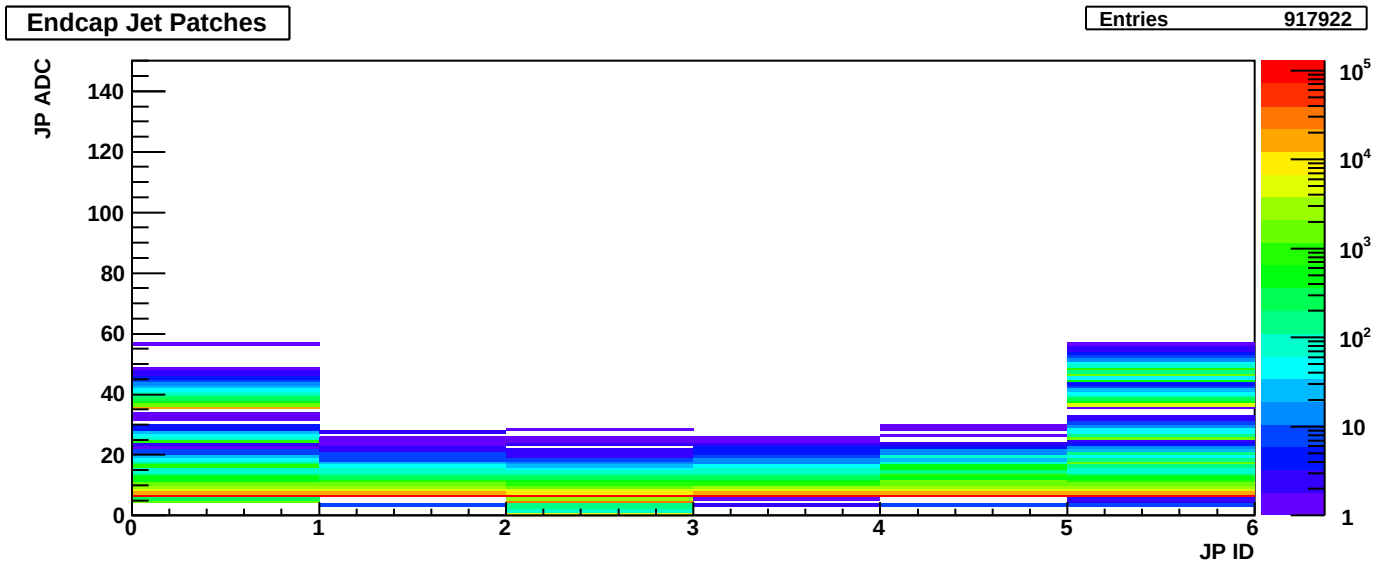

Barrel EMC L0 Input - High Tower
Entries 4.58961e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

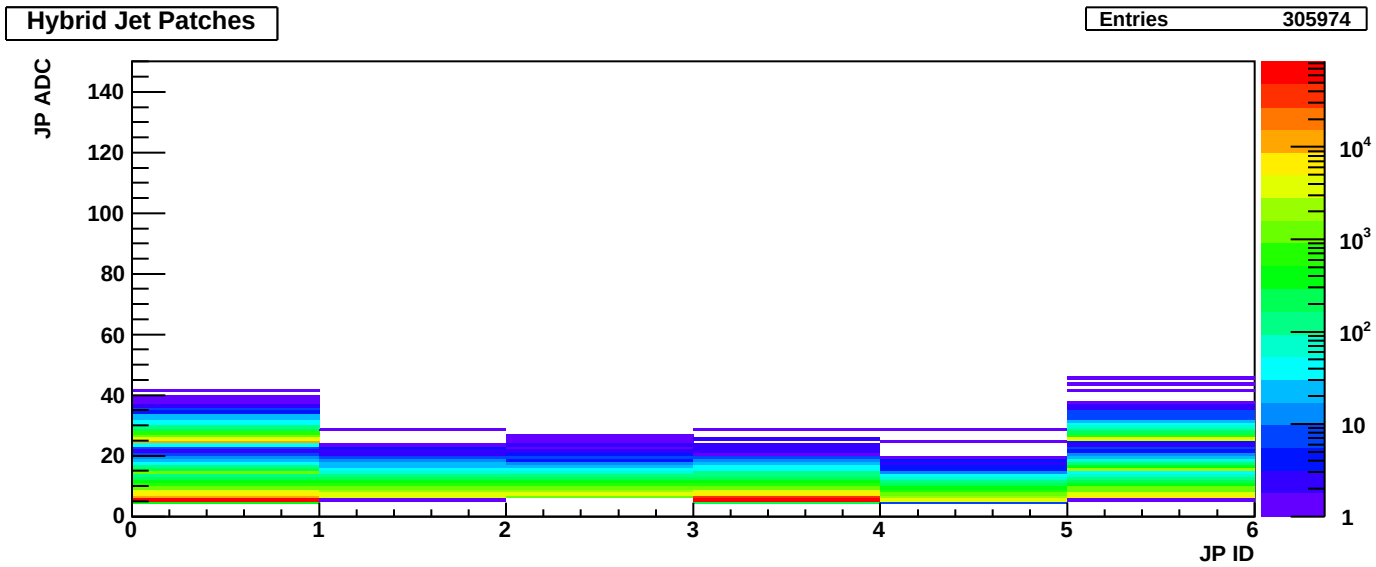

Barrel EMC L0 Input - Patch Sum
Entries 4.58961e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

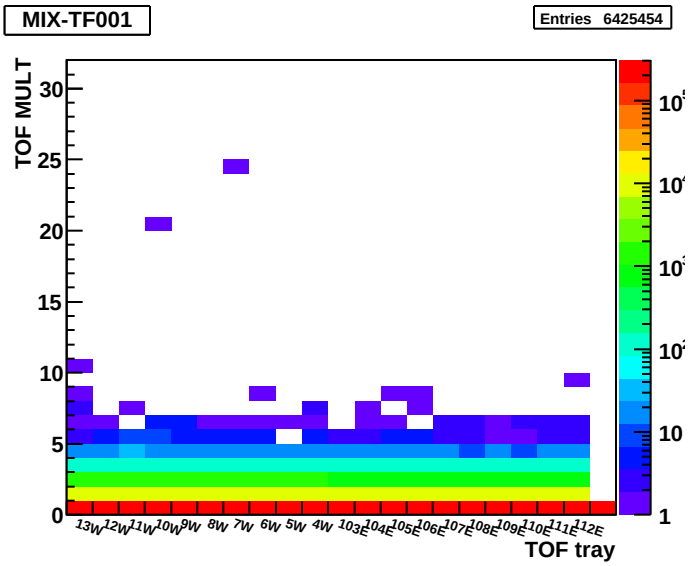

Endcap EMC L0 Input - High Tower
Entries 1.376883e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

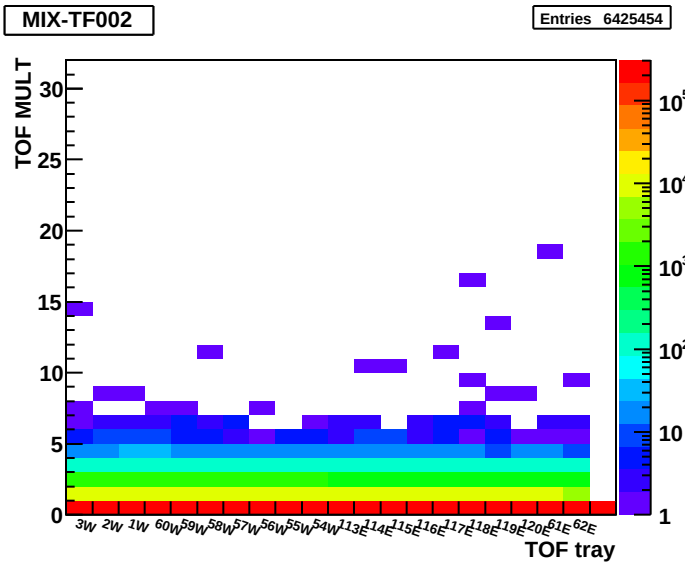

Endcap EMC L0 Input - Patch Sum
Entries 1.376883e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

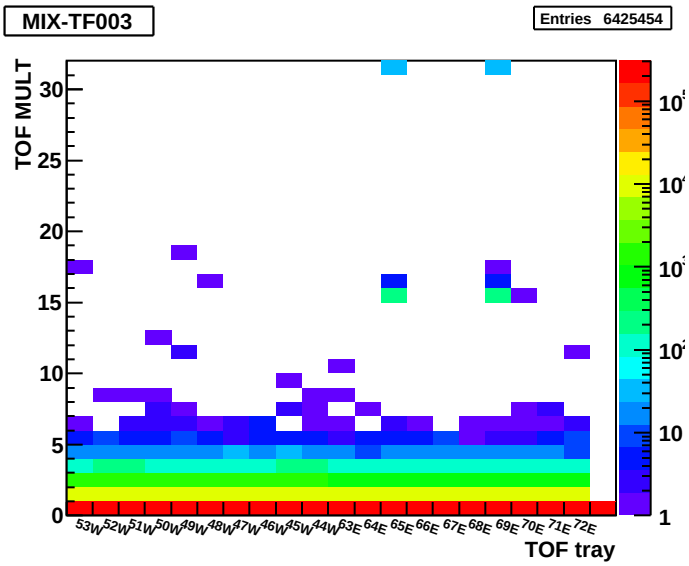

Barrel Jet Patches
Entries 2753766
JP ADC
JP ID

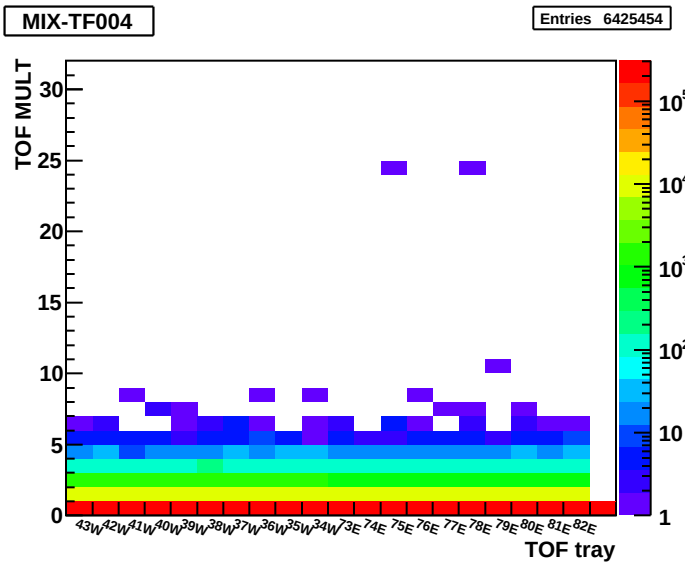

Endcap Jet Patches
Entries 917922
JP ADC
JP ID

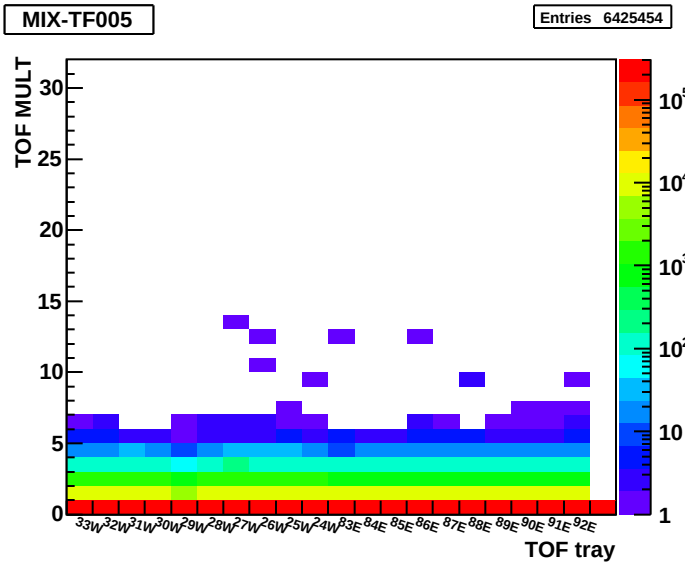

Hybrid Jet Patches
Entries 305974
JP ADC
JP ID

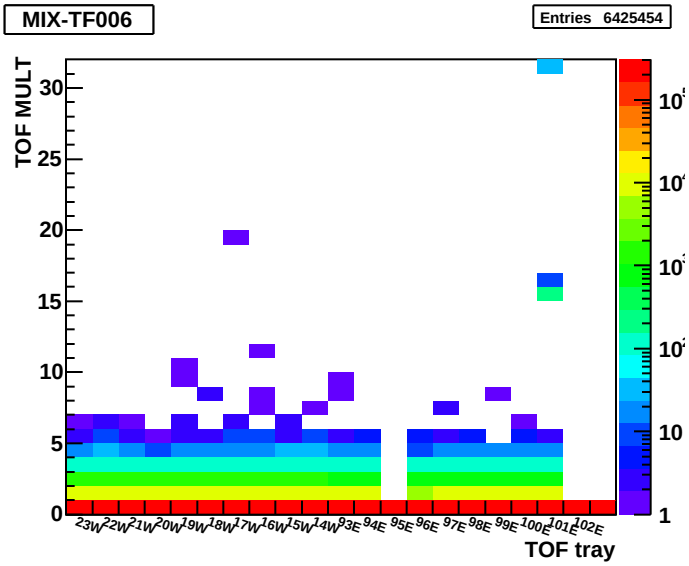

MIX-TF001
Entries 6425454
TOF MULT
TOF tray

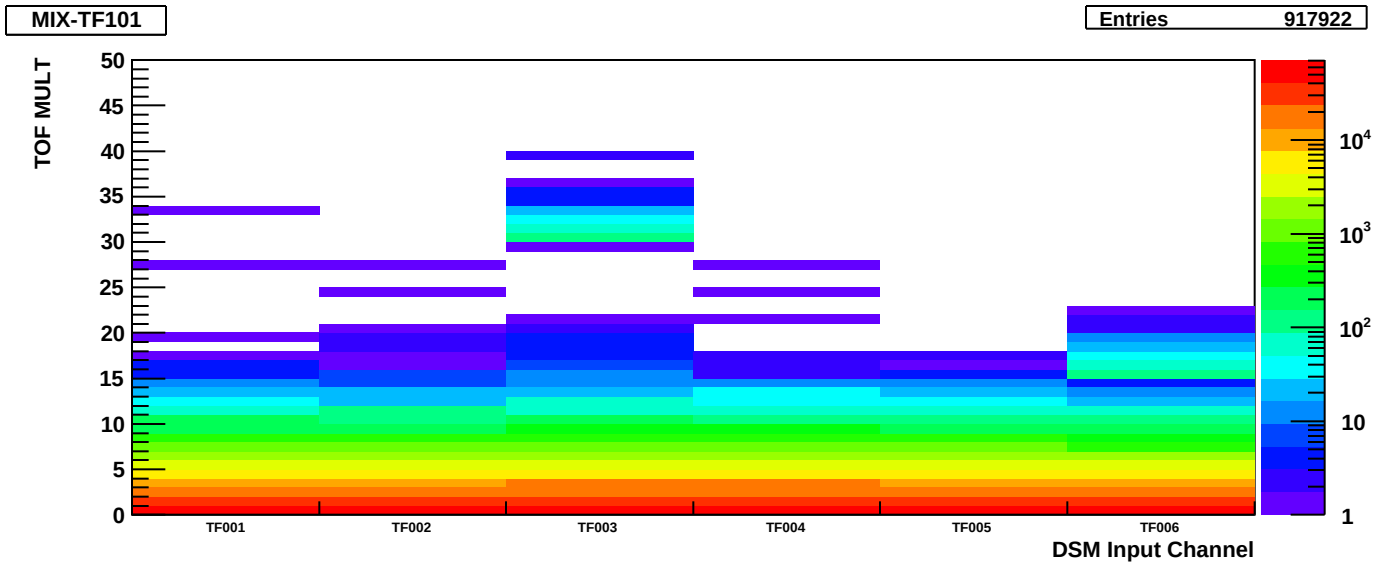

MIX-TF002
Entries 6425454
TOF MULT
TOF tray

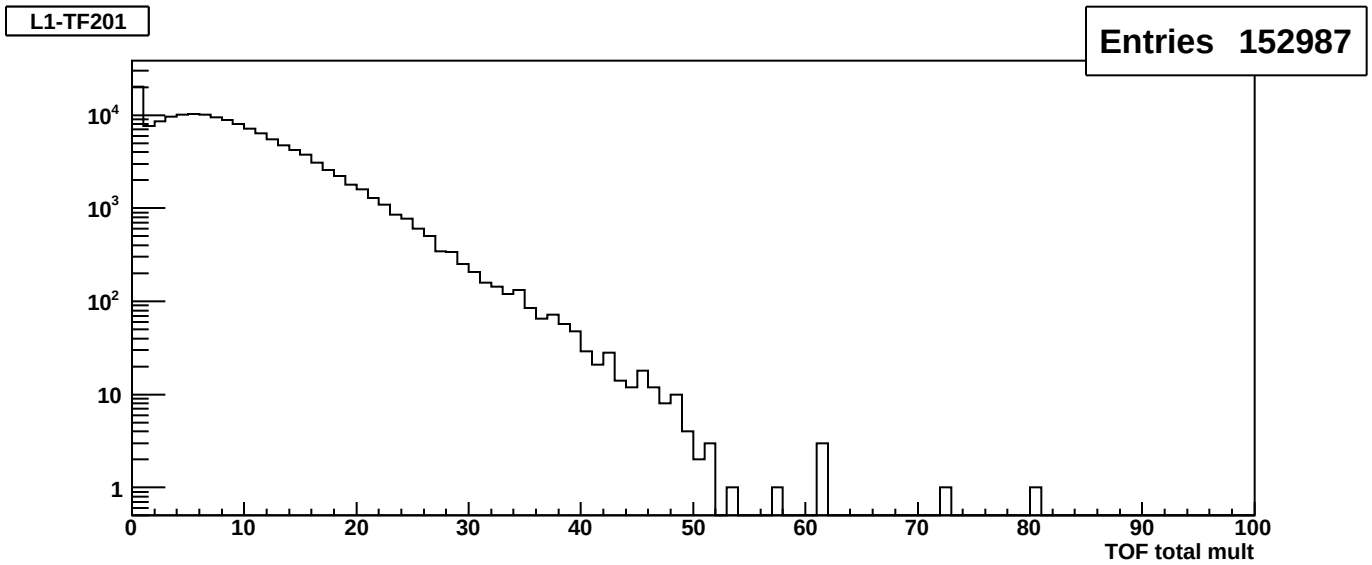

MIX-TF003
Entries 6425454
TOF MULT
TOF tray

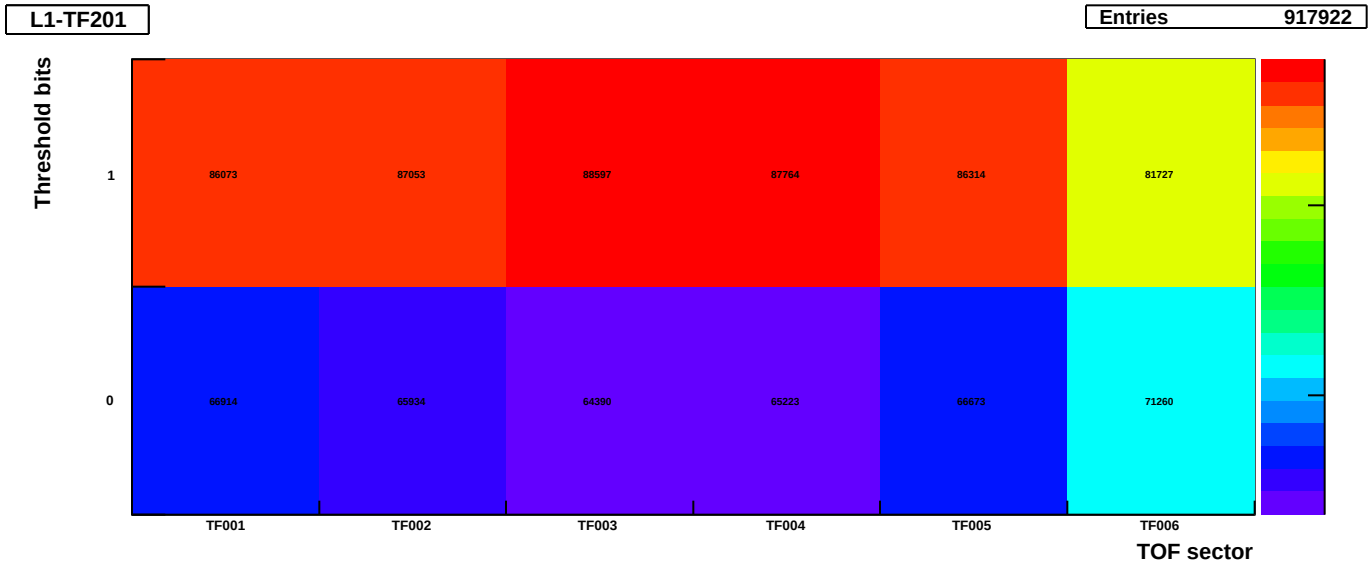

MIX-TF004
Entries 6425454
TOF MULT
TOF tray

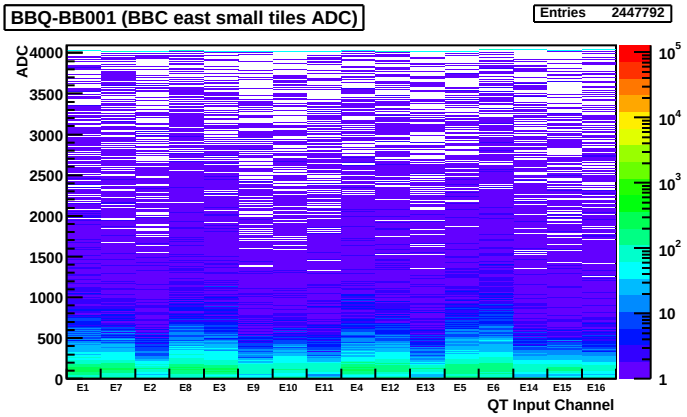

MIX-TF005
Entries 6425454
TOF MULT
TOF tray

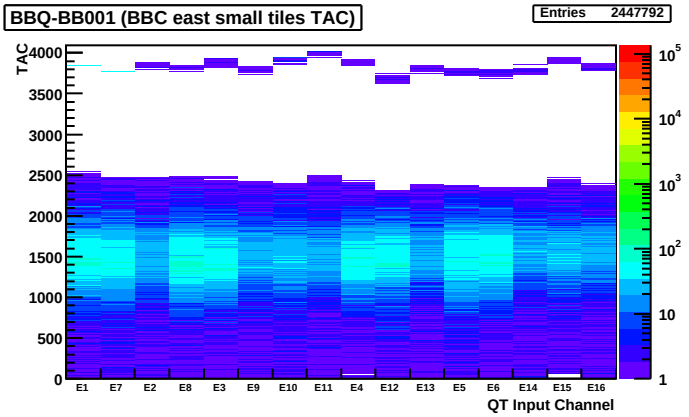

MIX-TF006
Entries 6425454
TOF MULT
TOF tray

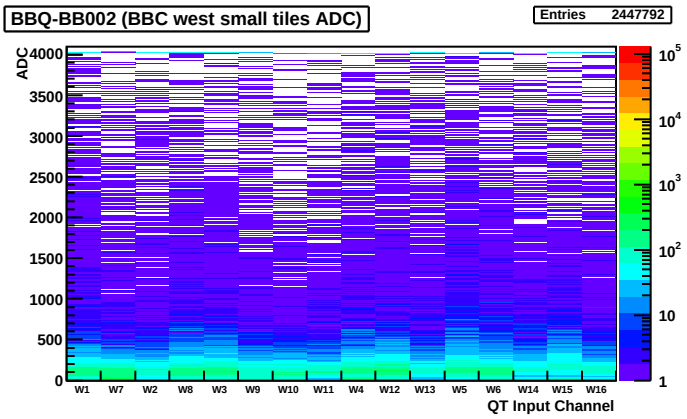

MIX-TF101
Entries 917922
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

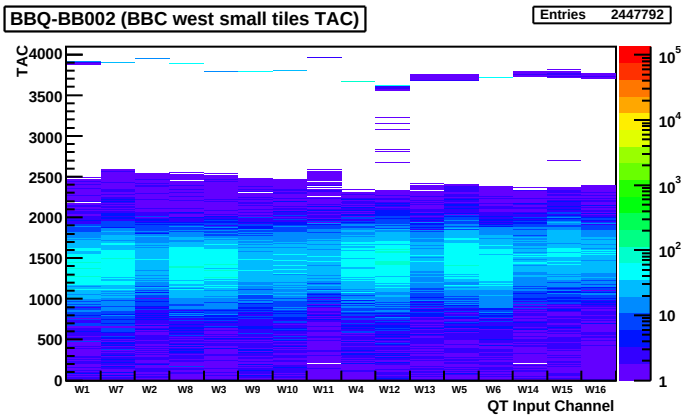

L1-TF201
Entries 152987
TOF total mult

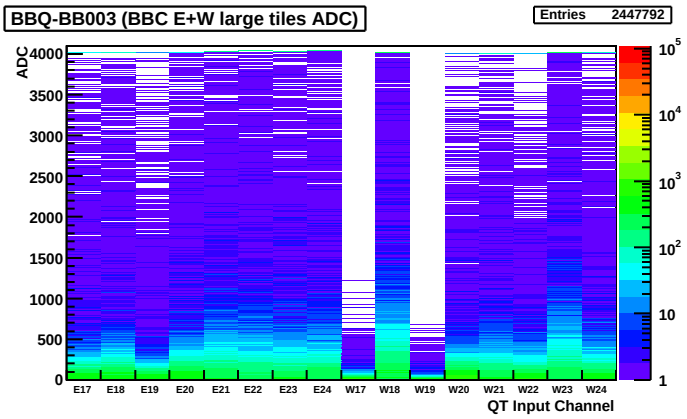

L1-TF201
Entries 917922
Threshold bits
86073
87053
88597
87764
86314
81727
66914
65934
64390
65223
66673
71260
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

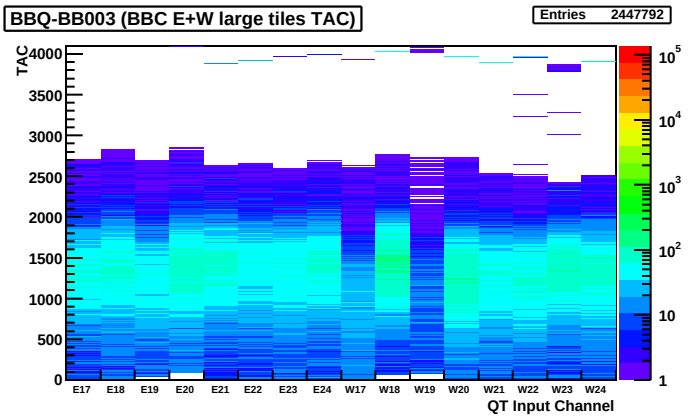

BBQ-BB001 (BBC east small tiles ADC)
Entries 2447792
ADC
QT Input Channel

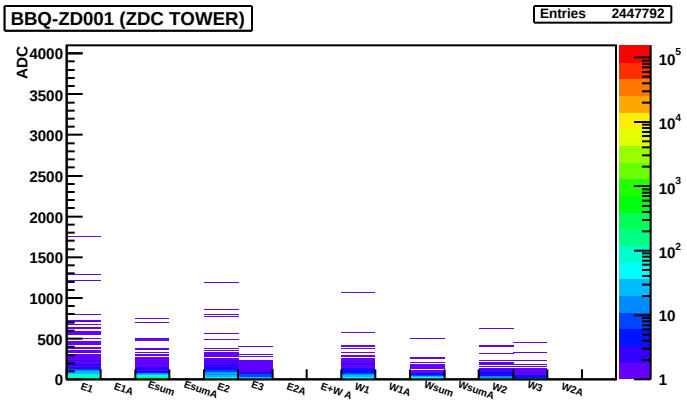

BBQ-BB001 (BBC east small tiles TAC)
Entries 2447792
TAC
QT Input Channel

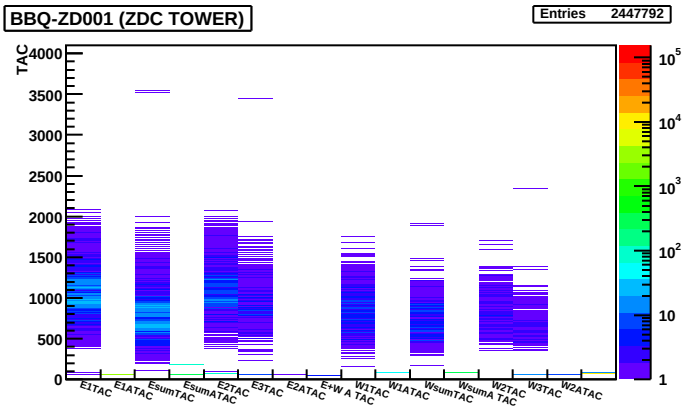

BBQ-BB002 (BBC west small tiles ADC)
Entries 2447792
ADC
QT Input Channel

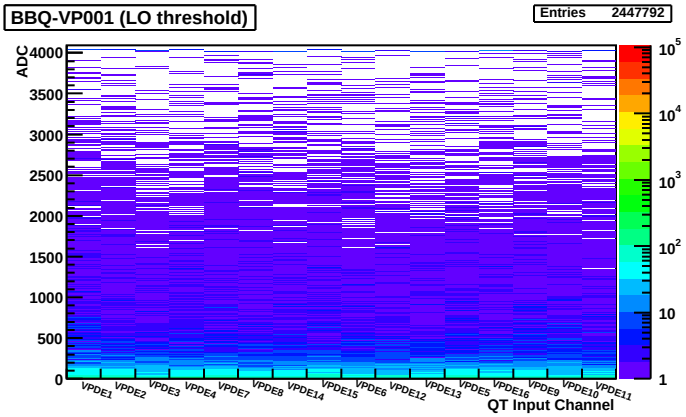

BBQ-BB002 (BBC west small tiles TAC)
Entries 2447792
TAC
QT Input Channel

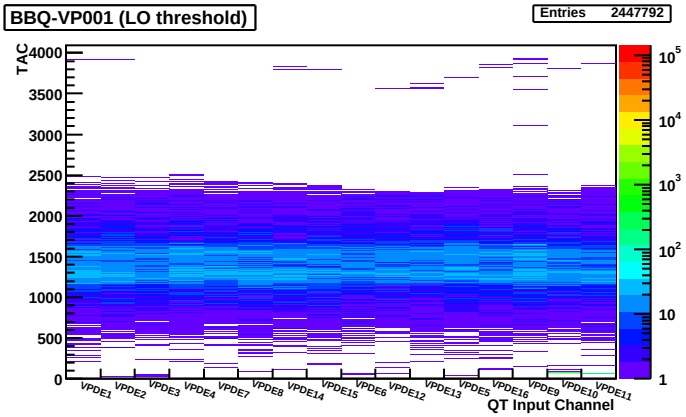

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2447792
ADC
QT Input Channel

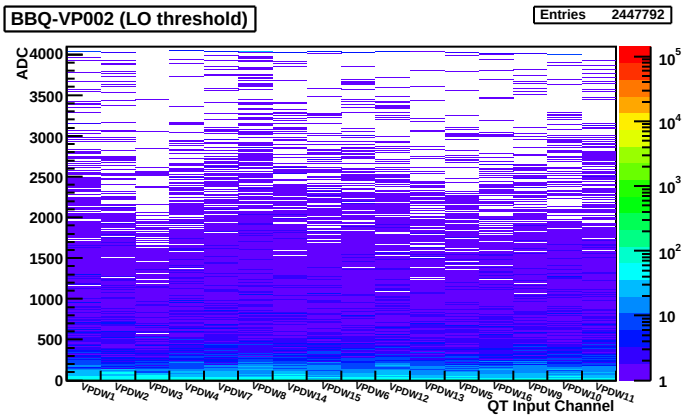

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2447792
TAC
QT Input Channel

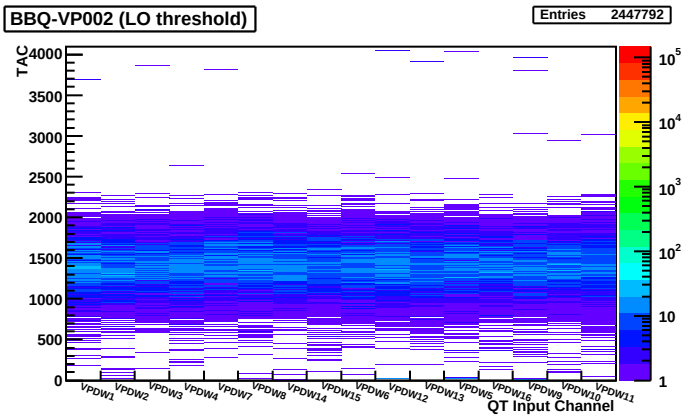

BBQ-ZD001 (ZDC TOWER)
Entries 2447792
ADC

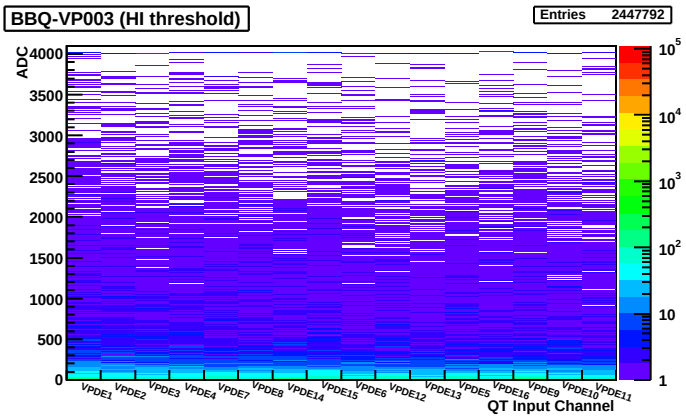

BBQ-ZD001 (ZDC TOWER)
Entries 2447792
TAC

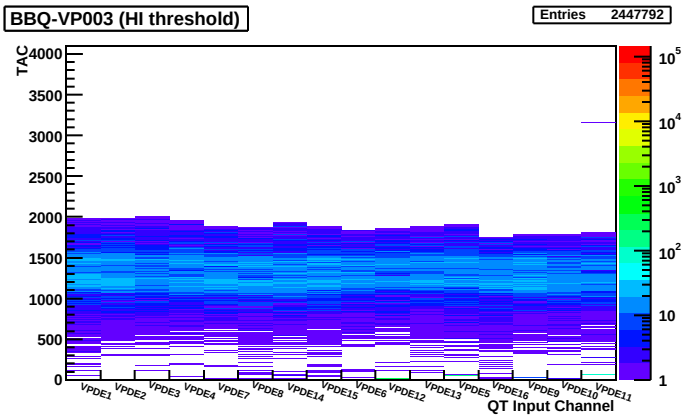

BBQ-VP001 (LO threshold)
Entries 2447792
ADC
QT Input Channel

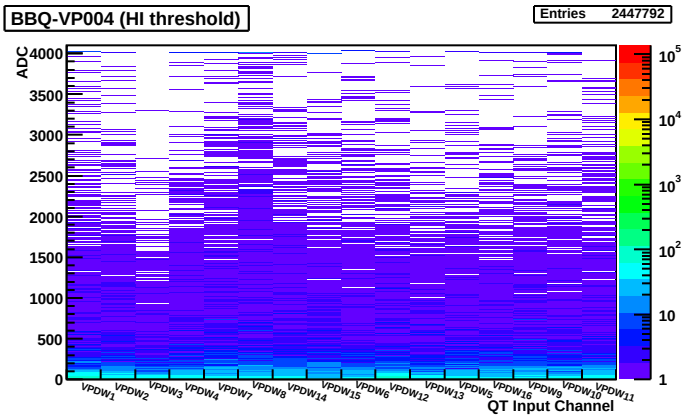

BBQ-VP001 (LO threshold)
Entries 2447792
TAC
QT Input Channel

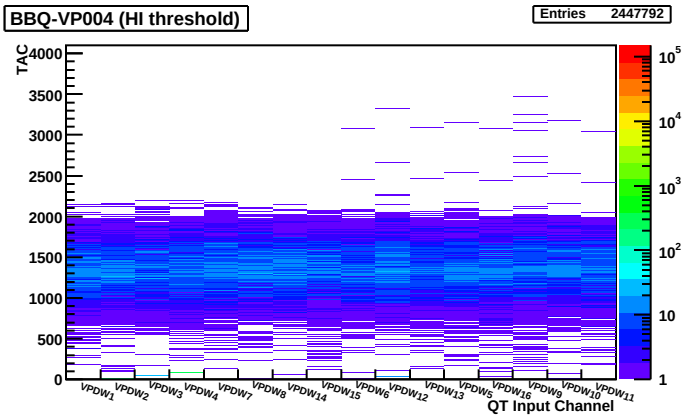

BBQ-VP002 (LO threshold)
Entries 2447792
ADC
QT Input Channel

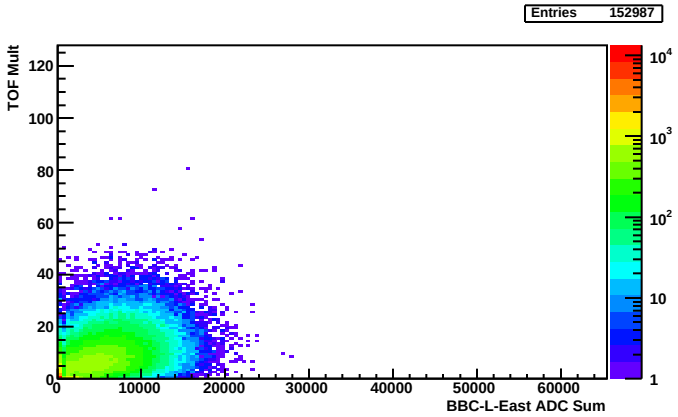

BBQ-VP002 (LO threshold)
Entries 2447792
TAC
QT Input Channel

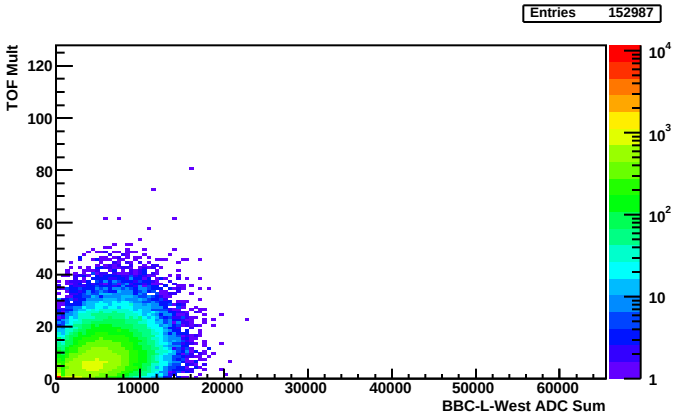

BBQ-VP003 (HI threshold)
Entries 2447792
ADC
QT Input Channel

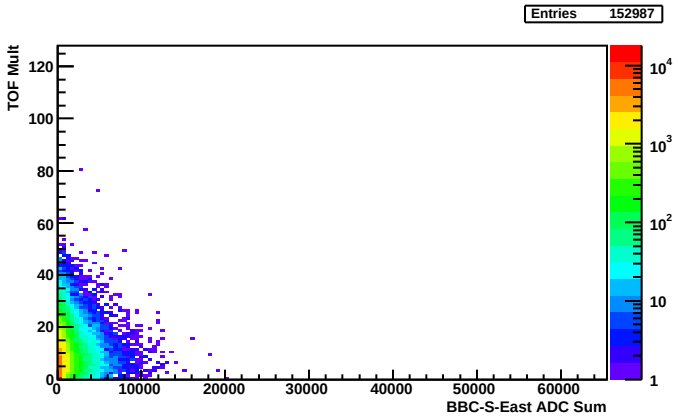

BBQ-VP003 (HI threshold)
Entries 2447792
TAC
QT Input Channel

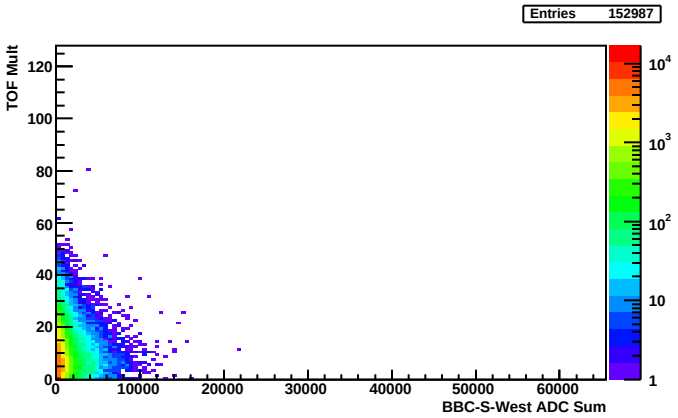

BBQ-VP004 (HI threshold)
Entries 2447792
ADC
QT Input Channel

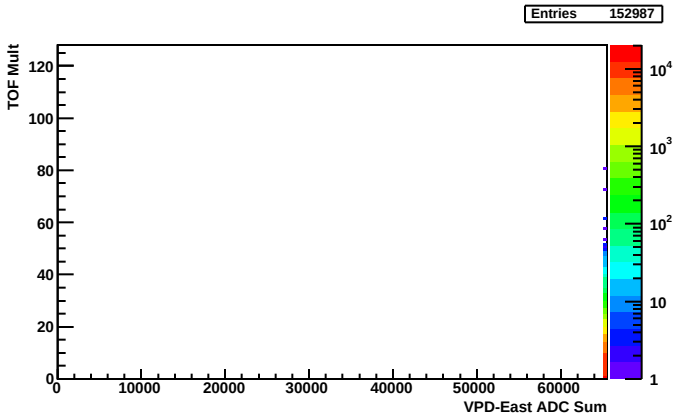

BBQ-VP004 (HI threshold)
Entries 2447792
TAC
QT Input Channel

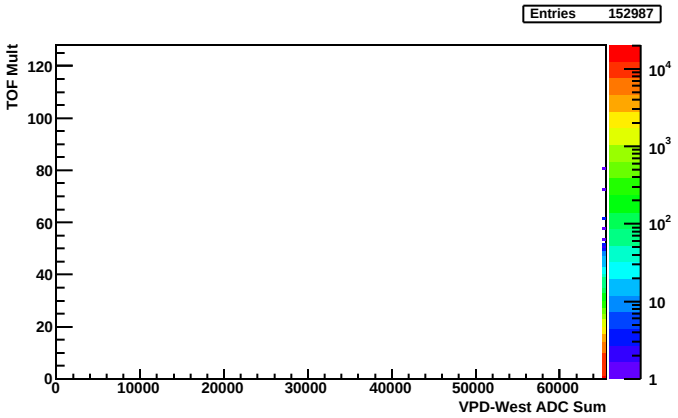

Entries 152987
TOF Mult
BBC-L-East ADC Sum

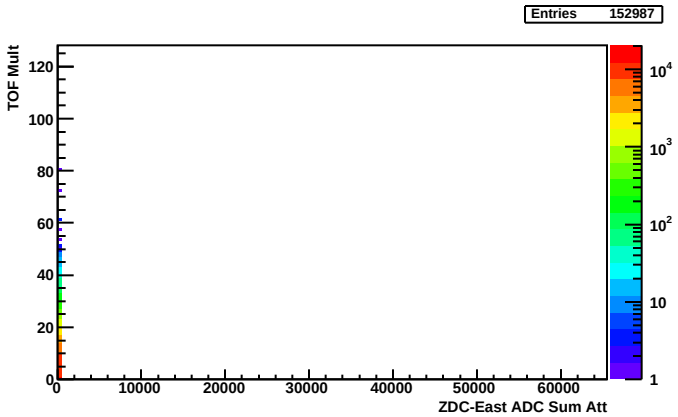

Entries 152987
TOF Mult
BBC-L-West ADC Sum

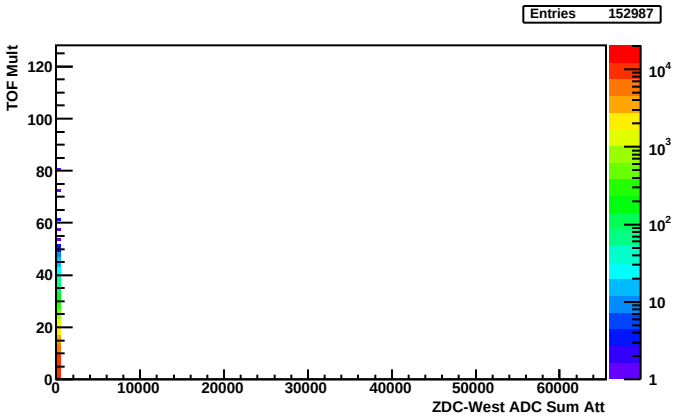

Entries 152987
TOF Mult
BBC-S-East ADC Sum

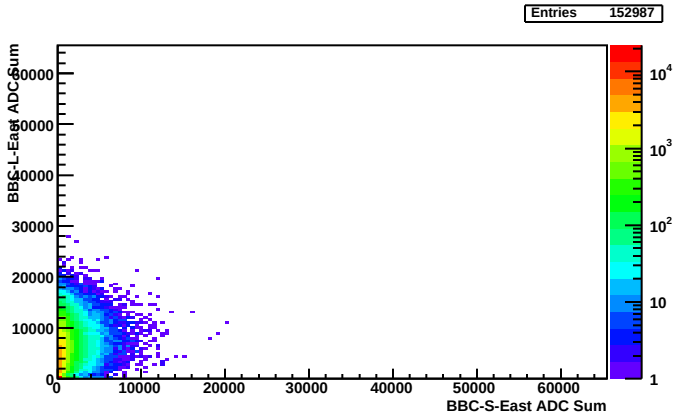

Entries 152987
TOF Mult
BBC-S-West ADC Sum

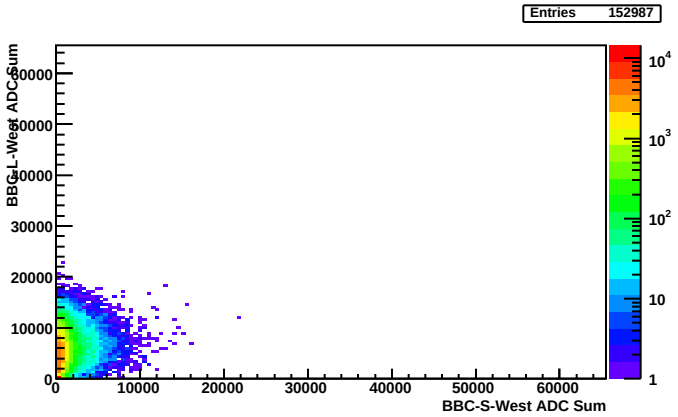

Entries 152987
TOF Mult
VPD-East ADC Sum

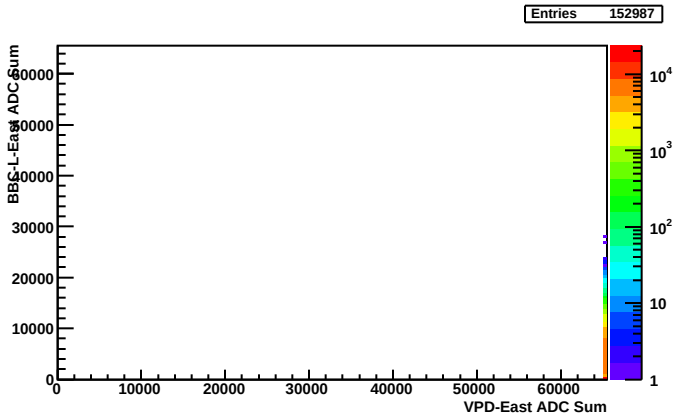

Entries 152987
TOF Mult
VPD-West ADC Sum

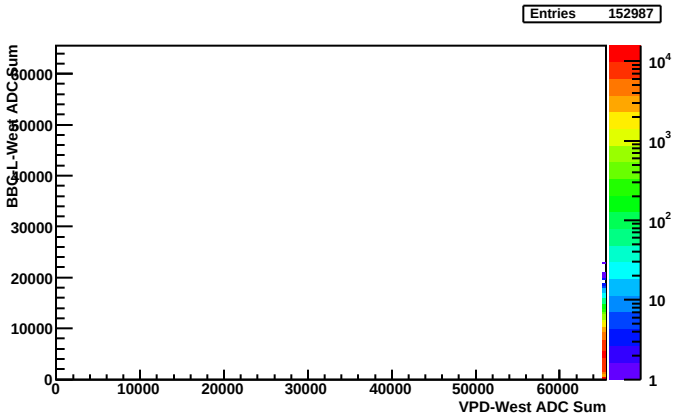

Entries 152987
TOF Mult
ZDC-East ADC Sum Att

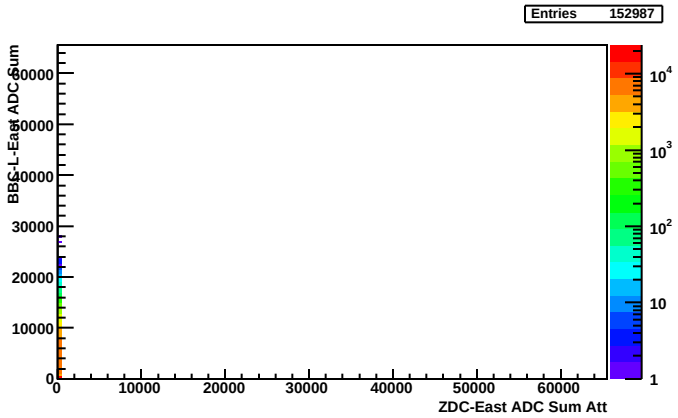

Entries 152987
TOF Mult
ZDC-West ADC Sum Att

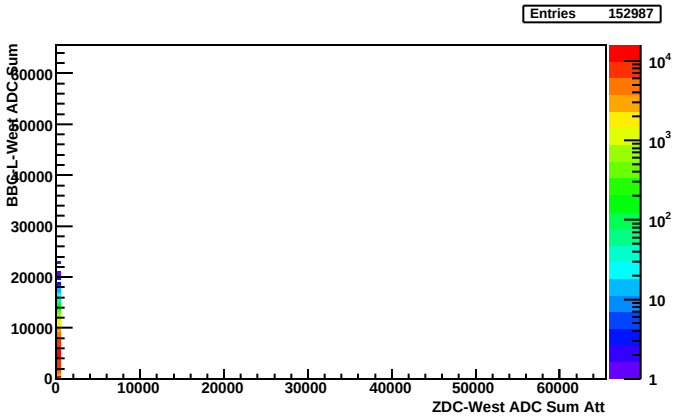

Entries 152987
BBC-L-East ADC Sum
BBC-S-East ADC Sum

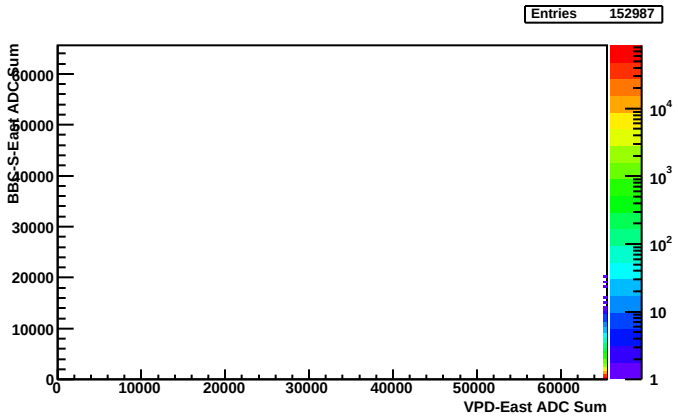

Entries 152987
BBC-L-West ADC Sum
BBC-S-West ADC Sum

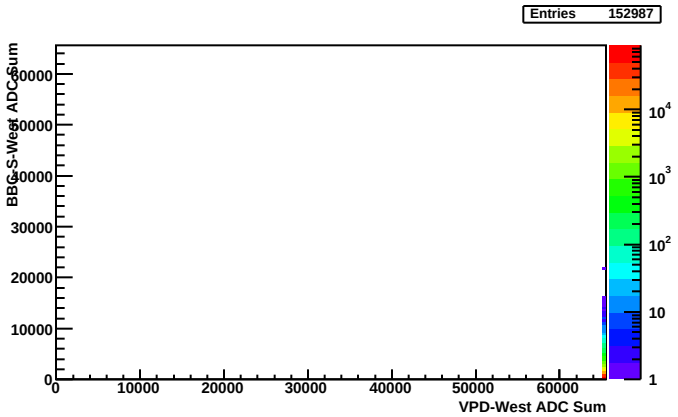

Entries 152987
BBC-L-East ADC Sum
VPD-East ADC Sum

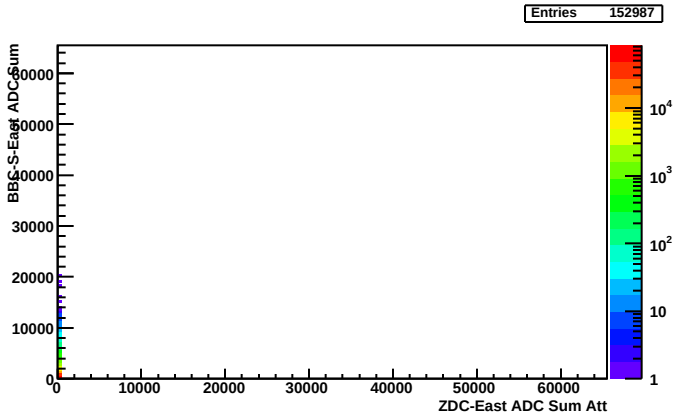

Entries 152987
BBC-L-West ADC Sum
VPD-West ADC Sum

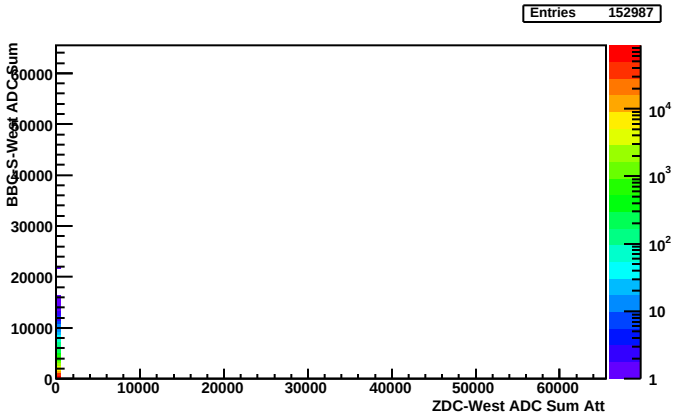

Entries 152987
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

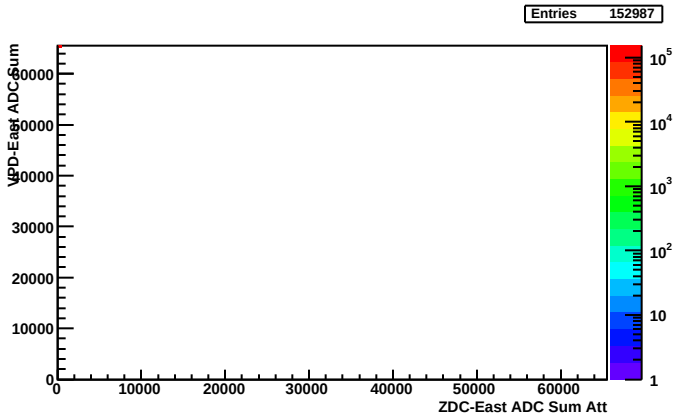

Entries 152987
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

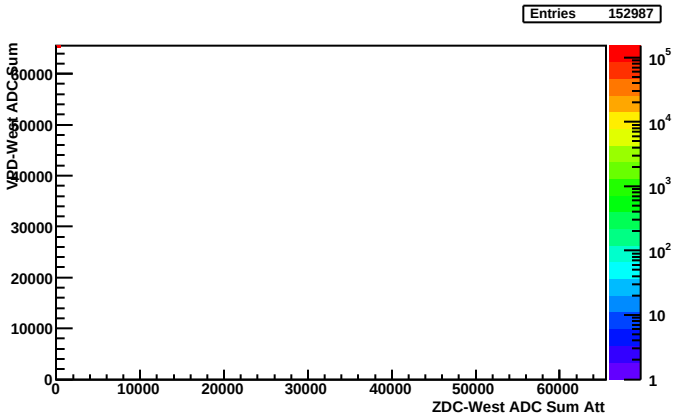

Entries 152987
BBC-S-East ADC Sum
VPD-East ADC Sum

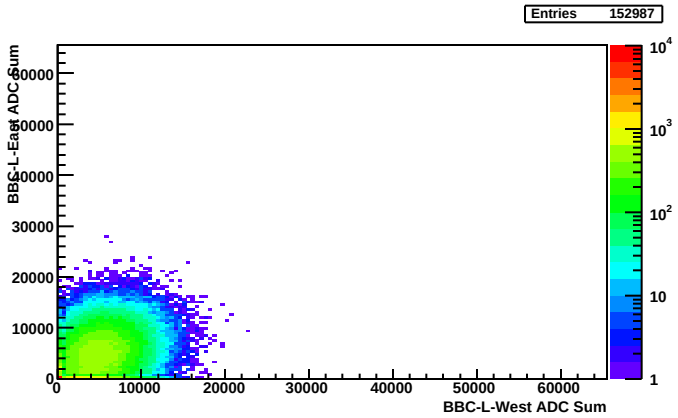

Entries 152987
BBC-S-West ADC Sum
VPD-West ADC Sum

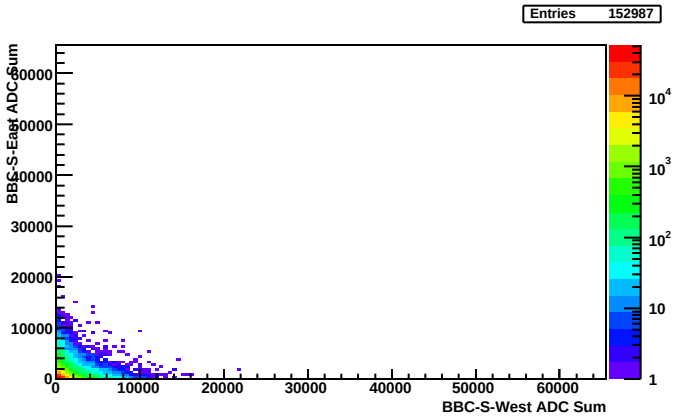

Entries 152987
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

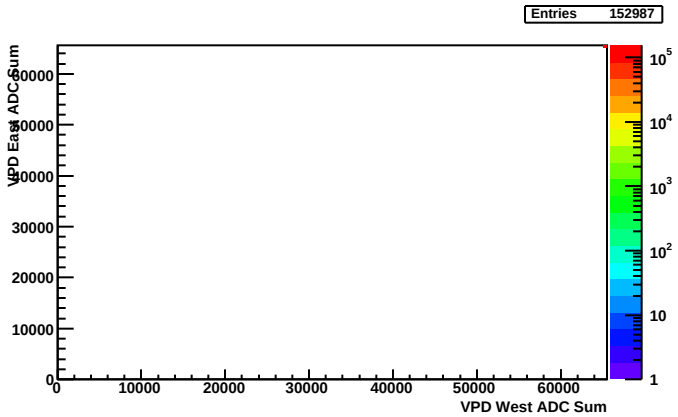

Entries 152987
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

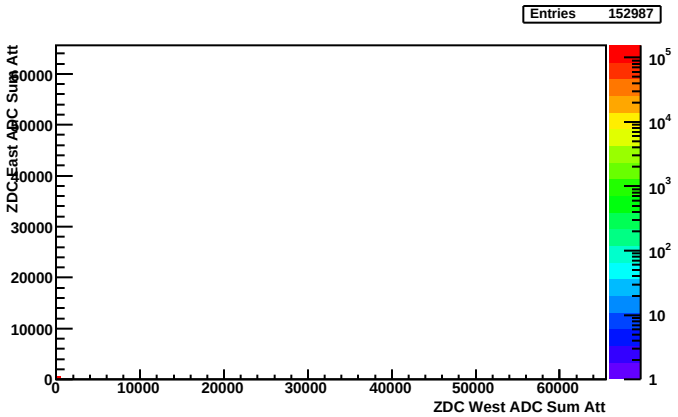

Entries 152987
VPD-East ADC Sum
ZDC-East ADC Sum Att

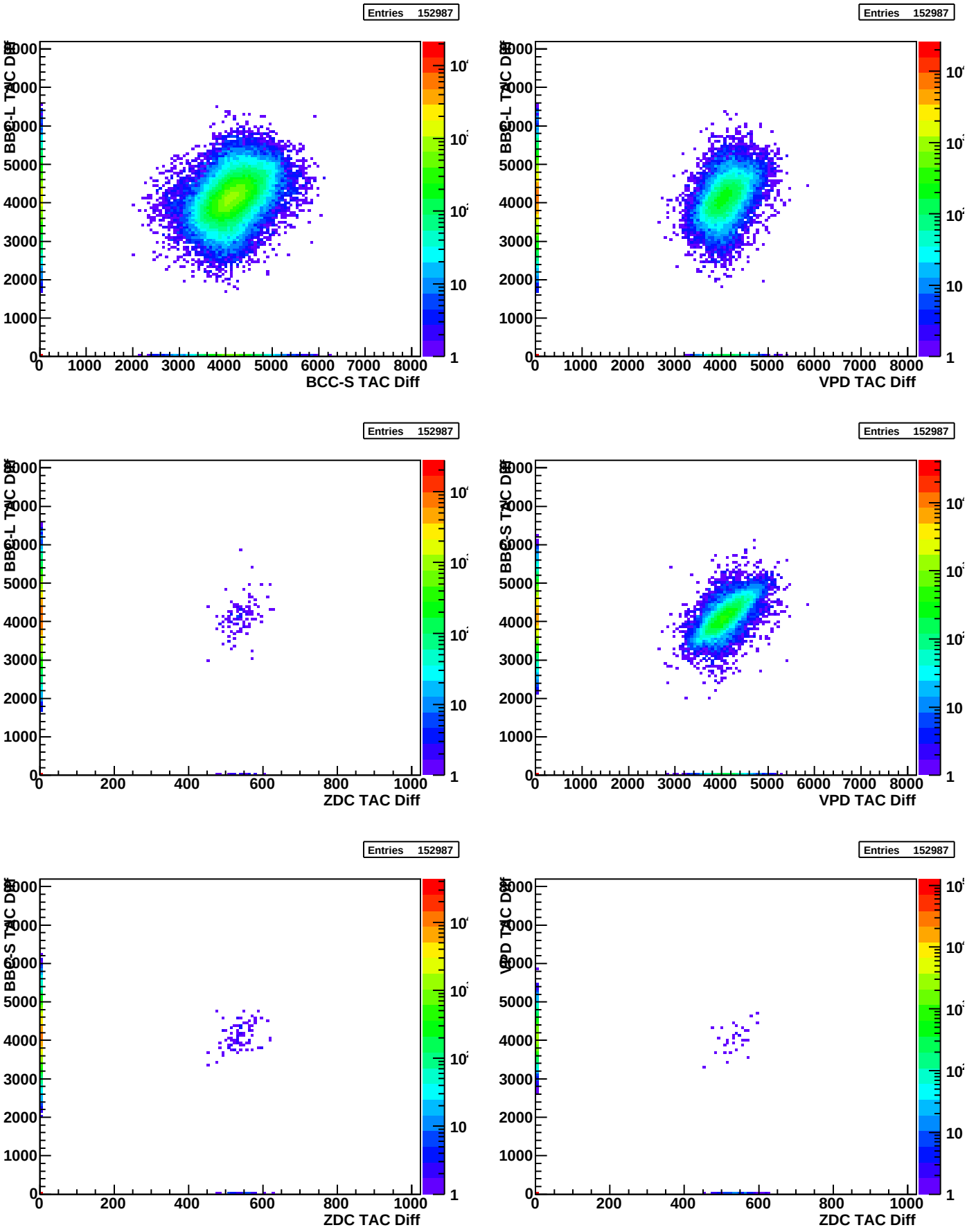

Entries 152987
VPD-West ADC Sum
ZDC-West ADC Sum Att

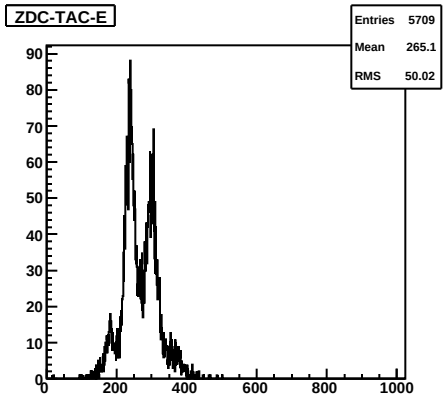

Entries 152987
BBC-L-East ADC Sum
BBC-L-West ADC Sum

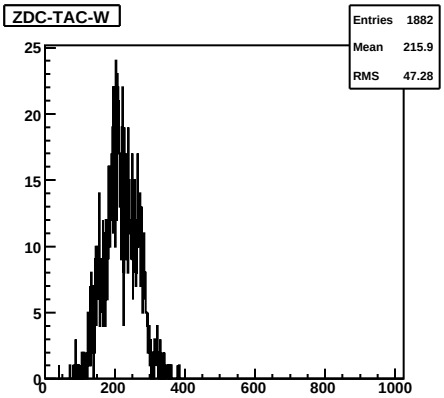

Entries 152987
BBC-S-East ADC Sum
BBC-S-West ADC Sum

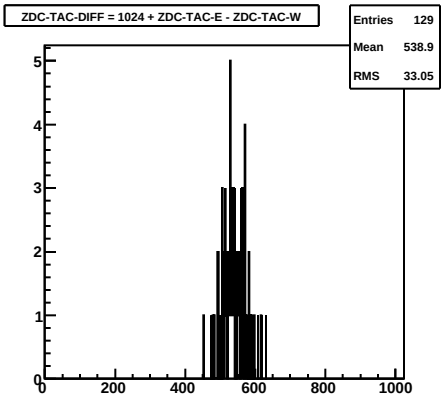

Entries 152987
VPD East ADC Sum
VPD West ADC Sum

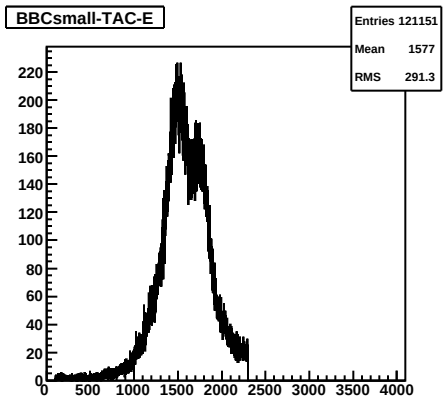

Entries 152987
ZDC East ADC Sum Att
ZDC West ADC Sum Att

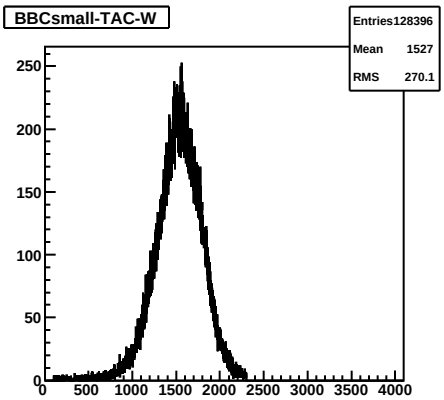

Entries 152987
BBC-L TAC Diff
BCC-S TAC Diff
Entries 152987
BBC-L TAC Diff
VPD TAC Diff
Entries 152987
BBC-L TAC Diff
ZDC TAC Diff
Entries 152987
BBC-S TAC Diff
VPD TAC Diff
Entries 152987
BBC-S TAC Diff
ZDC TAC Diff
Entries 152987
VPD TAC Diff
ZDC TAC Diff

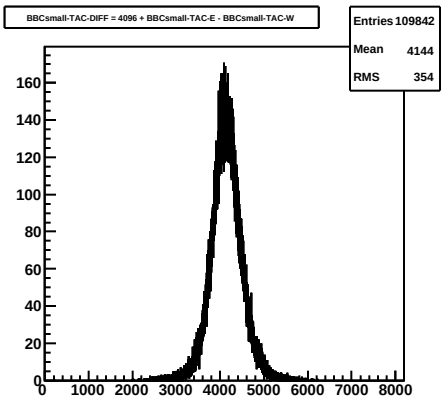

ZDC-TAC-E
Entries 5709
Mean 265.1
RMS 50.02

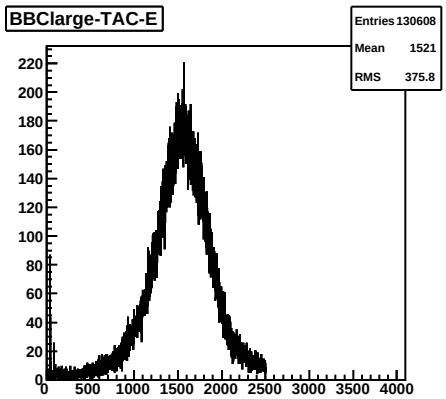

ZDC-TAC-W
Entries 1882
Mean 215.9
RMS 47.28

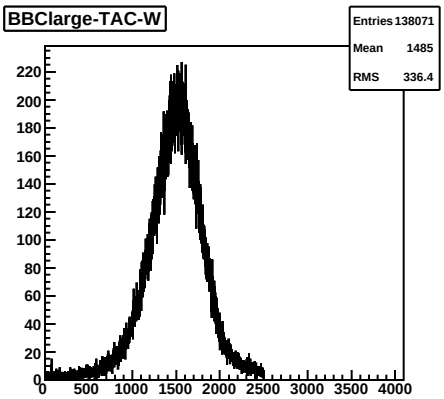

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 129
Mean 538.9
RMS 33.05

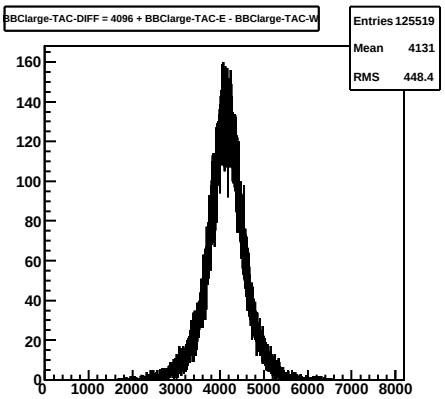

BBCsmall-TAC-E
Entries 121151
Mean 1577
RMS 291.3

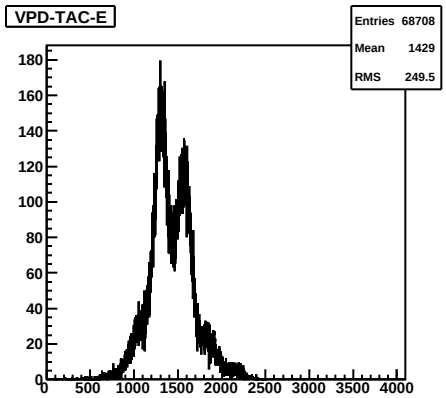

BBCsmall-TAC-W
Entries 128396
Mean 1527
RMS 270.1

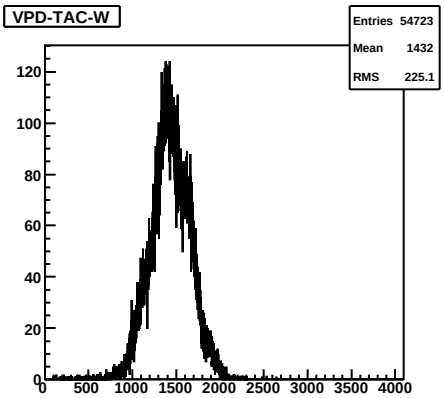

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 109842
Mean 4144
RMS 354

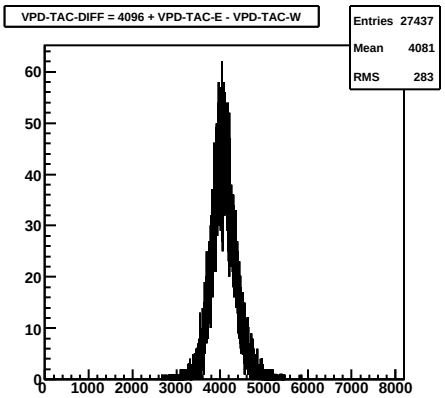

BBClarge-TAC-E
Entries 130608
Mean 1521
RMS 375.8

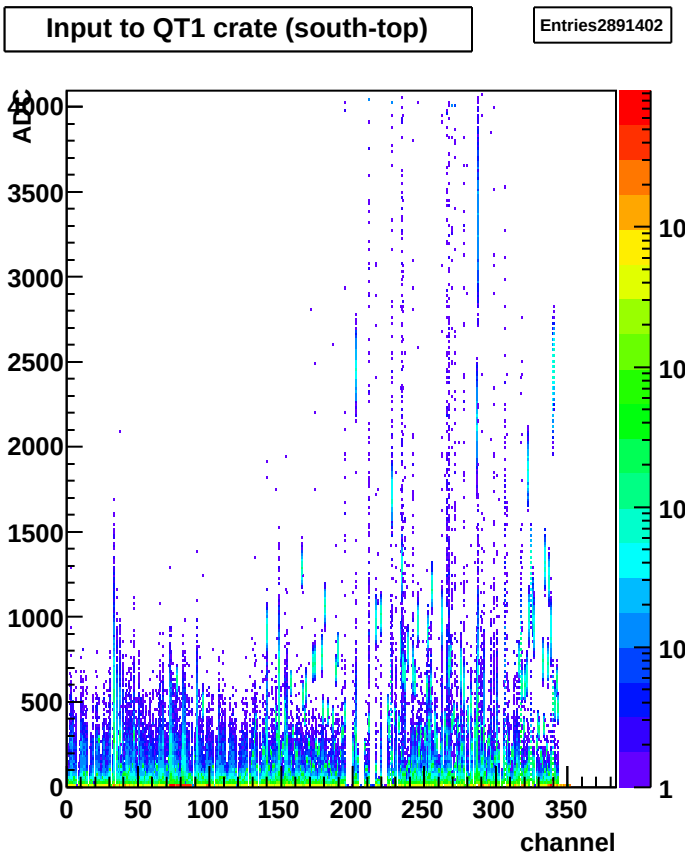

BBClarge-TAC-W
Entries 138071
Mean 1485
RMS 336.4

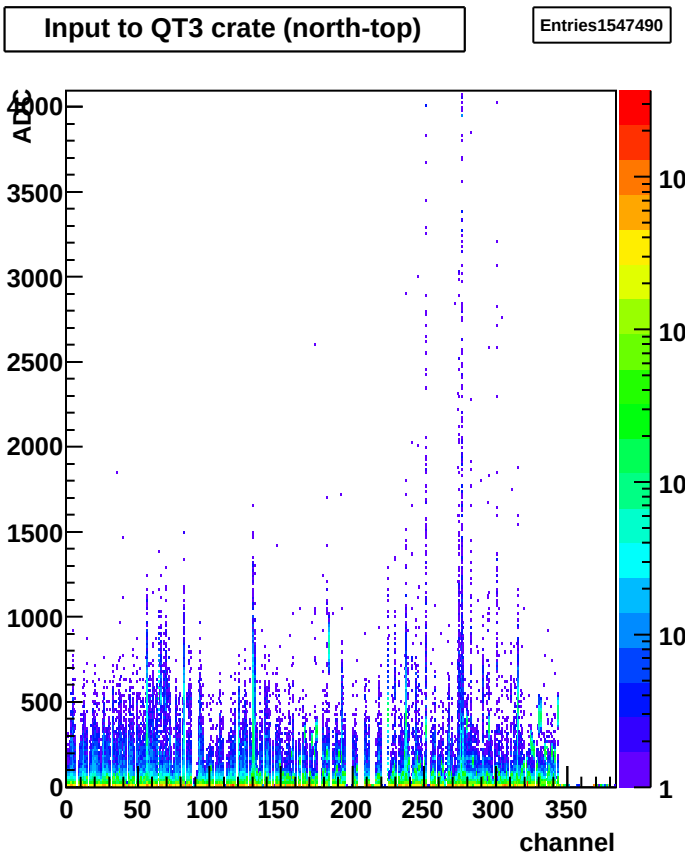

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 125519
Mean 4131
RMS 448.4

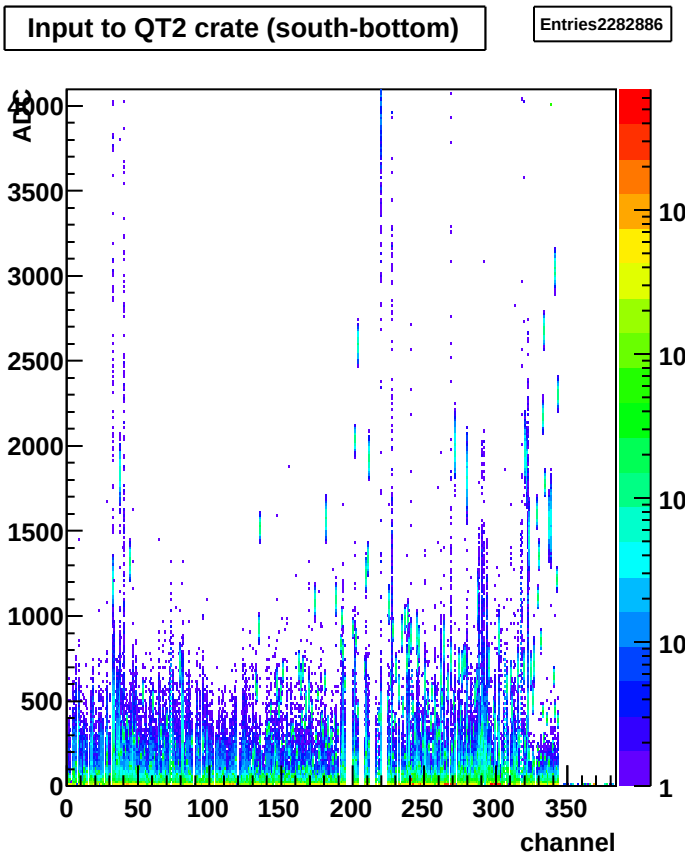

VPD-TAC-E
Entries 68708
Mean 1429
RMS 249.5

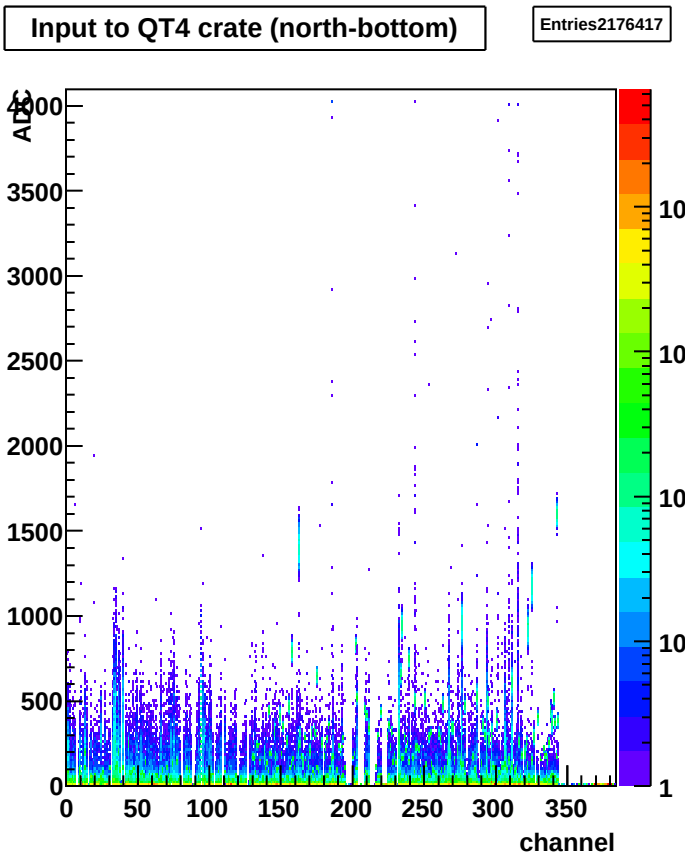

VPD-TAC-W
Entries 54723
Mean 1432
RMS 225.1

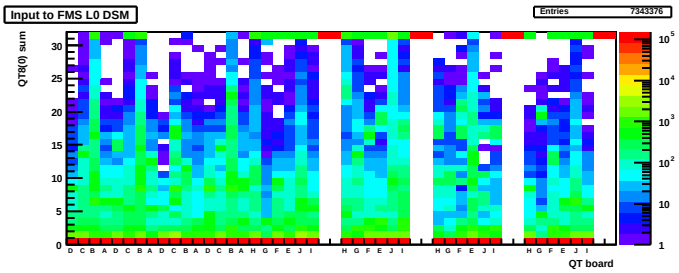

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 27437
Mean 4081
RMS 283

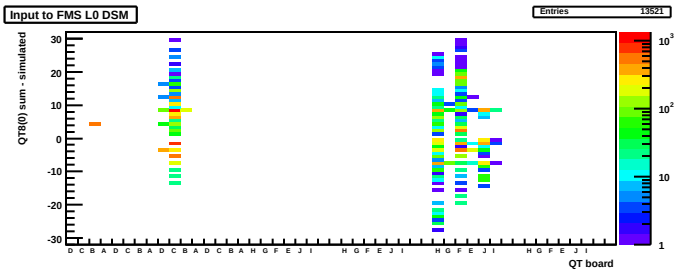

Input to QT1 crate (south-top)
Entries2891402
ADC
channel

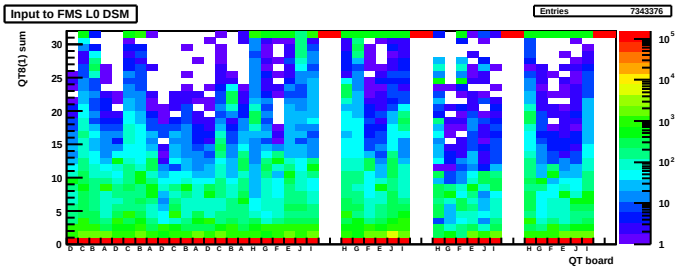

Input to QT3 crate (north-top)
Entries1547490
ADC
channel

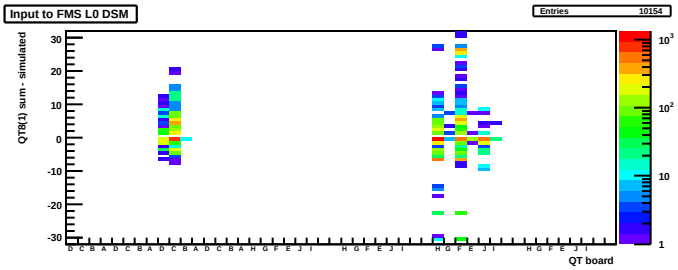

Input to QT2 crate (south-bottom)
Entries2282886
ADC
channel

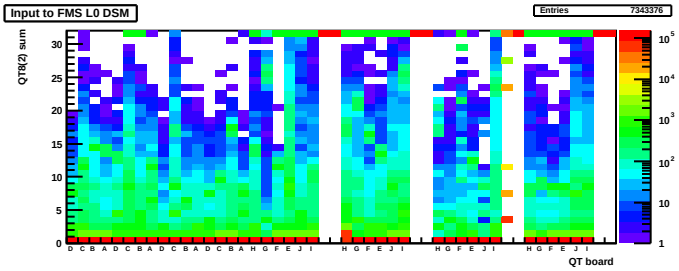

Input to QT4 crate (north-bottom)
Entries2176417
ADC
channel

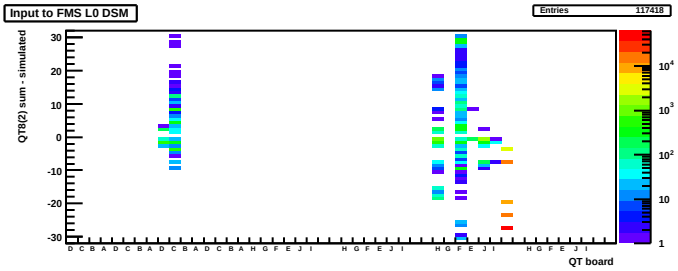

Input to FMS L0 DSM
Entries 7343376
QT8(0) sum
QT board

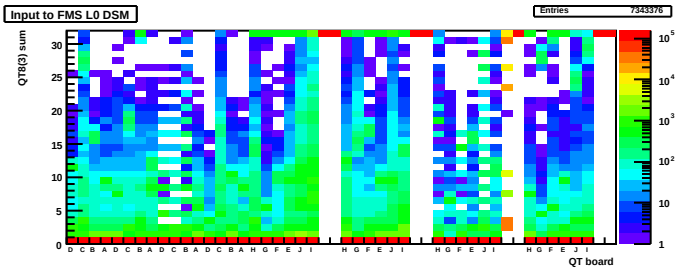

Input to FMS L0 DSM
Entries 13521
QT8(0) sum - simulated
QT board

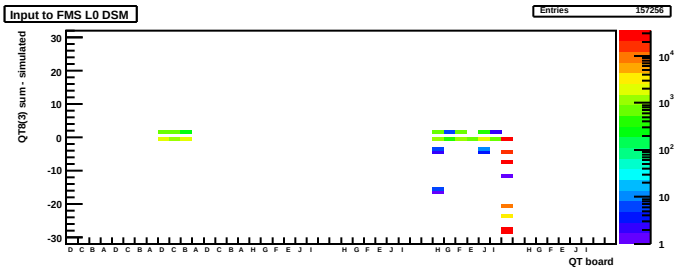

Input to FMS L0 DSM
Entries 7343376
QT8(1) sum
QT board

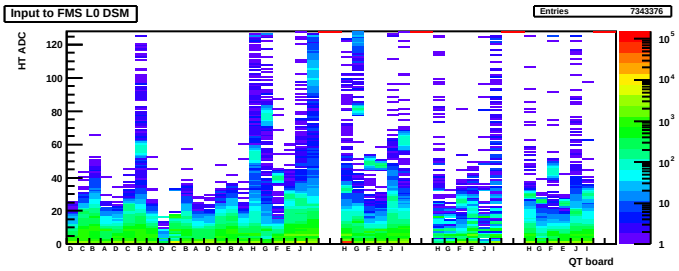

Input to FMS L0 DSM
Entries 10154
QT8(1) sum - simulated
QT board

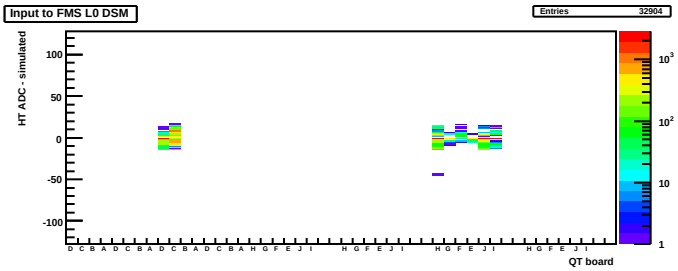

Input to FMS L0 DSM
Entries 7343376
QT8(2) sum
QT board

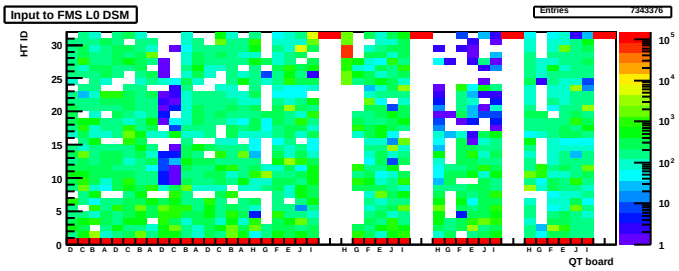

Input to FMS L0 DSM
Entries 117418
QT8(2) sum - simulated
QT board

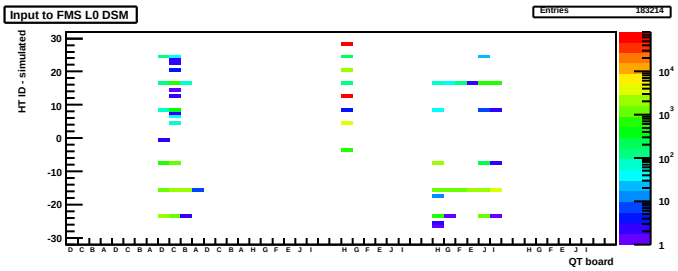

Input to FMS L0 DSM
Entries 7343376
QT8(3) sum
QT board

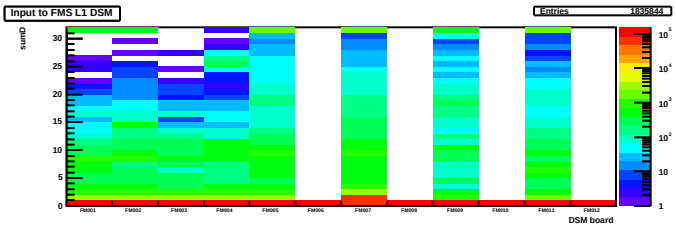

Input to FMS L0 DSM
Entries 157256
QT8(3) sum - simulated
QT board

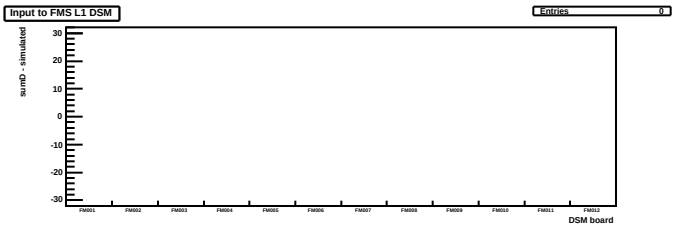

Input to FMS L0 DSM
Entries 7343376
HT ADC
QT board

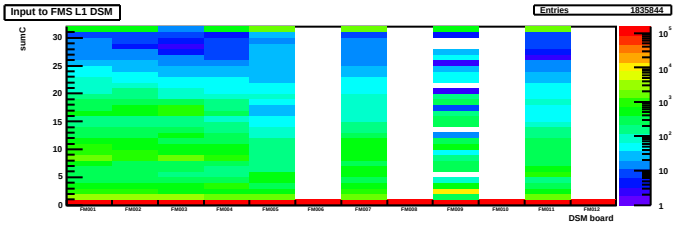

Input to FMS L0 DSM
Entries 32904
HT ADC - simulated
QT board

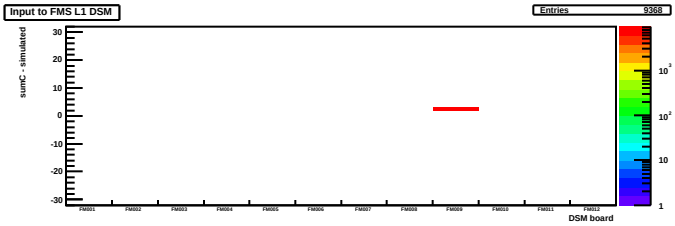

Input to FMS L0 DSM
Entries 7343376
HT ID
QT board

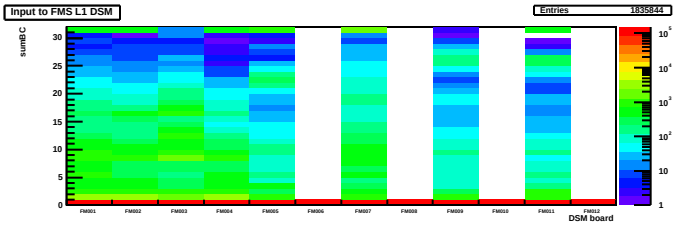

Input to FMS L0 DSM
Entries 183214
HT ID - simulated
QT board

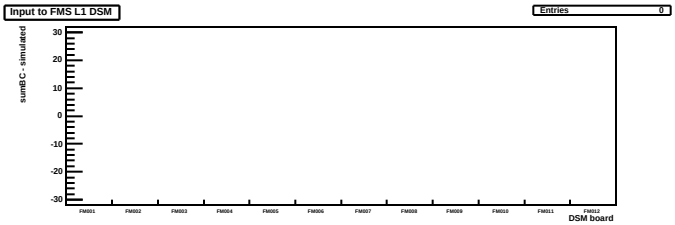

Input to FMS L1 DSM
Entries 1835844
sumD
DSM board

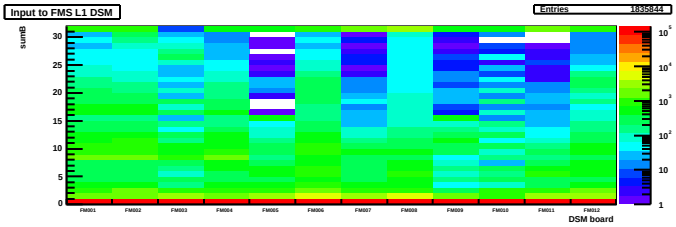

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

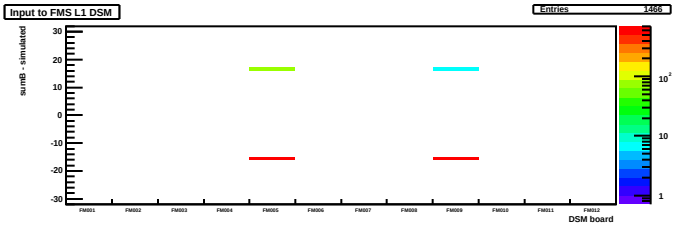

Input to FMS L1 DSM
Entries 1835844
sumC
DSM board

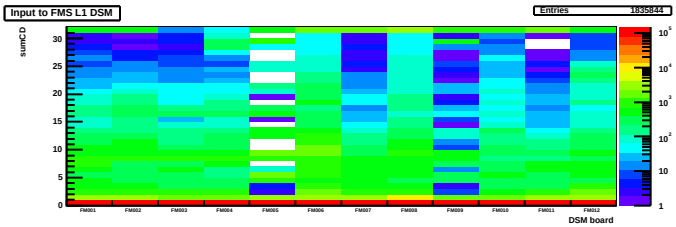

Input to FMS L1 DSM
Entries 9368
sumC - simulated
DSM board

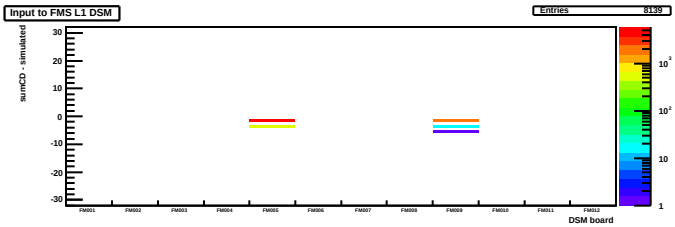

Input to FMS L1 DSM
Entries 1835844
sumBC
DSM board

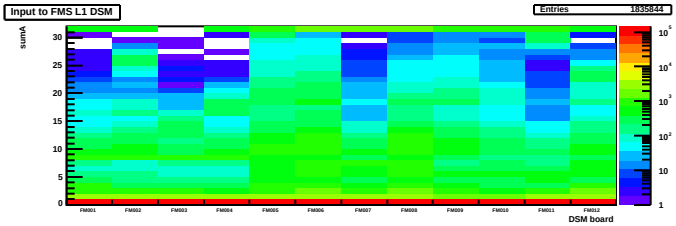

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

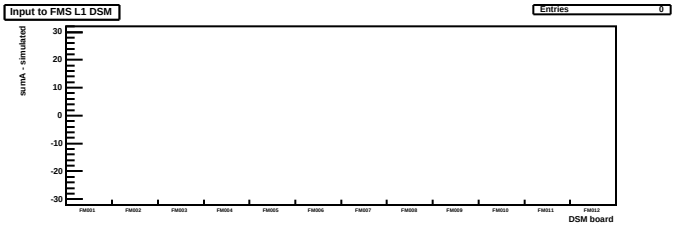

Input to FMS L1 DSM
Entries 1835844
sumB
DSM board

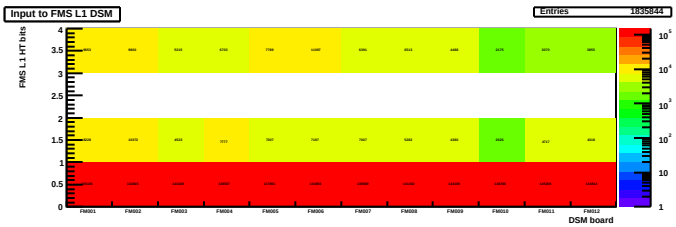

Input to FMS L1 DSM
Entries 1466
sumB - simulated
DSM board

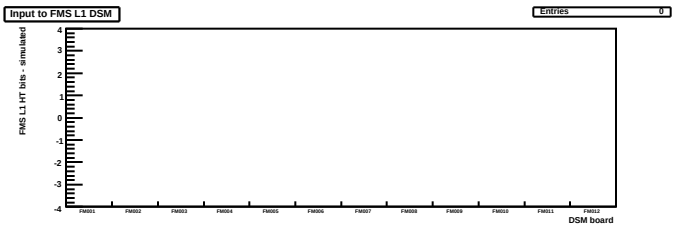

Input to FMS L1 DSM
Entries 1835844
sumCD
DSM board

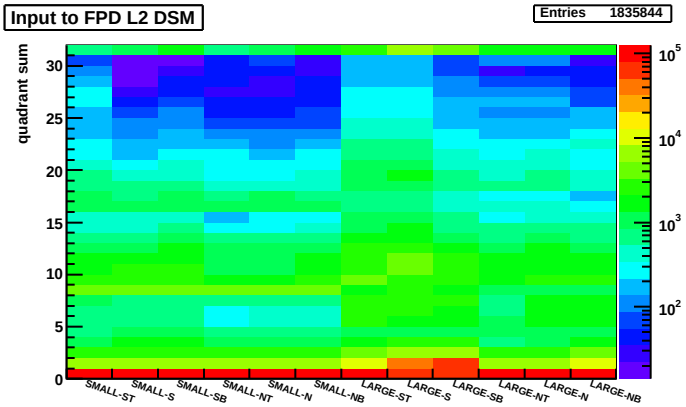

Input to FMS L1 DSM
Entries 8139
sumCD - simulated
DSM board

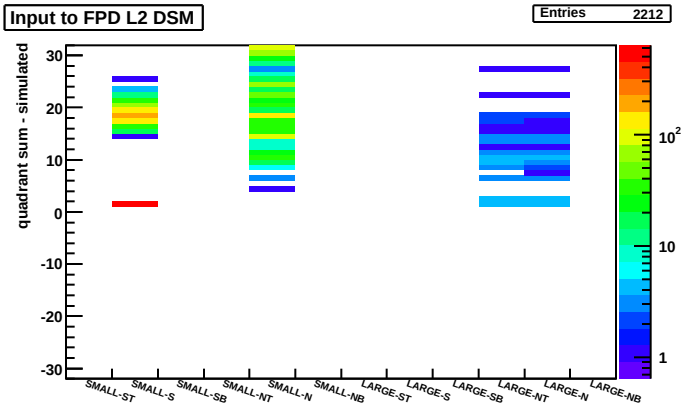

Input to FMS L1 DSM
Entries 1835844
sumA
DSM board

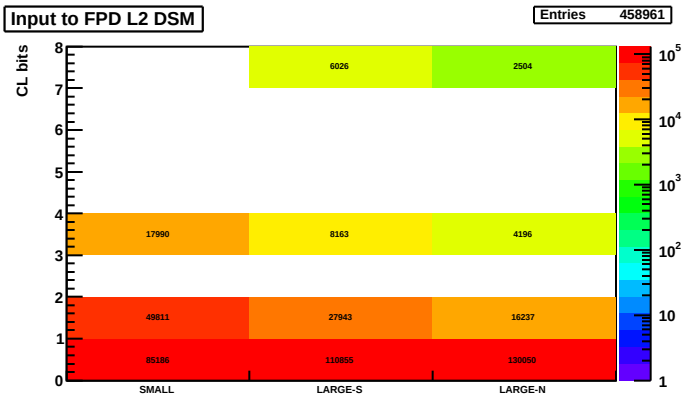

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

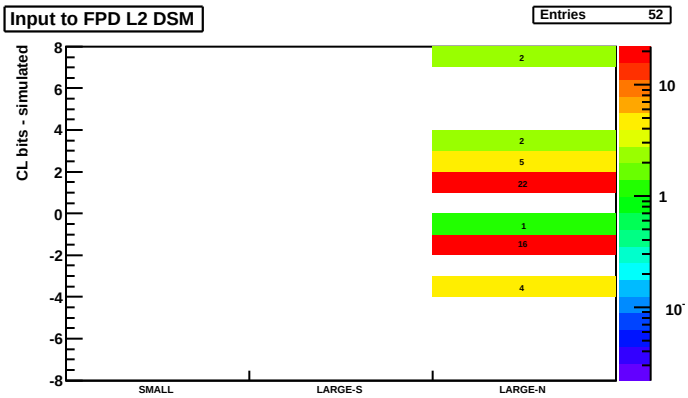

Input to FMS L1 DSM
Entries 1835844
FMS L1 HT bits
DSM board

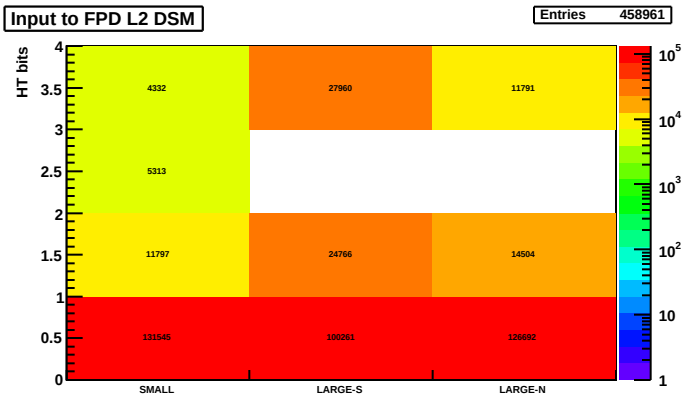

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

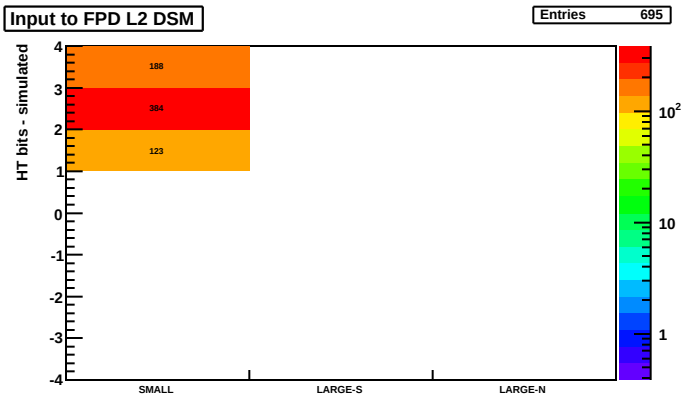

Input to FPD L2 DSM
Entries 1835844
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

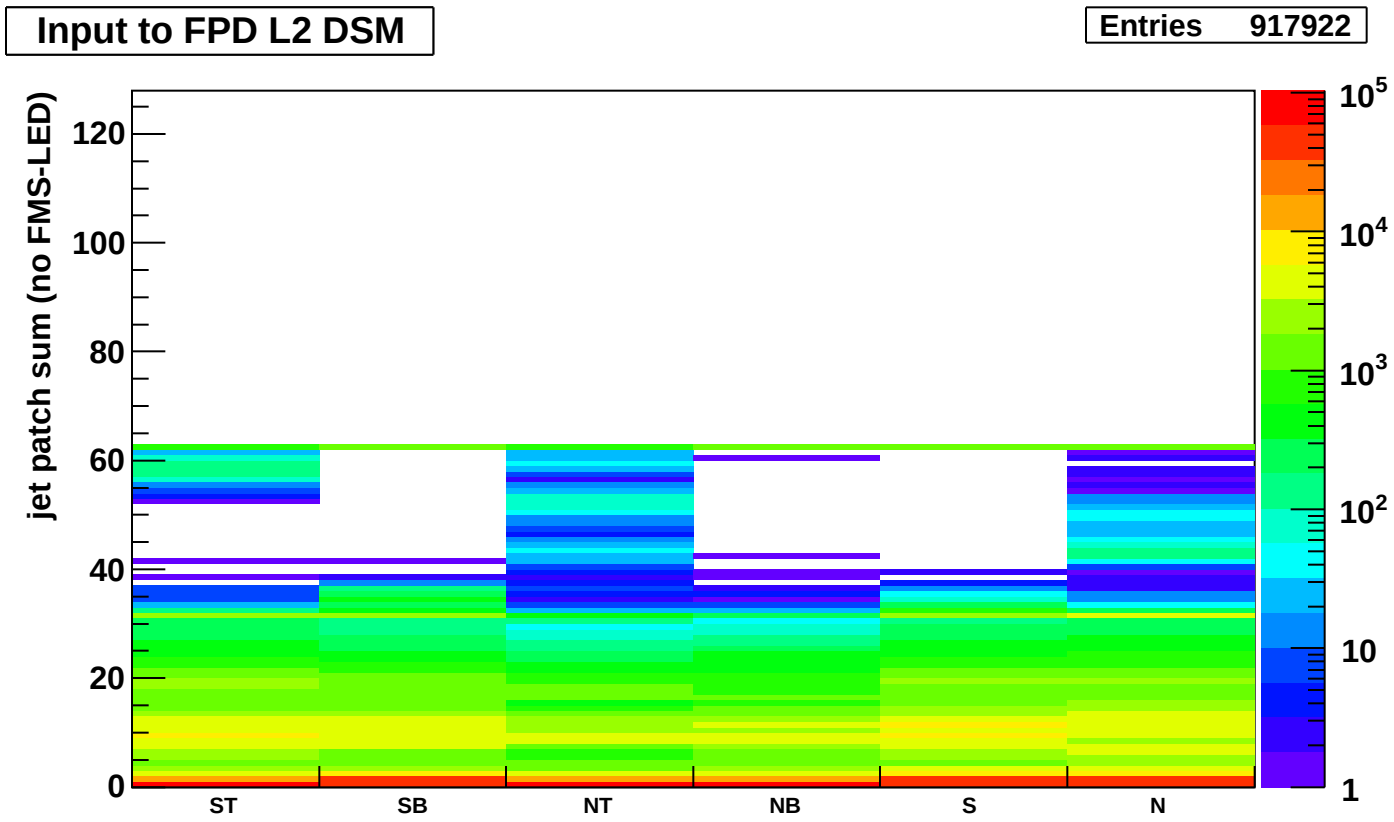

Input to FPD L2 DSM
Entries 2212
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

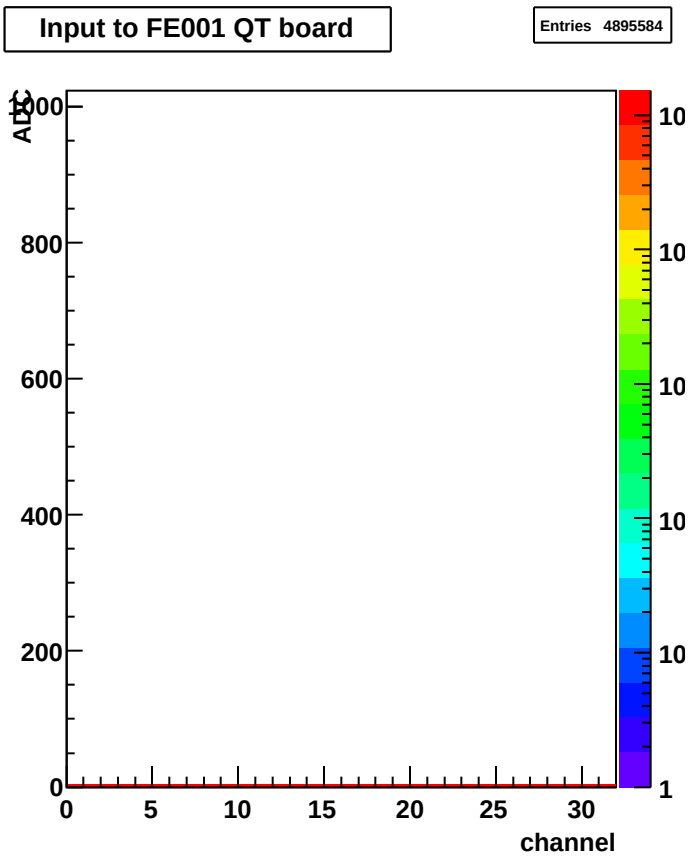

Input to FPD L2 DSM
Entries 458961
CL bits
6026
2504
17990
8163
4196
49811
27943
16237
85186
110855
130050
SMALL
LARGE-S
LARGE-N

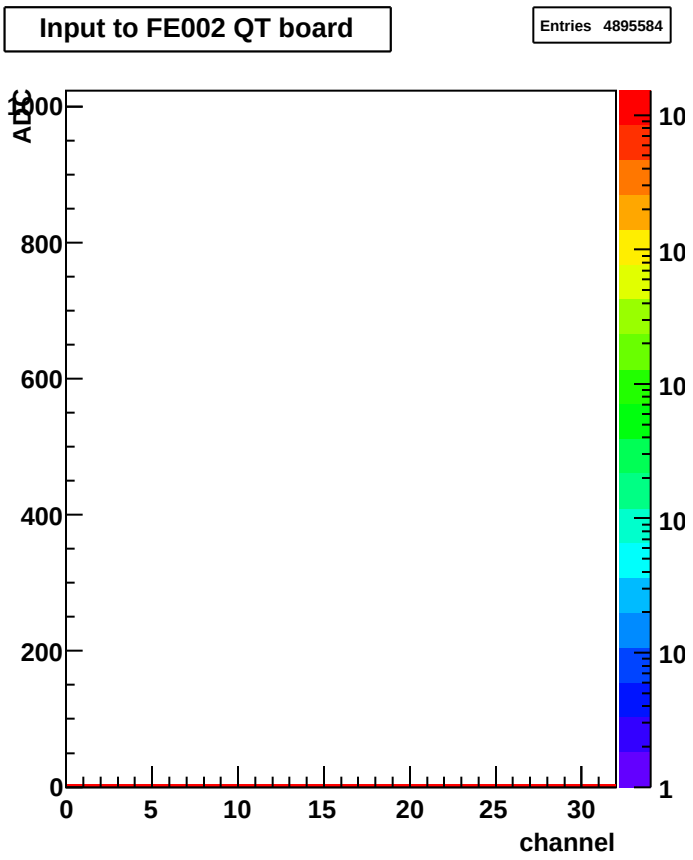

Input to FPD L2 DSM
Entries 52
CL bits - simulated
2
2
5
22
1
16
4
SMALL
LARGE-S
LARGE-N

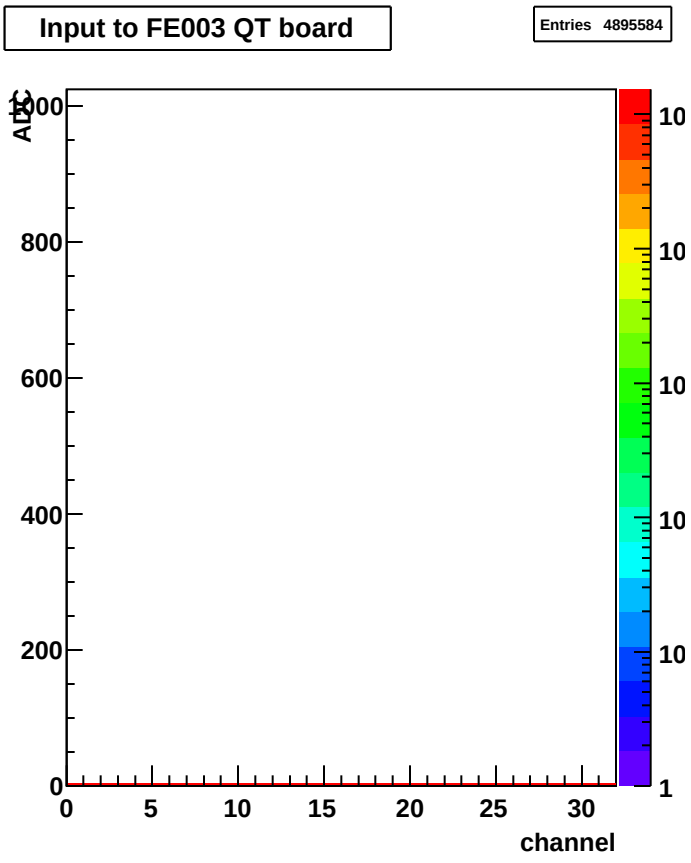

Input to FPD L2 DSM
Entries 458961
HT bits
4332
27960
11791
5313
11797
24766
14504
131545
100261
126692
SMALL
LARGE-S
LARGE-N

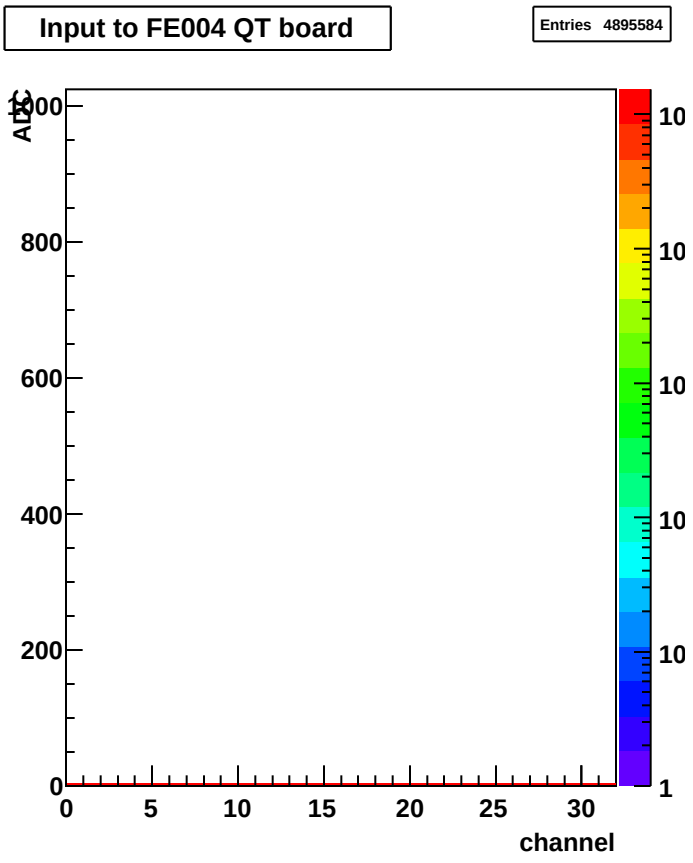

Input to FPD L2 DSM
Entries 695
HT bits - simulated
188
384
123
SMALL
LARGE-S
LARGE-N

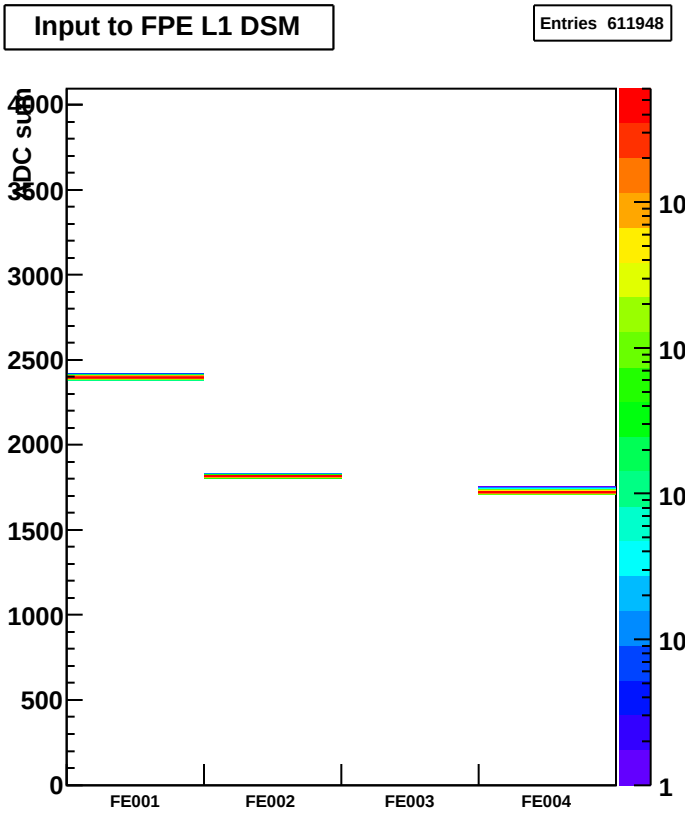

Input to FPD L2 DSM
Entries 917922
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
$10^2$
$10^3$
$10^4$
$10^5$

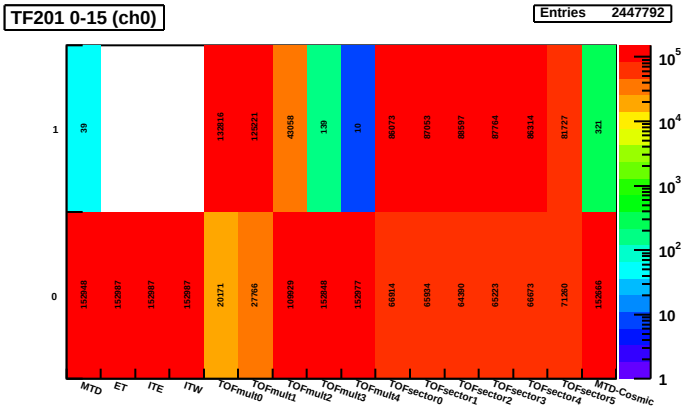

Input to FE001 QT board
Entries 4895584
ADC
channel

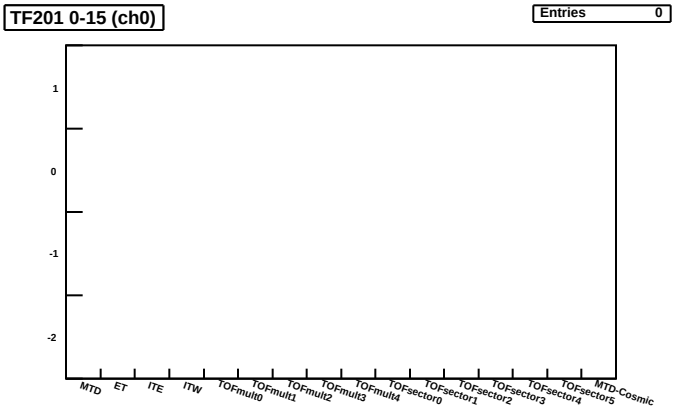

Input to FE002 QT board
Entries 4895584
ADC
channel

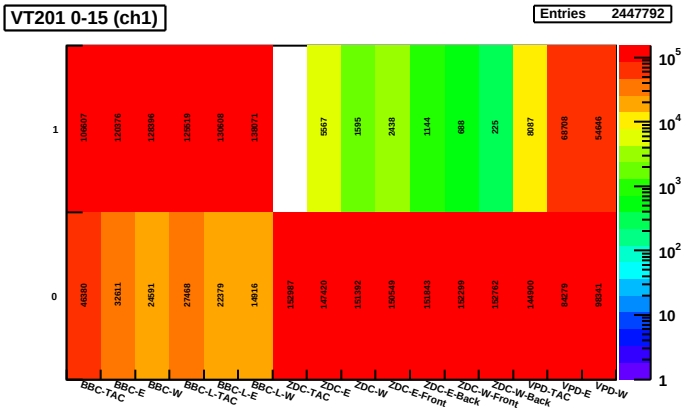

Input to FE003 QT board
Entries 4895584
ADC
channel

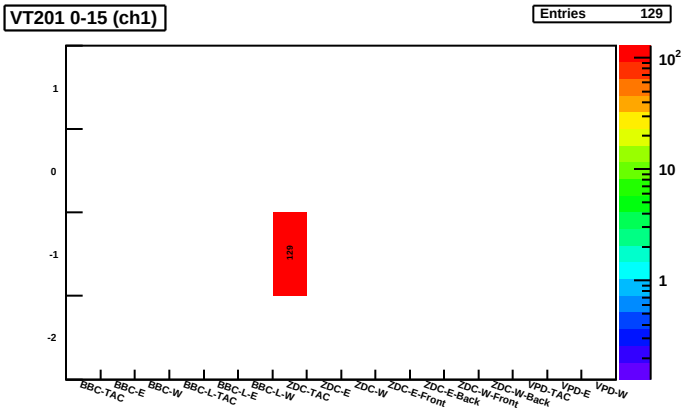

Input to FE004 QT board
Entries 4895584
ADC
channel

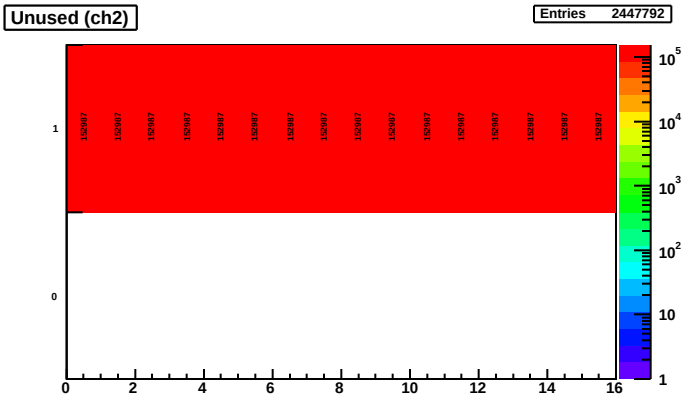

Input to FPE L1 DSM
Entries 611948
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
1

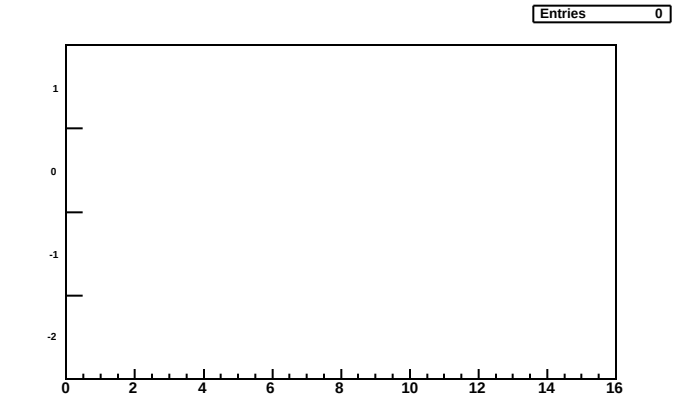

TF201 0-15 (ch0)
Entries 2447792
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

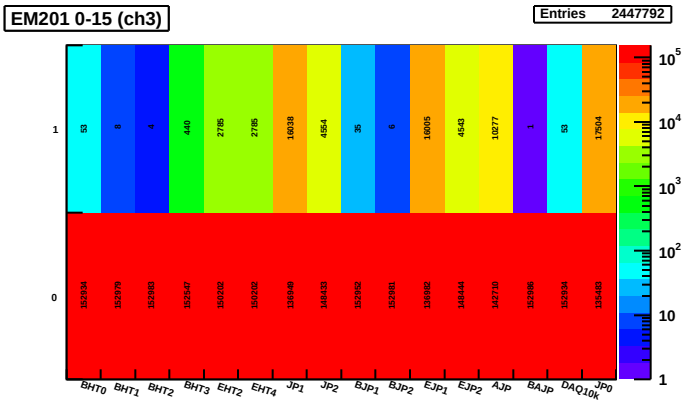

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

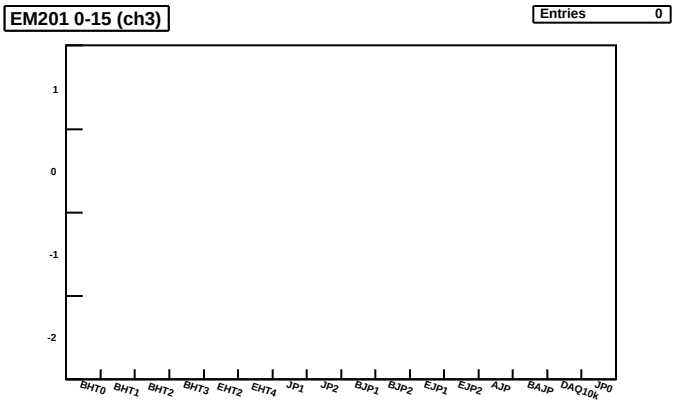

VT201 0-15 (ch1)
Entries 2447792
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 129
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

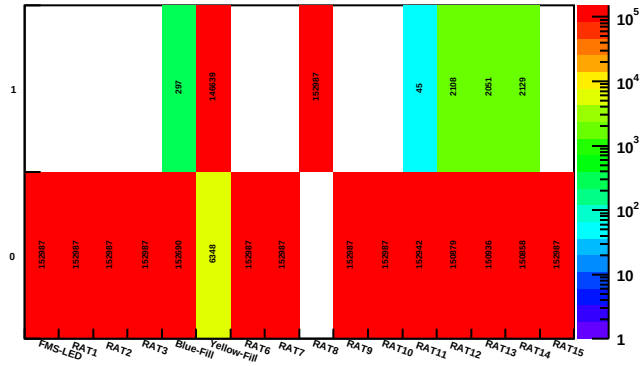

Unused (ch2)
Entries 2447792

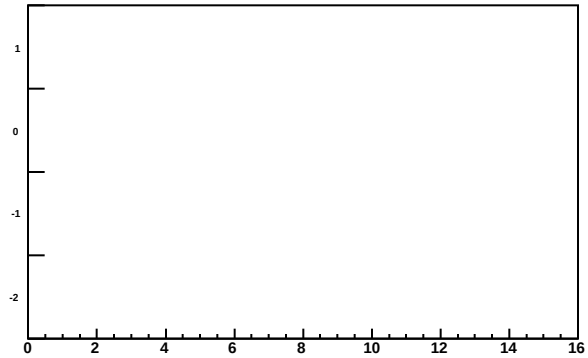

Entries 0


EM201 0-15 (ch3)
Entries 2447792
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

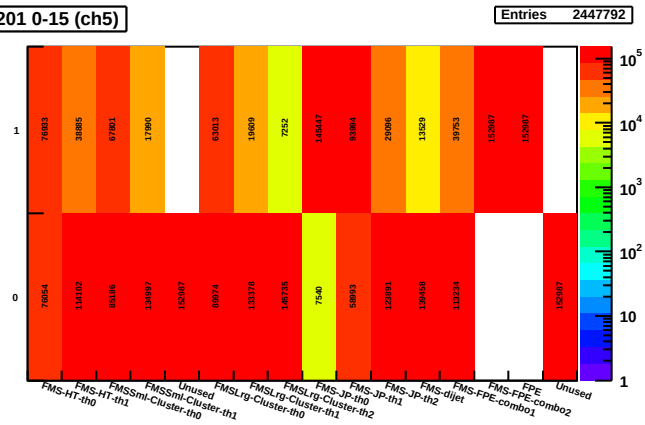

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP DAQ10k JP0

**RAT board (ch4)**

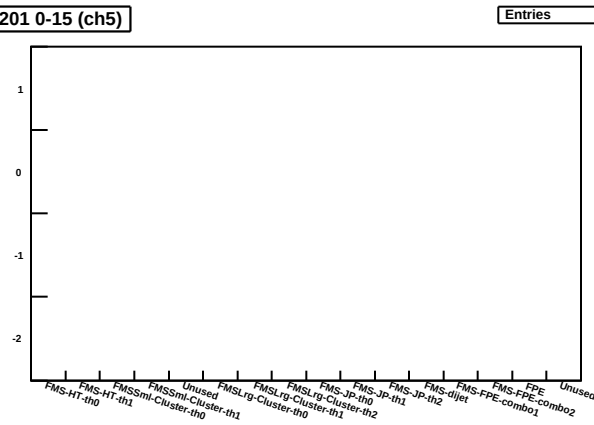


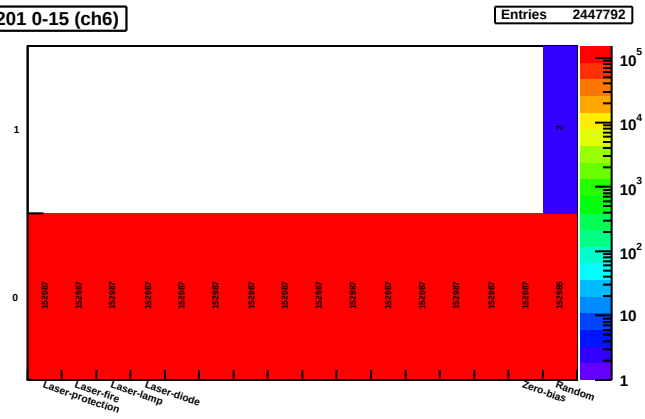

**FP201 0-15 (ch5)**

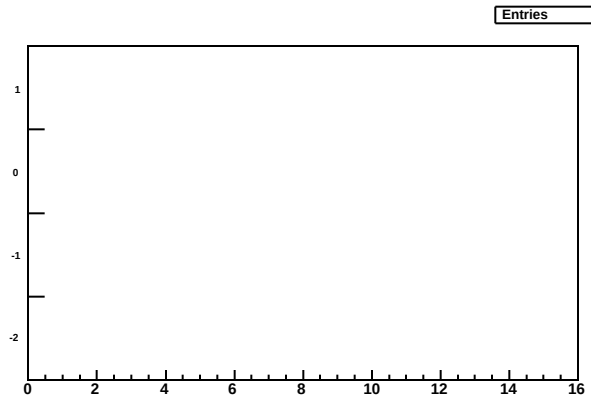


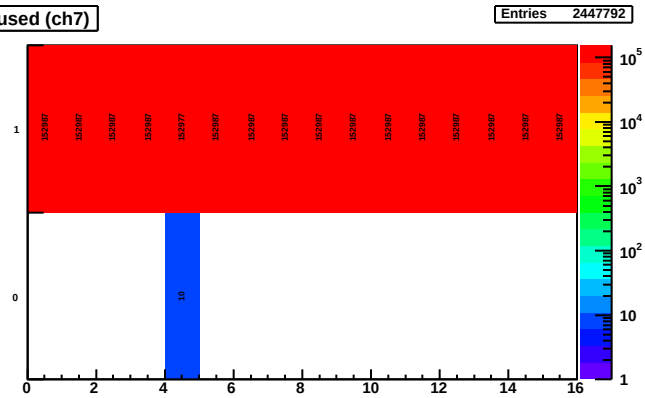
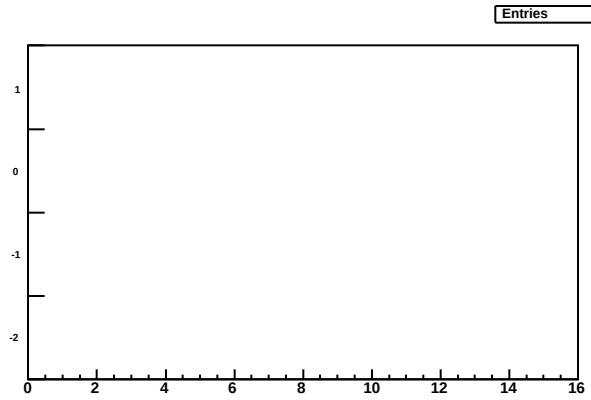

**FP201 0-15 (ch5)**




**ST201 0-15 (ch6)**




**Unused (ch7)**