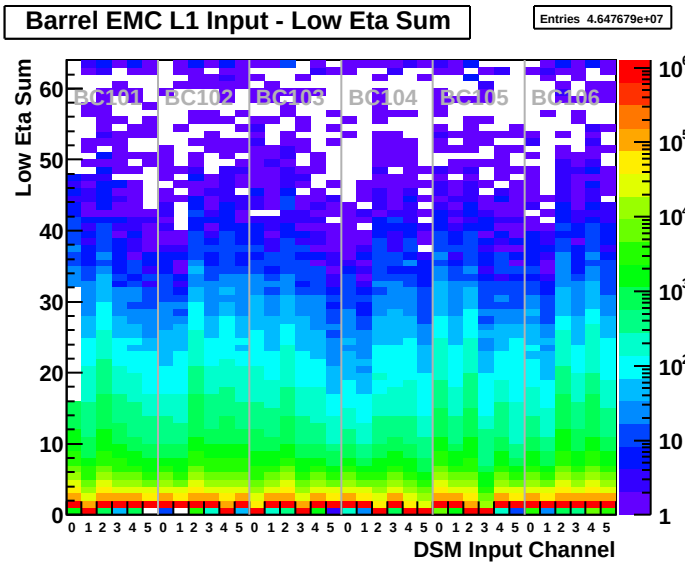
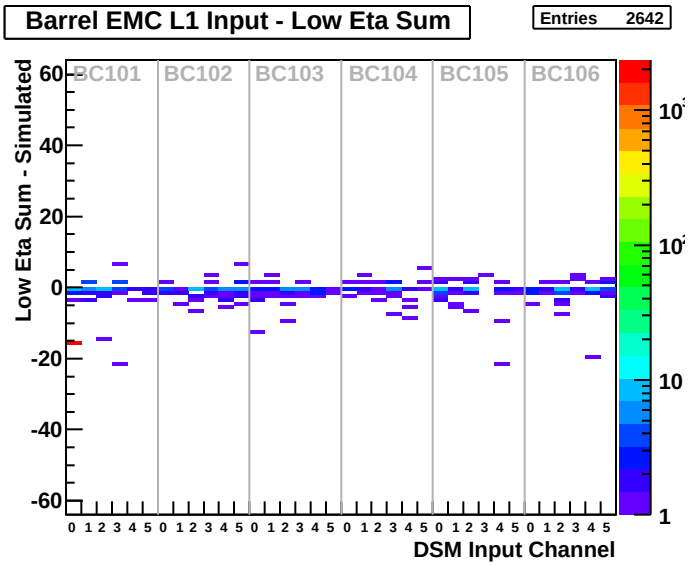
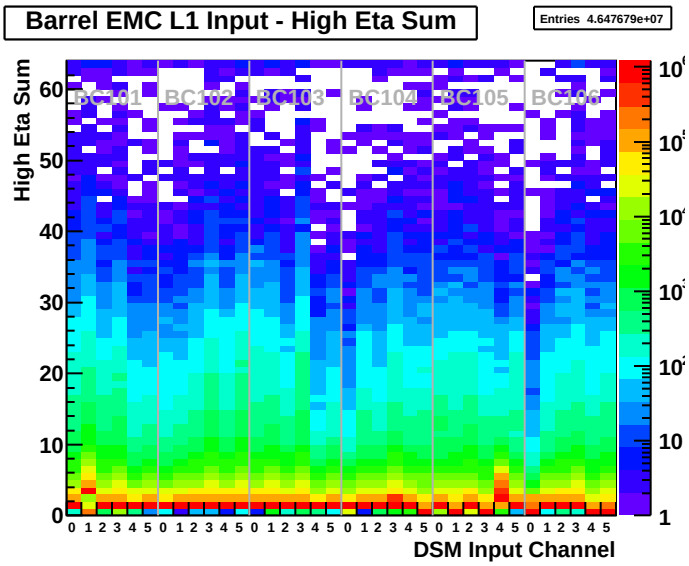
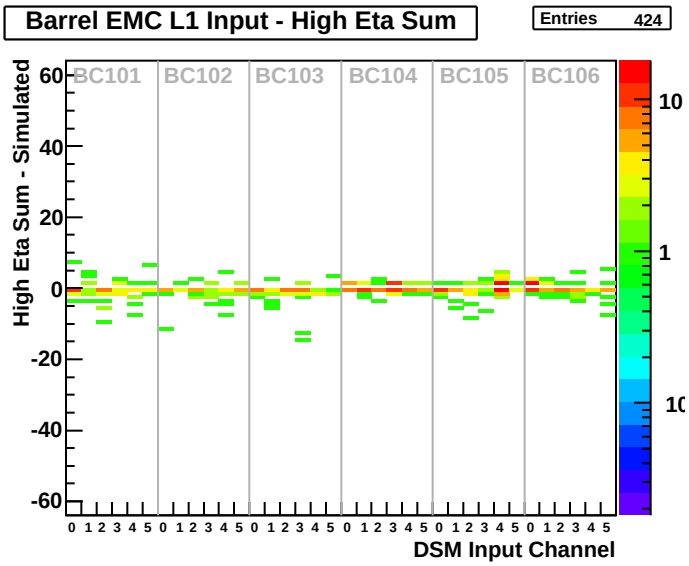
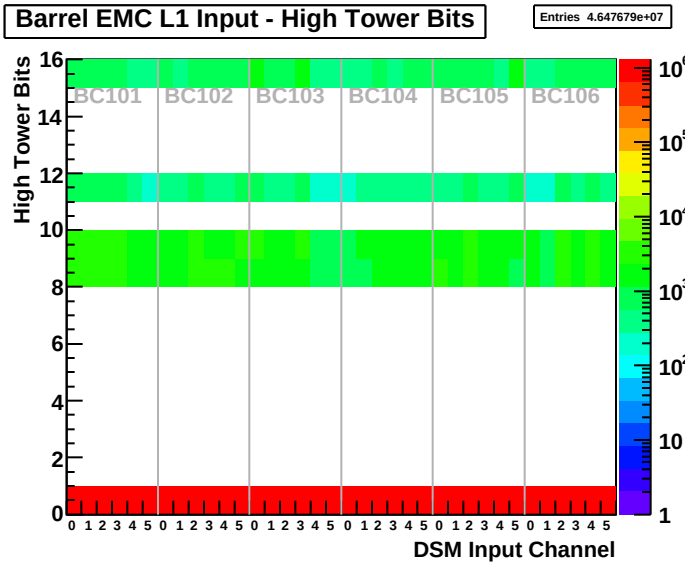
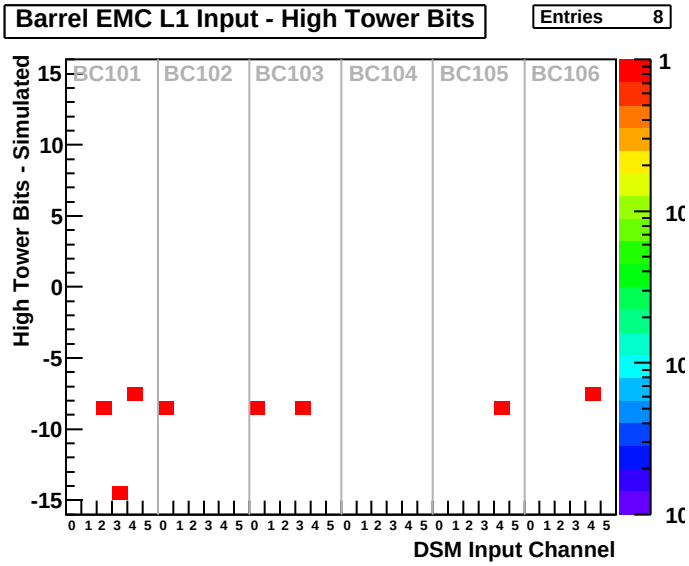

Barrel EMC L1 Input - Low Eta Sum
Entries 4.647679e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

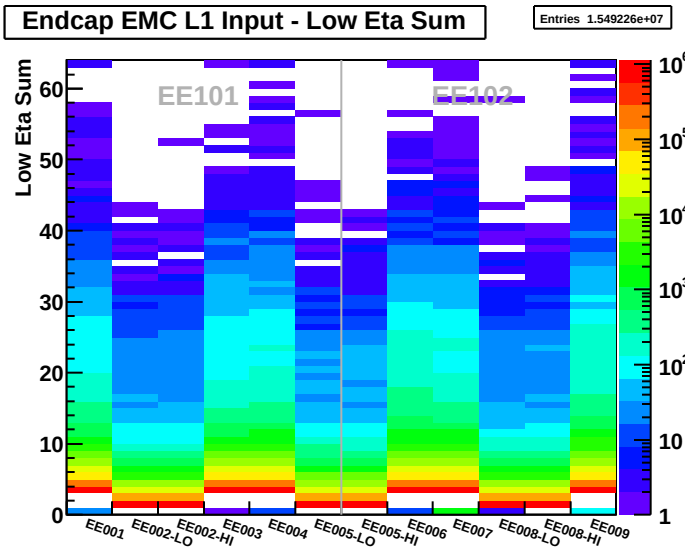

Barrel EMC L1 Input - Low Eta Sum
Entries 2642
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

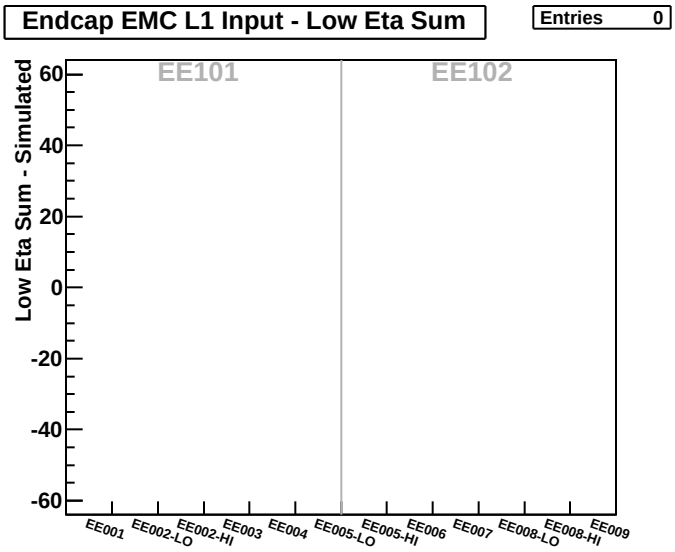

Barrel EMC L1 Input - High Eta Sum
Entries 4.647679e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

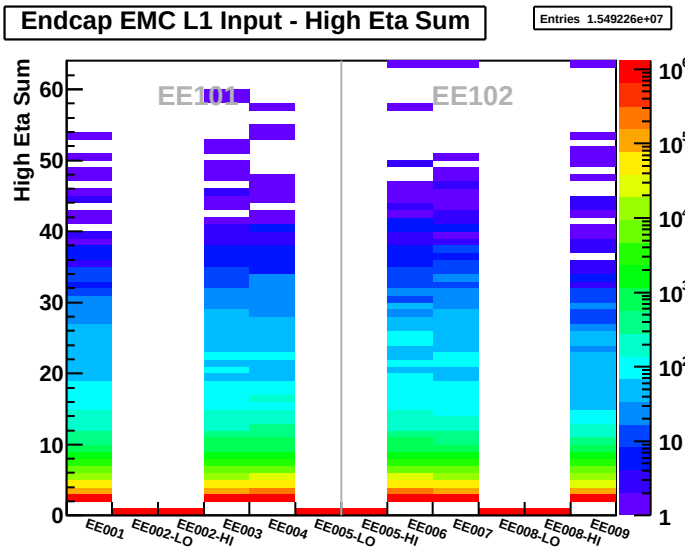

Barrel EMC L1 Input - High Eta Sum
Entries 424
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

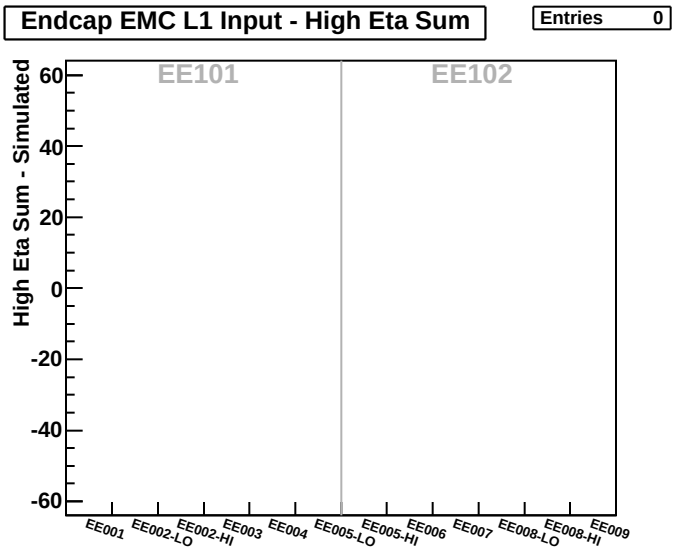

Barrel EMC L1 Input - High Tower Bits
Entries 4.647679e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

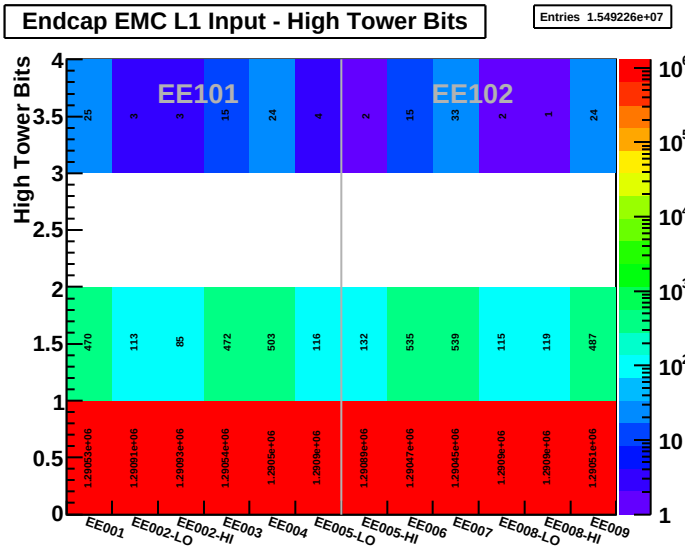

Barrel EMC L1 Input - High Tower Bits
Entries 8
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

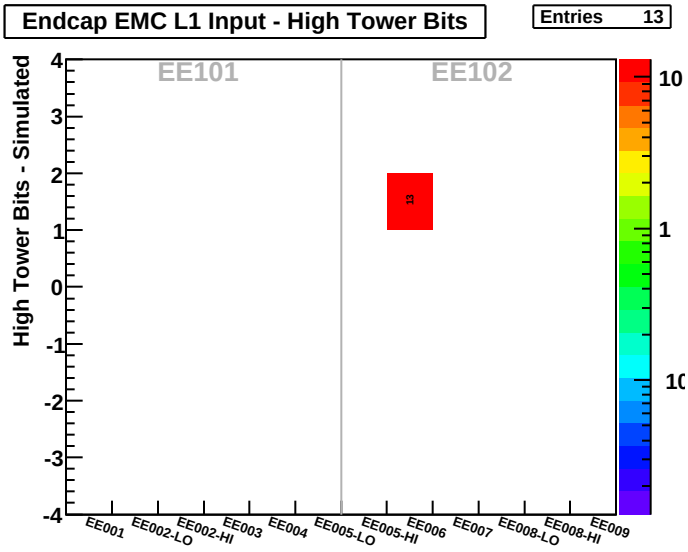

Endcap EMC L1 Input - Low Eta Sum
Entries 1.549226e+07
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

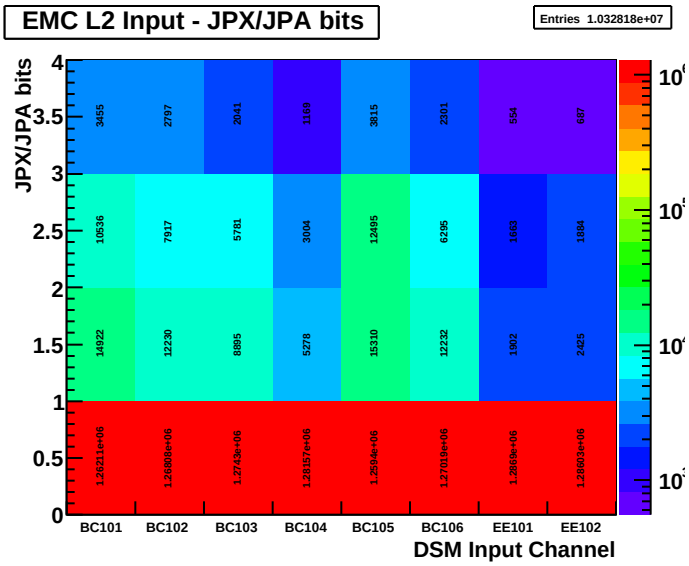

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

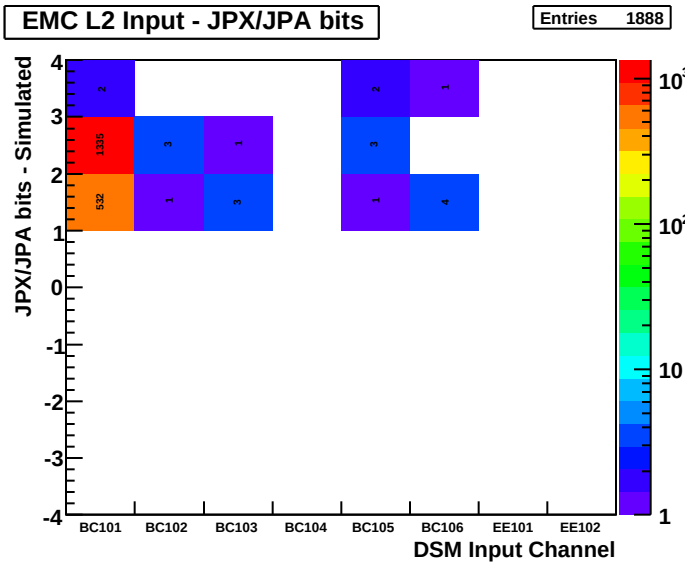

Endcap EMC L1 Input - High Eta Sum
Entries 1.549226e+07
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

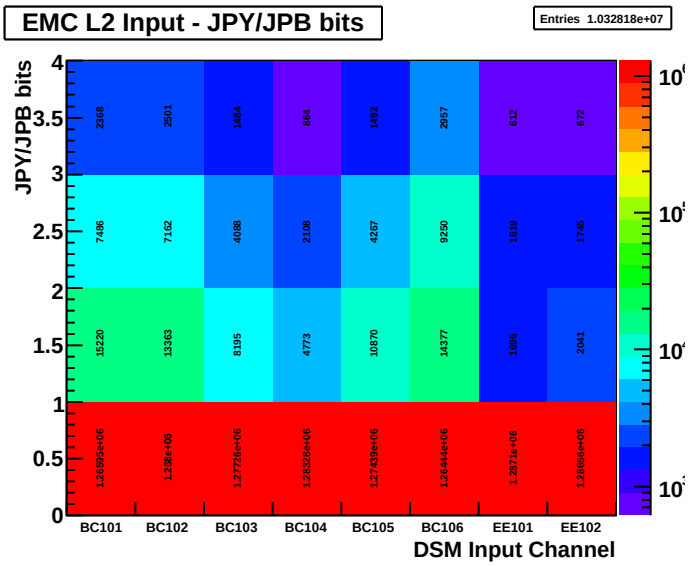

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

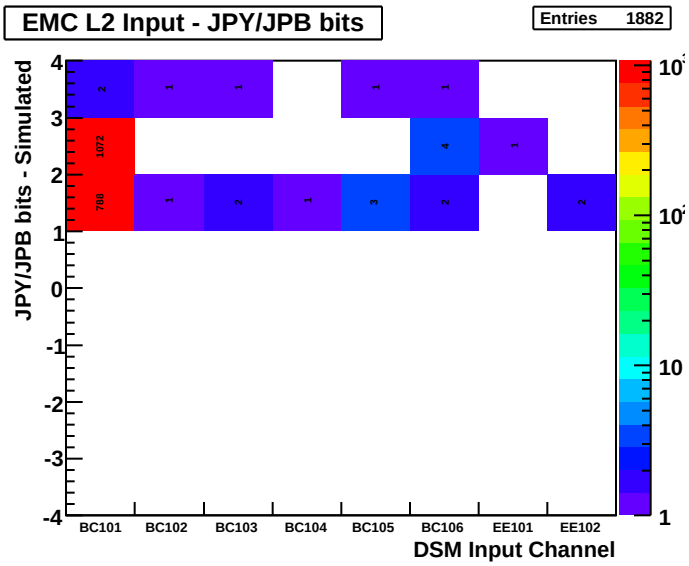

Endcap EMC L1 Input - High Tower Bits
Entries 1.549226e+07
High Tower Bits
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

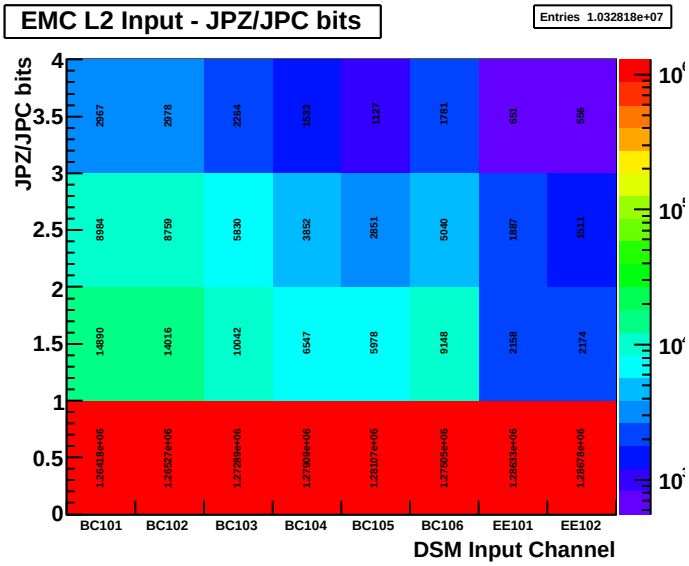

Endcap EMC L1 Input - High Tower Bits
Entries 13
High Tower Bits - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

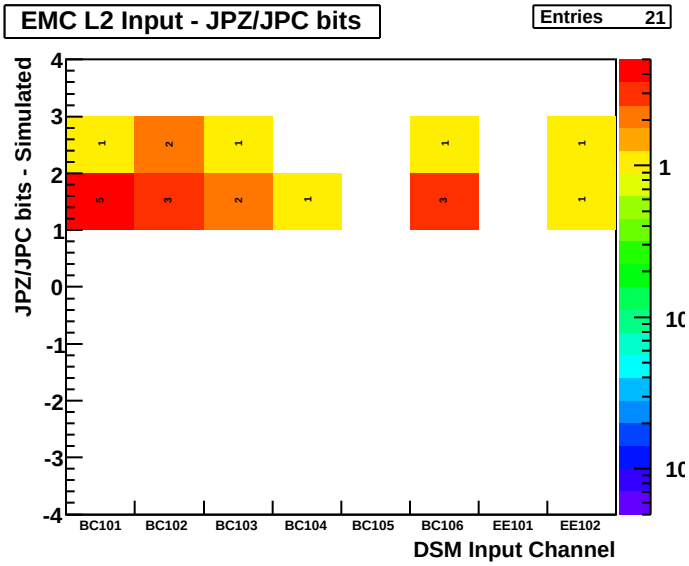

EMC L2 Input - JPX/JPA bits
Entries 1.032818e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3455 2797 2041 1169 3815 2301 554 687
10536 7917 5781 3004 12495 6295 1683 1884
14922 12230 8895 5278 15310 12232 1902 2425
1.26211e+06 1.26808e+06 1.27432e+06 1.28157e+06 1.25946e+06 1.27019e+06 1.28598e+06 1.28603e+06

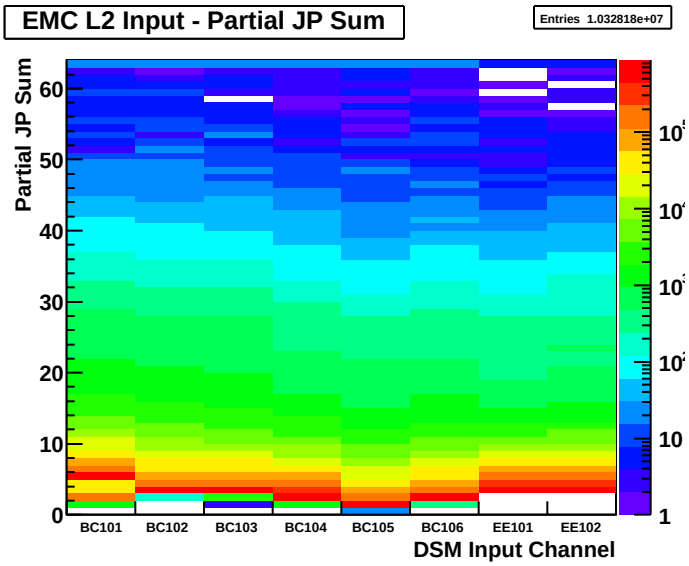

EMC L2 Input - JPX/JPA bits
Entries 1888
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 2 1
1335 3 1 3
532 1 3 1 4

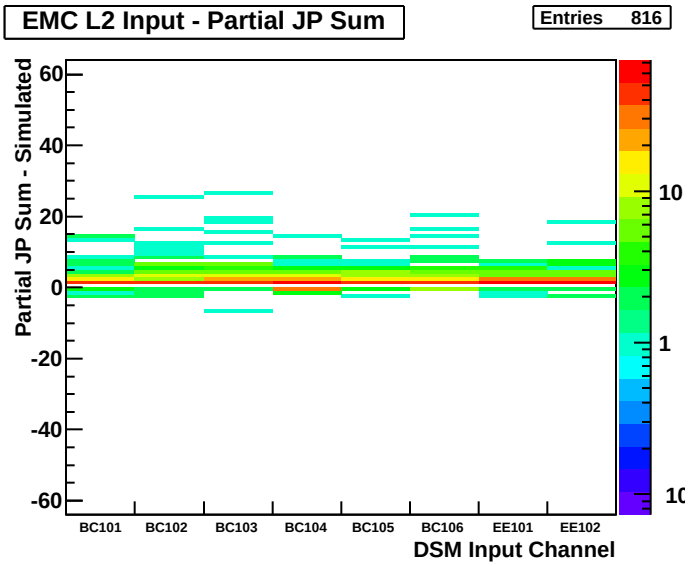

EMC L2 Input - JPY/JPB bits
Entries 1.032818e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2368 2501 1484 864 1492 2957 612 672
7486 7162 4088 2108 4267 9250 1619 1745
15220 13363 8195 4773 10870 14377 1696 2041
1.26595e+06 1.2682e+06 1.27726e+06 1.28328e+06 1.27439e+06 1.26444e+06 1.2871e+06 1.28656e+06

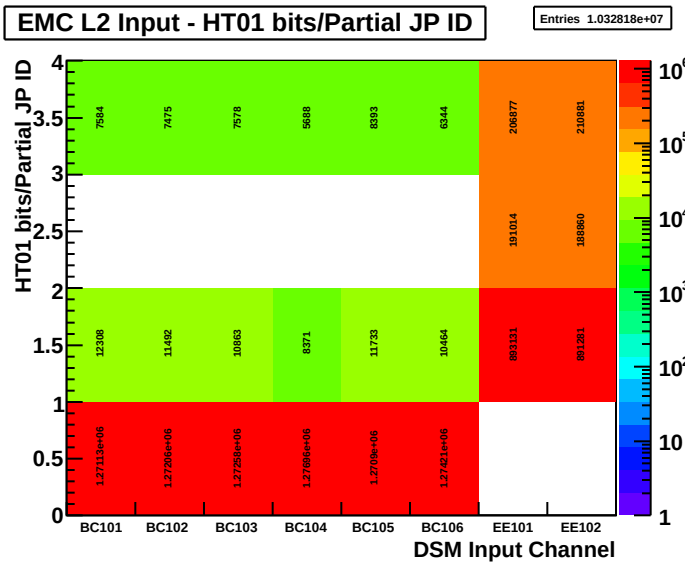

EMC L2 Input - JPY/JPB bits
Entries 1882
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2 1 1 1 1
1072 4 1
788 1 2 1 3 2 2

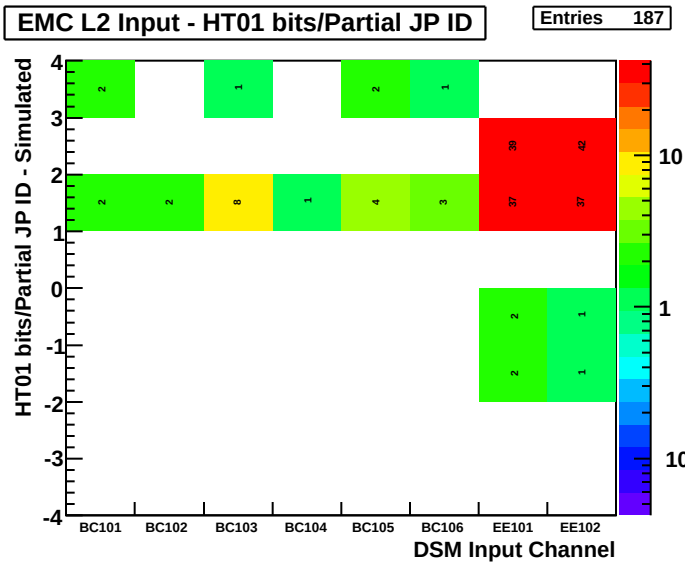

EMC L2 Input - JPZ/JPC bits
Entries 1.032818e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2967 2978 2264 1533 1127 1781 651 556
8984 8759 5830 3852 2951 5040 1887 1511
14890 14016 10042 6547 5978 9148 2158 2174
1.26418e+06 1.26527e+06 1.27289e+06 1.27903e+06 1.28107e+06 1.27595e+06 1.28633e+06 1.28678e+06

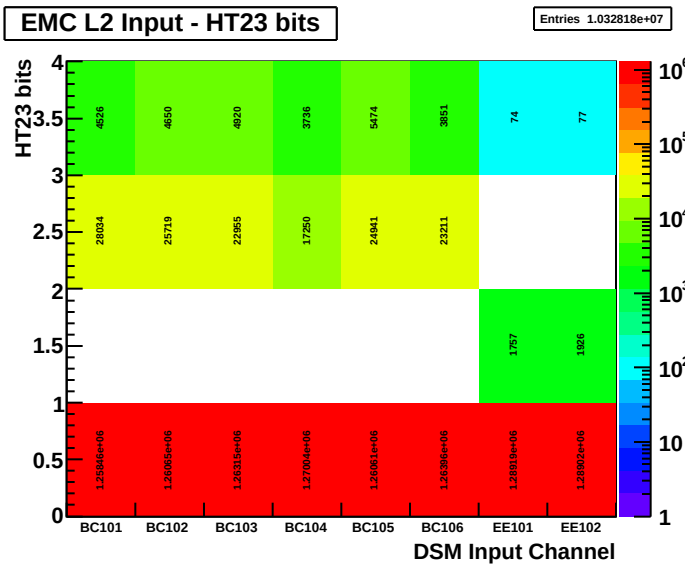

EMC L2 Input - JPZ/JPC bits
Entries 21
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 2 1 1 1
5 3 2 1 3 1

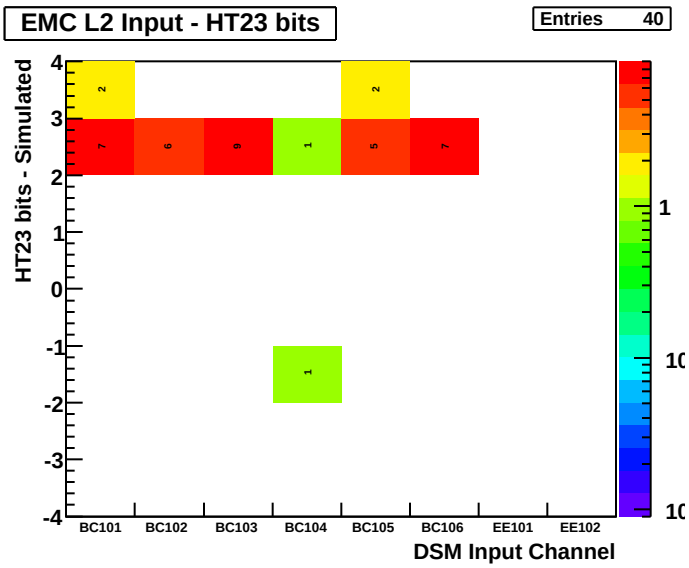

EMC L2 Input - Partial JP Sum
Entries 1.032818e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

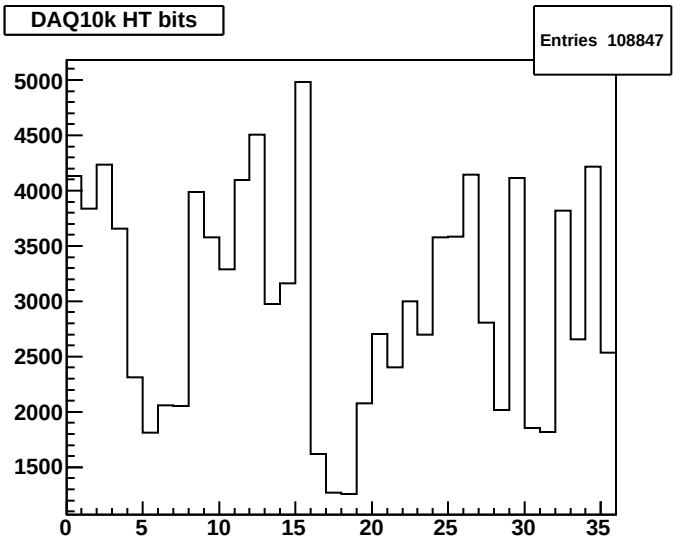

EMC L2 Input - Partial JP Sum
Entries 816
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

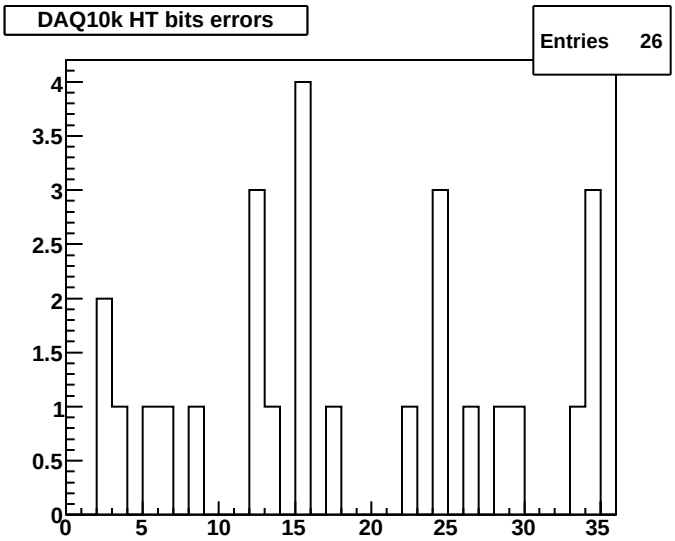

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.032818e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

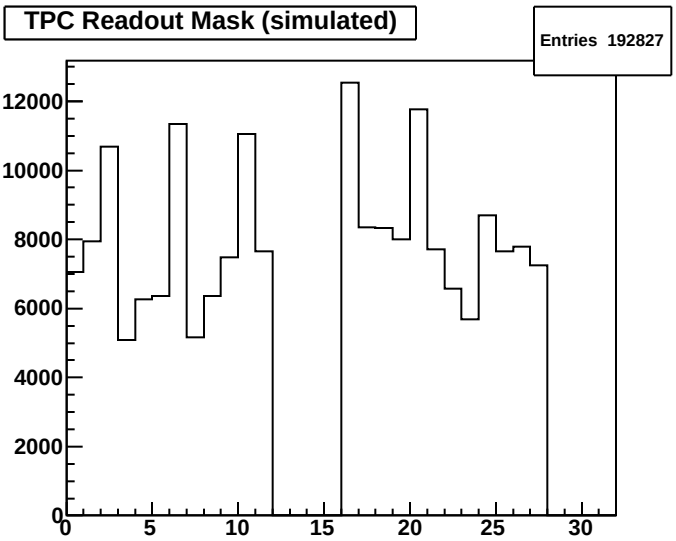

EMC L2 Input - HT01 bits/Partial JP ID
Entries 187
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

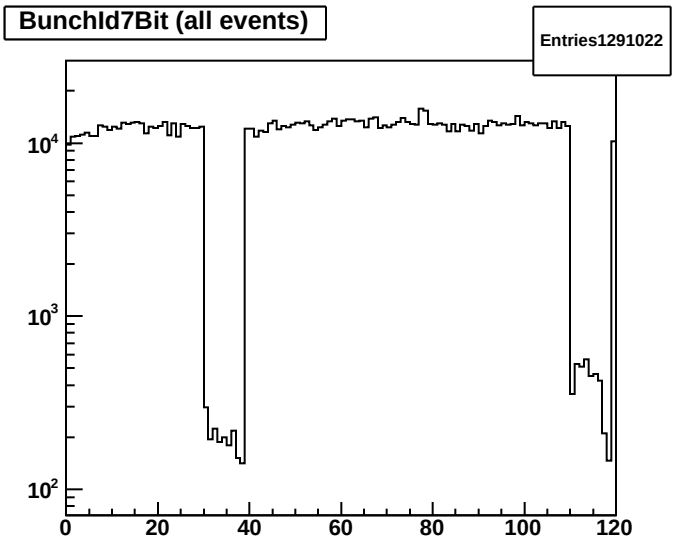

EMC L2 Input - HT23 bits
Entries 1.032818e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

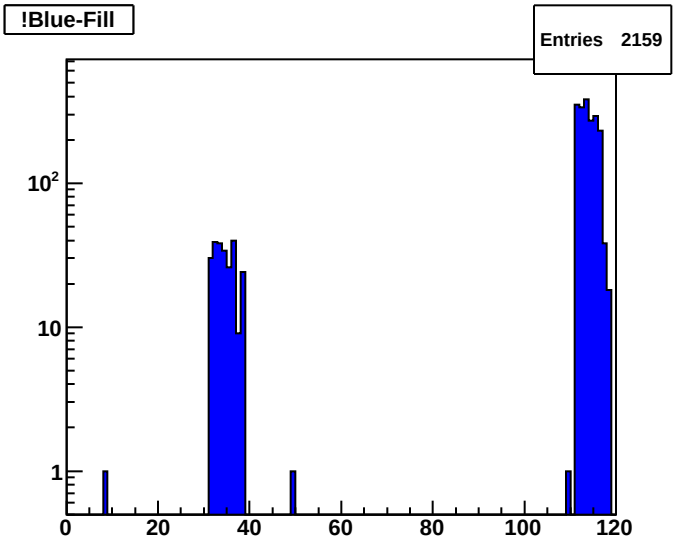

EMC L2 Input - HT23 bits
Entries 40
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

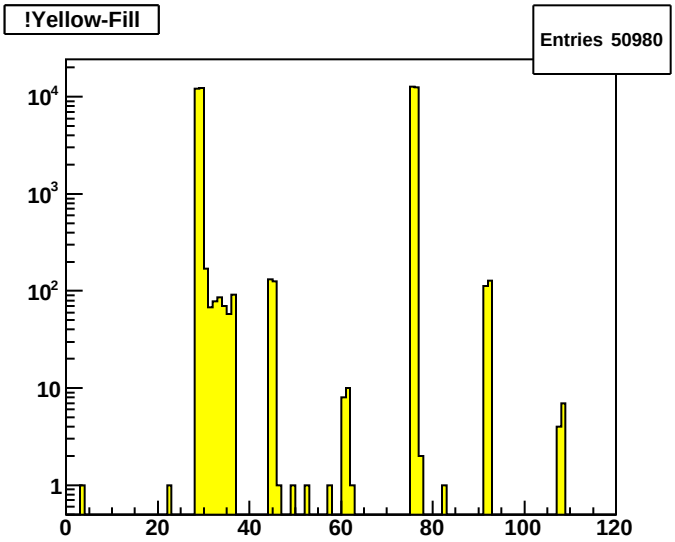

DAQ10k HT bits
Entries 108847

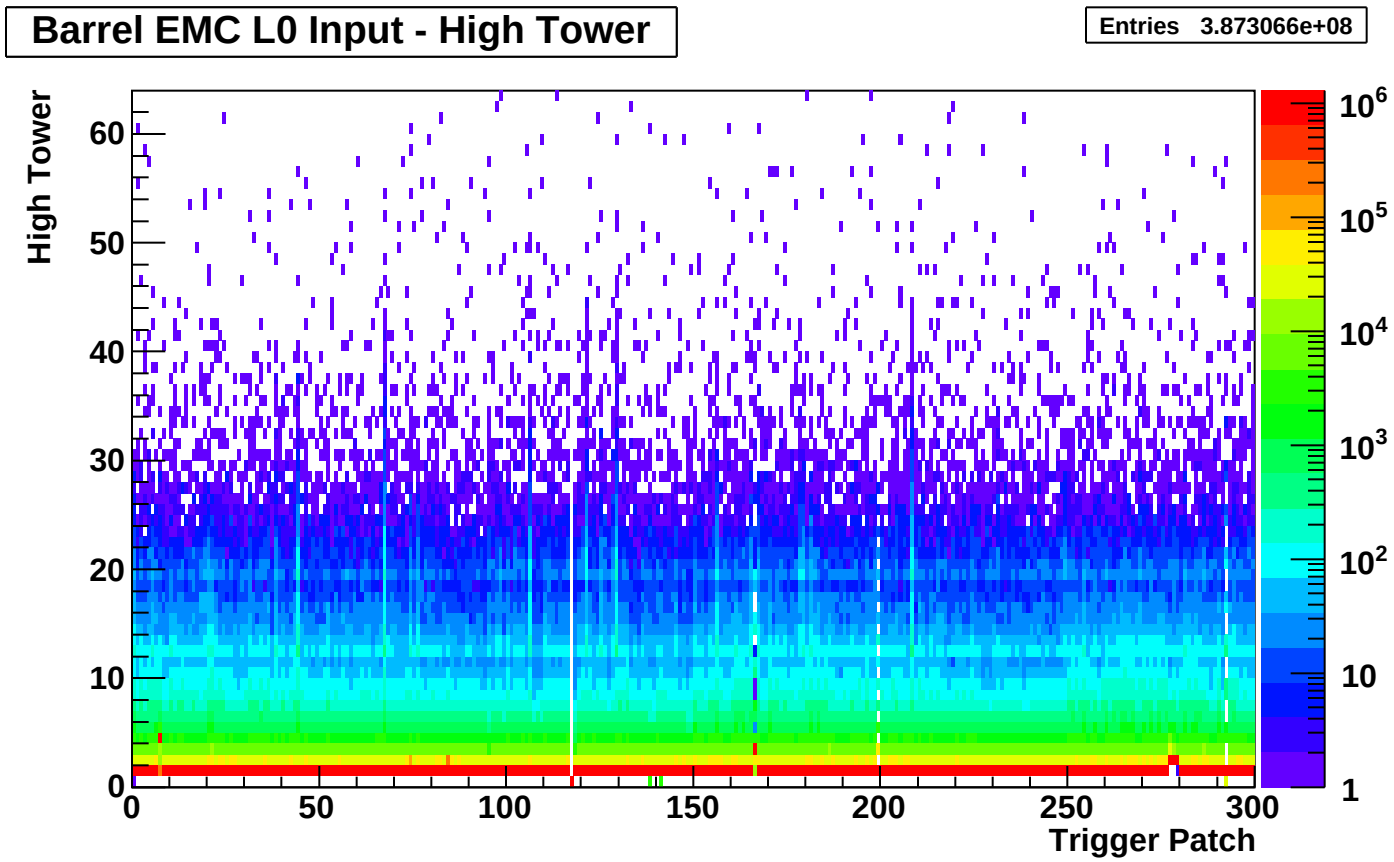

DAQ10k HT bits errors
Entries 26

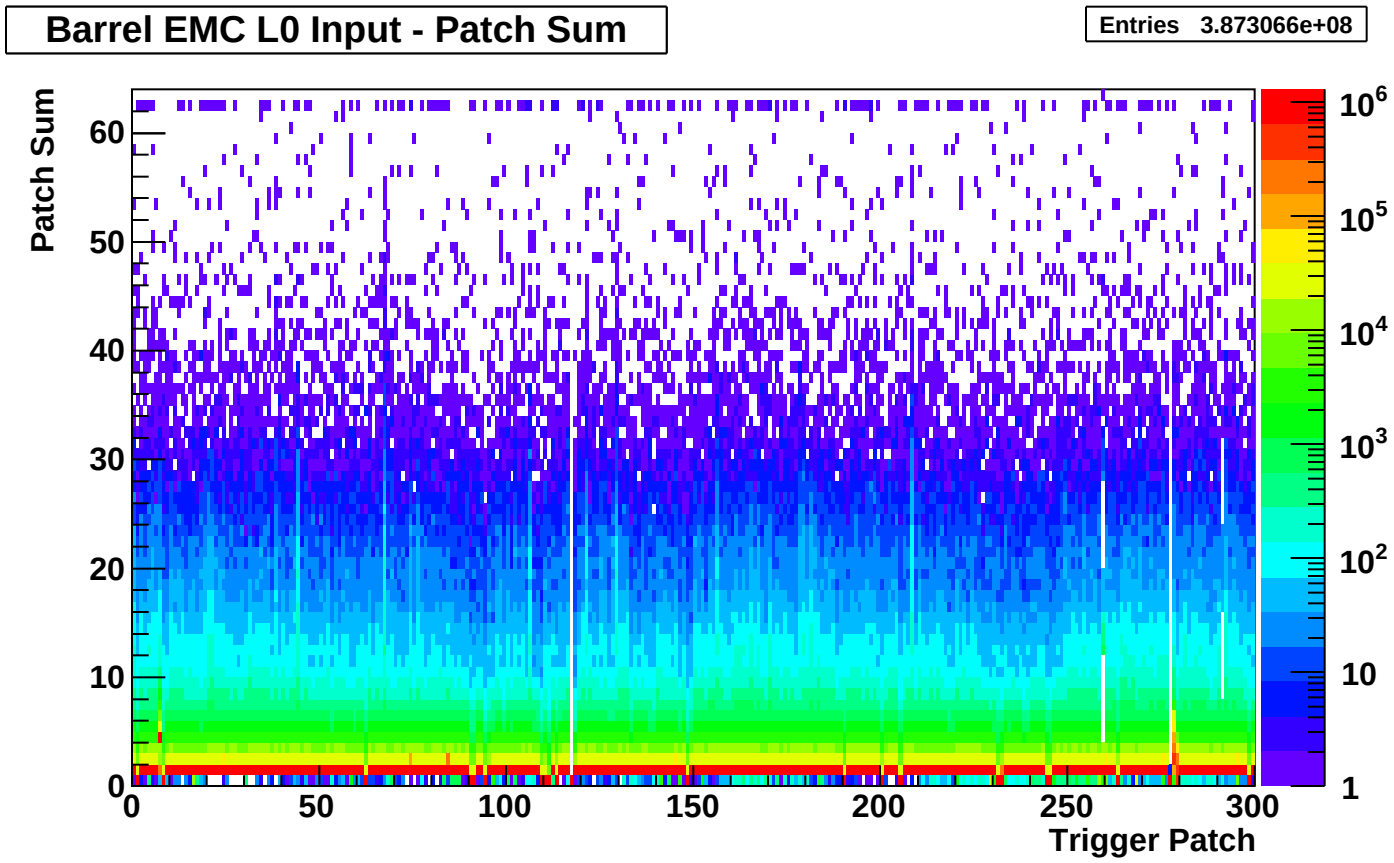

TPC Readout Mask (simulated)
Entries 192827

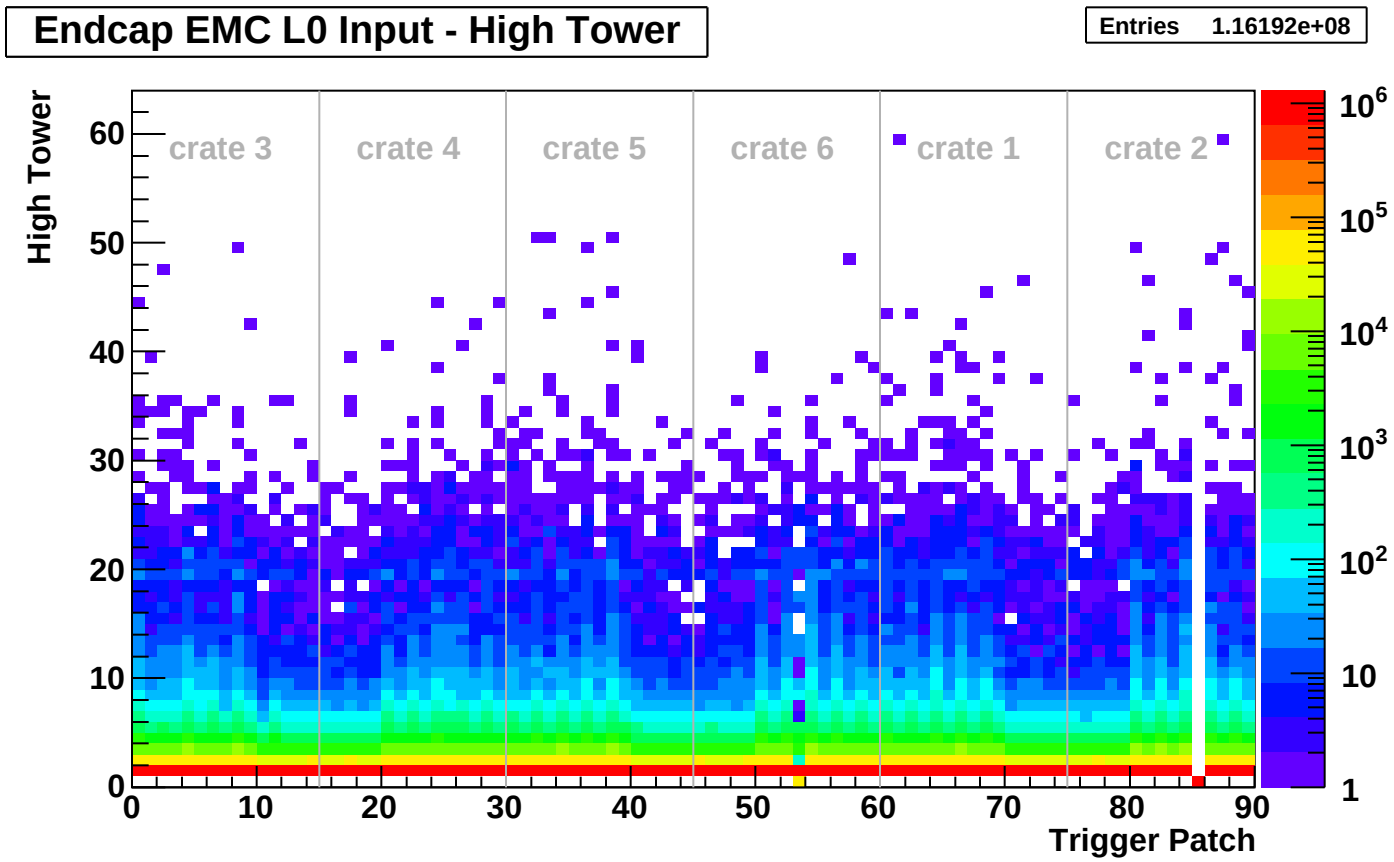

BunchId7Bit (all events)
Entries1291022

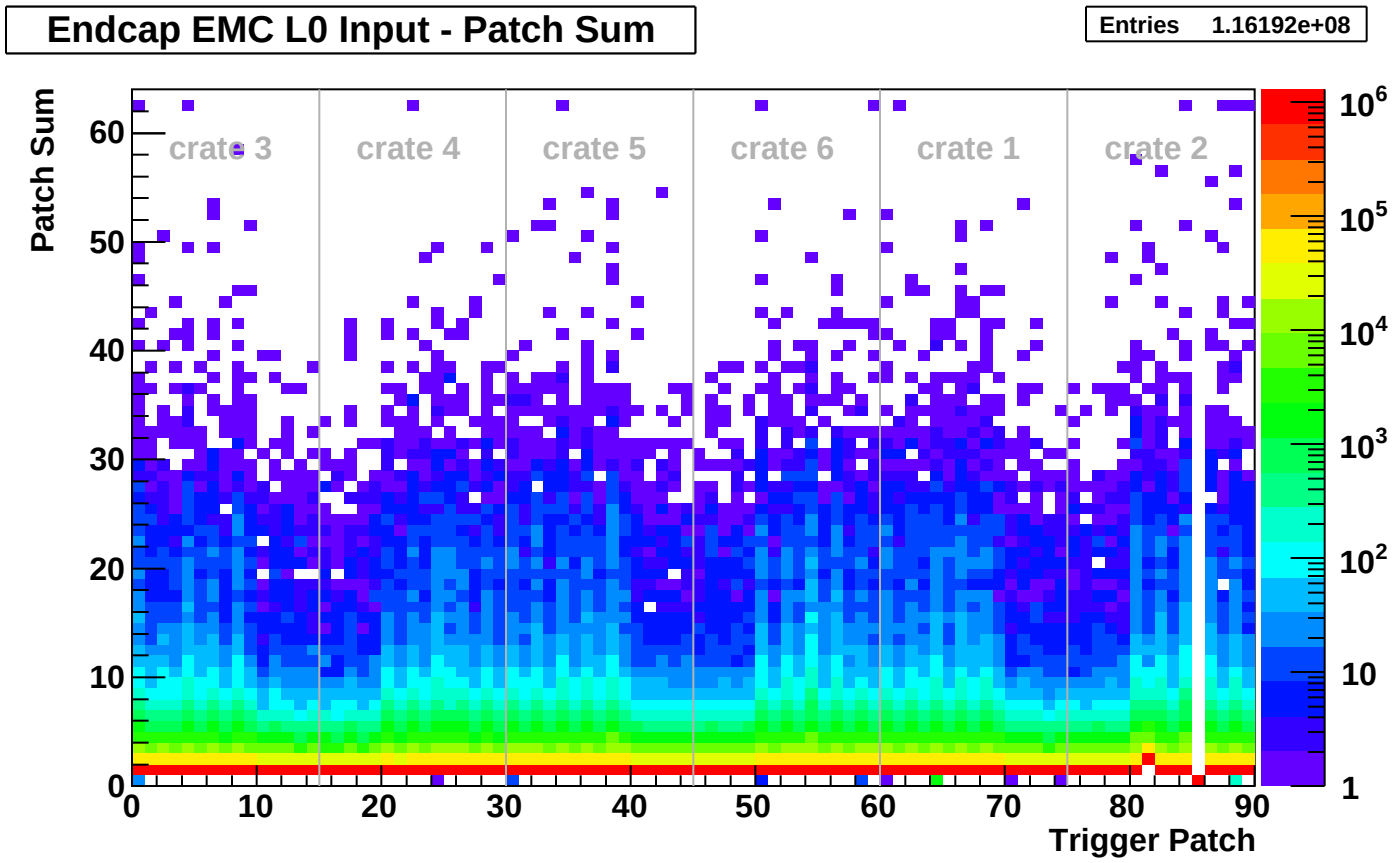

!Blue-Fill
Entries 2159

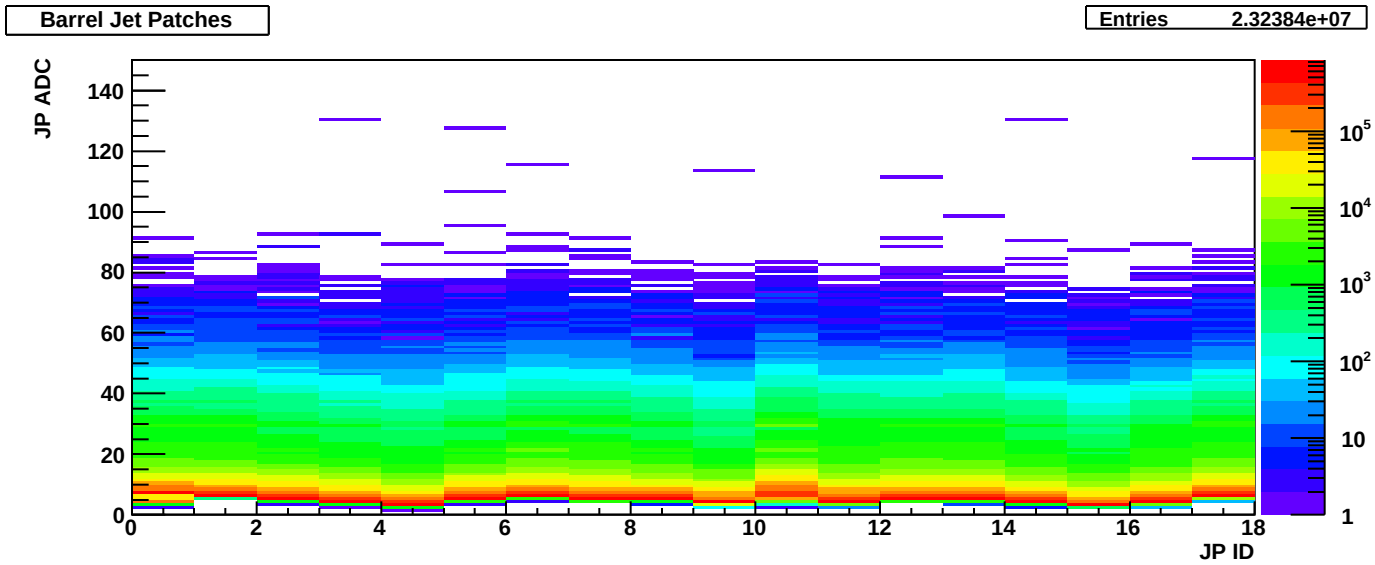

!Yellow-Fill
Entries 50980

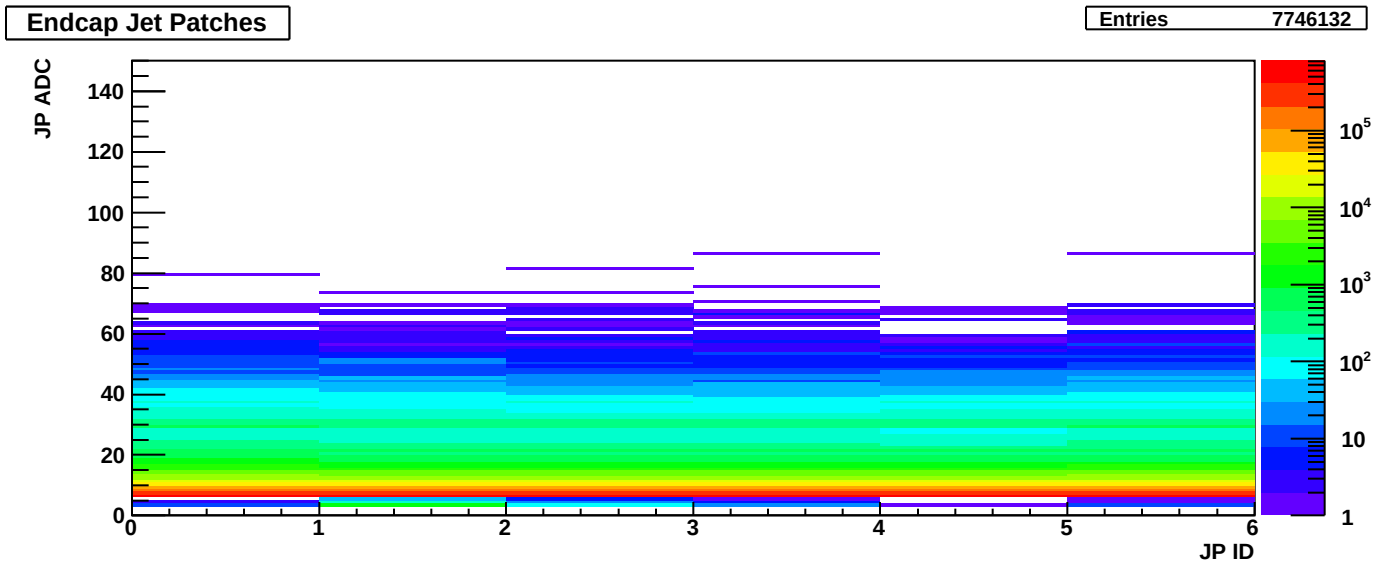

Barrel EMC L0 Input - High Tower
Entries 3.873066e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

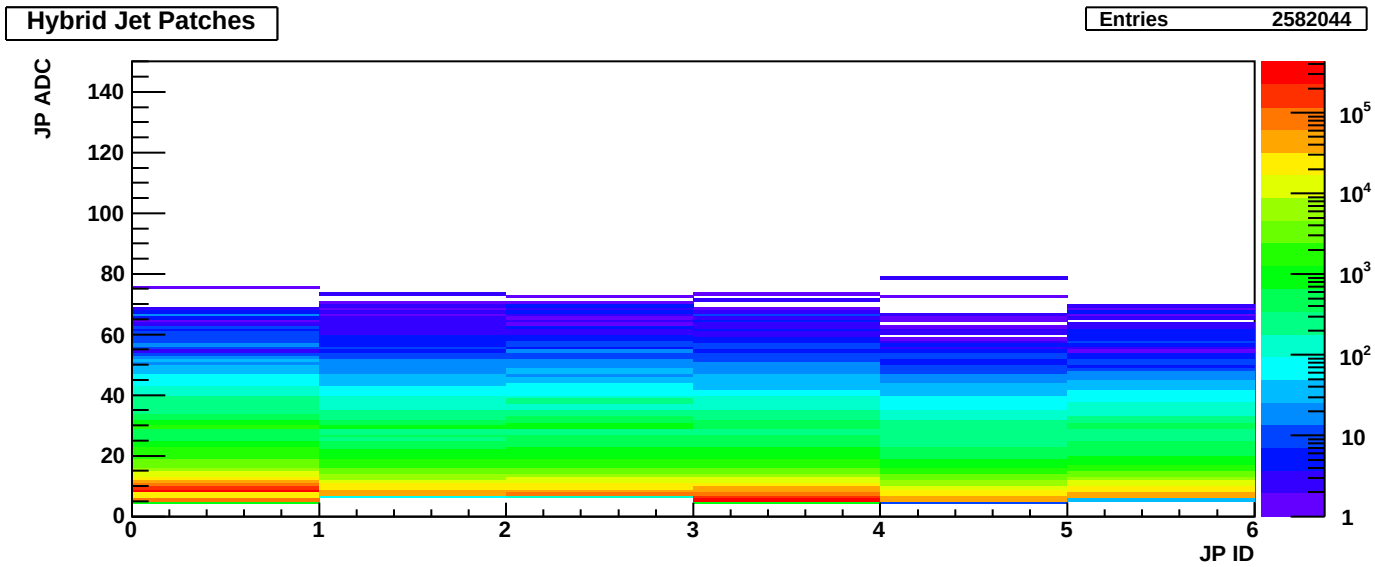

Barrel EMC L0 Input - Patch Sum
Entries 3.873066e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

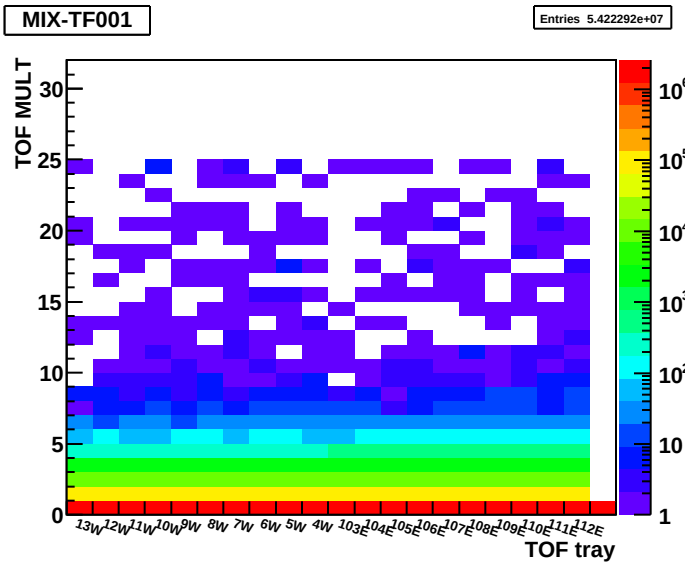

Endcap EMC L0 Input - High Tower
Entries 1.16192e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

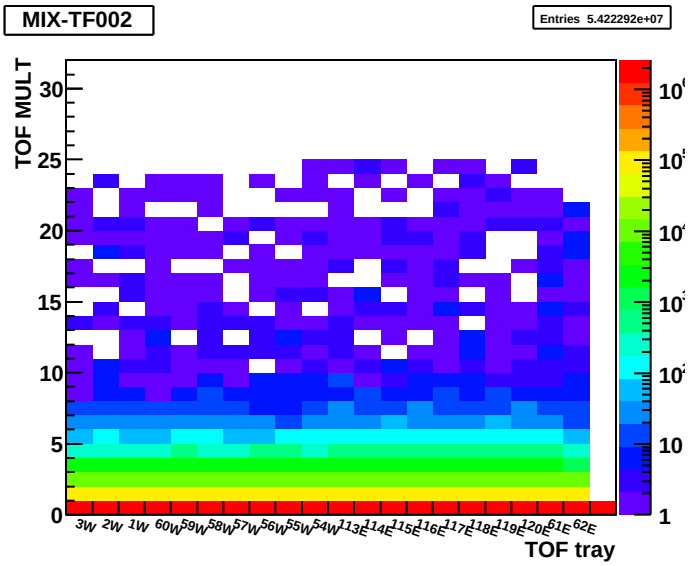

Endcap EMC L0 Input - Patch Sum
Entries 1.16192e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

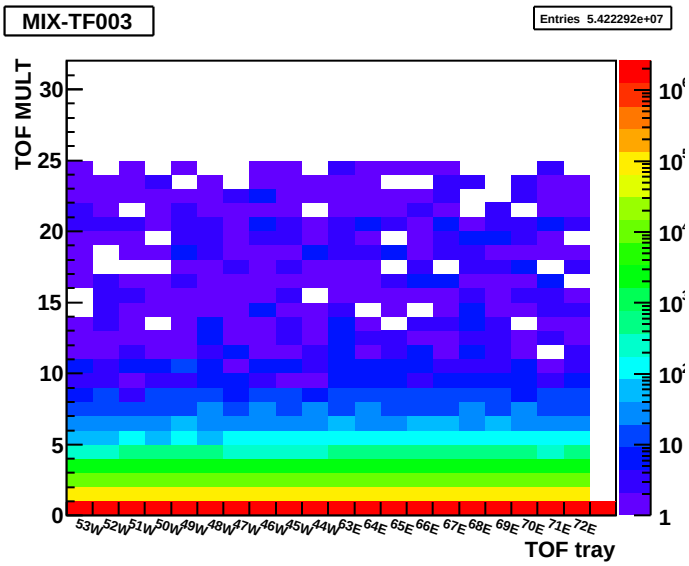

Barrel Jet Patches
Entries 2.32384e+07
JP ADC
JP ID

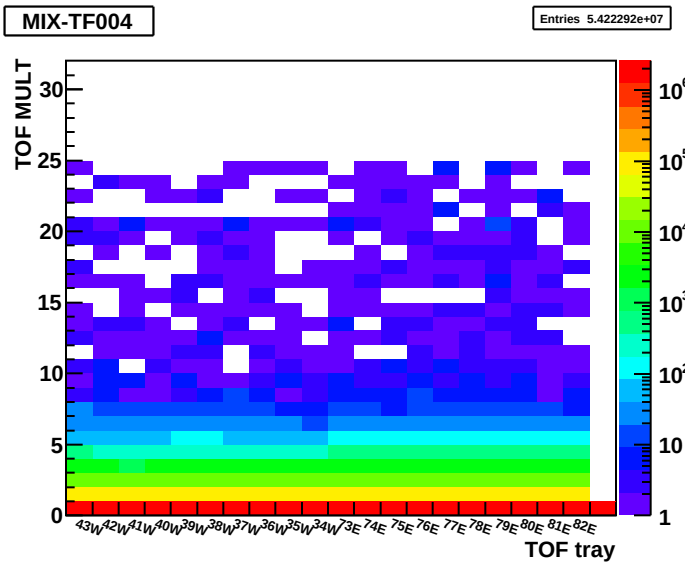

Endcap Jet Patches
Entries 7746132
JP ADC
JP ID

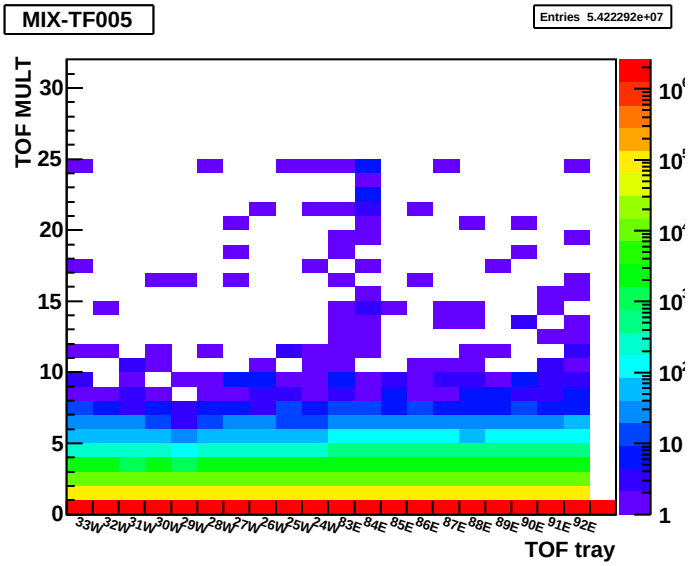

Hybrid Jet Patches
Entries 2582044
JP ADC
JP ID

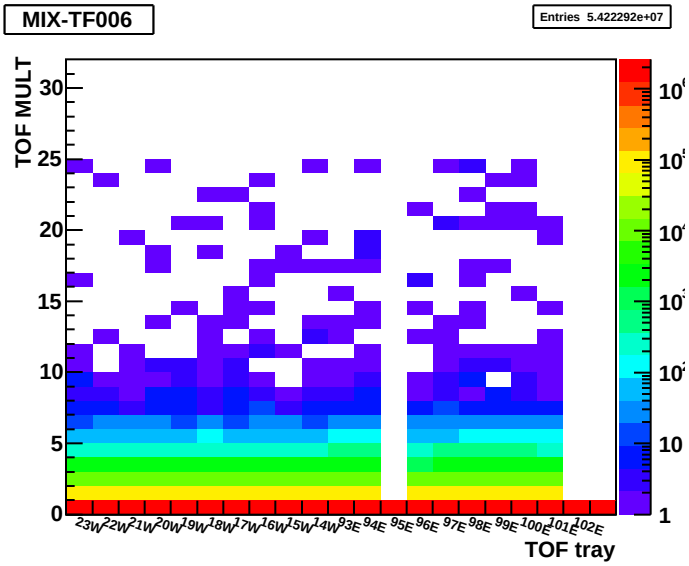

MIX-TF001
Entries 5.422292e+07
TOF MULT
TOF tray

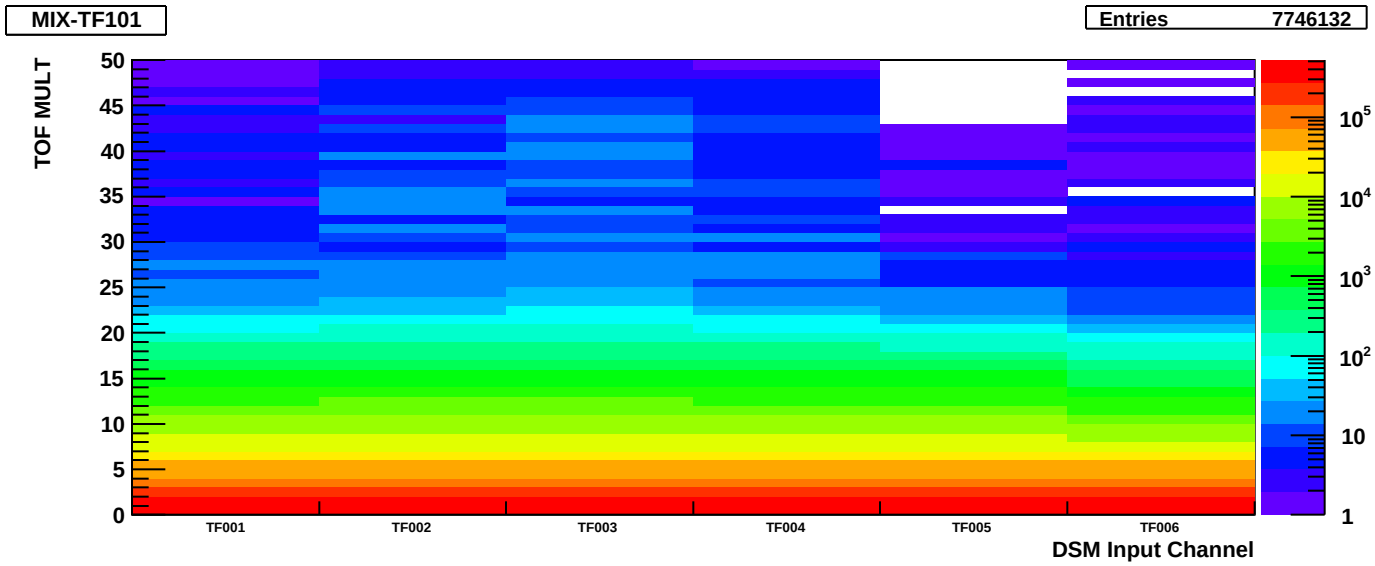

MIX-TF002
Entries 5.422292e+07
TOF MULT
TOF tray

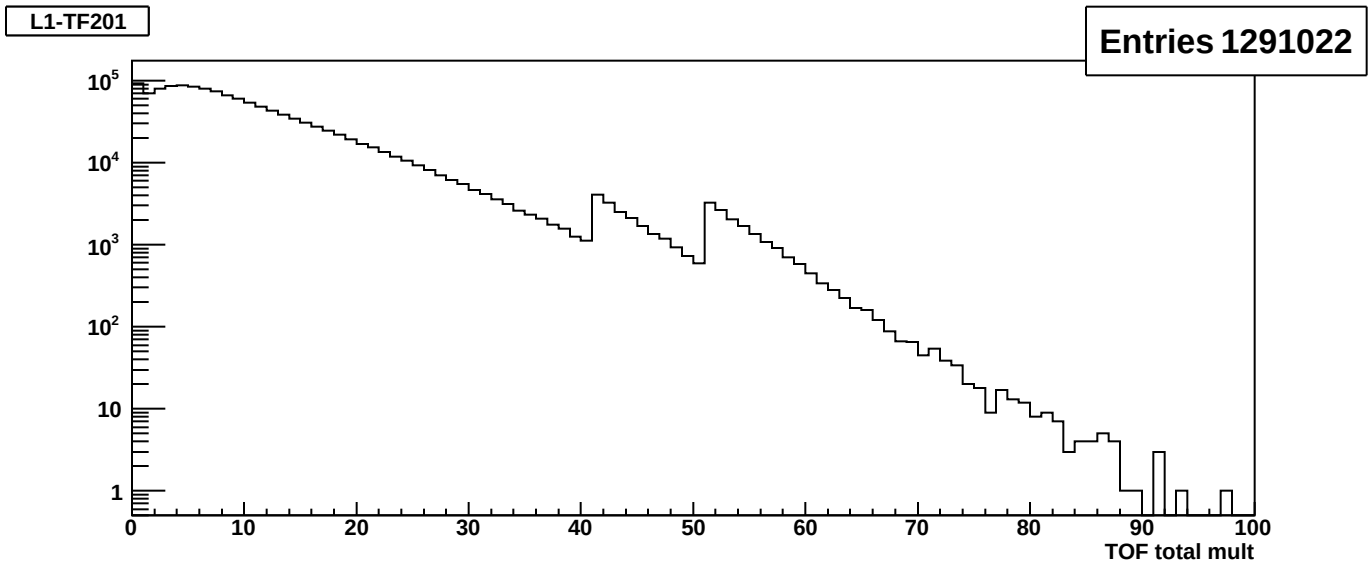

MIX-TF003
Entries 5.422292e+07
TOF MULT
TOF tray

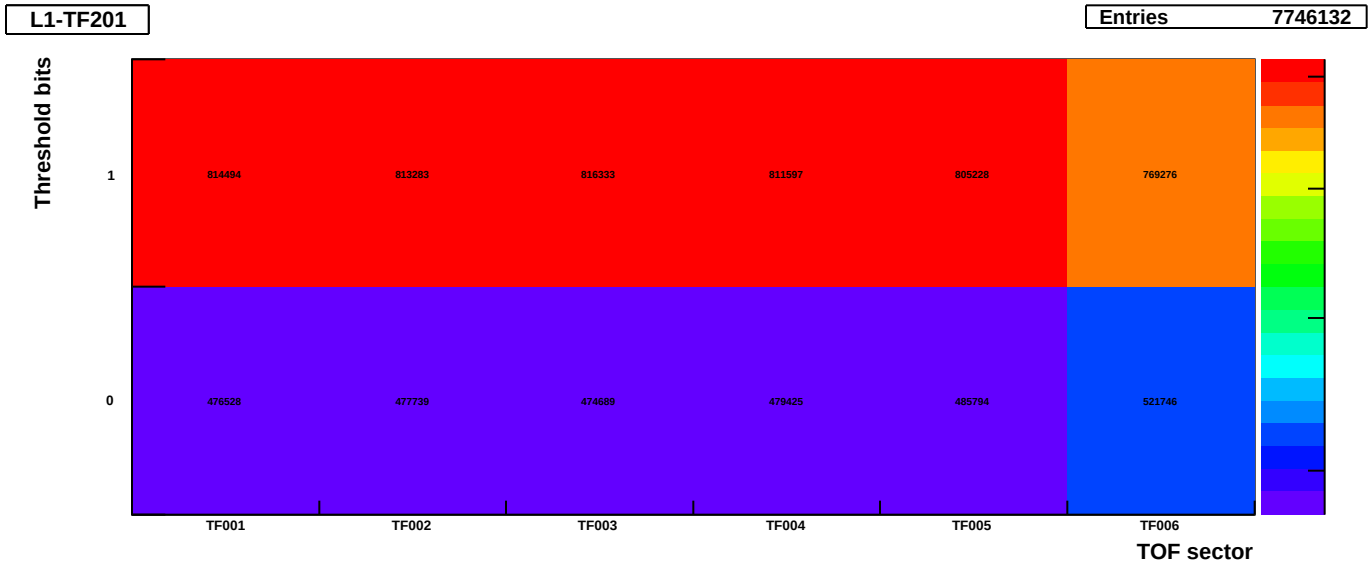

MIX-TF004
Entries 5.422292e+07
TOF MULT
TOF tray

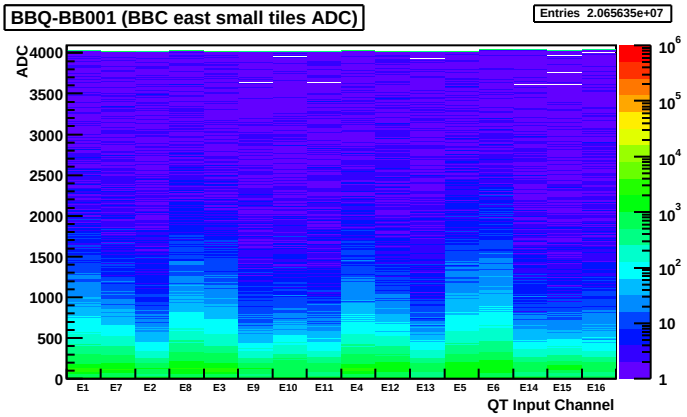

MIX-TF005
Entries 5.422292e+07
TOF MULT
TOF tray

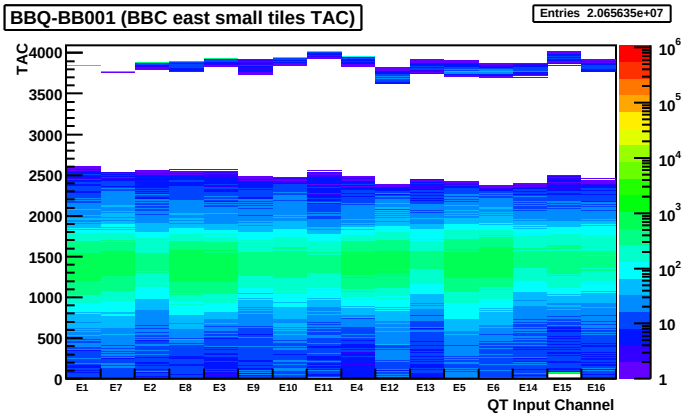

MIX-TF006
Entries 5.422292e+07
TOF MULT
TOF tray

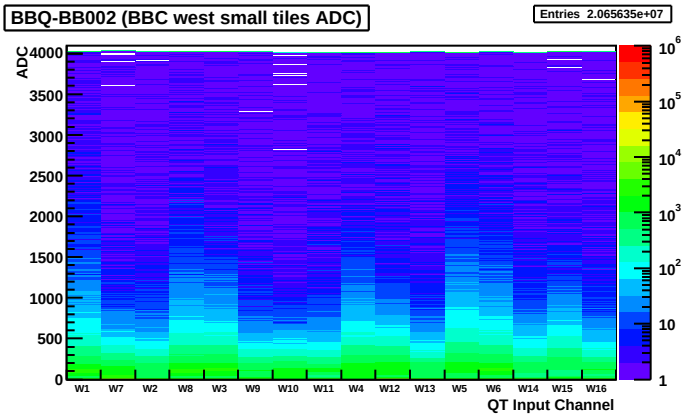

MIX-TF101
Entries 7746132
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

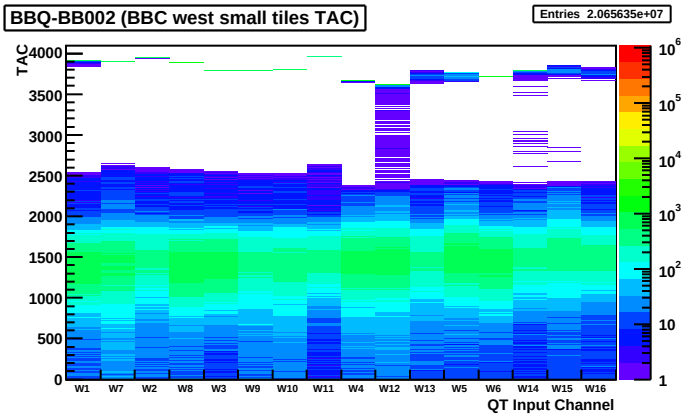

L1-TF201
Entries 1291022
TOF total mult

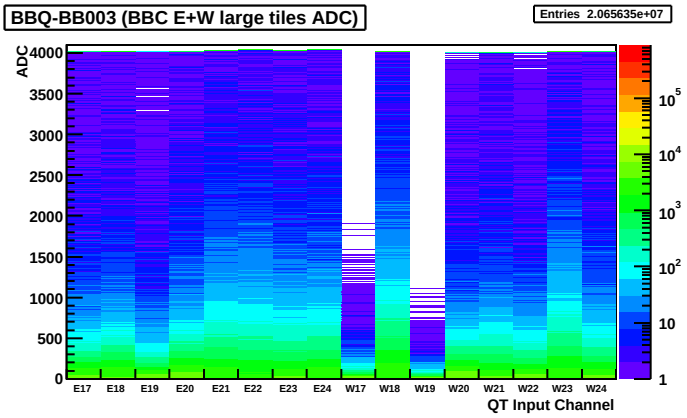

L1-TF201
Entries 7746132
Threshold bits
814494
813283
816333
811597
805228
769276
476528
477739
474689
479425
485794
521746
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

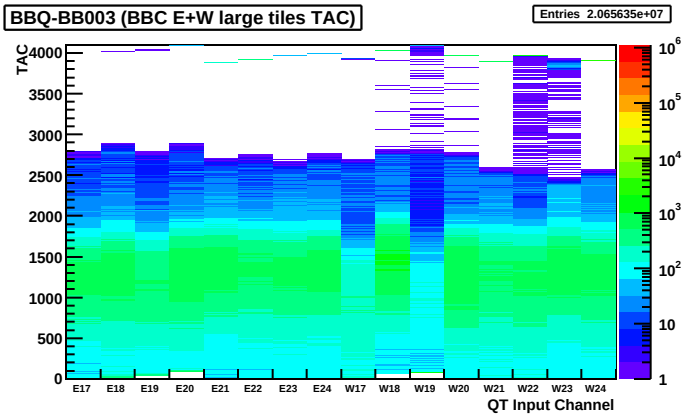

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.065635e+07
ADC
QT Input Channel

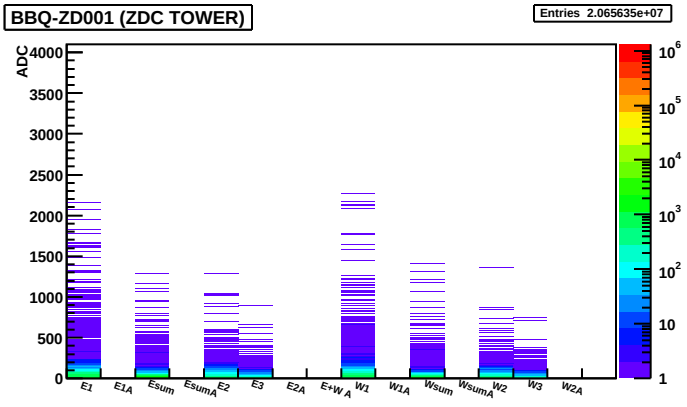

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.065635e+07
TAC
QT Input Channel

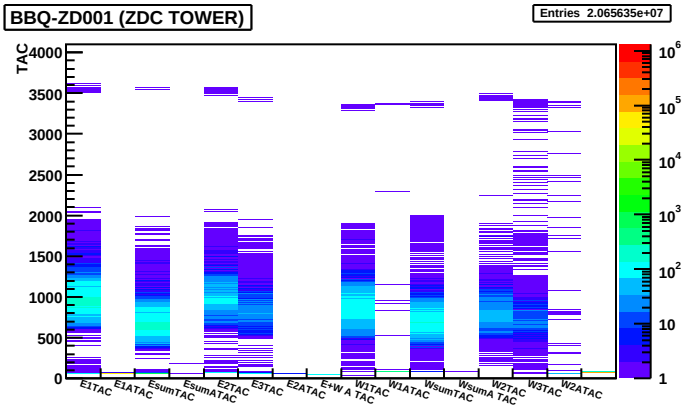

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.065635e+07
ADC
QT Input Channel

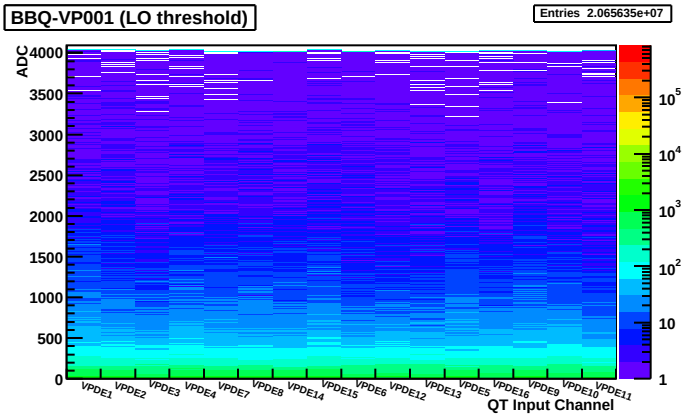

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.065635e+07
TAC
QT Input Channel

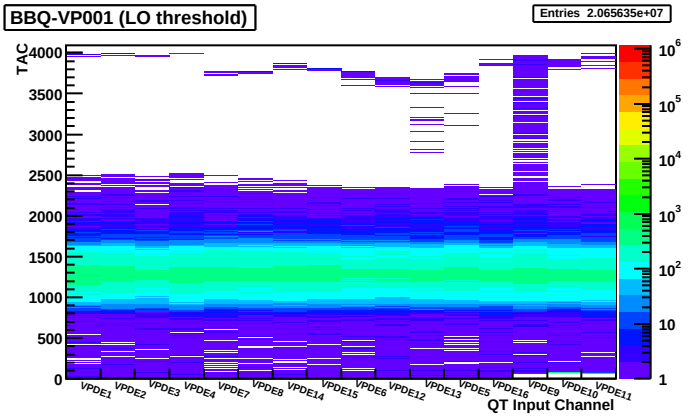

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.065635e+07
ADC
QT Input Channel

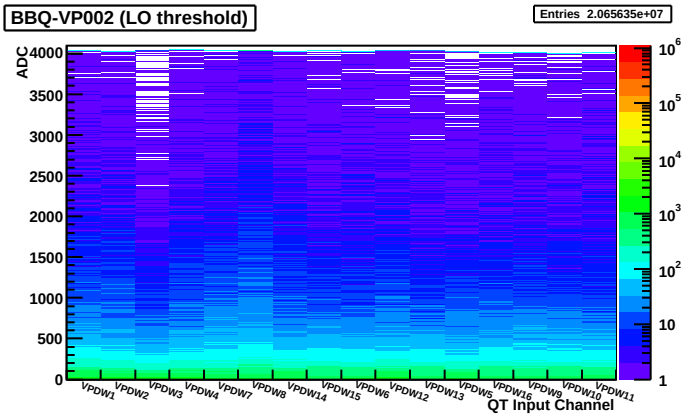

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.065635e+07
TAC
QT Input Channel

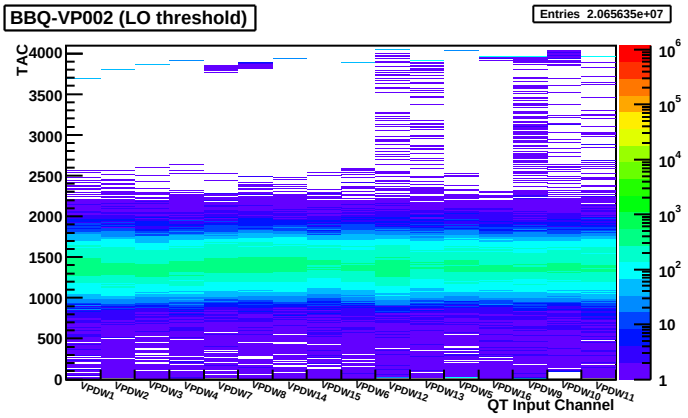

BBQ-ZD001 (ZDC TOWER)
Entries 2.065635e+07
ADC

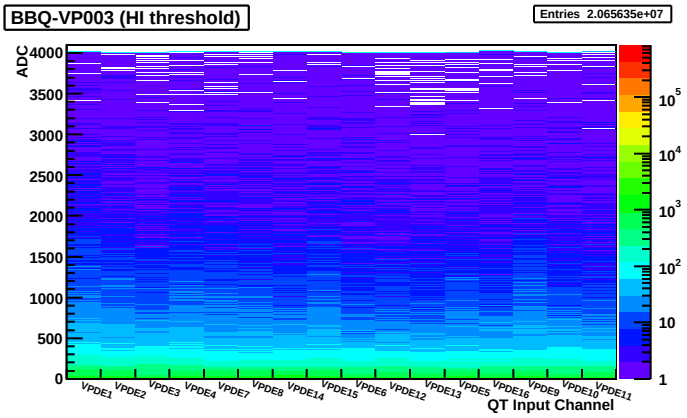

BBQ-ZD001 (ZDC TOWER)
Entries 2.065635e+07
TAC

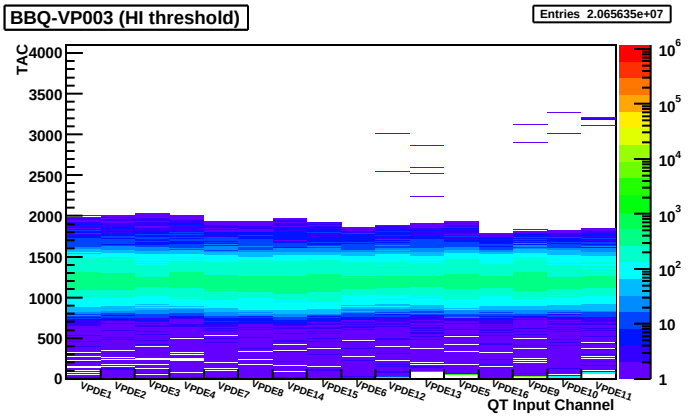

BBQ-VP001 (LO threshold)
Entries 2.065635e+07
ADC
QT Input Channel

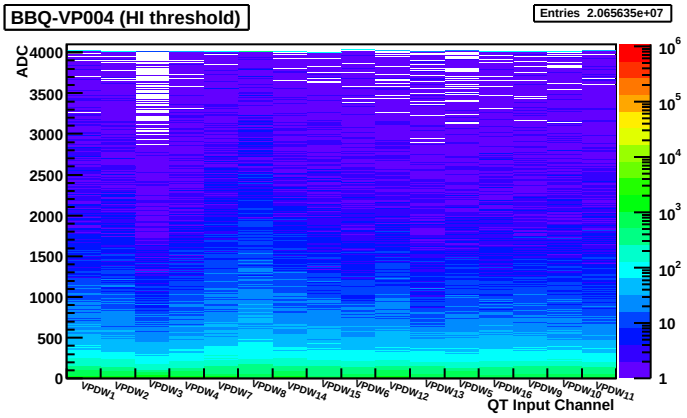

BBQ-VP001 (LO threshold)
Entries 2.065635e+07
TAC
QT Input Channel

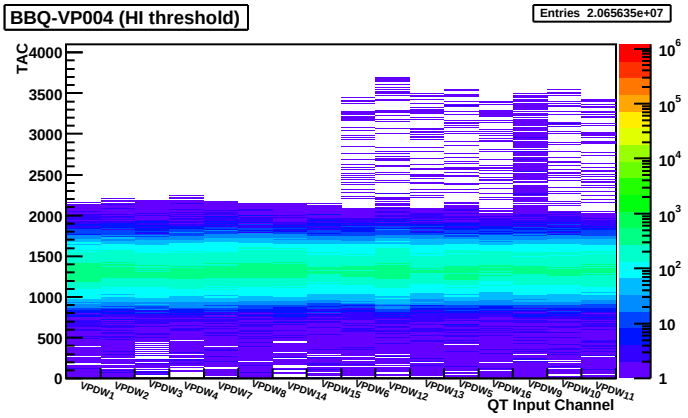

BBQ-VP002 (LO threshold)
Entries 2.065635e+07
ADC
QT Input Channel

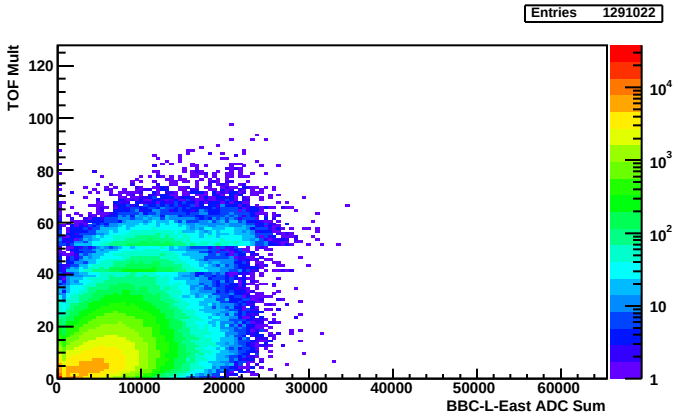

BBQ-VP002 (LO threshold)
Entries 2.065635e+07
TAC
QT Input Channel

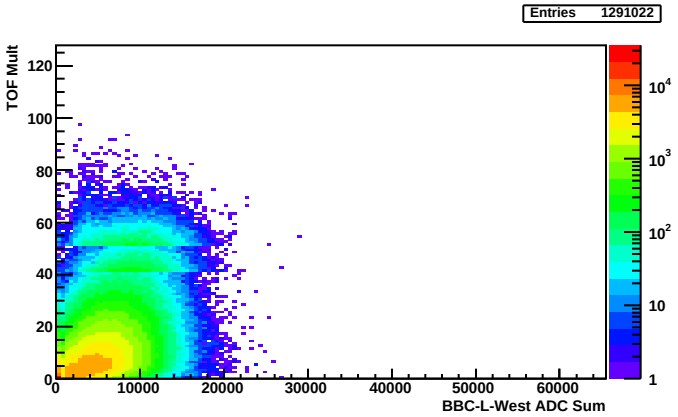

BBQ-VP003 (HI threshold)
Entries 2.065635e+07
ADC
QT Input Channel

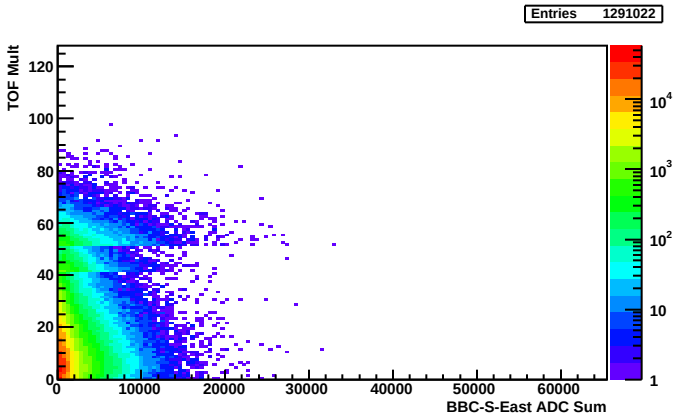

BBQ-VP003 (HI threshold)
Entries 2.065635e+07
TAC
QT Input Channel

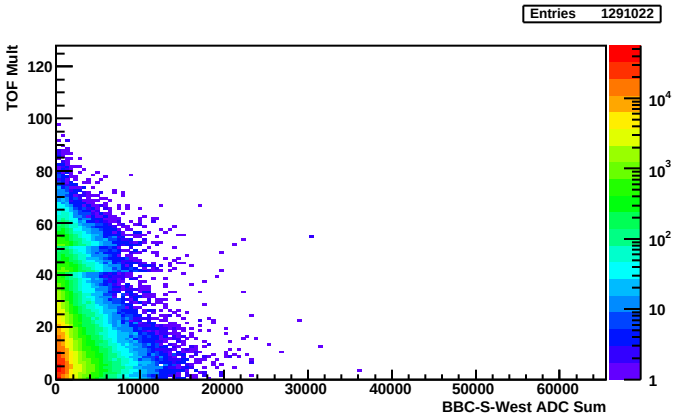

BBQ-VP004 (HI threshold)
Entries 2.065635e+07
ADC
QT Input Channel

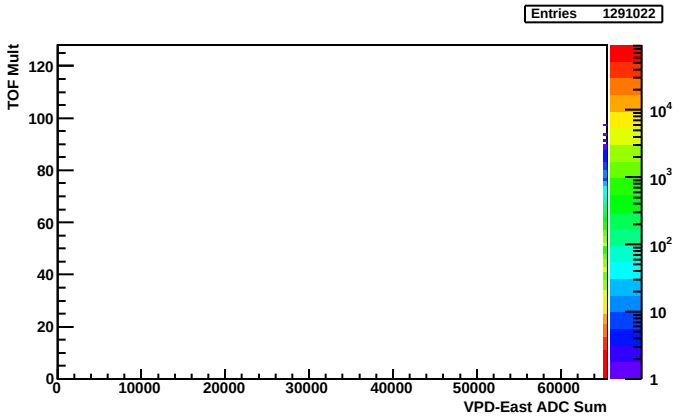

BBQ-VP004 (HI threshold)
Entries 2.065635e+07
TAC
QT Input Channel

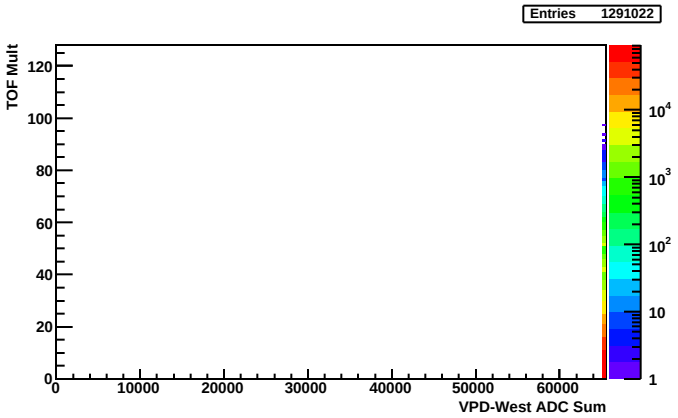

Entries 1291022
TOF Mult
BBC-L-East ADC Sum

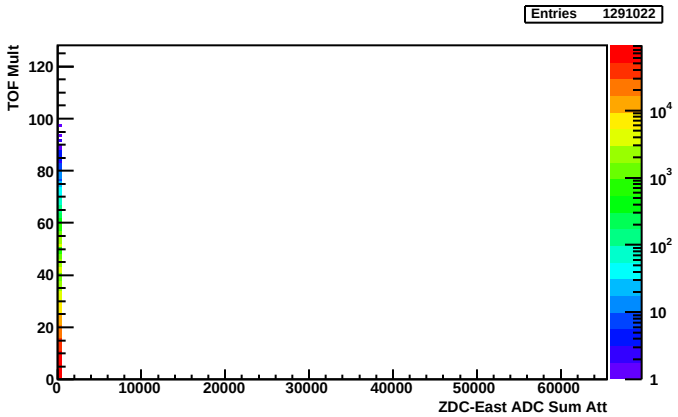

Entries 1291022
TOF Mult
BBC-L-West ADC Sum

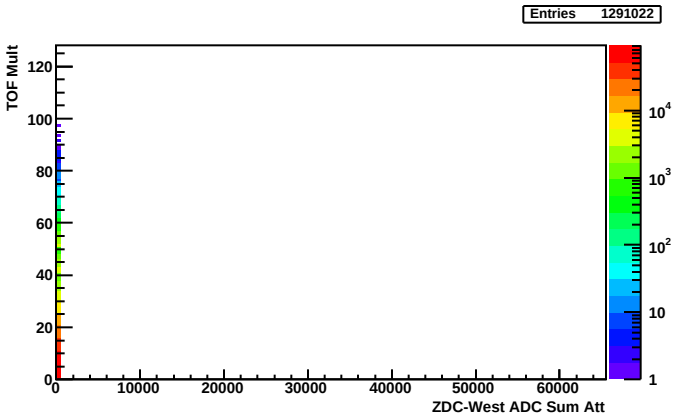

Entries 1291022
TOF Mult
BBC-S-East ADC Sum

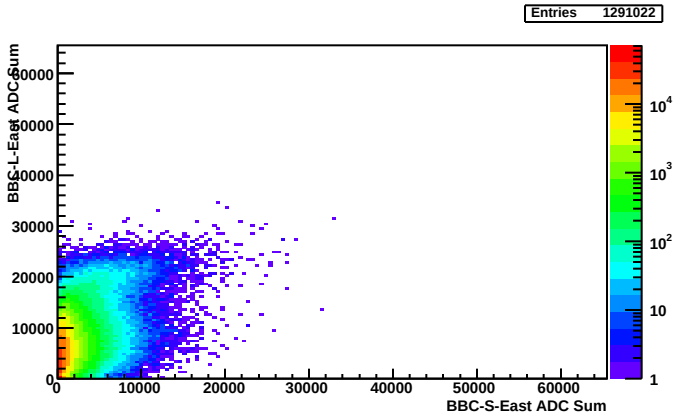

Entries 1291022
TOF Mult
BBC-S-West ADC Sum

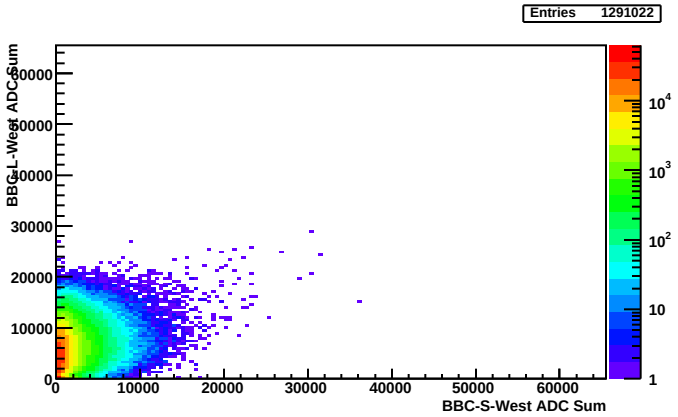

Entries 1291022
TOF Mult
VPD-East ADC Sum

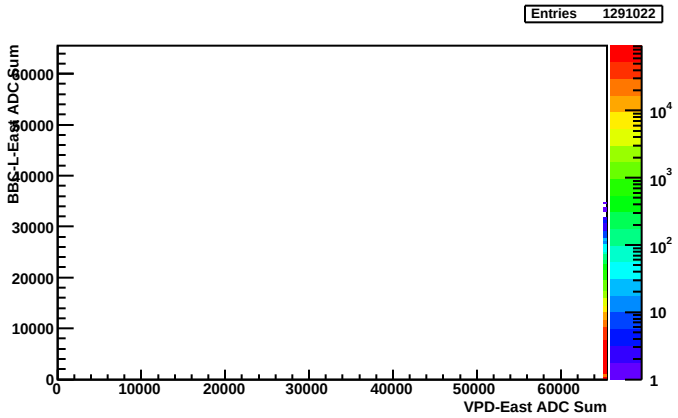

Entries 1291022
TOF Mult
VPD-West ADC Sum

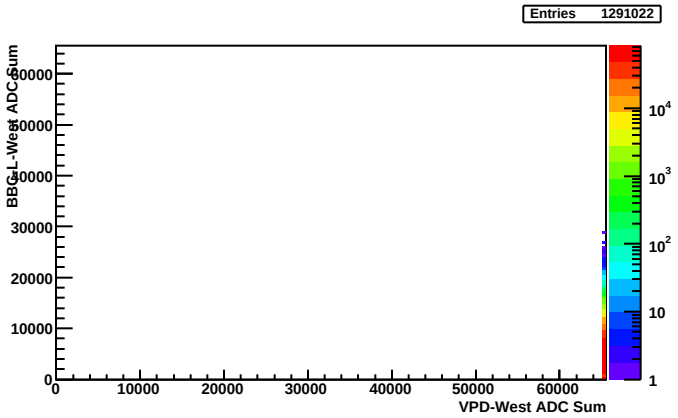

Entries 1291022
TOF Mult
ZDC-East ADC Sum Att

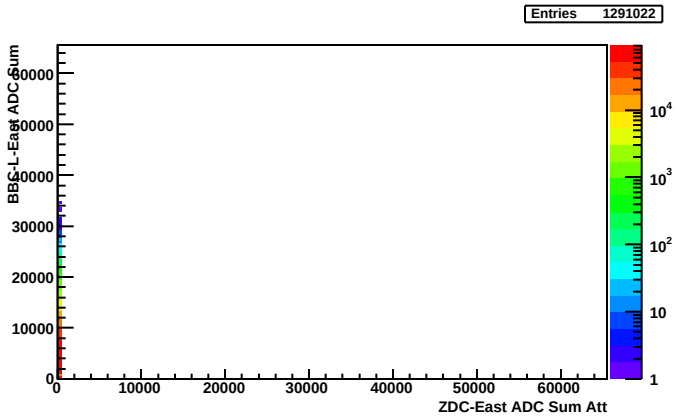

Entries 1291022
TOF Mult
ZDC-West ADC Sum Att

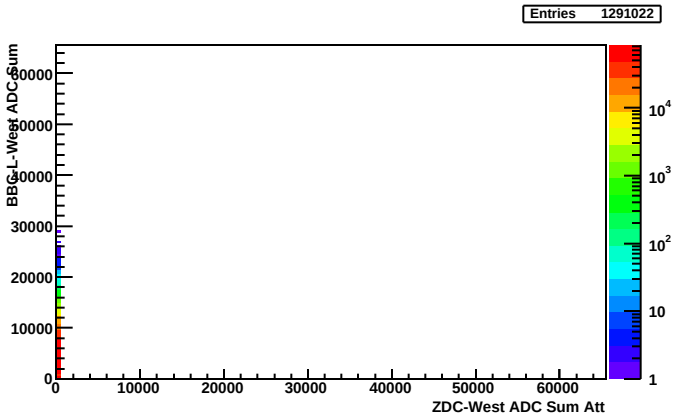

Entries 1291022
BBC-L-East ADC Sum
BBC-S-East ADC Sum

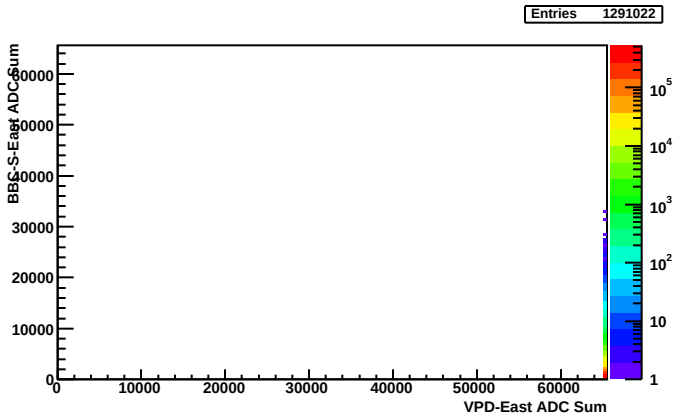

Entries 1291022
BBC-L-West ADC Sum
BBC-S-West ADC Sum

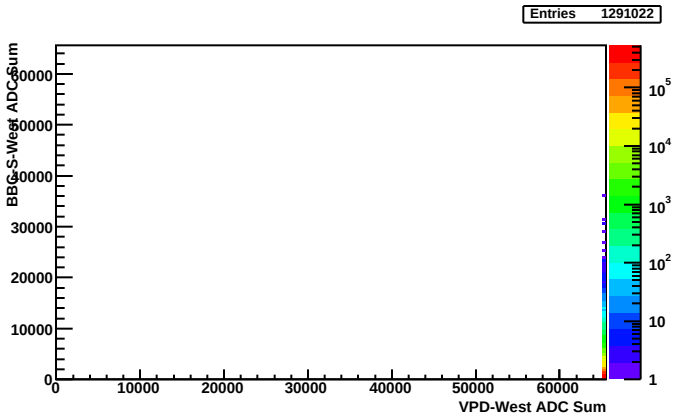

Entries 1291022
BBC-L-East ADC Sum
VPD-East ADC Sum

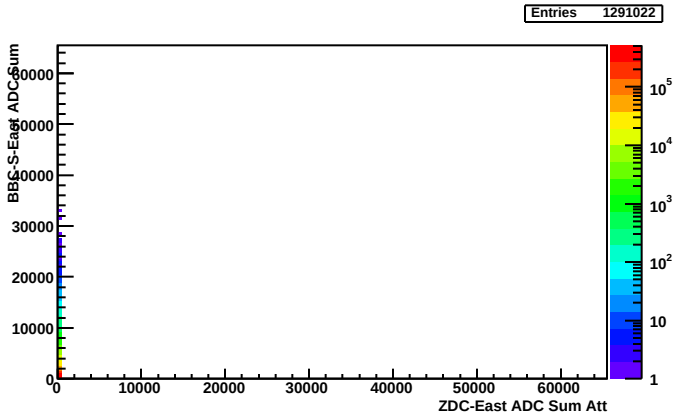

Entries 1291022
BBC-L-West ADC Sum
VPD-West ADC Sum

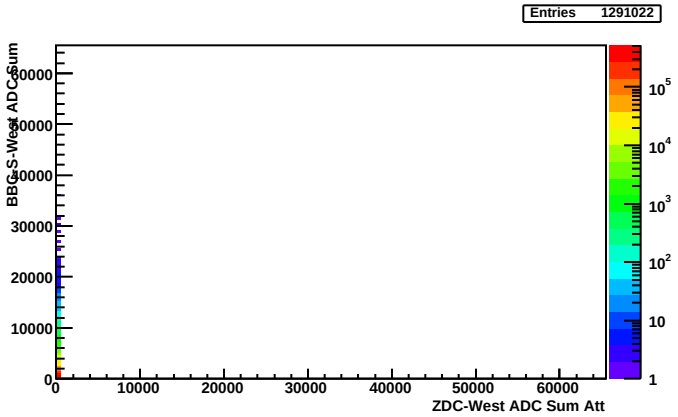

Entries 1291022
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

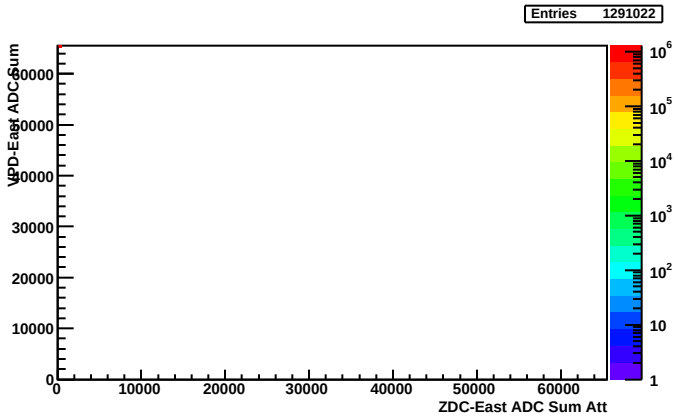

Entries 1291022
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

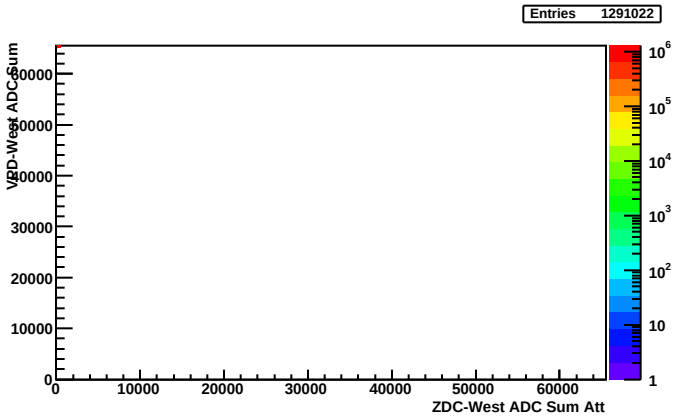

Entries 1291022
BBC-S-East ADC Sum
VPD-East ADC Sum

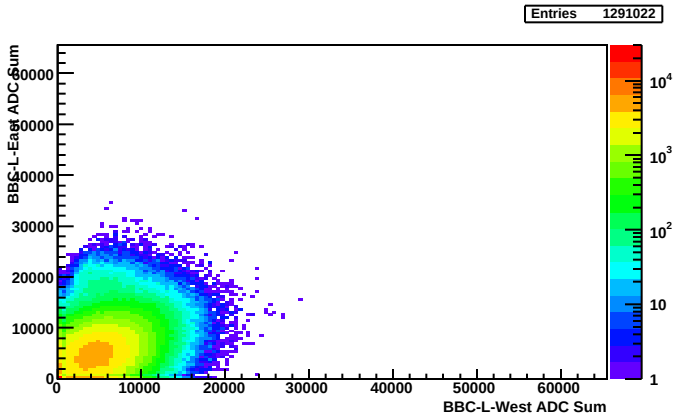

Entries 1291022
BBC-S-West ADC Sum
VPD-West ADC Sum

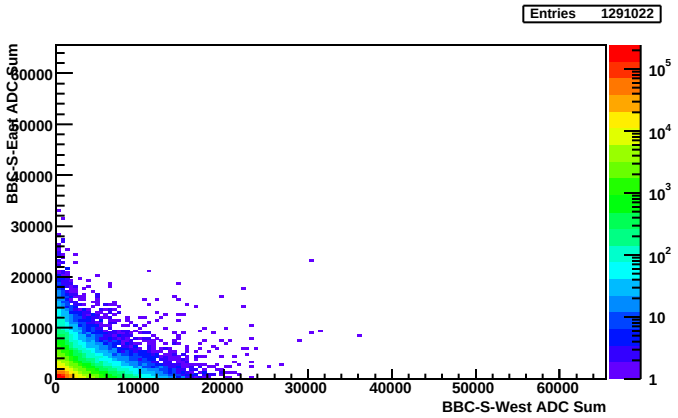

Entries 1291022
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

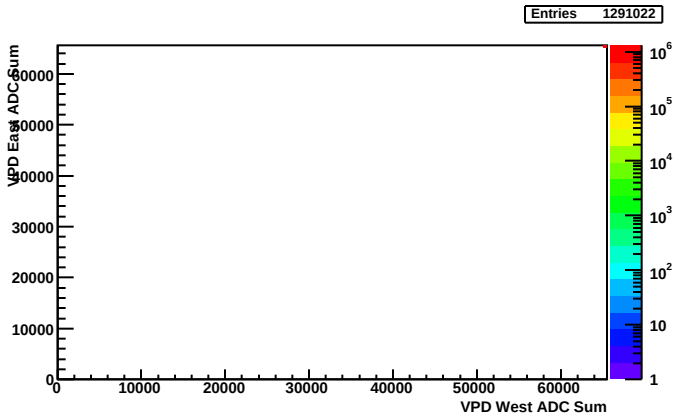

Entries 1291022
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

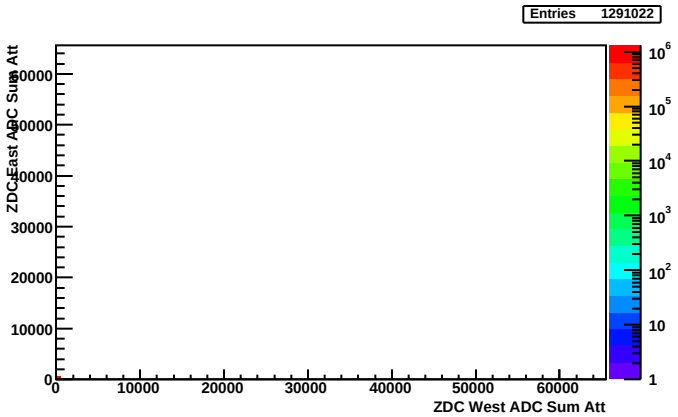

Entries 1291022
VPD-East ADC Sum
ZDC-East ADC Sum Att

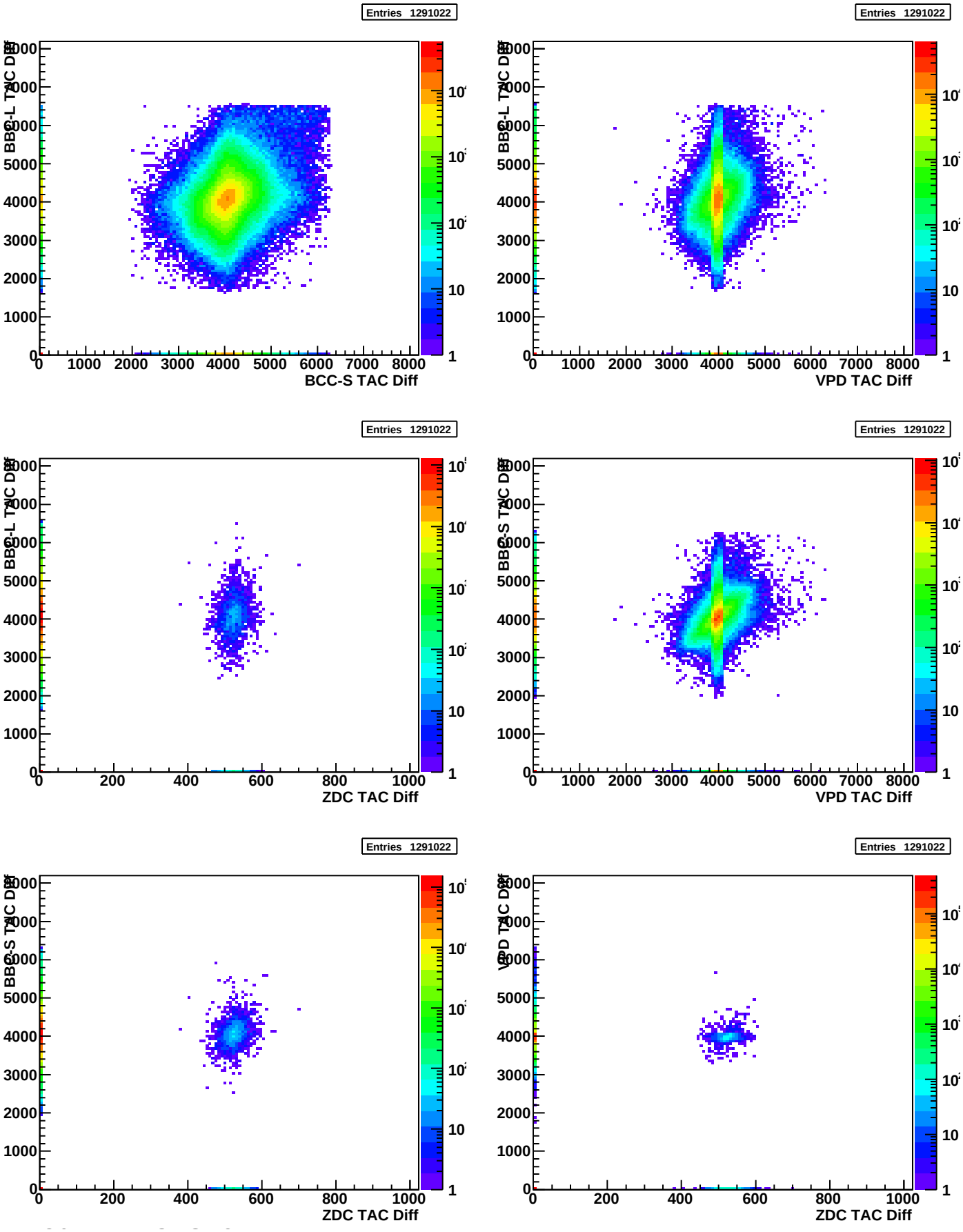

Entries 1291022
VPD-West ADC Sum
ZDC-West ADC Sum Att

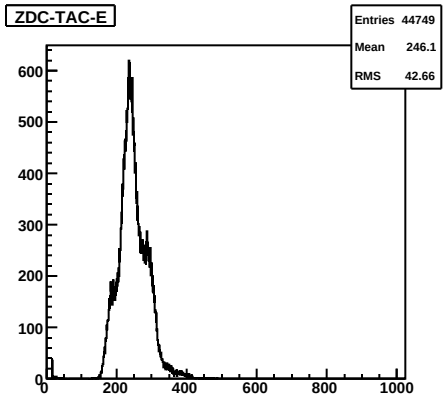

Entries 1291022
BBC-L-East ADC Sum
BBC-L-West ADC Sum

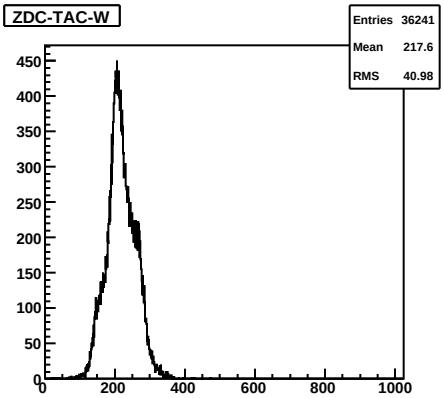

Entries 1291022
BBC-S-East ADC Sum
BBC-S-West ADC Sum

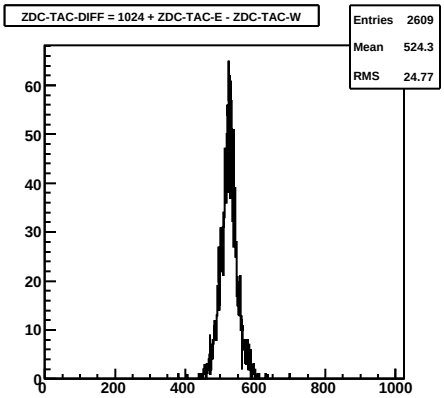

Entries 1291022
VPD East ADC Sum
VPD West ADC Sum

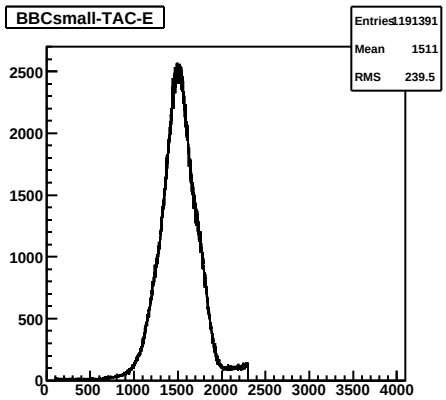

Entries 1291022
ZDC East ADC Sum Att
ZDC West ADC Sum Att

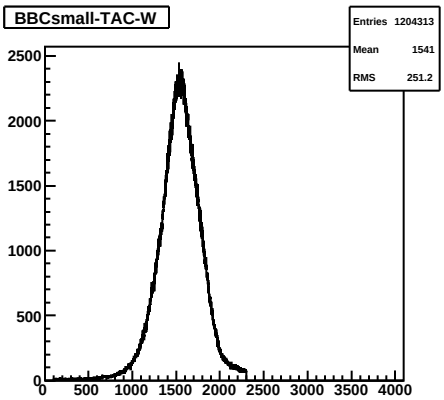

Entries 1291022
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1291022
BBC-L TAC Diff
VPD TAC Diff
Entries 1291022
BBC-L TAC Diff
ZDC TAC Diff
Entries 1291022
BBC-S TAC Diff
VPD TAC Diff
Entries 1291022
BBC-S TAC Diff
ZDC TAC Diff
Entries 1291022
VPD TAC Diff
ZDC TAC Diff

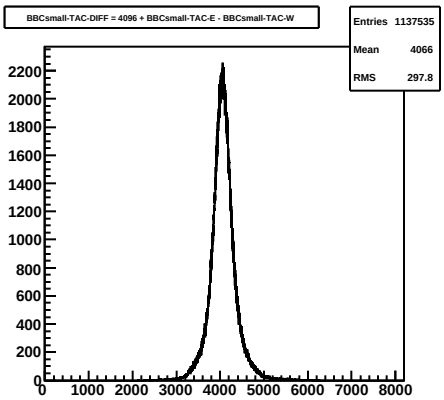

ZDC-TAC-E
Entries 44749
Mean 246.1
RMS 42.66

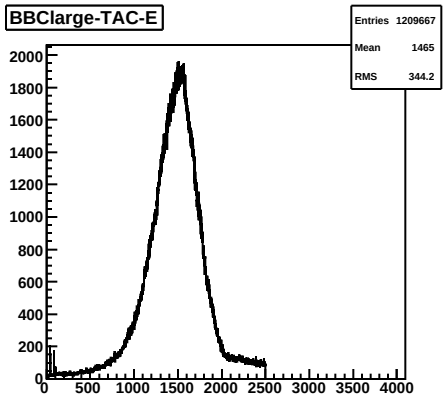

ZDC-TAC-W
Entries 36241
Mean 217.6
RMS 40.98

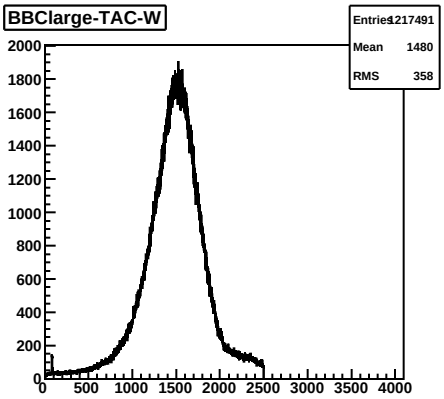

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 2609
Mean 524.3
RMS 24.77

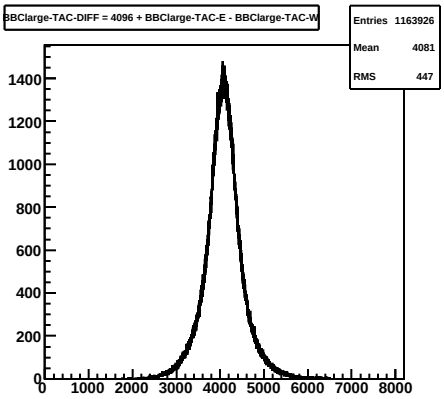

BBCsmall-TAC-E
Entries 1191391
Mean 1511
RMS 239.5

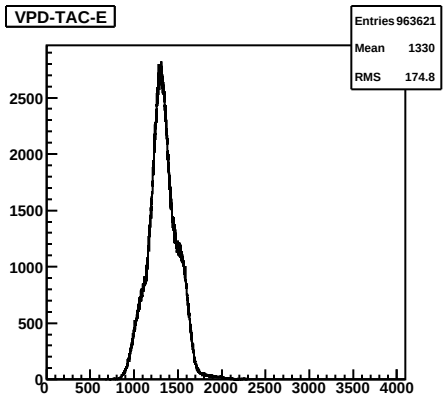

BBCsmall-TAC-W
Entries 1204313
Mean 1541
RMS 251.2

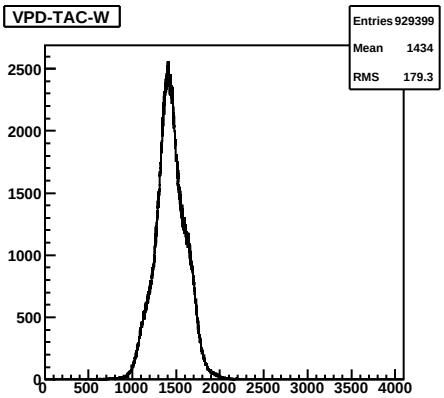

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 1137535
Mean 4066
RMS 297.8

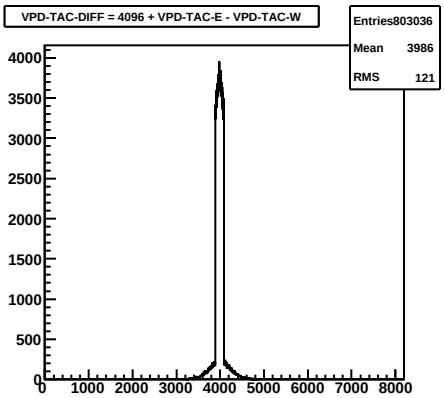

BBClarge-TAC-E
Entries 1209667
Mean 1465
RMS 344.2

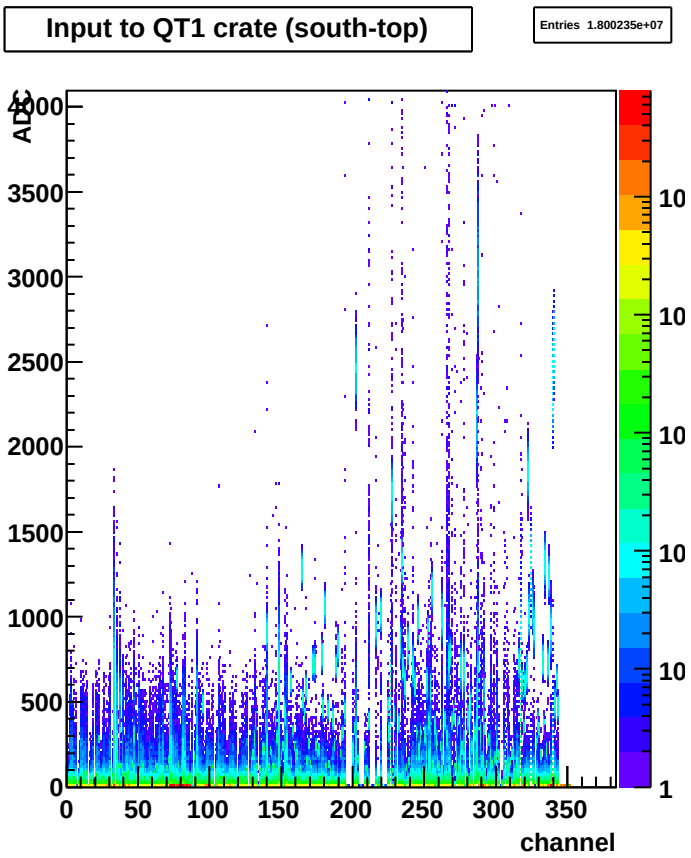

BBClarge-TAC-W
Entries 1217491
Mean 1480
RMS 358

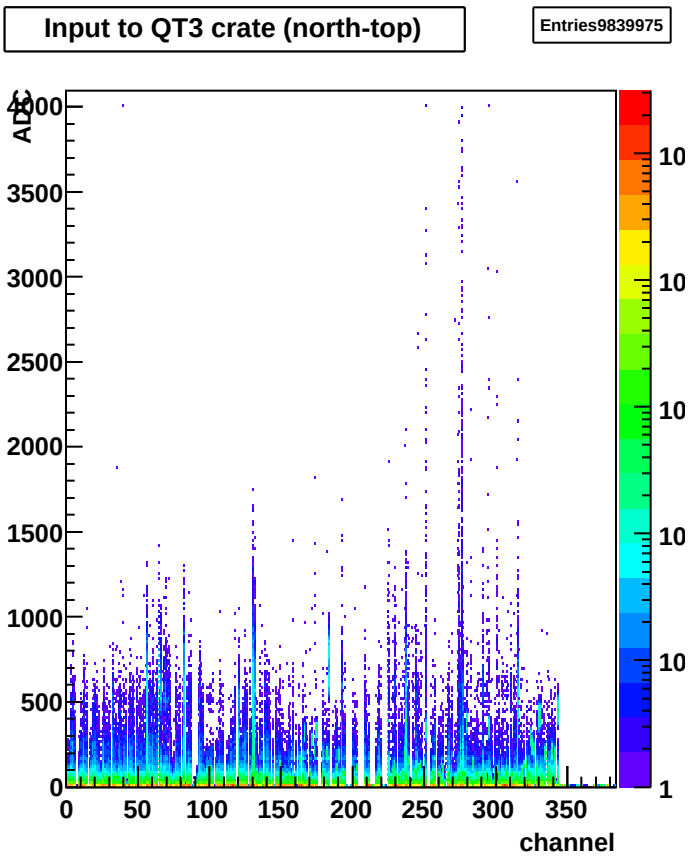

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 1163926
Mean 4081
RMS 447

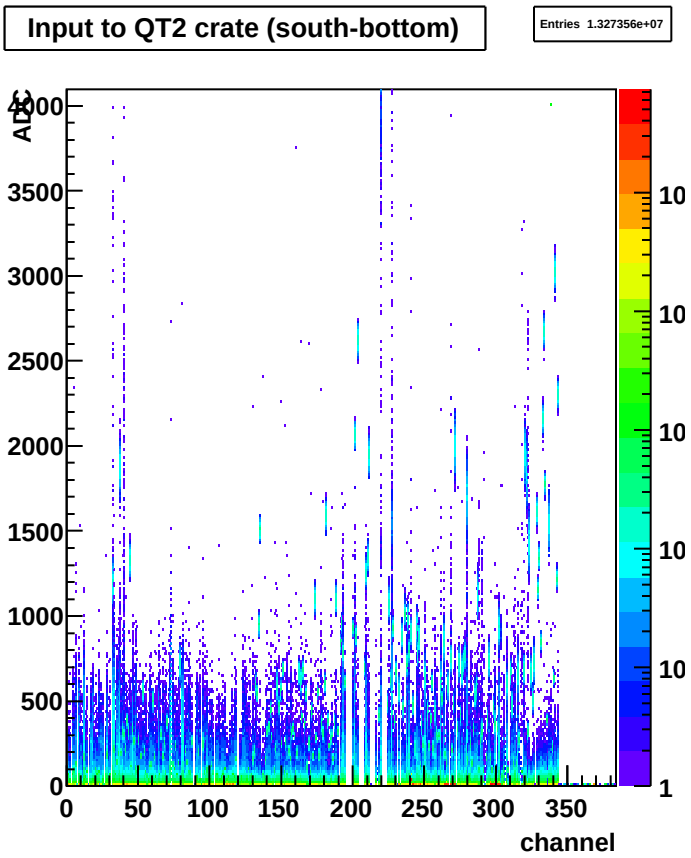

VPD-TAC-E
Entries 963621
Mean 1330
RMS 174.8

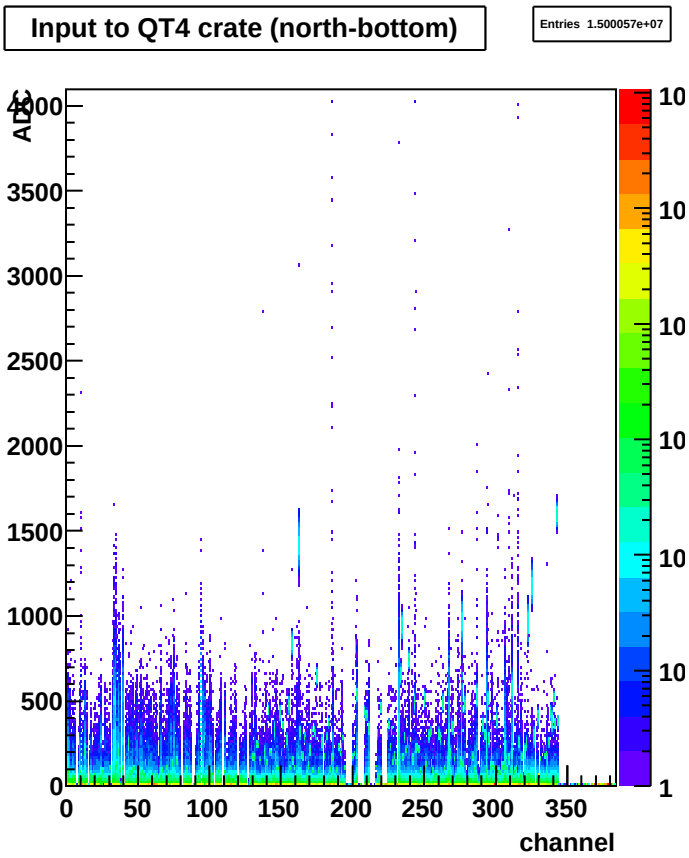

VPD-TAC-W
Entries 929399
Mean 1434
RMS 179.3

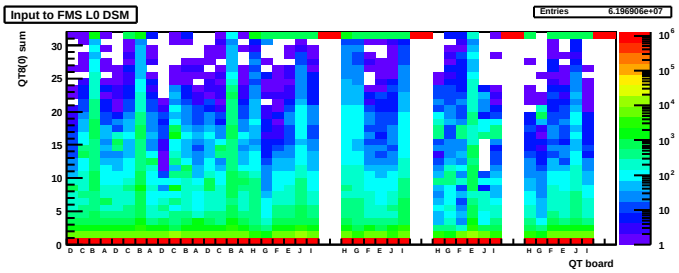

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 803036
Mean 3986
RMS 121

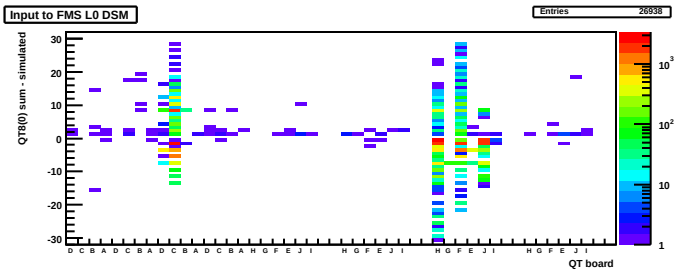

Input to QT1 crate (south-top)
Entries 1.800235e+07
ADC
channel

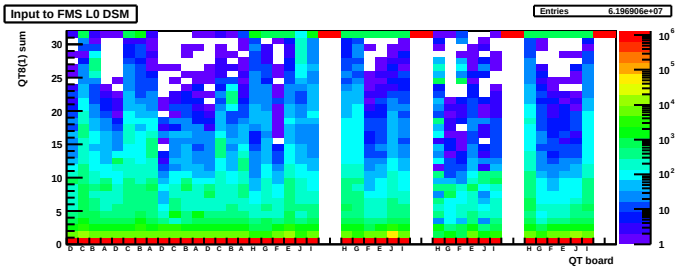

Input to QT3 crate (north-top)
Entries9839975
ADC
channel

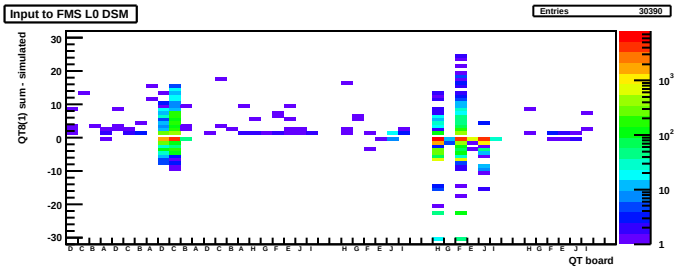

Input to QT2 crate (south-bottom)
Entries 1.327356e+07
ADC
channel

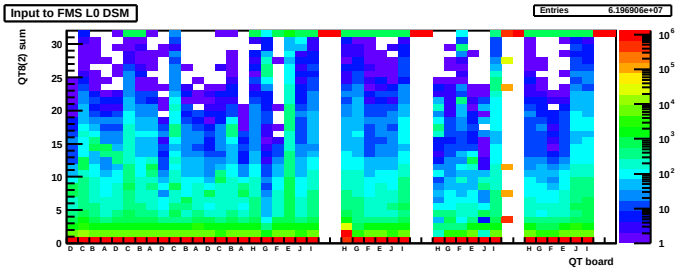

Input to QT4 crate (north-bottom)
Entries 1.500057e+07
ADC
channel

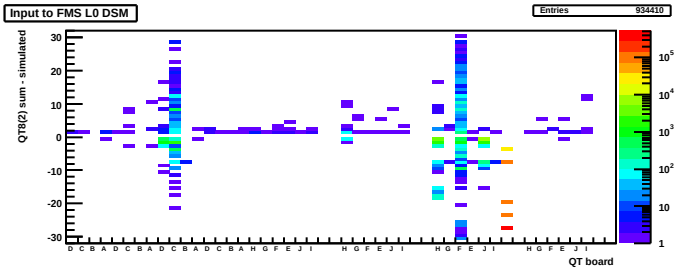

Input to FMS L0 DSM
Entries 6.196906e+07
QT8(0) sum
QT board

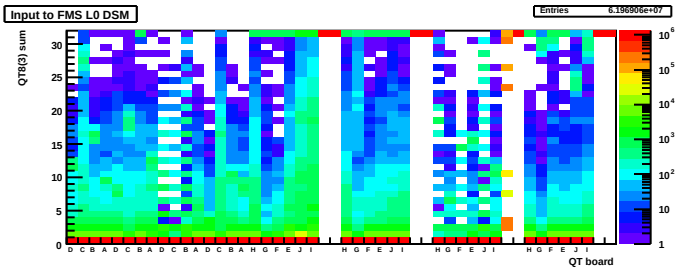

Input to FMS L0 DSM
Entries 26938
QT8(0) sum - simulated
QT board

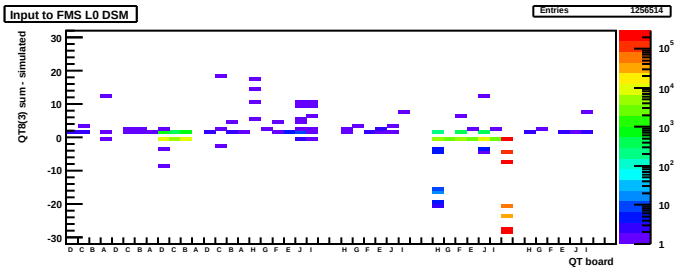

Input to FMS L0 DSM
Entries 6.196906e+07
QT8(1) sum
QT board

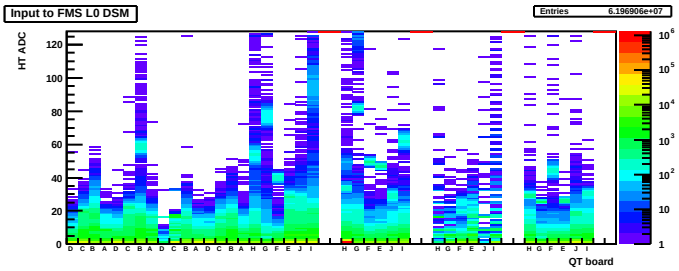

Input to FMS L0 DSM
Entries 30390
QT8(1) sum - simulated
QT board

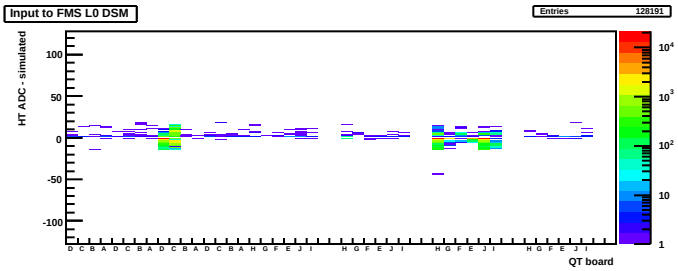

Input to FMS L0 DSM
Entries 6.196906e+07
QT8(2) sum
QT board

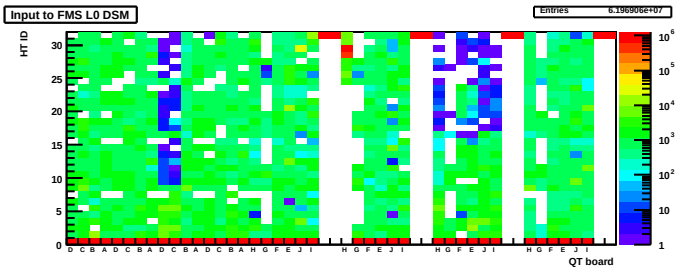

Input to FMS L0 DSM
Entries 934410
QT8(2) sum - simulated
QT board

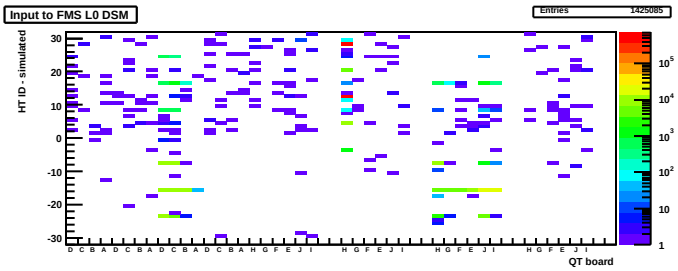

Input to FMS L0 DSM
Entries 6.196906e+07
QT8(3) sum
QT board

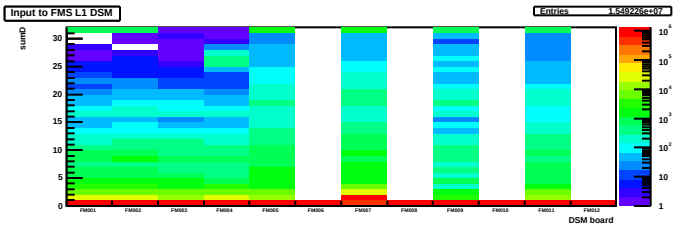

Input to FMS L0 DSM
Entries 1256514
QT8(3) sum - simulated
QT board

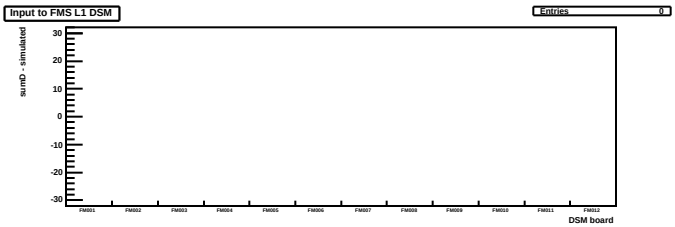

Input to FMS L0 DSM
Entries 6.196906e+07
HT ADC
QT board

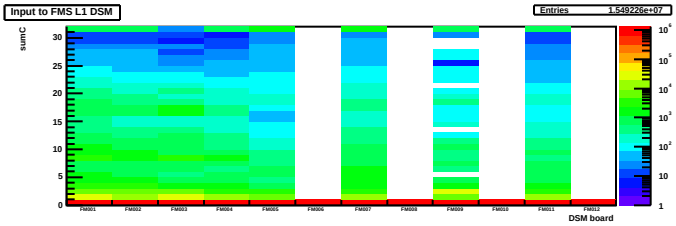

Input to FMS L0 DSM
Entries 128191
HT ADC - simulated
QT board

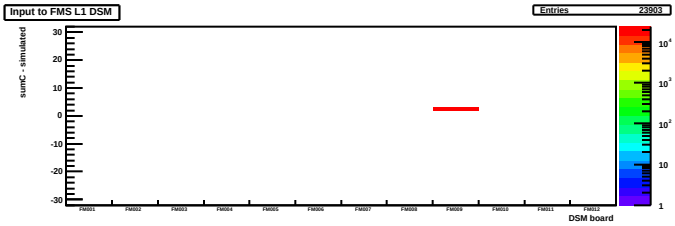

Input to FMS L0 DSM
Entries 6.196906e+07
HT ID
QT board

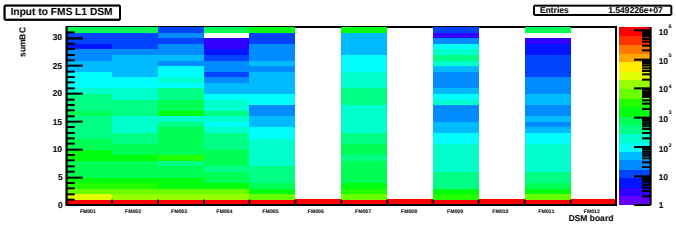

Input to FMS L0 DSM
Entries 1425085
HT ID - simulated
QT board

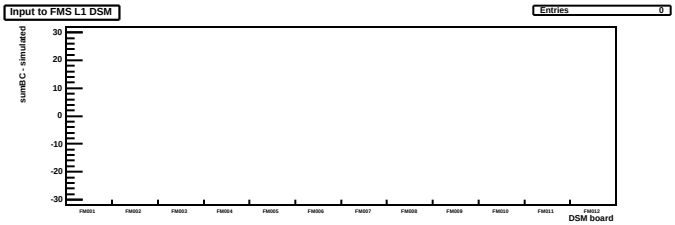

Input to FMS L1 DSM
Entries 1.549226e+07
sumD
DSM board

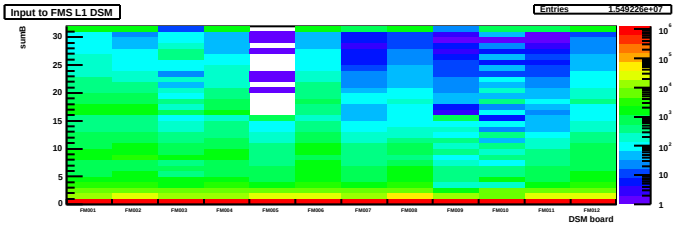

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

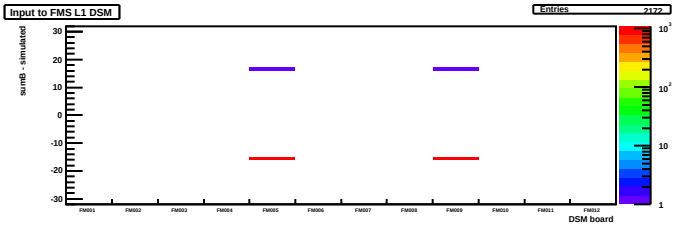

Input to FMS L1 DSM
Entries 1.549226e+07
sumC
DSM board

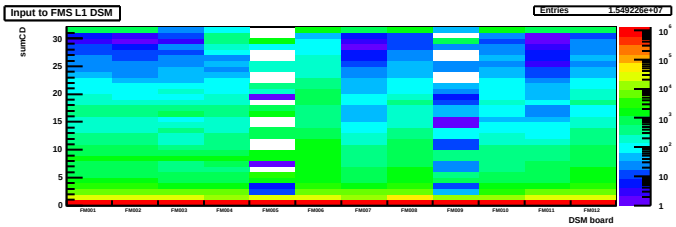

Input to FMS L1 DSM
Entries 23903
sumC - simulated
DSM board

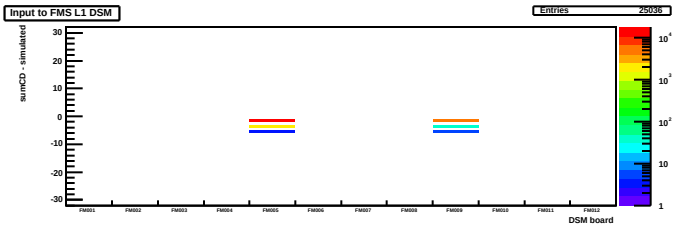

Input to FMS L1 DSM
Entries 1.549226e+07
sumBC
DSM board

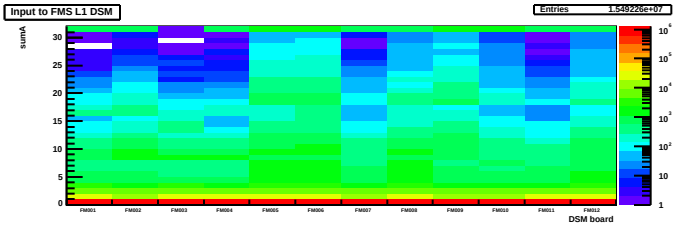

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

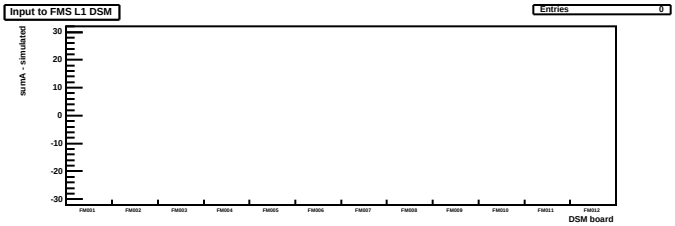

Input to FMS L1 DSM
Entries 1.549226e+07
sumB
DSM board

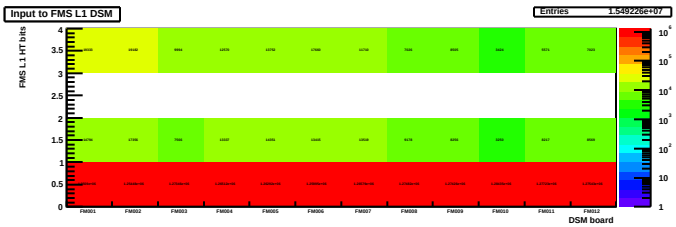

Input to FMS L1 DSM
Entries 2172
sumB - simulated
DSM board

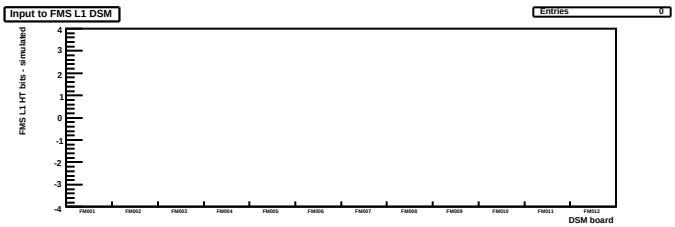

Input to FMS L1 DSM
Entries 1.549226e+07
sumCD
DSM board

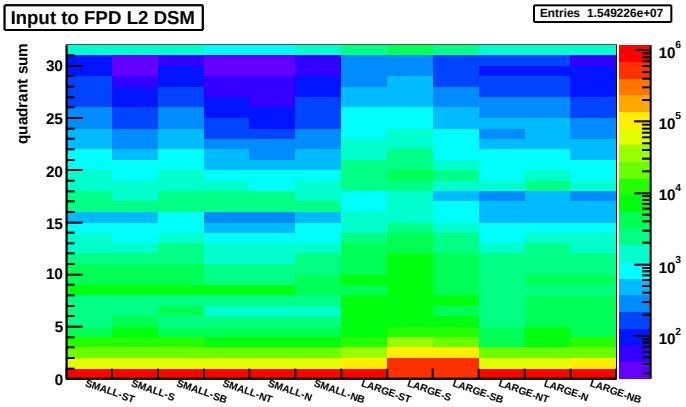

Input to FMS L1 DSM
Entries 25036
sumCD - simulated
DSM board

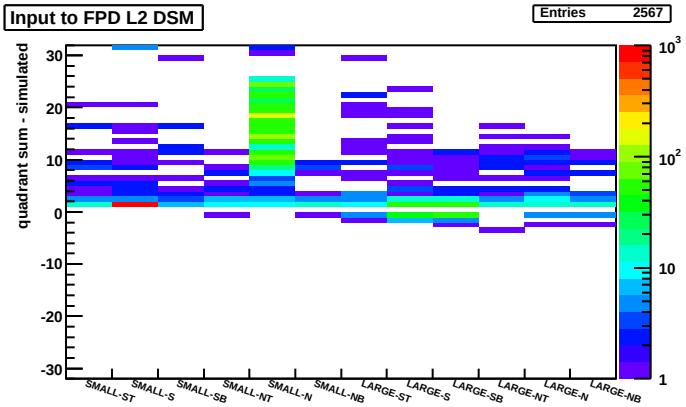

Input to FMS L1 DSM
Entries 1.549226e+07
sumA
DSM board

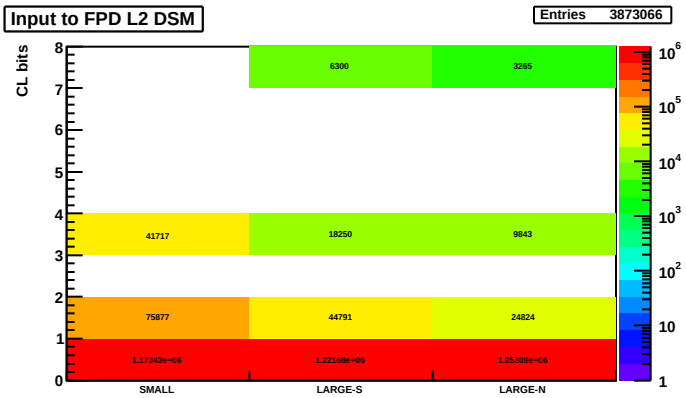

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

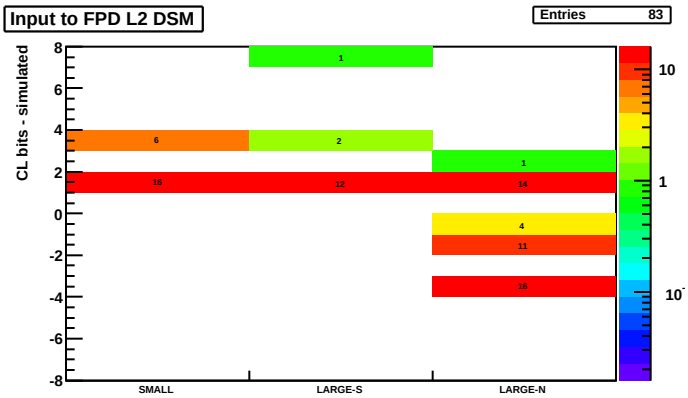

Input to FMS L1 DSM
Entries 1.549226e+07
FMS L1 HT bits
DSM board

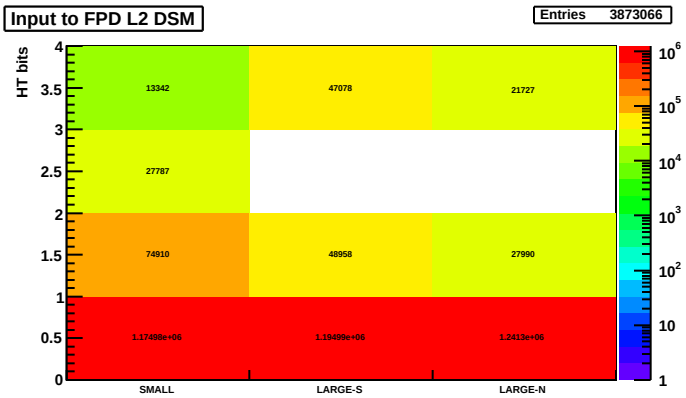

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

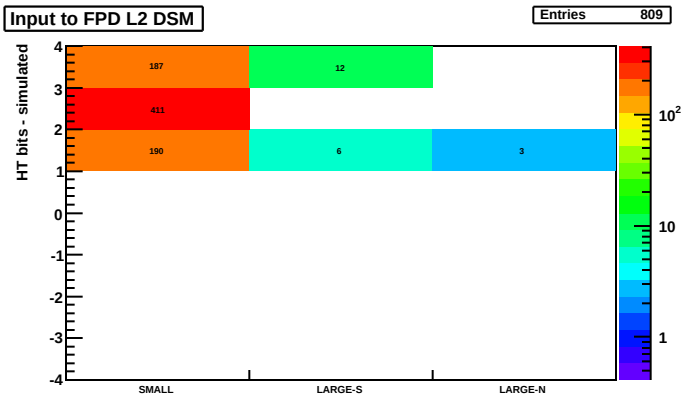

Input to FPD L2 DSM
Entries 1.549226e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

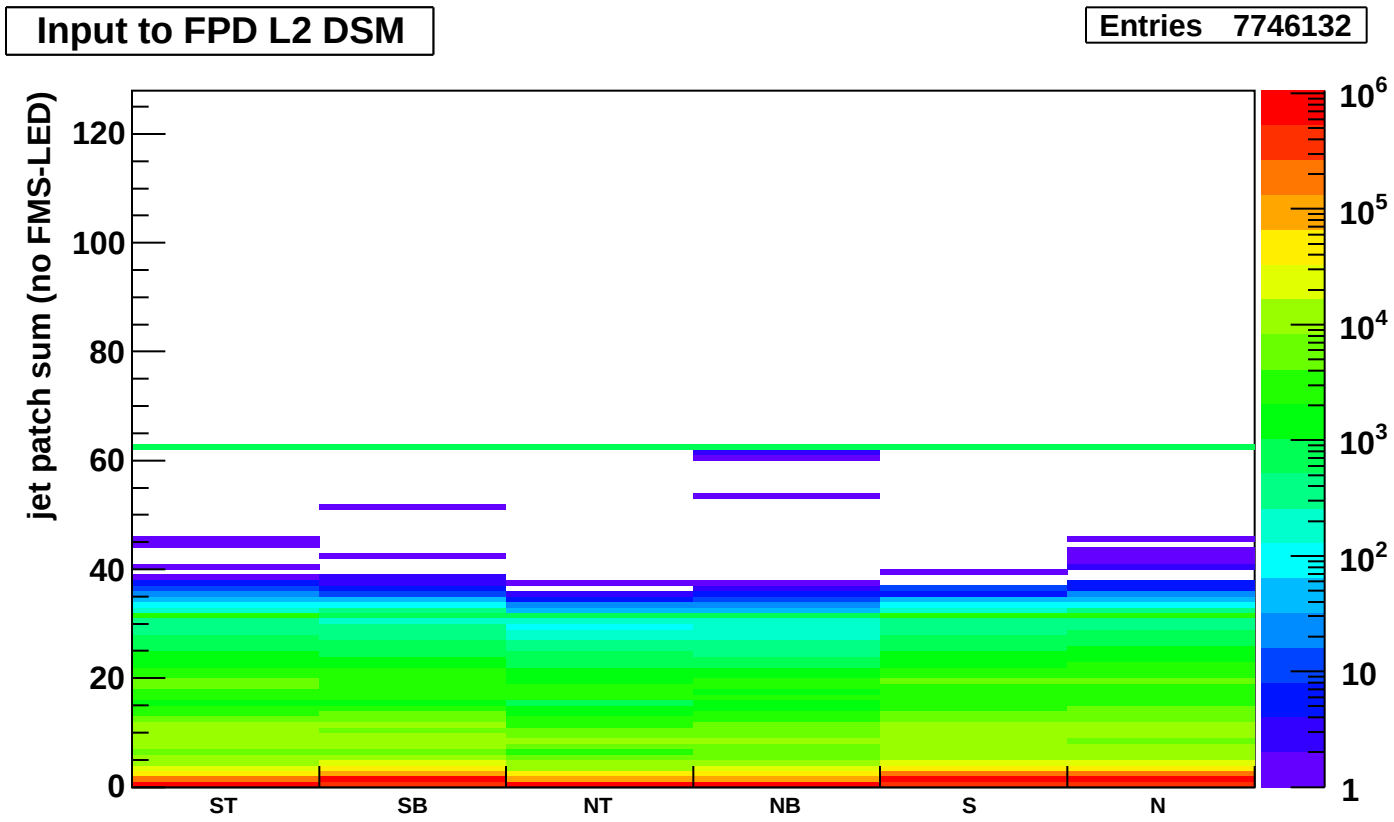

Input to FPD L2 DSM
Entries 2567
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

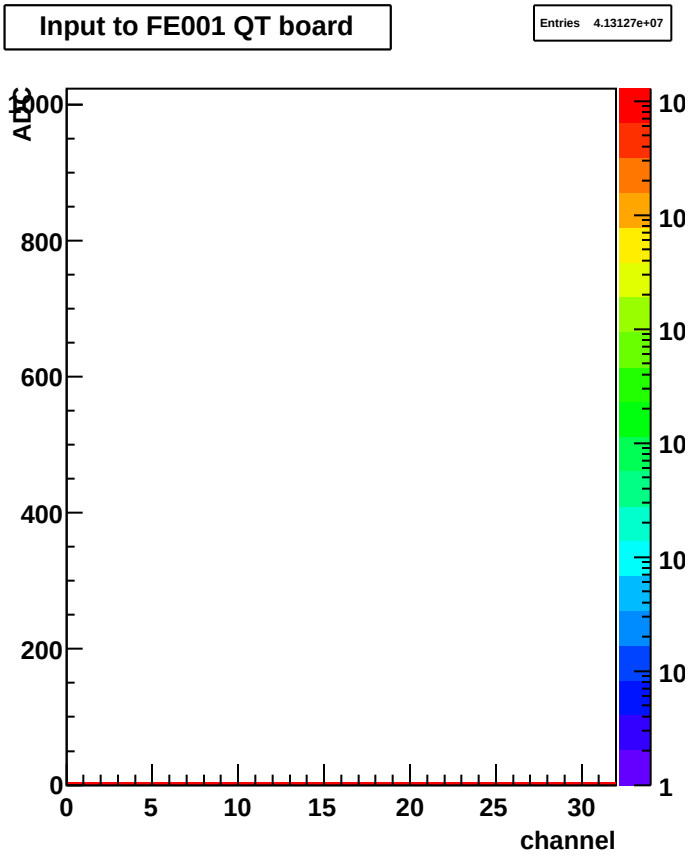

Input to FPD L2 DSM
Entries 3873066
CL bits
6300
3265
41717
18250
9843
75877
44791
24824
1.17343e+06
1.22168e+06
1.25309e+06
SMALL
LARGE-S
LARGE-N

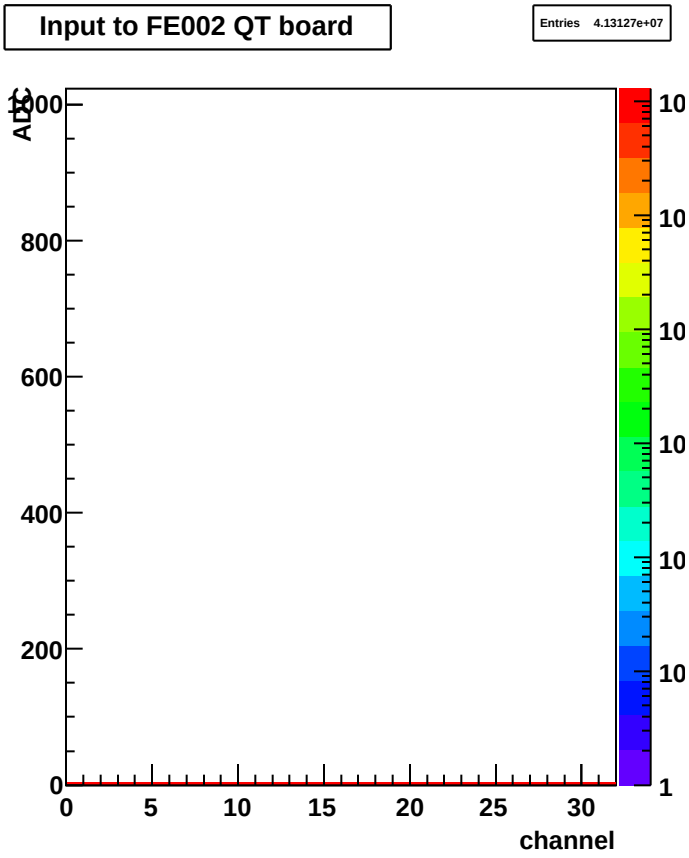

Input to FPD L2 DSM
Entries 83
CL bits - simulated
1
6
2
1
16
12
14
4
11
16
SMALL
LARGE-S
LARGE-N

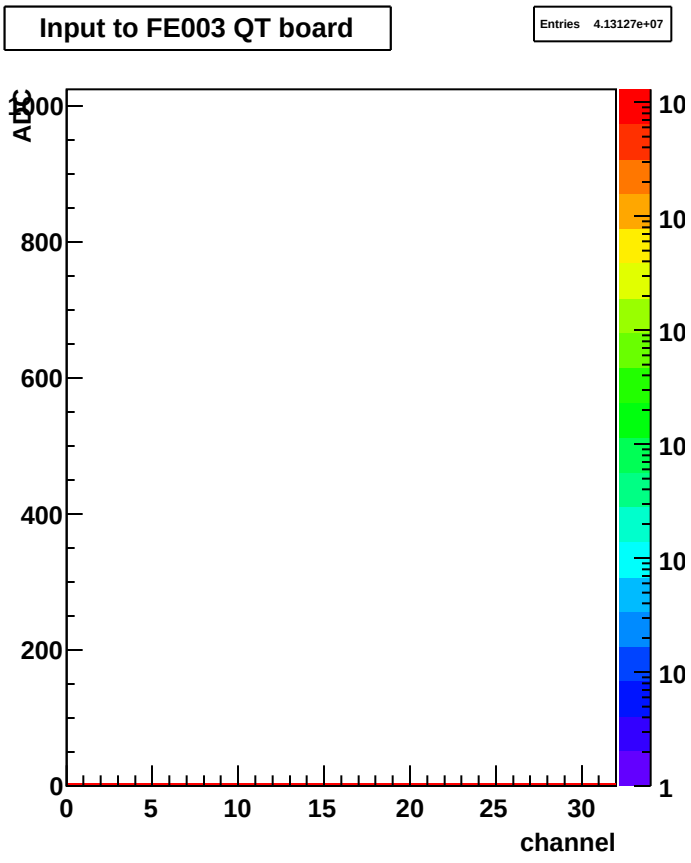

Input to FPD L2 DSM
Entries 3873066
HT bits
13342
47078
21727
27787
74910
48958
27990
1.17498e+06
1.19499e+06
1.2413e+06
SMALL
LARGE-S
LARGE-N

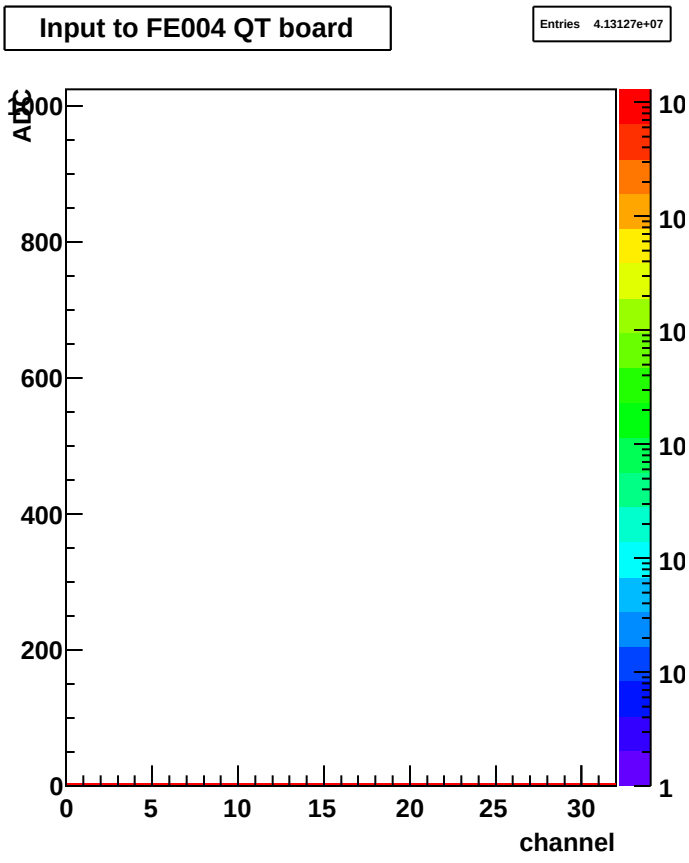

Input to FPD L2 DSM
Entries 809
HT bits - simulated
187
12
411
190
6
3
SMALL
LARGE-S
LARGE-N

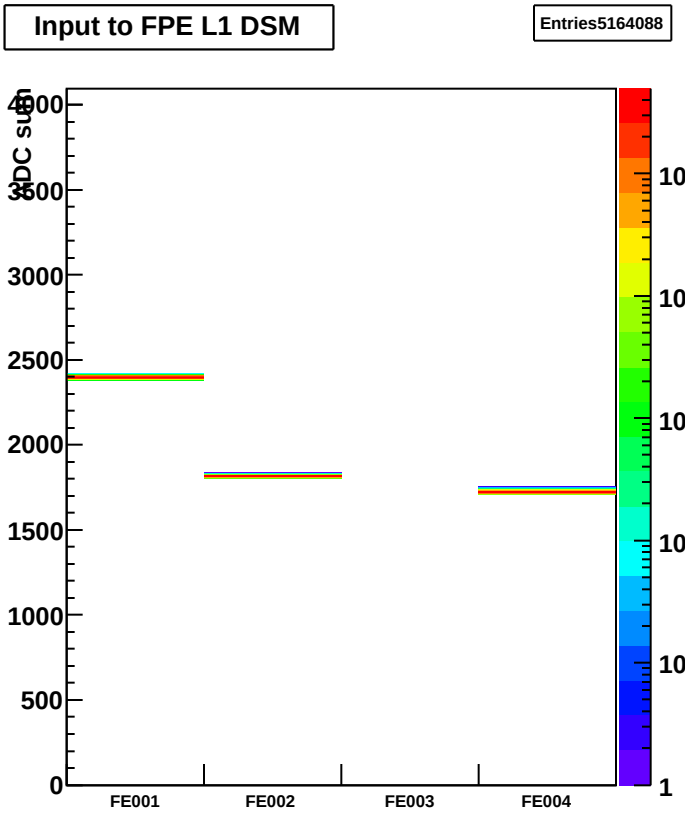

Input to FPD L2 DSM
Entries 7746132
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6

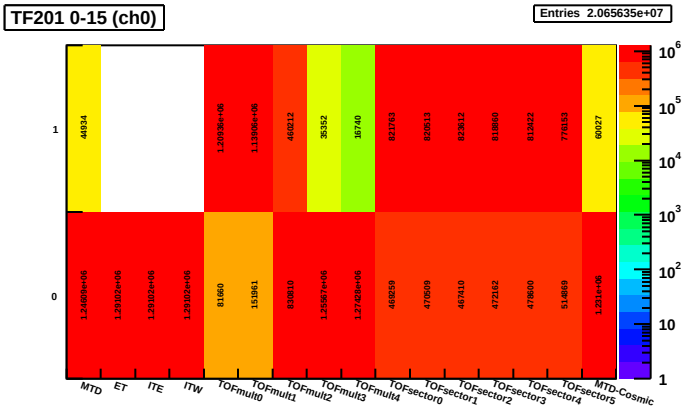

Input to FE001 QT board
Entries 4.13127e+07
ADC
channel

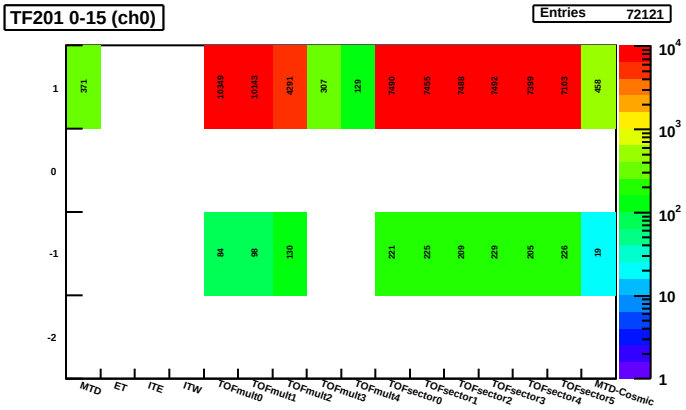

Input to FE002 QT board
Entries 4.13127e+07
ADC
channel


Input to FE003 QT board
Entries 4.13127e+07
ADC
channel


Input to FE004 QT board
Entries 4.13127e+07
ADC
channel


Input to FPE L1 DSM
Entries5164088
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


TF201 0-15 (ch0)
Entries 2.065635e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic


TF201 0-15 (ch0)
Entries 72121
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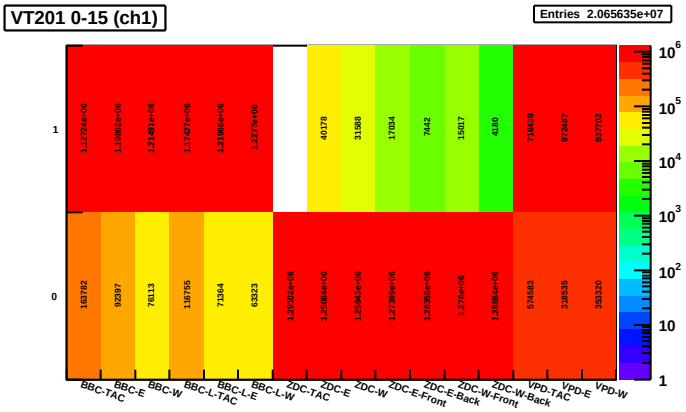
VT201 0-15 (ch1)
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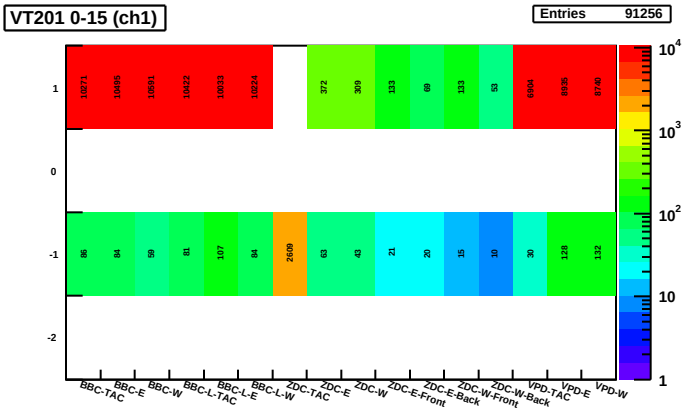
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W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Entries 91256
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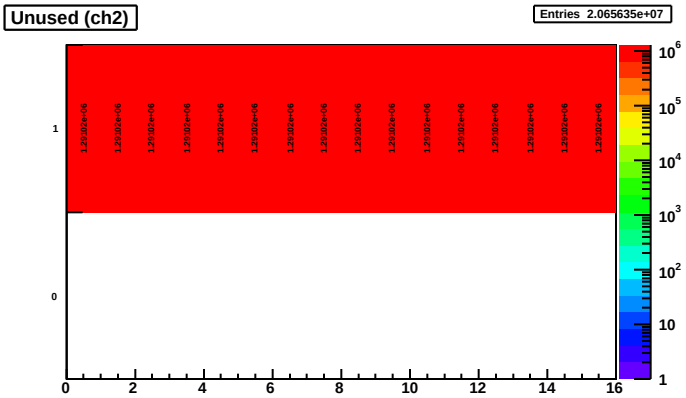
Unused (ch2)
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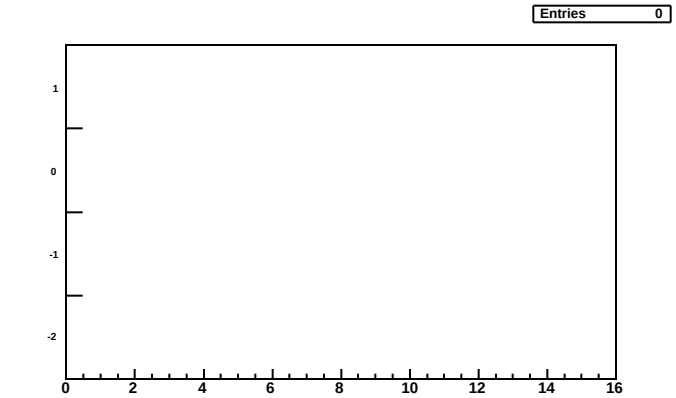
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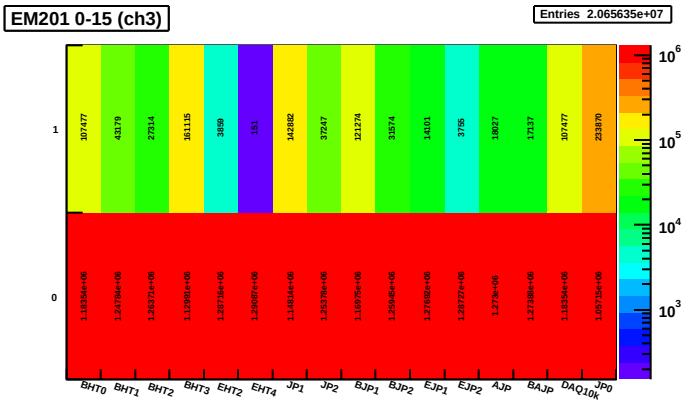
EM201 0-15 (ch3)
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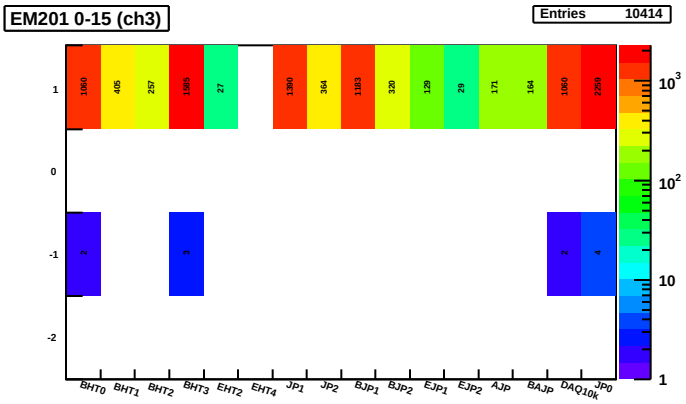
EM201 0-15 (ch3)
Entries 10414
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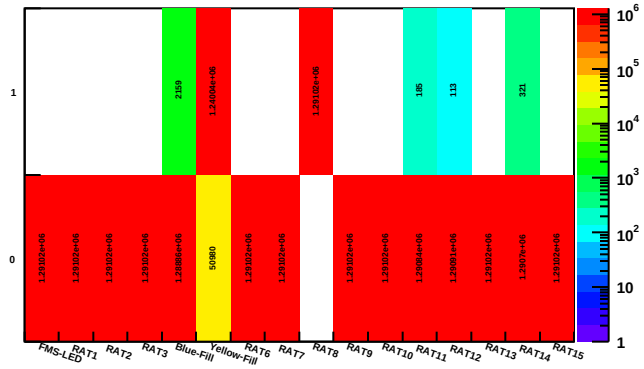
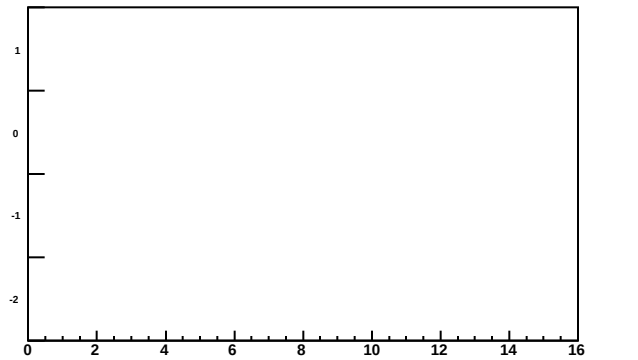
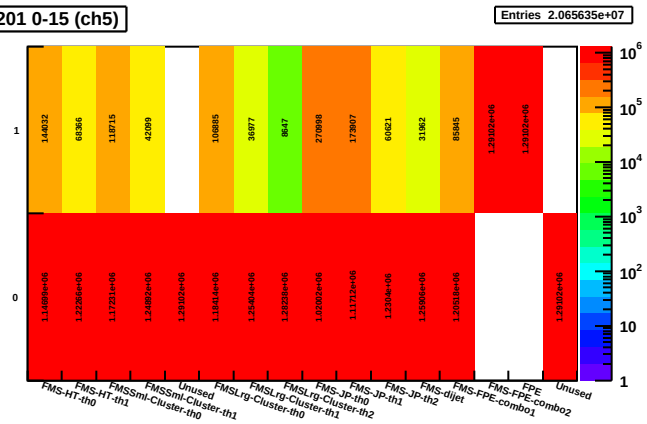
RAT board (ch4)





FP201 0-15 (ch5)



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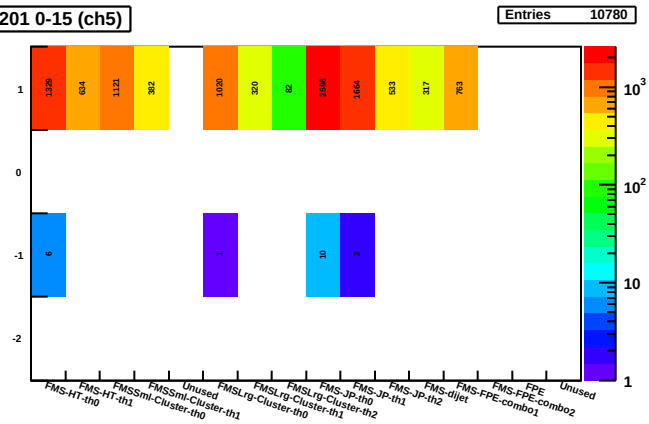
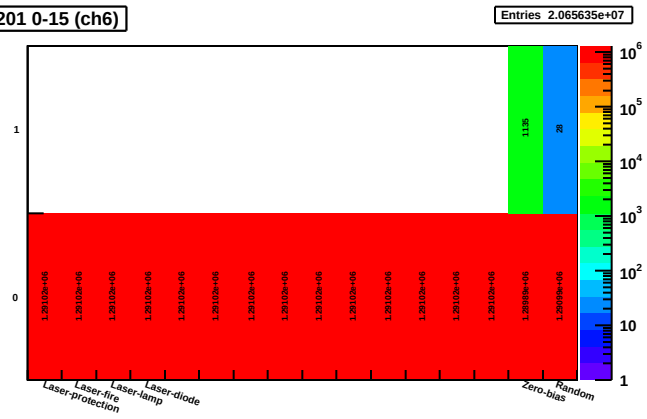
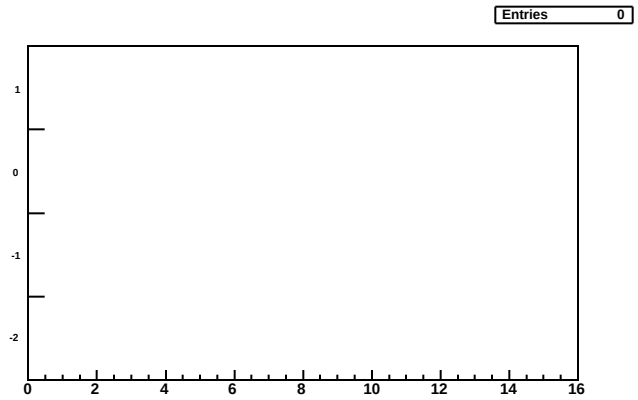
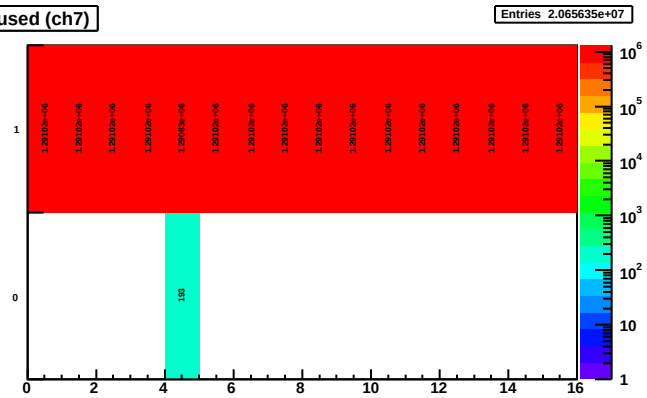
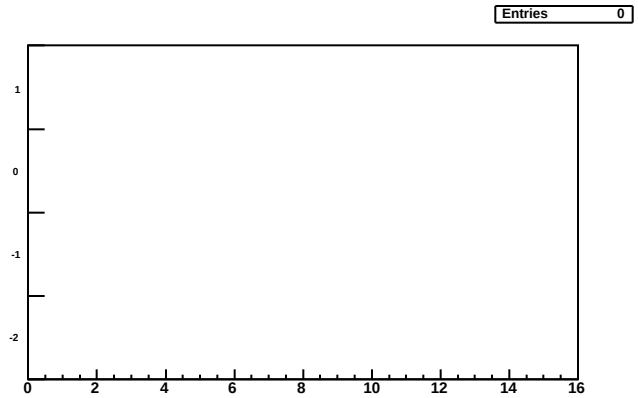


ST201 0-15 (ch6)





Unused (ch7)