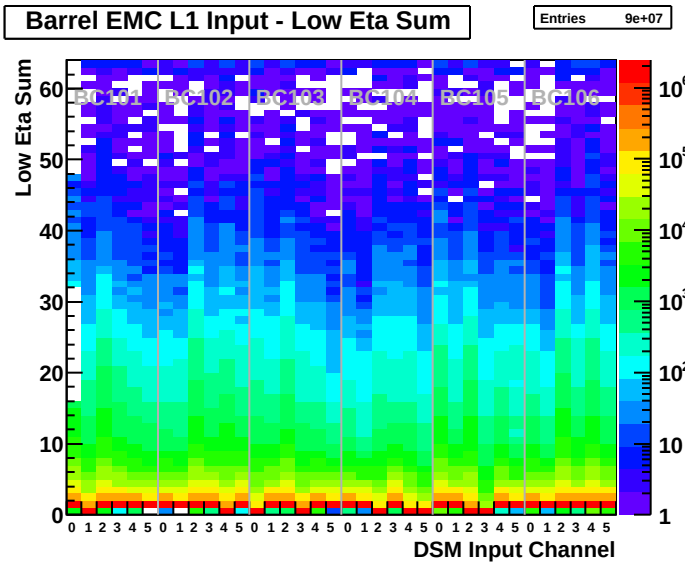
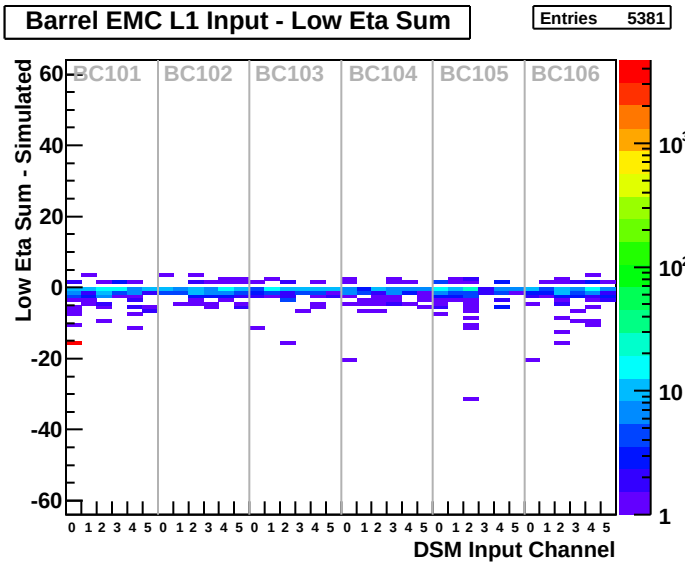
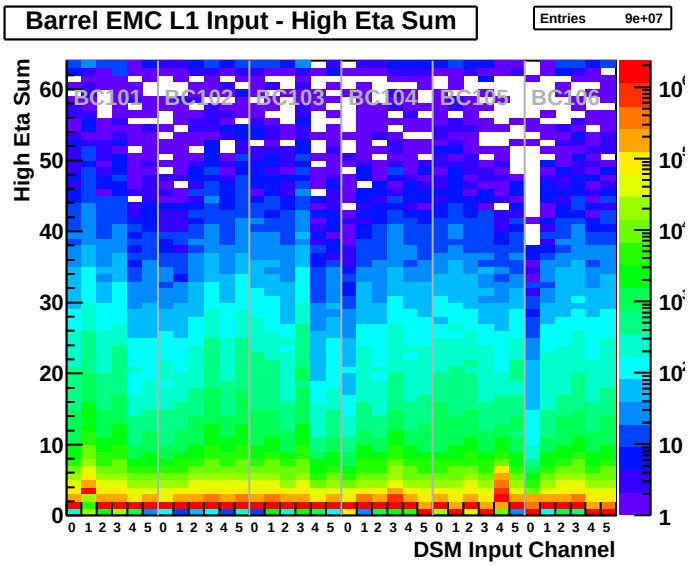
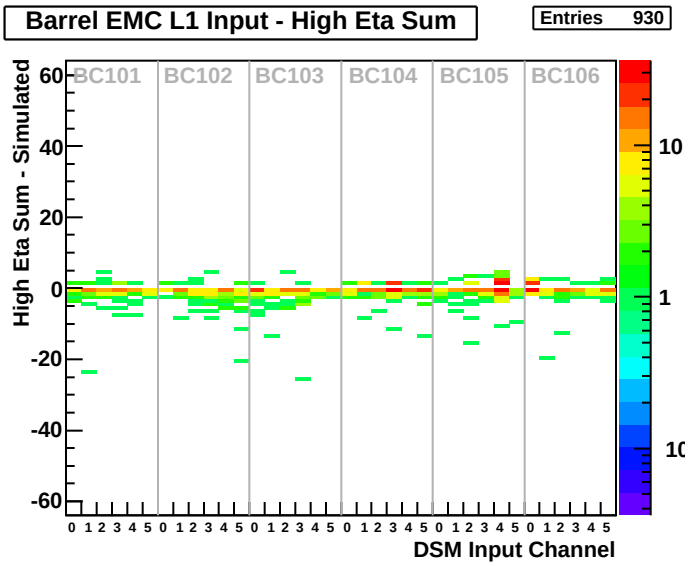
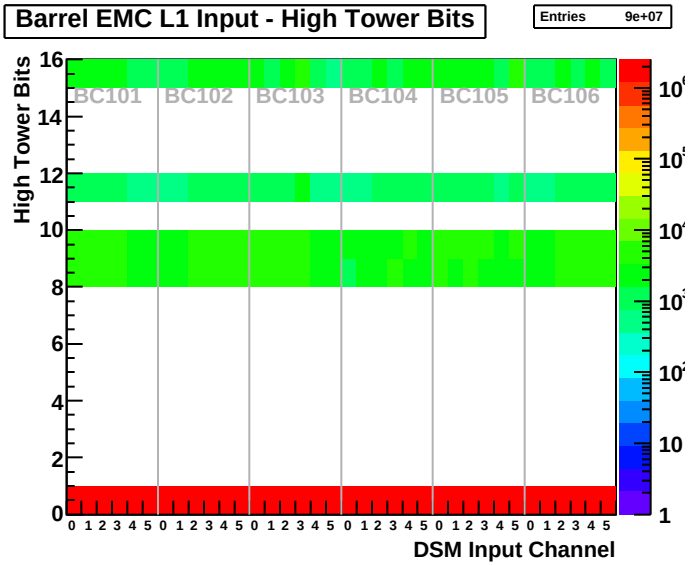
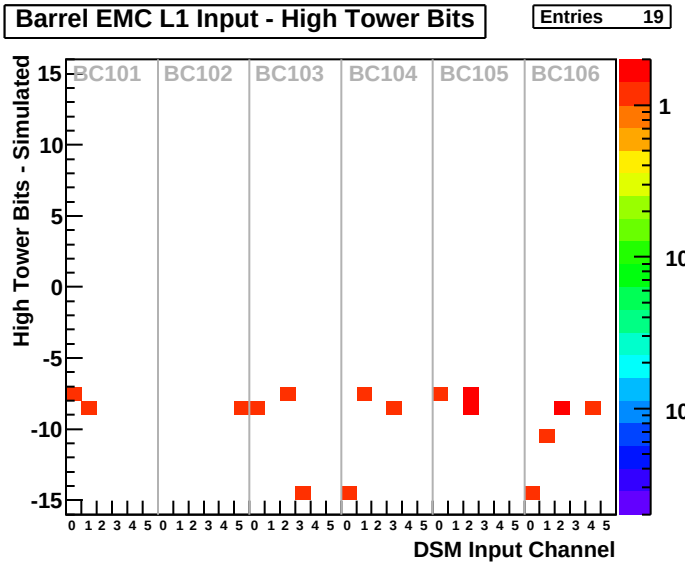

Barrel EMC L1 Input - Low Eta Sum
Entries 9e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

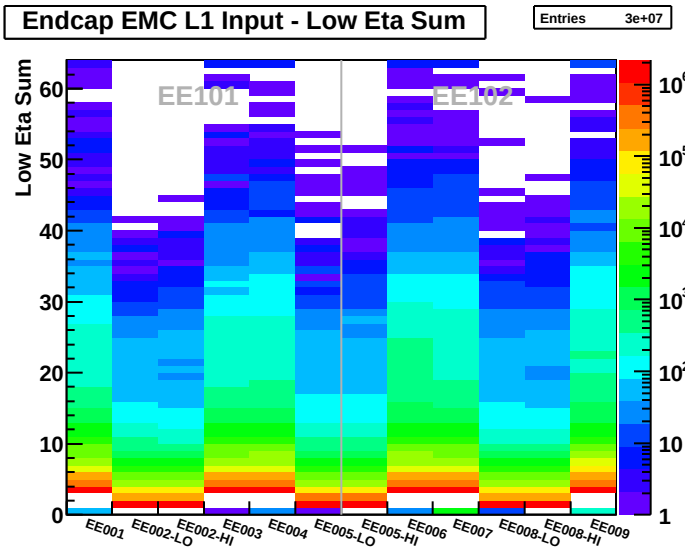

Barrel EMC L1 Input - Low Eta Sum
Entries 5381
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

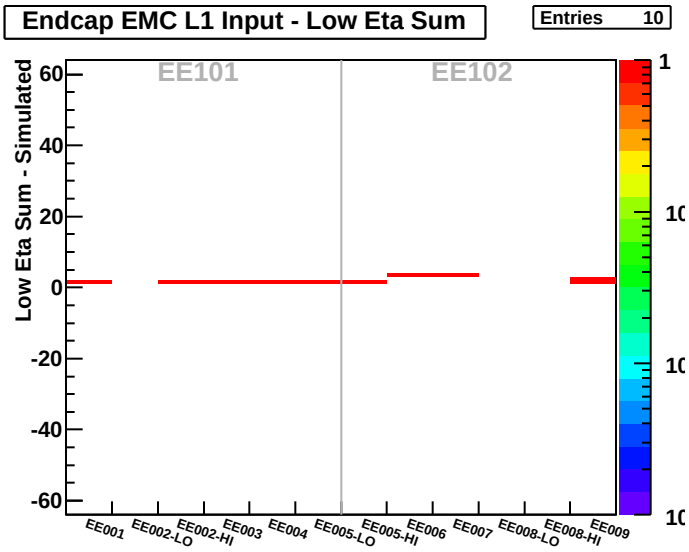

Barrel EMC L1 Input - High Eta Sum
Entries 9e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

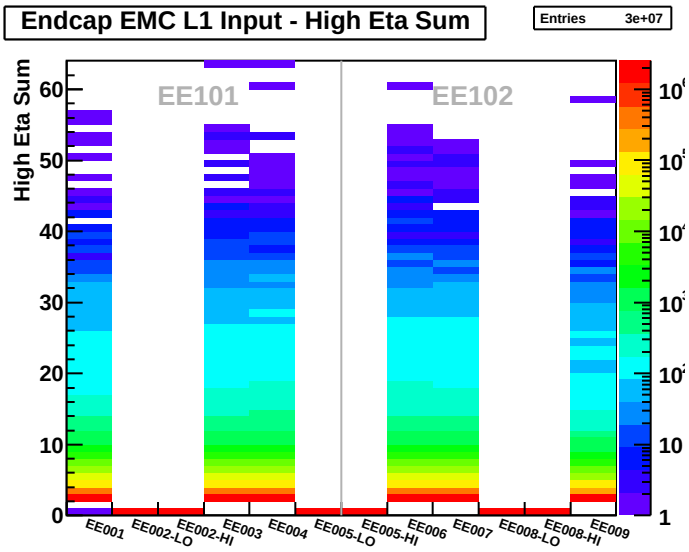

Barrel EMC L1 Input - High Eta Sum
Entries 930
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

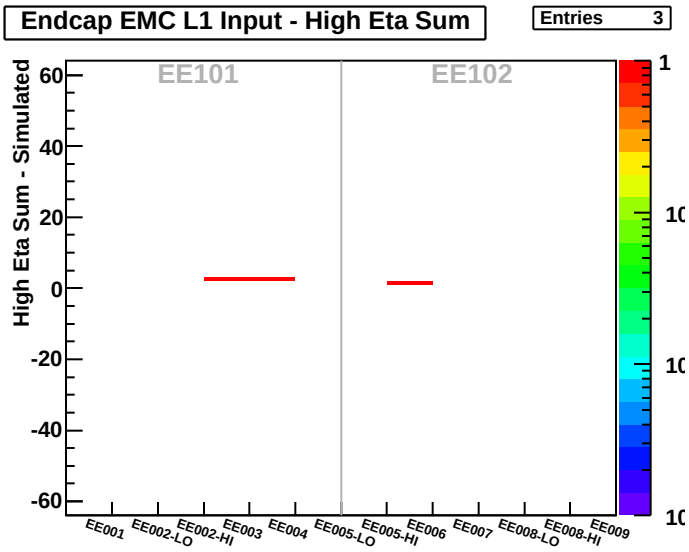

Barrel EMC L1 Input - High Tower Bits
Entries 9e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

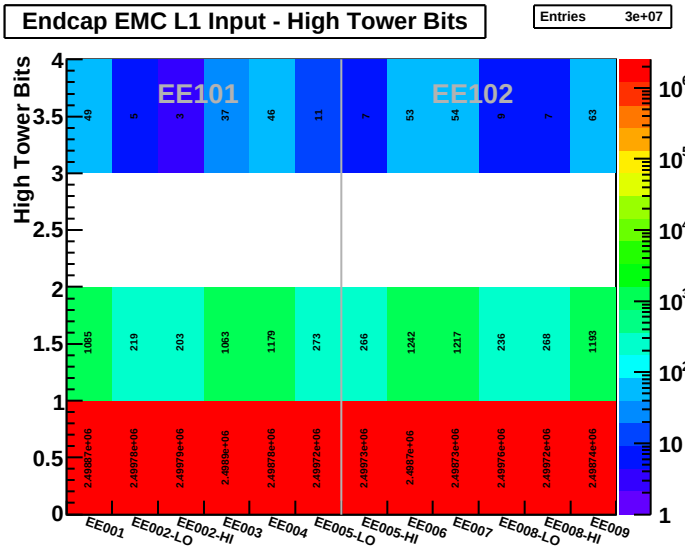

Barrel EMC L1 Input - High Tower Bits
Entries 19
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

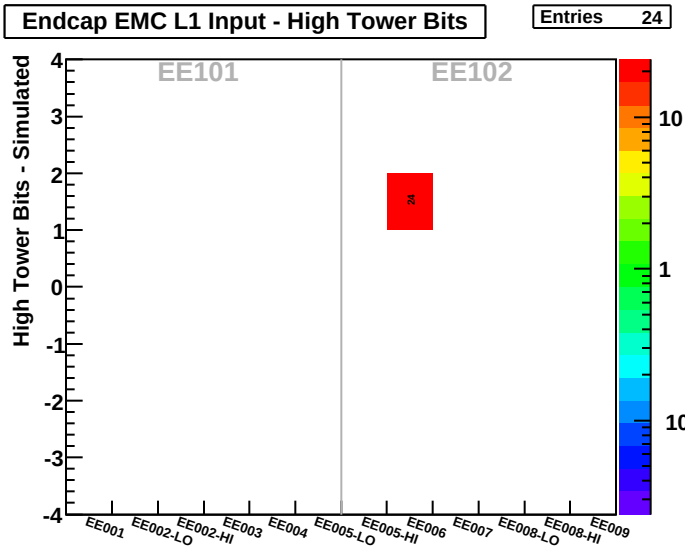

Endcap EMC L1 Input - Low Eta Sum
Entries 3e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

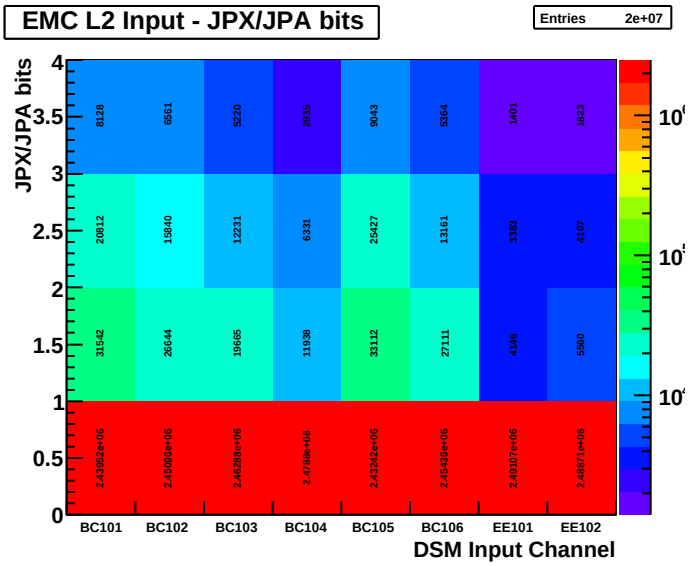

Endcap EMC L1 Input - Low Eta Sum
Entries 10
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

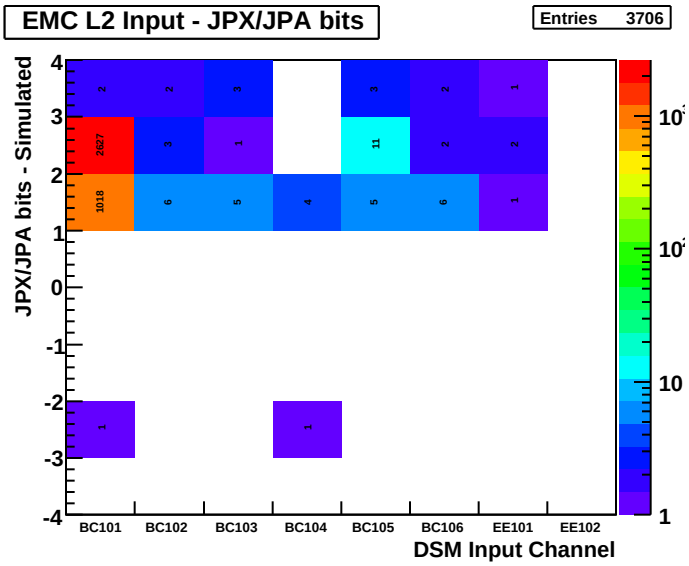

Endcap EMC L1 Input - High Eta Sum
Entries 3e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

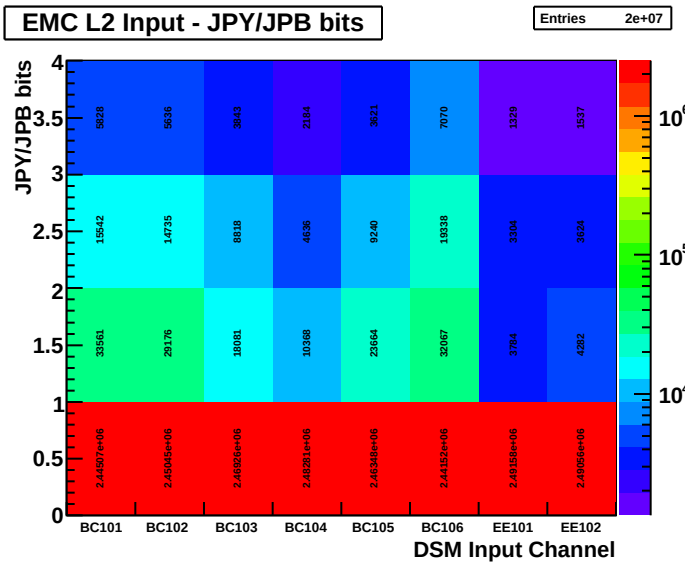

Endcap EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

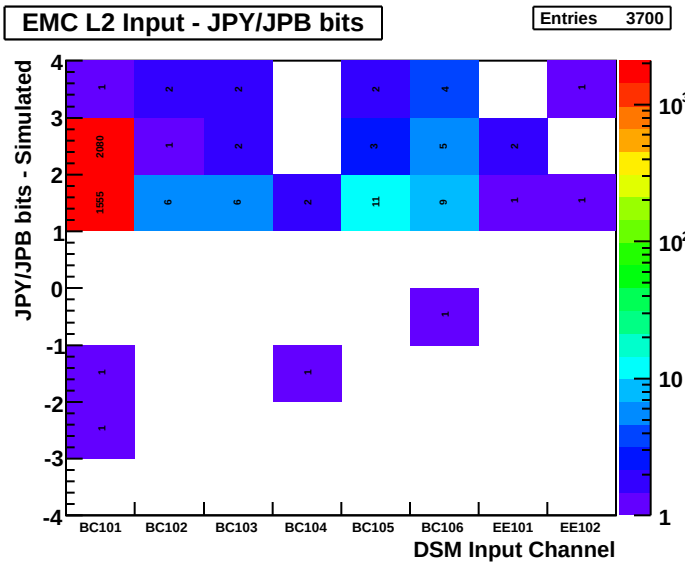

Endcap EMC L1 Input - High Tower Bits
Entries 3e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

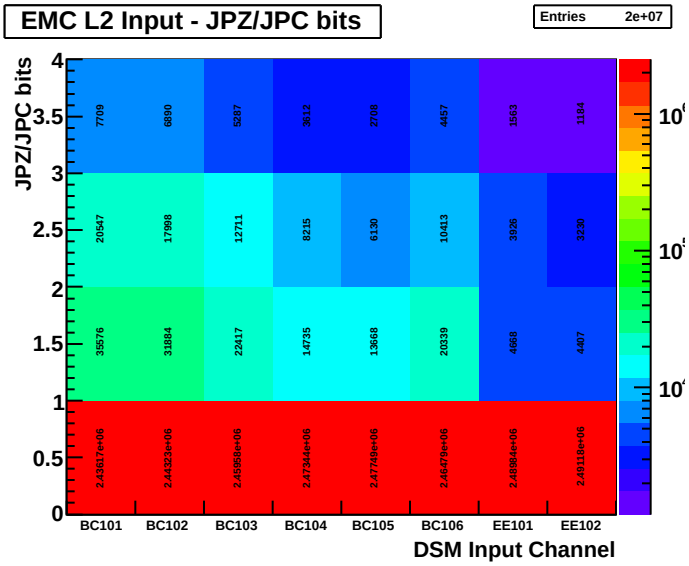

Endcap EMC L1 Input - High Tower Bits
Entries 24
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

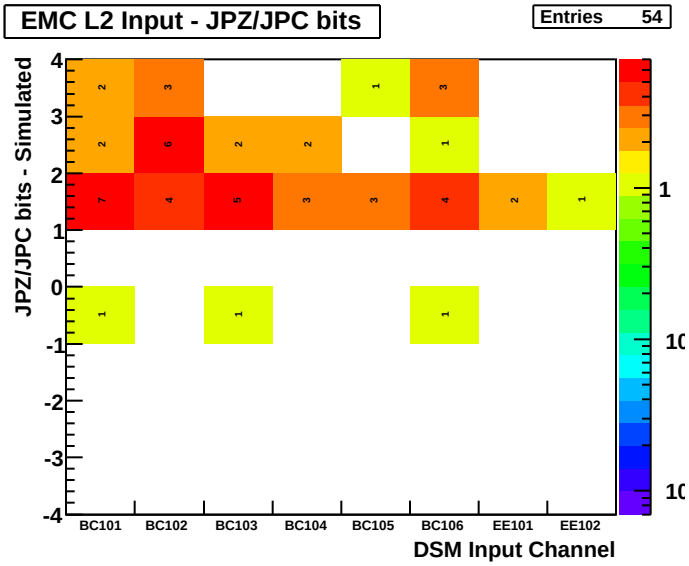

EMC L2 Input - JPX/JPA bits
Entries 2e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

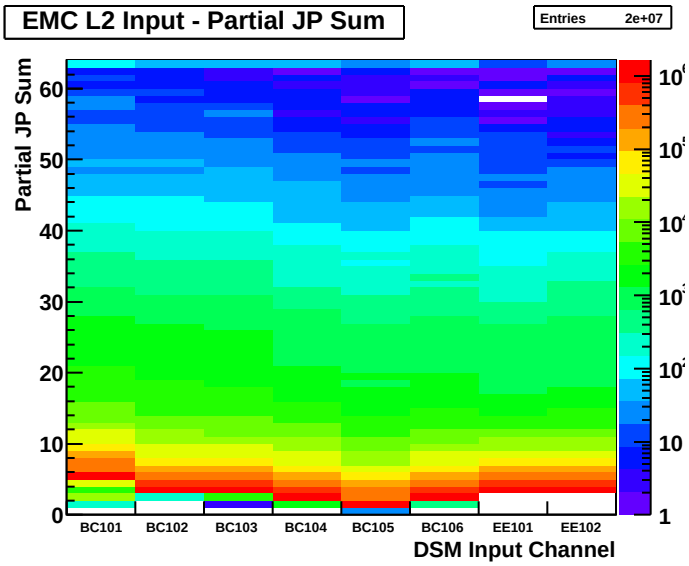

EMC L2 Input - JPX/JPA bits
Entries 3706
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

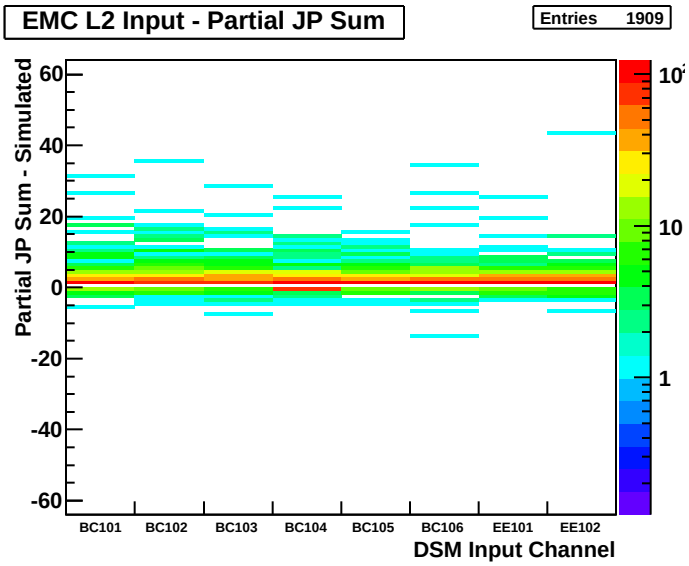

EMC L2 Input - JPY/JPB bits
Entries 2e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

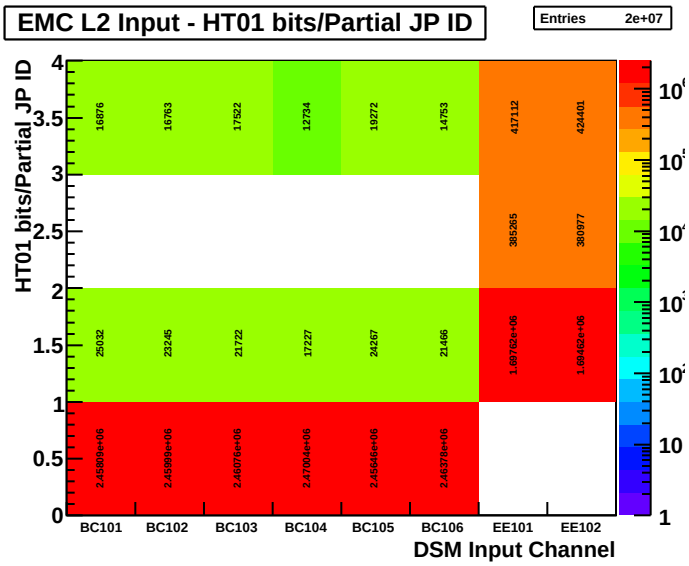

EMC L2 Input - JPY/JPB bits
Entries 3700
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

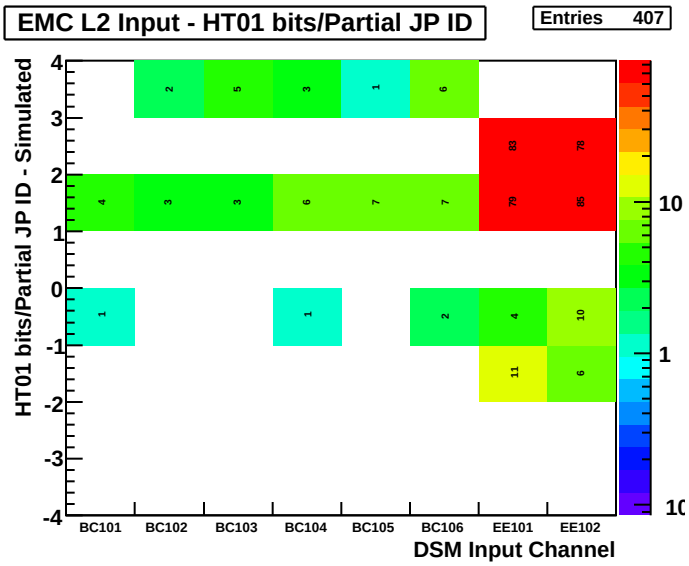

EMC L2 Input - JPZ/JPC bits
Entries 2e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

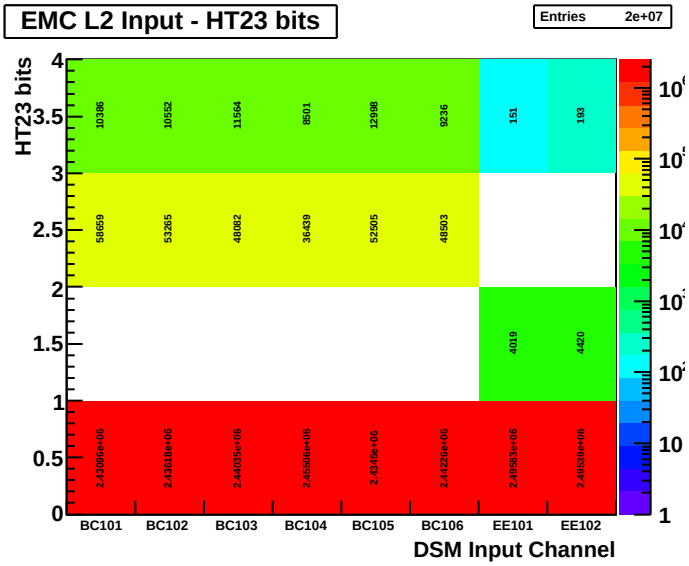

EMC L2 Input - JPZ/JPC bits
Entries 54
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

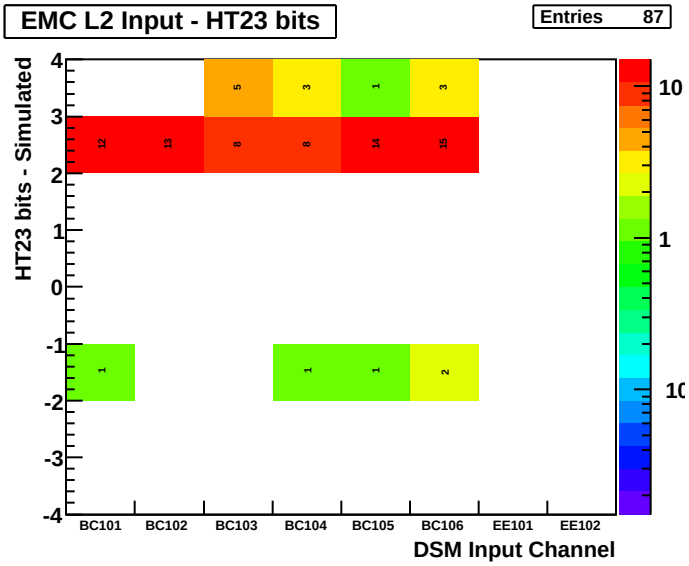

EMC L2 Input - Partial JP Sum
Entries 2e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

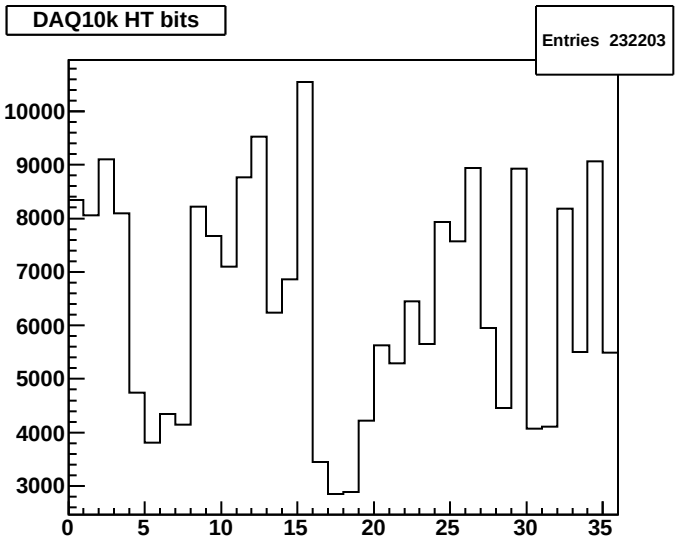

EMC L2 Input - Partial JP Sum
Entries 1909
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

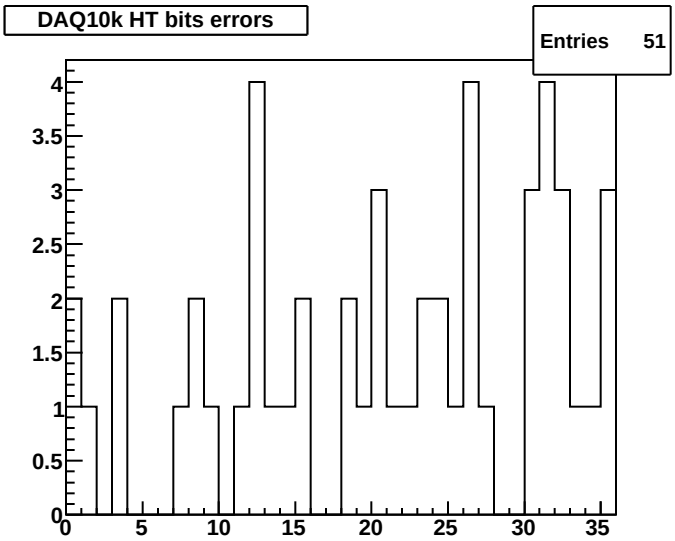

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

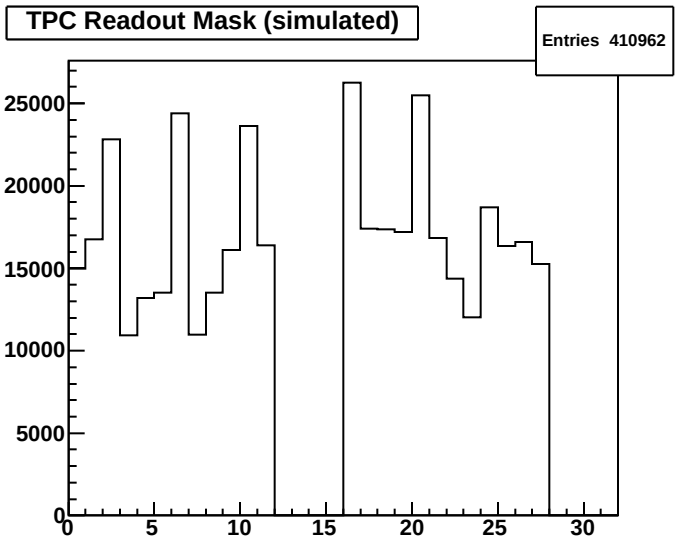

EMC L2 Input - HT01 bits/Partial JP ID
Entries 407
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

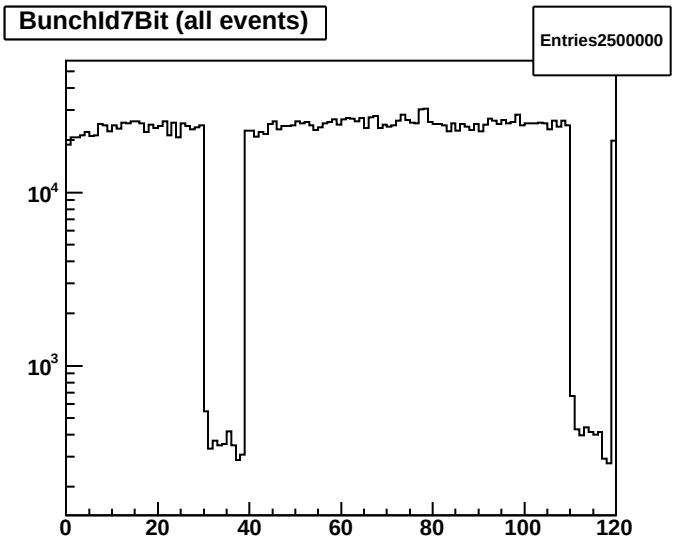

EMC L2 Input - HT23 bits
Entries 2e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

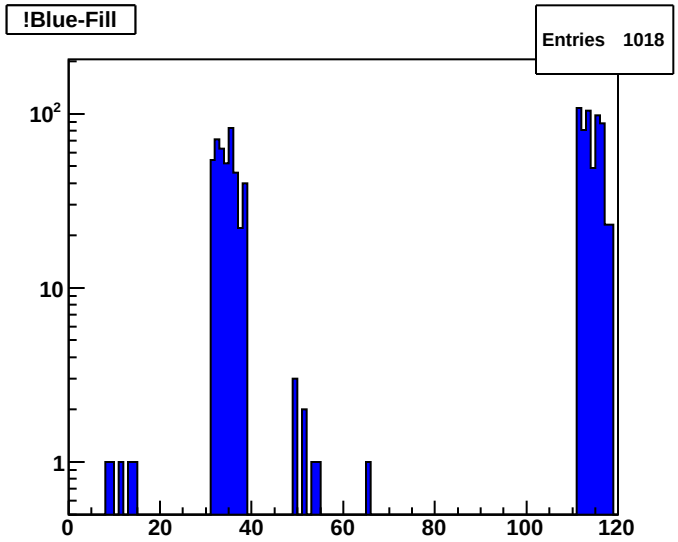

EMC L2 Input - HT23 bits
Entries 87
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

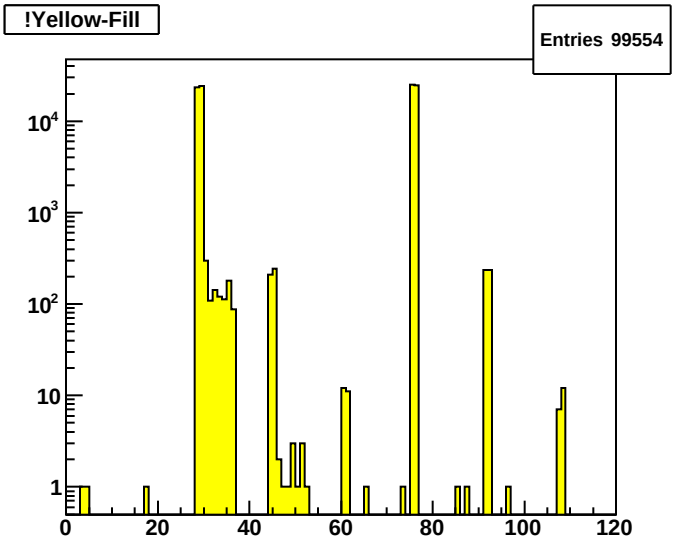

DAQ10k HT bits
Entries 232203

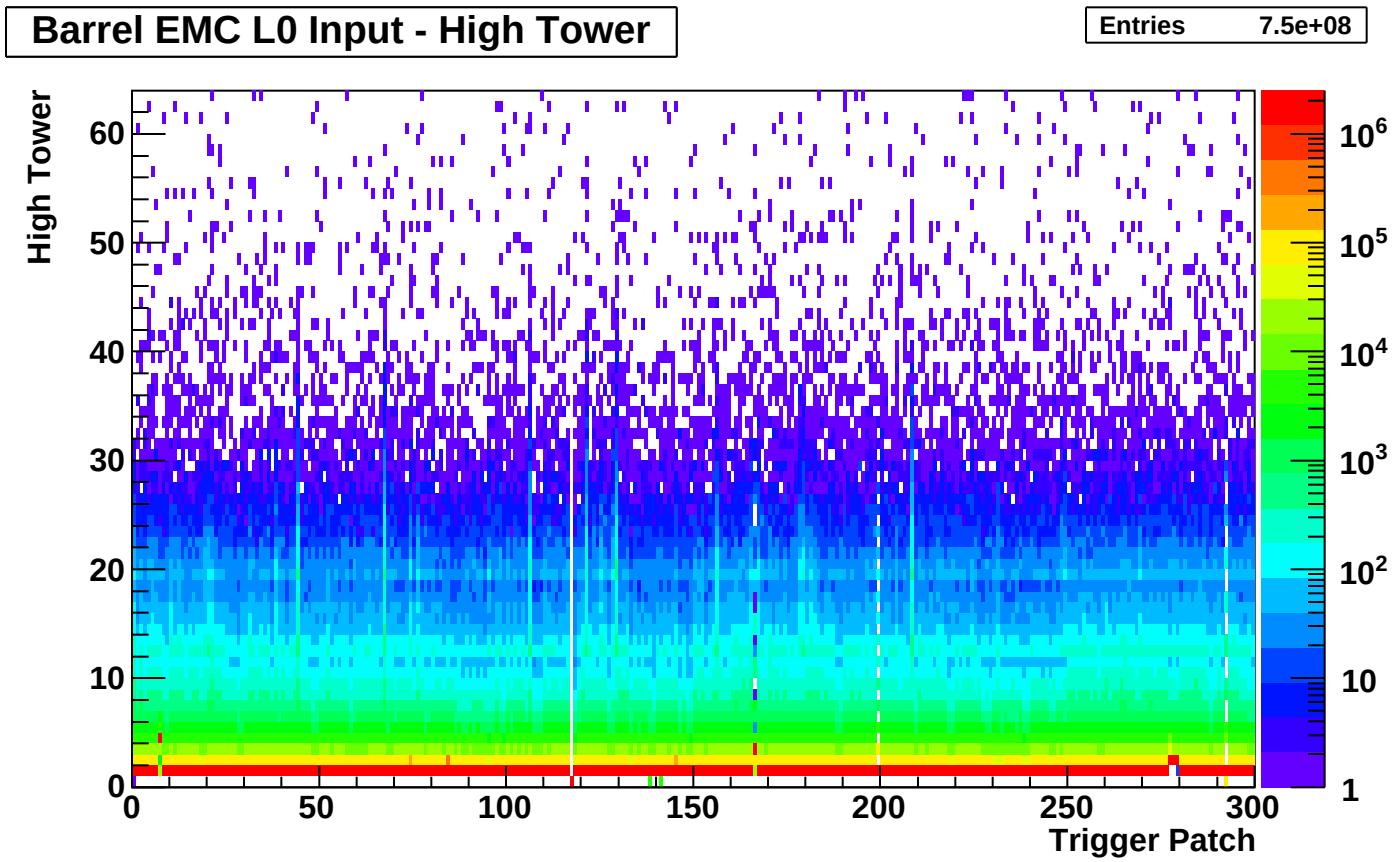

DAQ10k HT bits errors
Entries 51

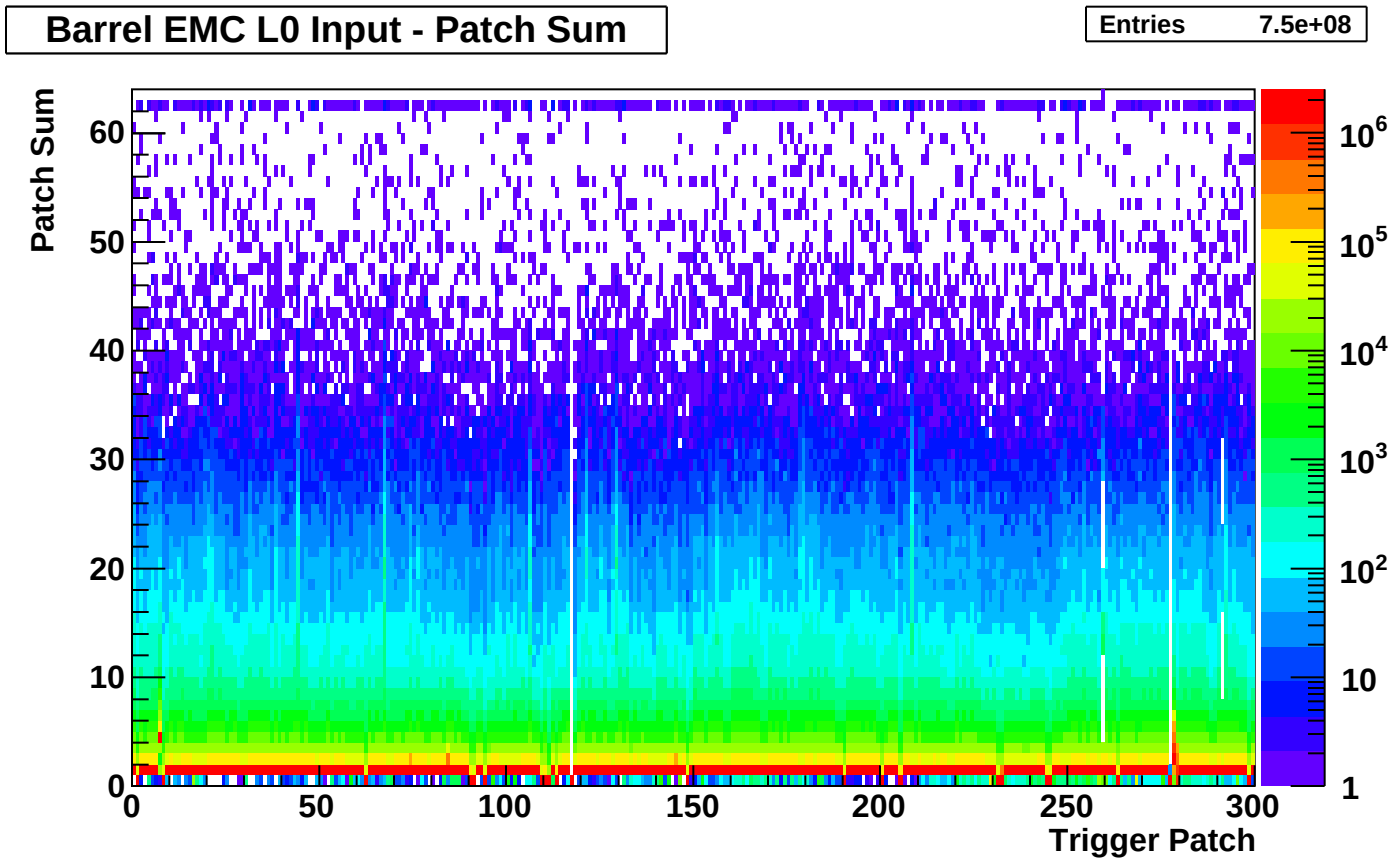

TPC Readout Mask (simulated)
Entries 410962

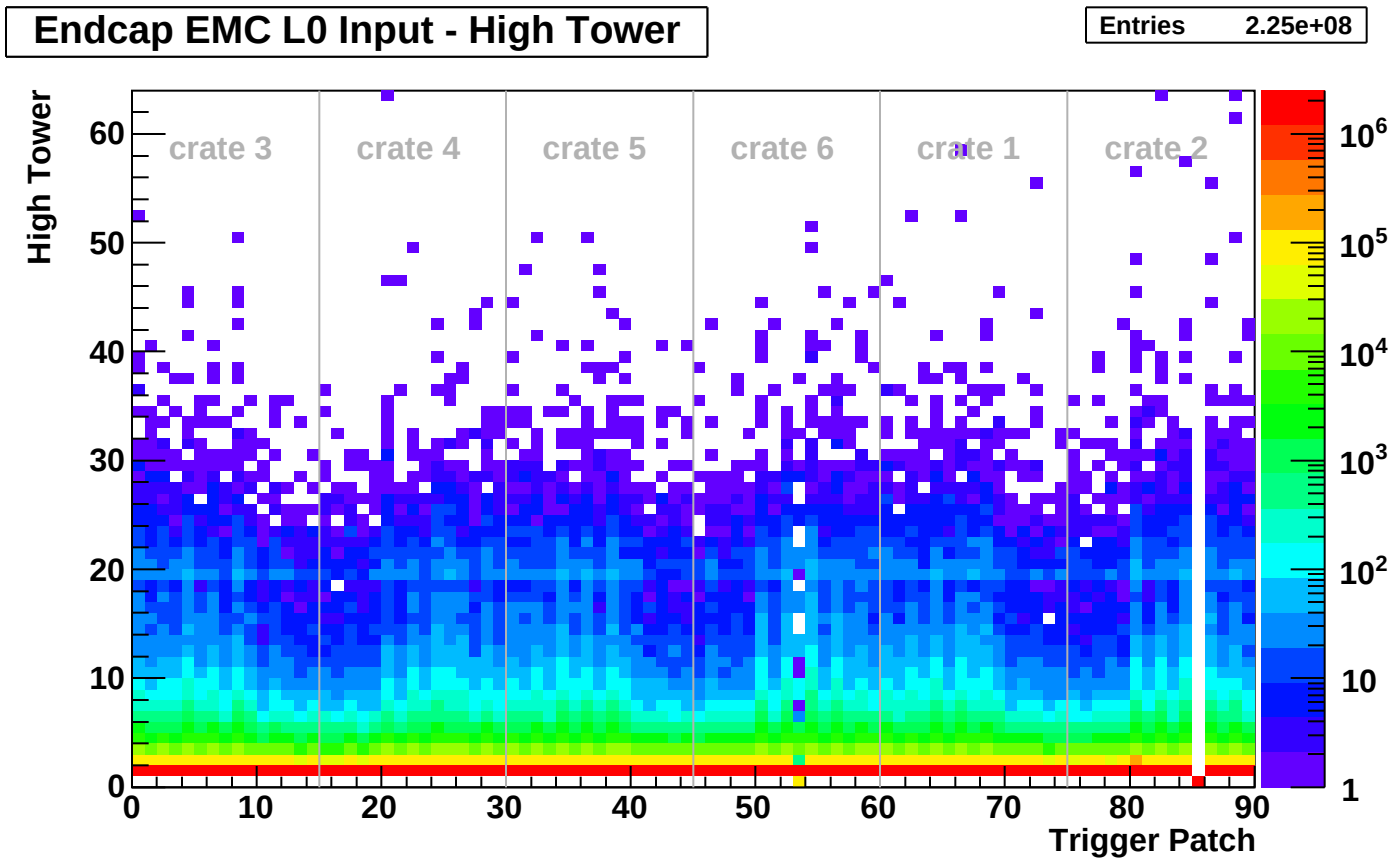

BunchId7Bit (all events)
Entries2500000

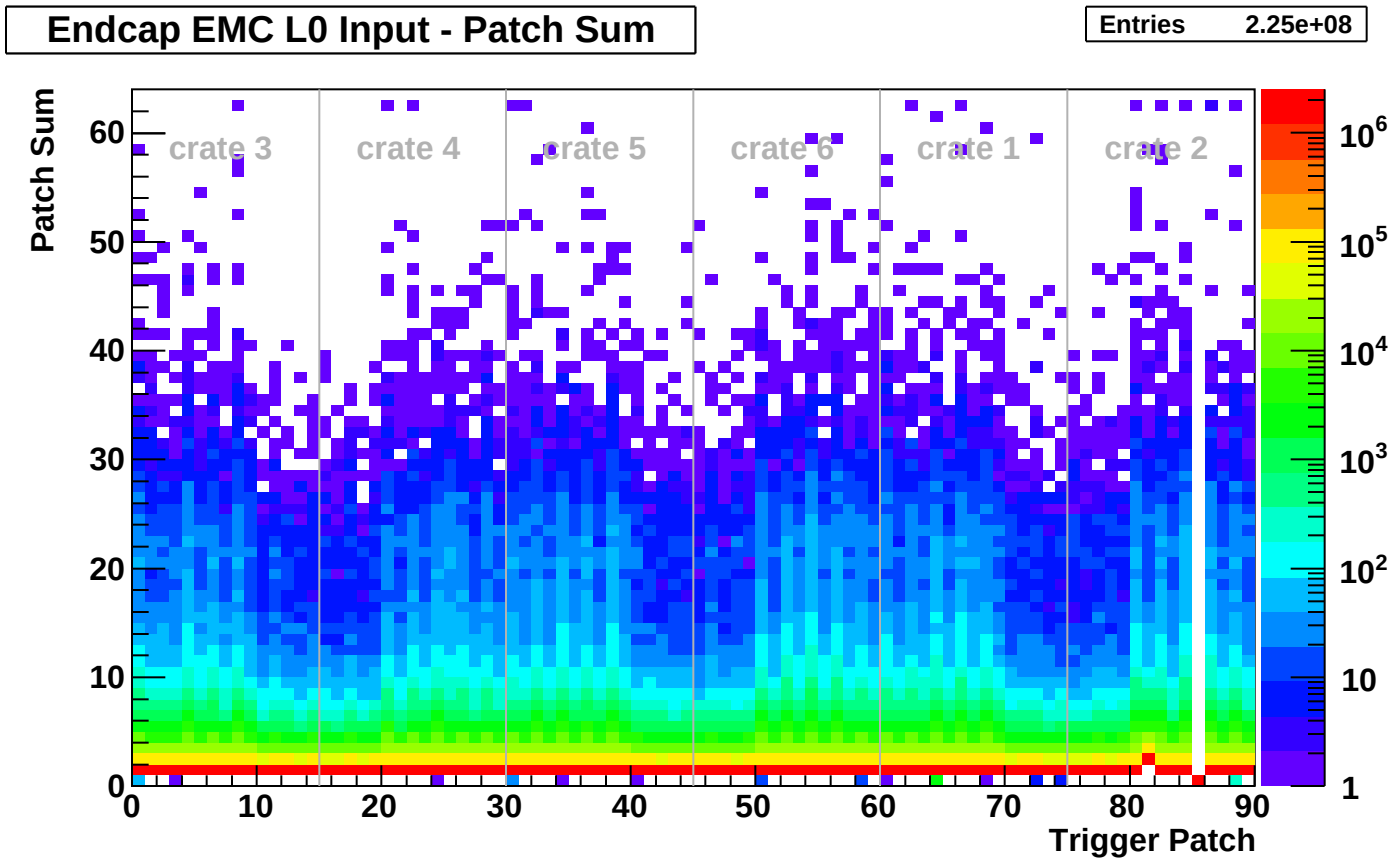

!Blue-Fill
Entries 1018

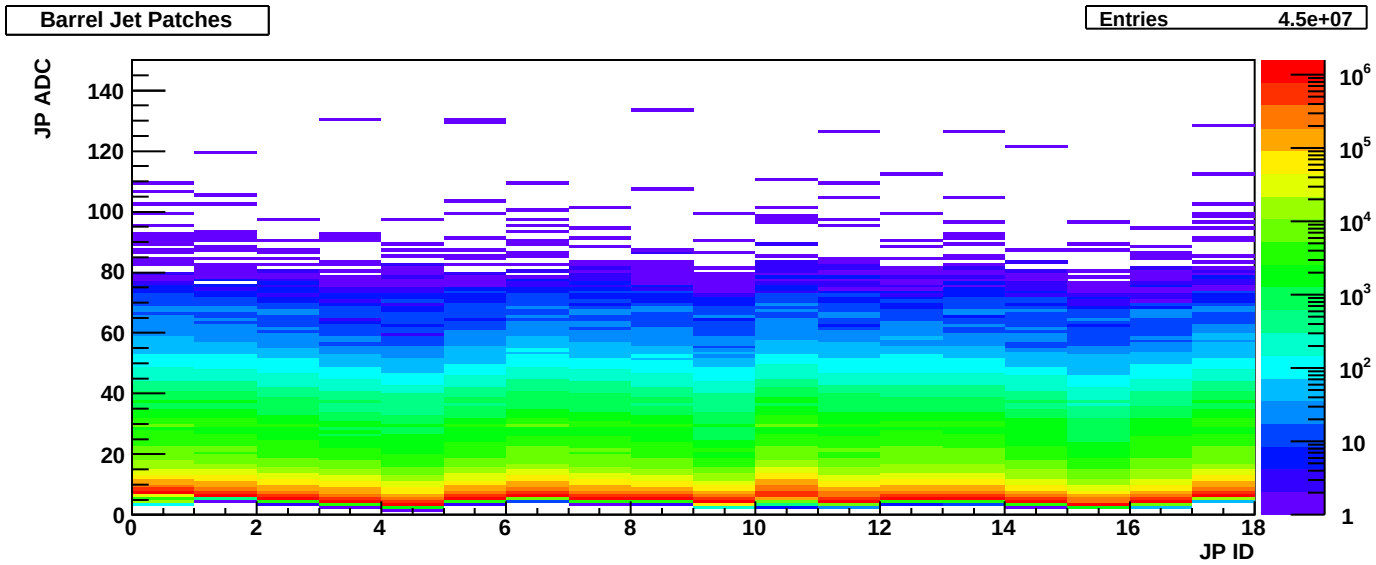

!Yellow-Fill
Entries 99554

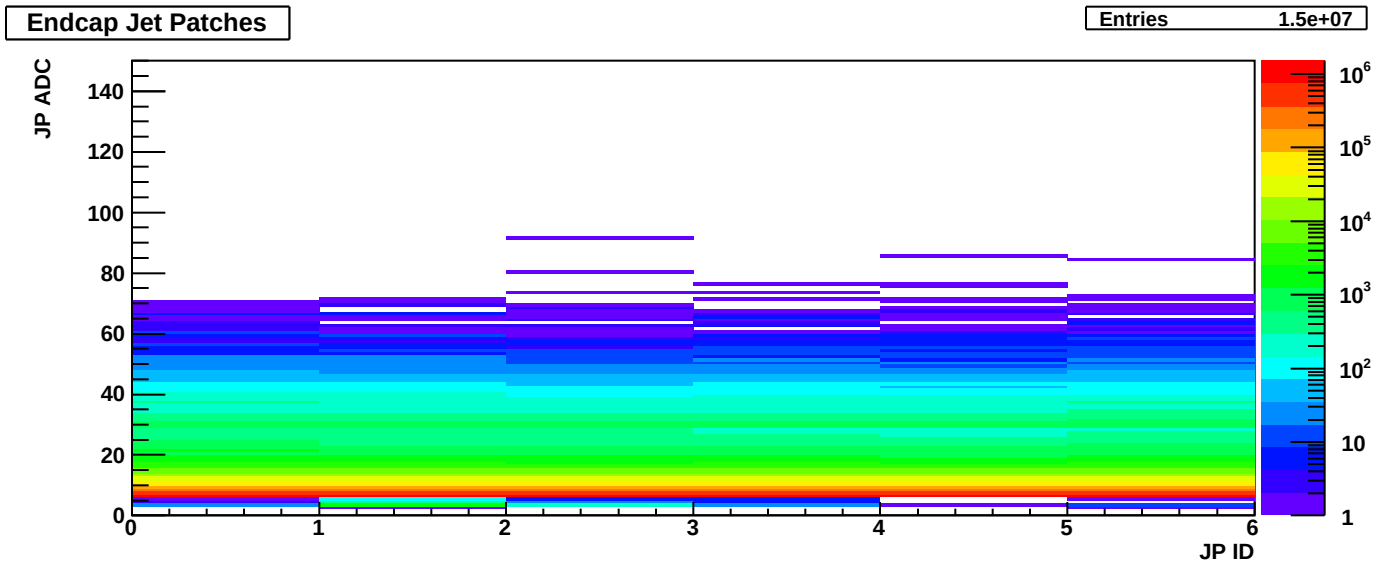

Barrel EMC L0 Input - High Tower
Entries 7.5e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

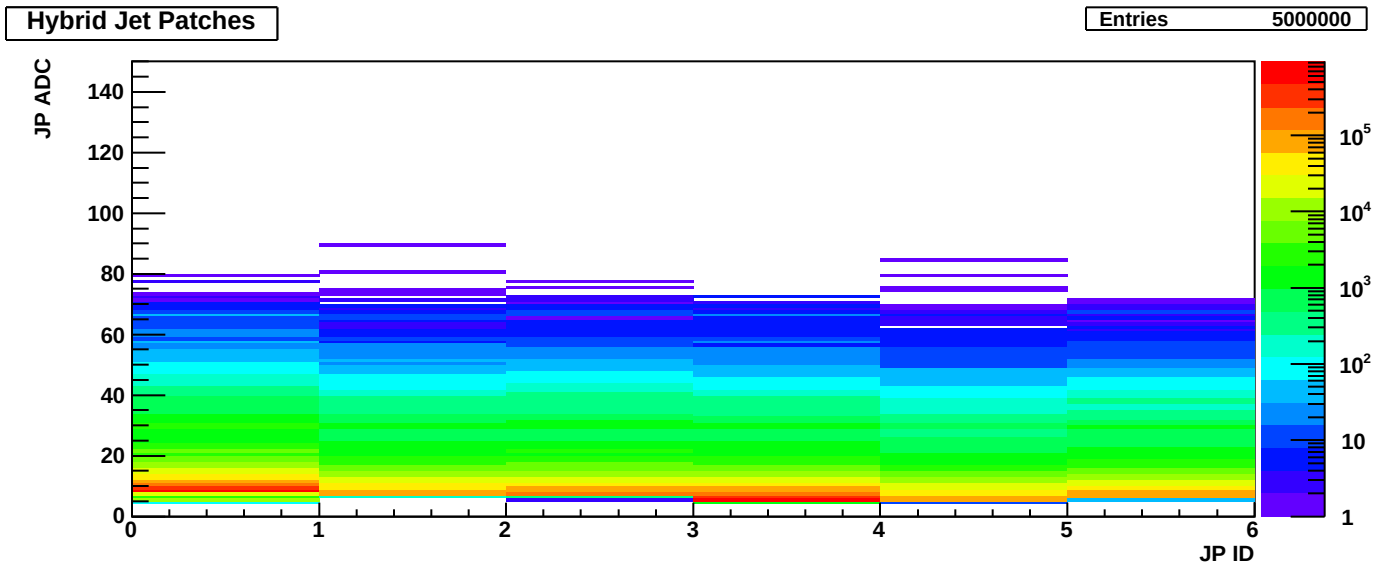

Barrel EMC L0 Input - Patch Sum
Entries 7.5e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

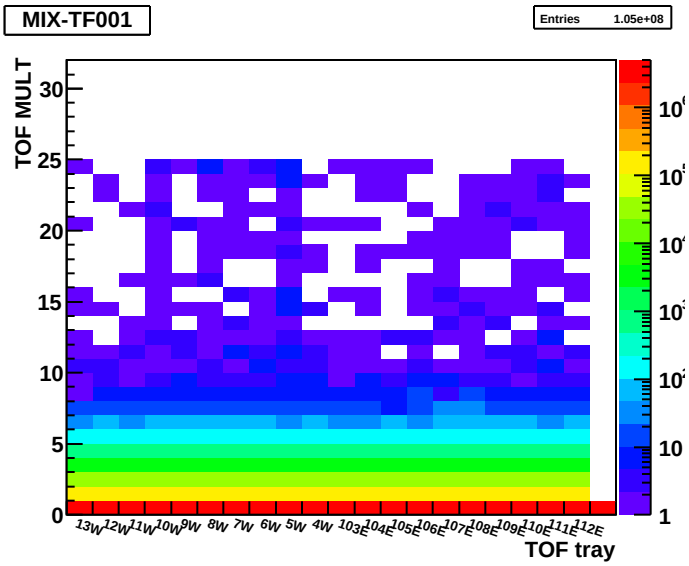

Endcap EMC L0 Input - High Tower
Entries 2.25e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

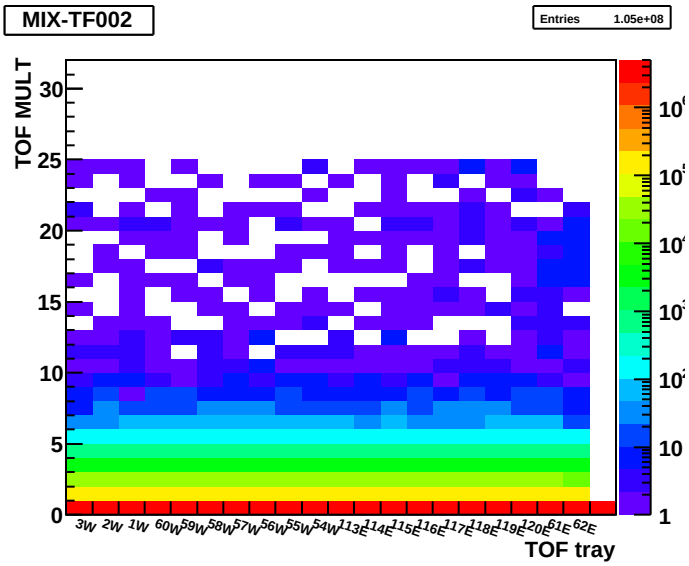

Endcap EMC L0 Input - Patch Sum
Entries 2.25e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

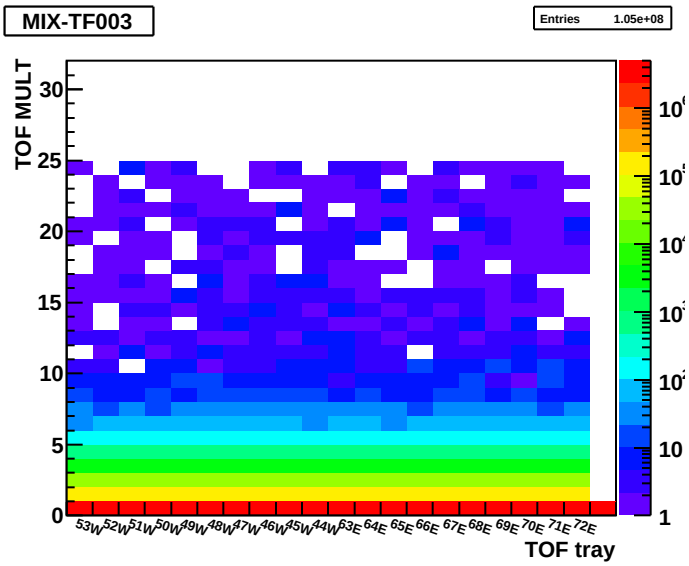

Barrel Jet Patches
Entries 4.5e+07
JP ADC
JP ID

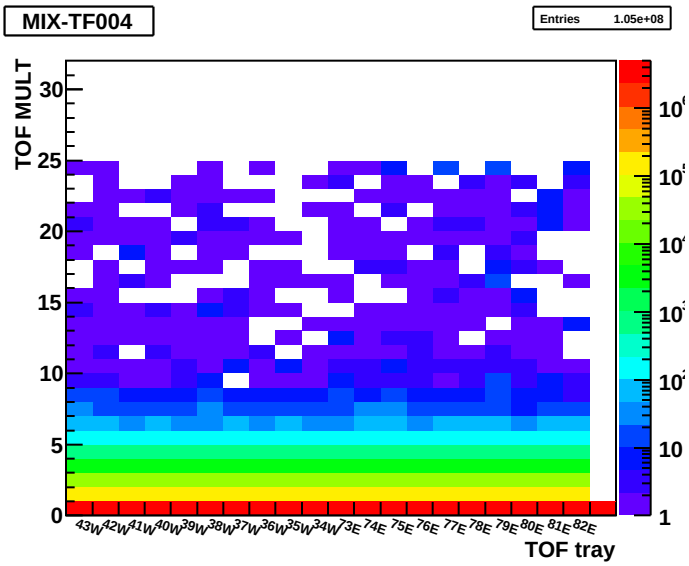

Endcap Jet Patches
Entries 1.5e+07
JP ADC
JP ID

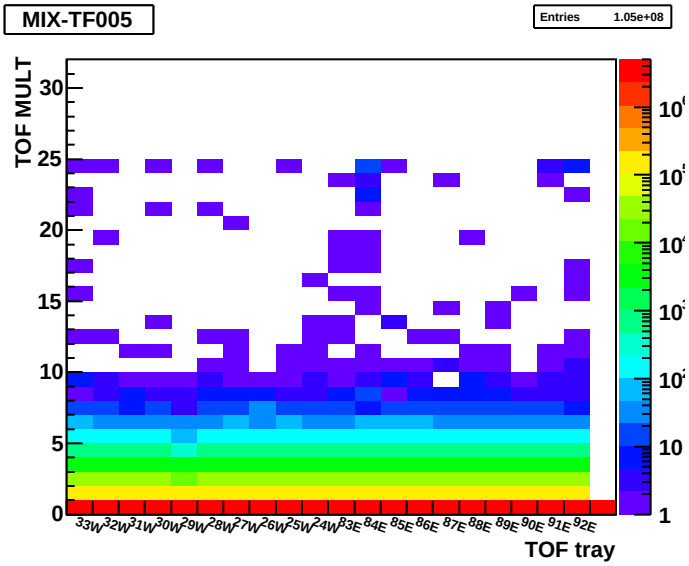

Hybrid Jet Patches
Entries 5000000
JP ADC
JP ID

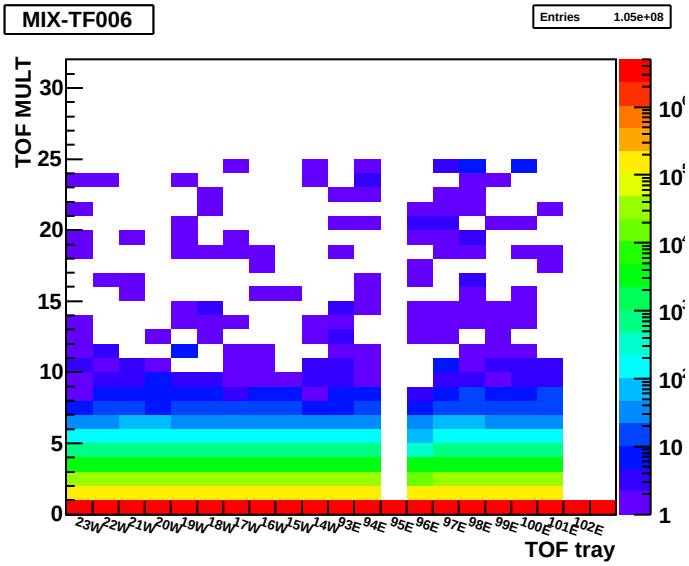

MIX-TF001
Entries 1.05e+08
TOF MULT
TOF tray

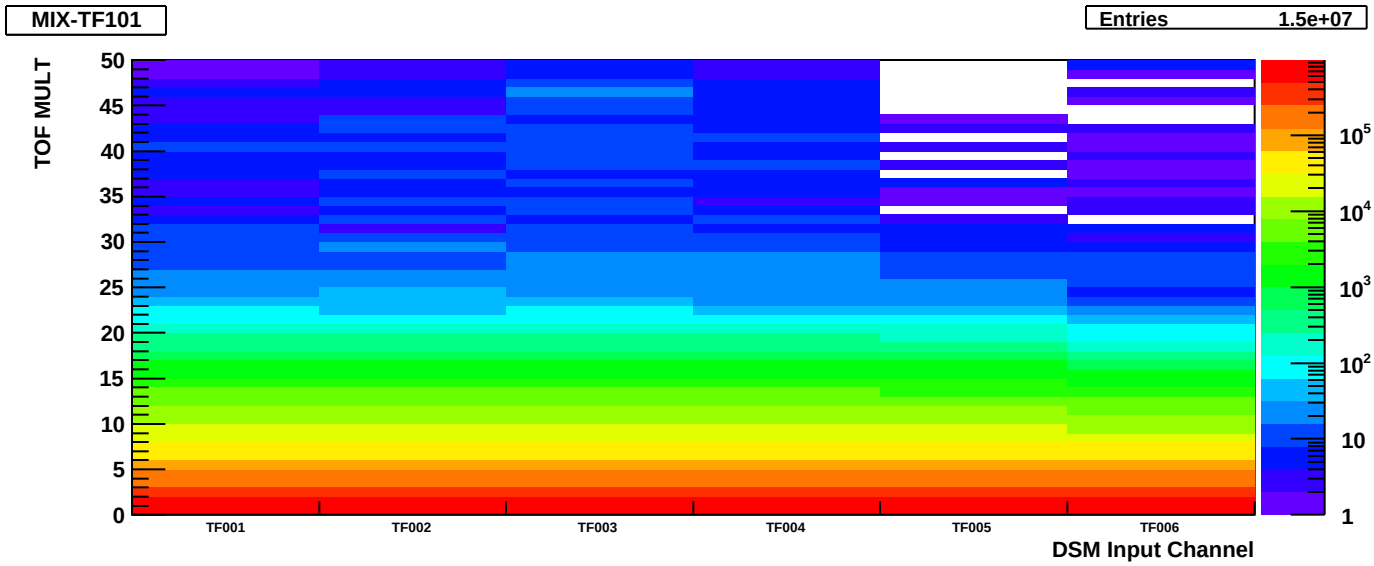

MIX-TF002
Entries 1.05e+08
TOF MULT
TOF tray

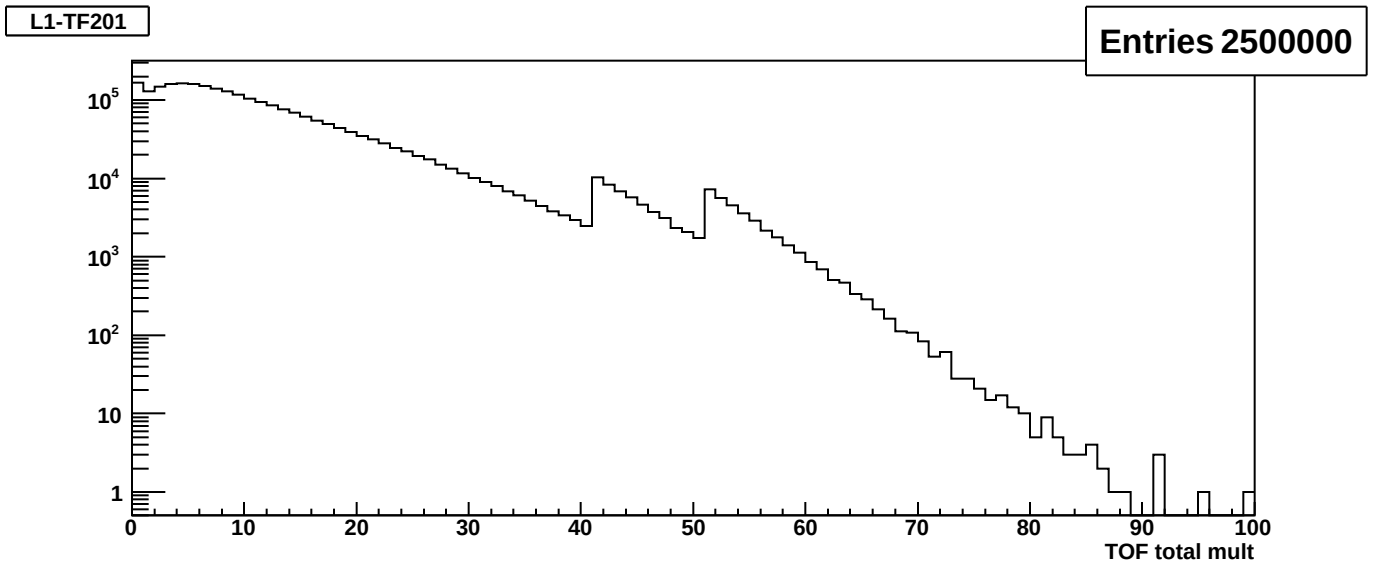

MIX-TF003
Entries 1.05e+08
TOF MULT
TOF tray

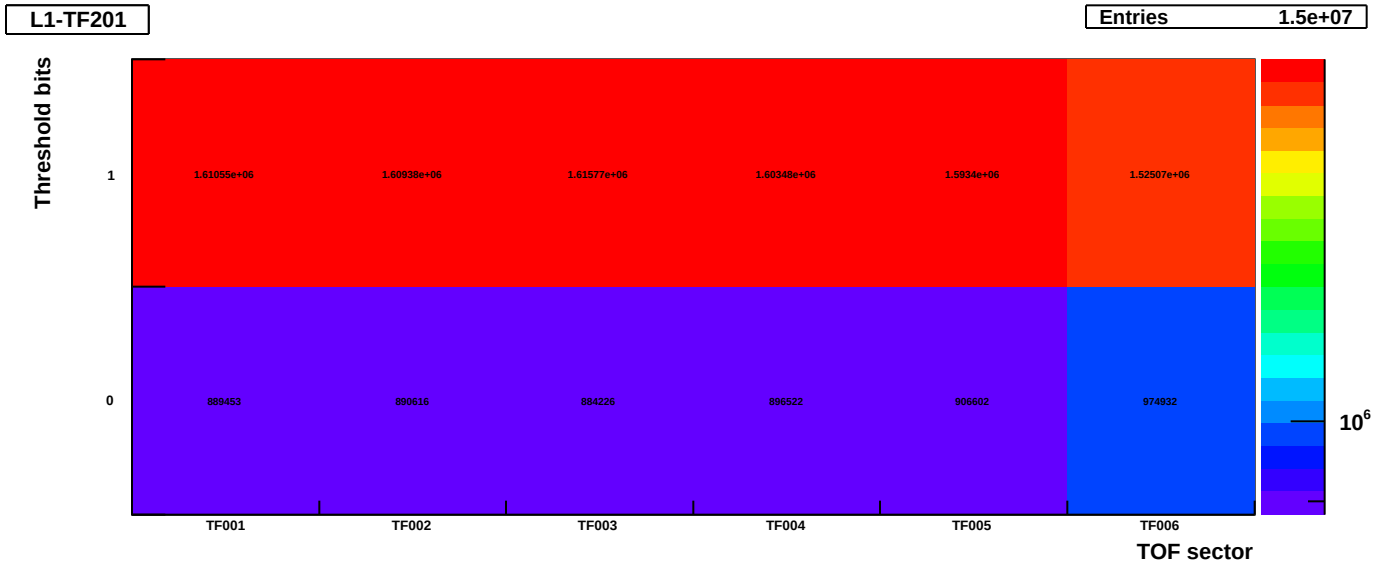

MIX-TF004
Entries 1.05e+08
TOF MULT
TOF tray

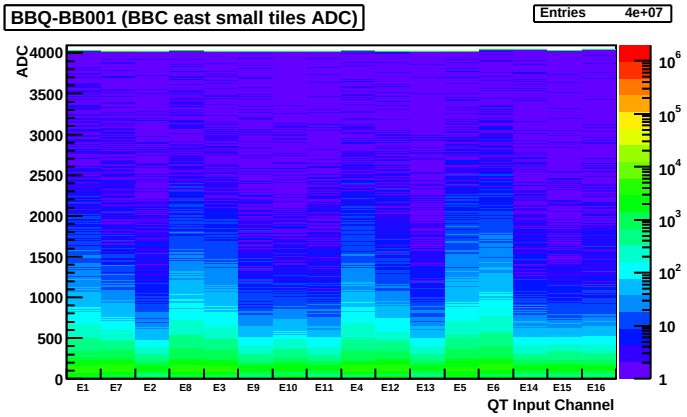

MIX-TF005
Entries 1.05e+08
TOF MULT
TOF tray

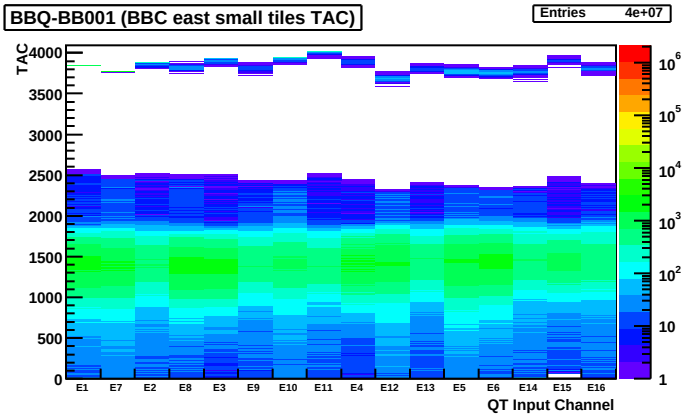

MIX-TF006
Entries 1.05e+08
TOF MULT
TOF tray

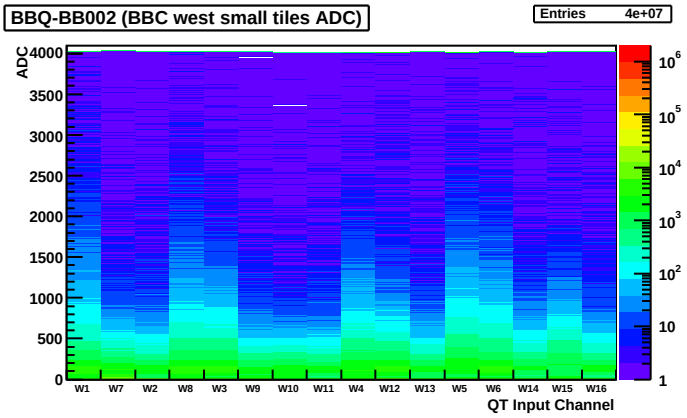

MIX-TF101
Entries 1.5e+07
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

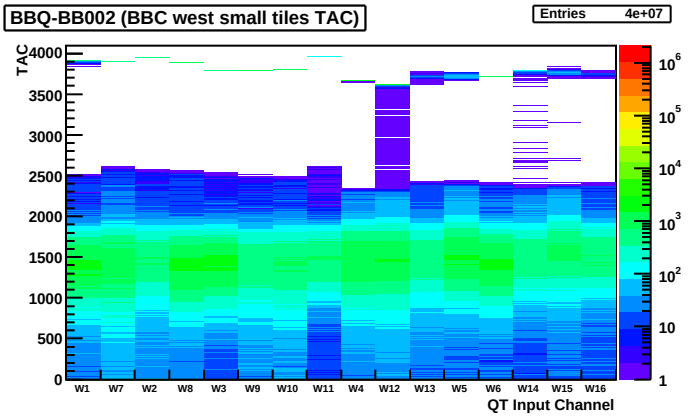

L1-TF201
Entries 2500000
TOF total mult

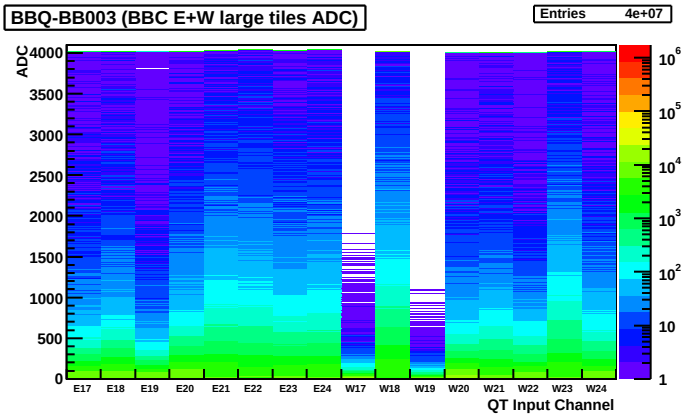

L1-TF201
Entries 1.5e+07
Threshold bits
1.61055e+06
1.60938e+06
1.61577e+06
1.60348e+06
1.5934e+06
1.52507e+06
889453
890616
884226
896522
906602
974932
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

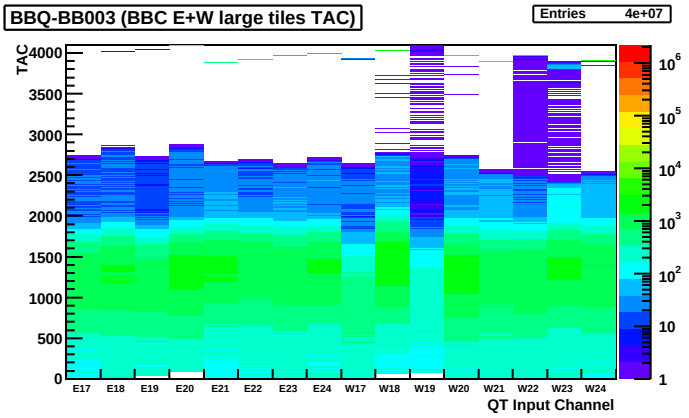

BBQ-BB001 (BBC east small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

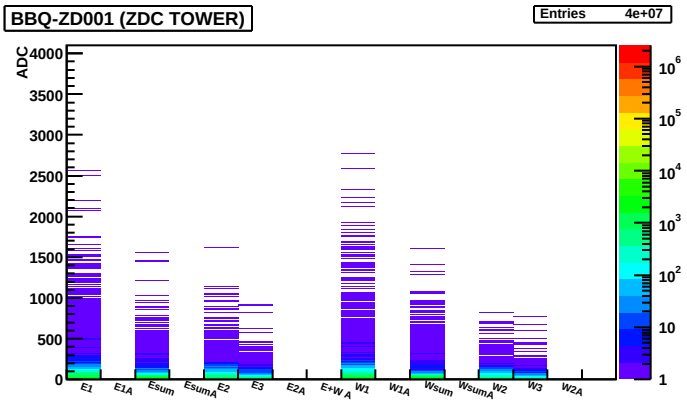

BBQ-BB001 (BBC east small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

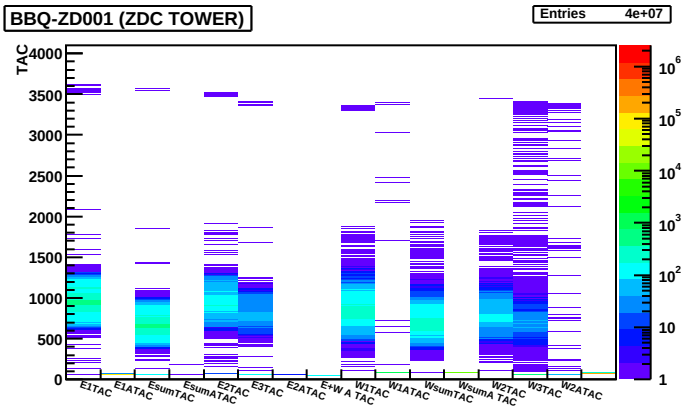

BBQ-BB002 (BBC west small tiles ADC)
Entries 4e+07
ADC
QT Input Channel

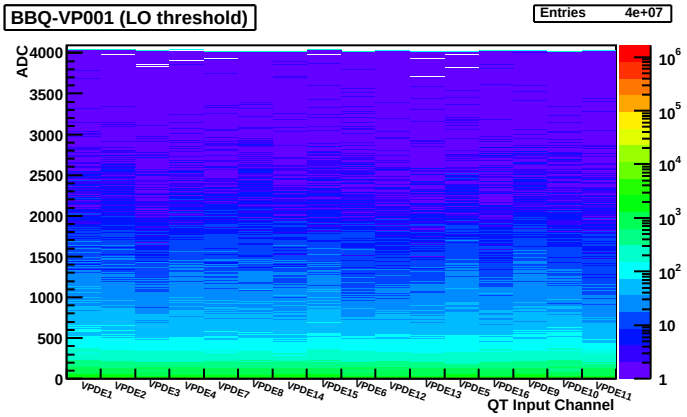

BBQ-BB002 (BBC west small tiles TAC)
Entries 4e+07
TAC
QT Input Channel

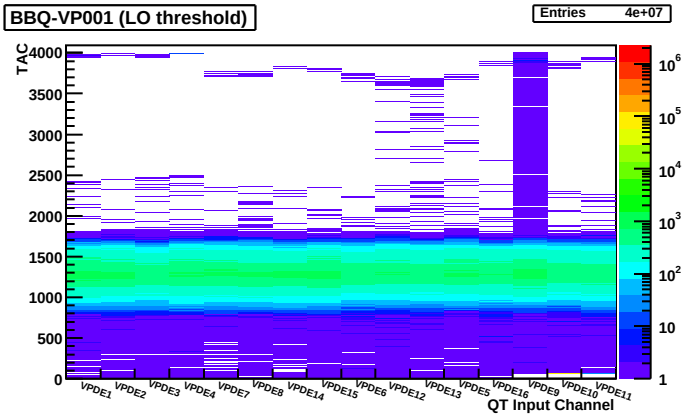

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4e+07
ADC
QT Input Channel

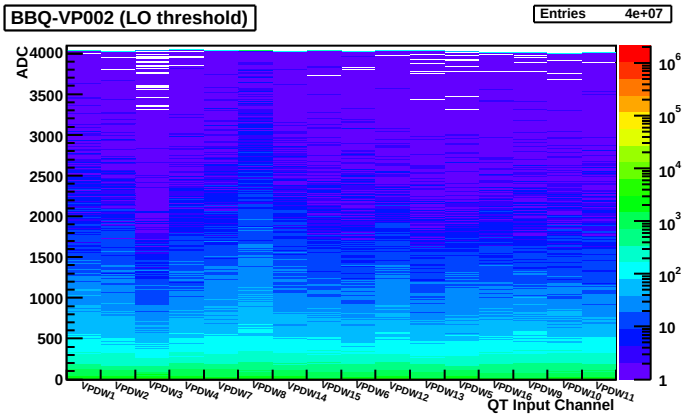

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4e+07
TAC
QT Input Channel

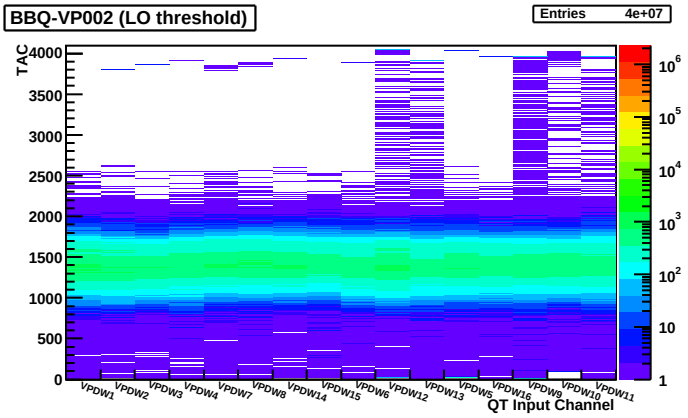

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
ADC

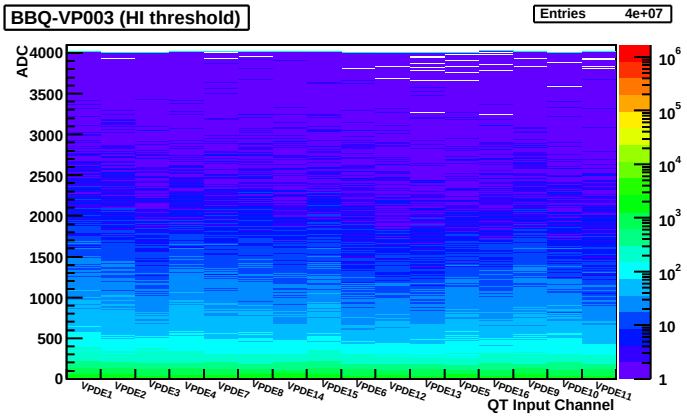

BBQ-ZD001 (ZDC TOWER)
Entries 4e+07
TAC

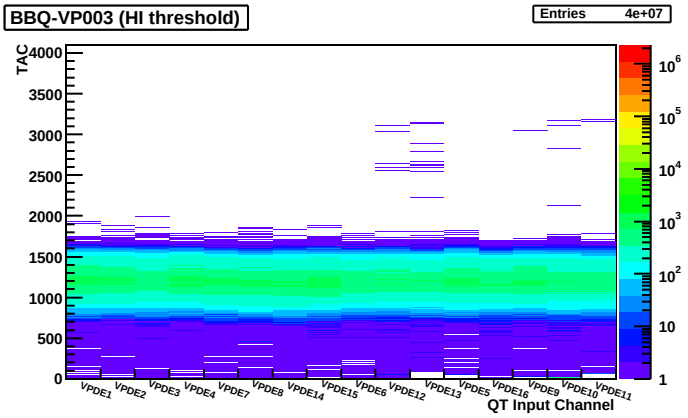

BBQ-VP001 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

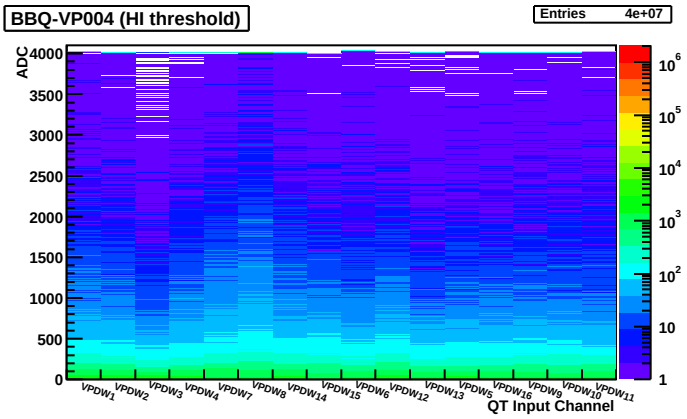

BBQ-VP001 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

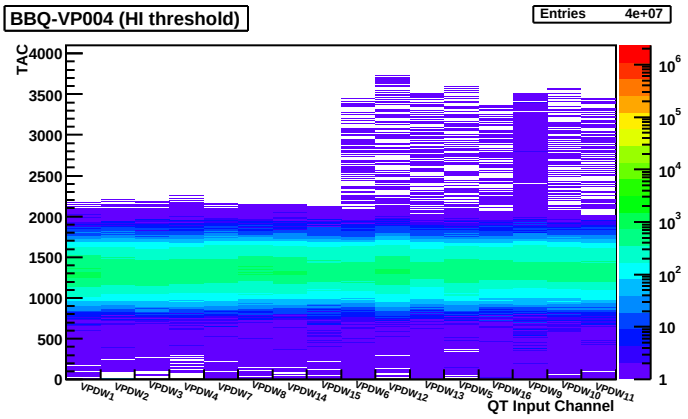

BBQ-VP002 (LO threshold)
Entries 4e+07
ADC
QT Input Channel

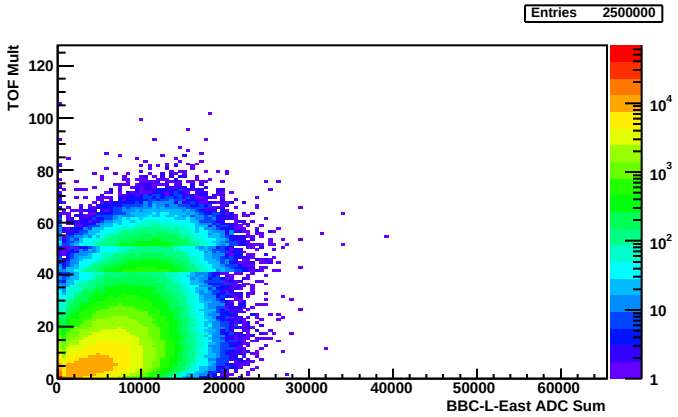

BBQ-VP002 (LO threshold)
Entries 4e+07
TAC
QT Input Channel

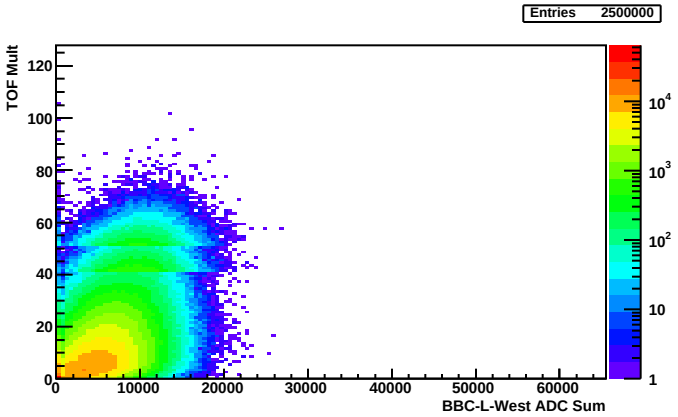

BBQ-VP003 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

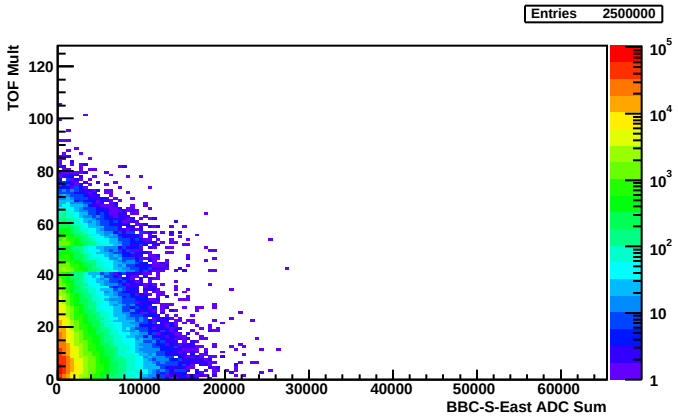

BBQ-VP003 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

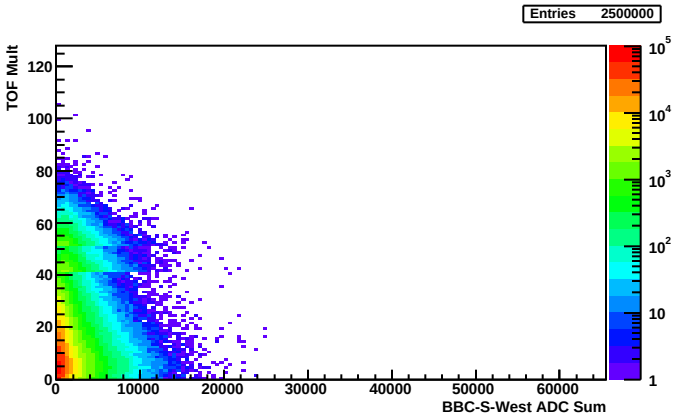

BBQ-VP004 (HI threshold)
Entries 4e+07
ADC
QT Input Channel

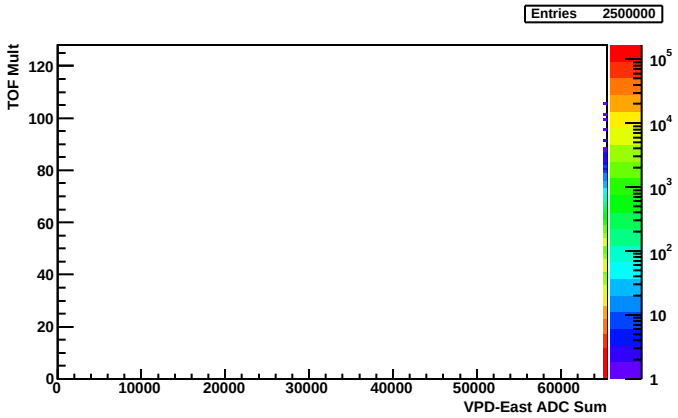

BBQ-VP004 (HI threshold)
Entries 4e+07
TAC
QT Input Channel

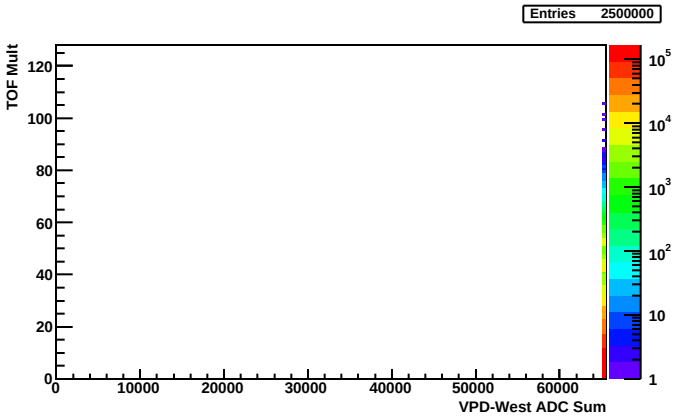

Entries 2500000
TOF Mult
BBC-L-East ADC Sum

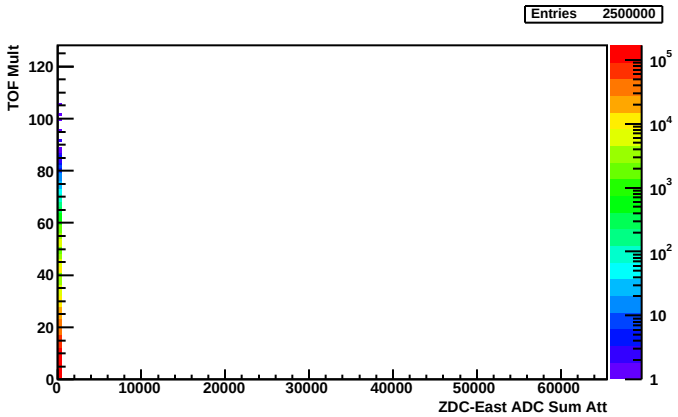

Entries 2500000
TOF Mult
BBC-L-West ADC Sum

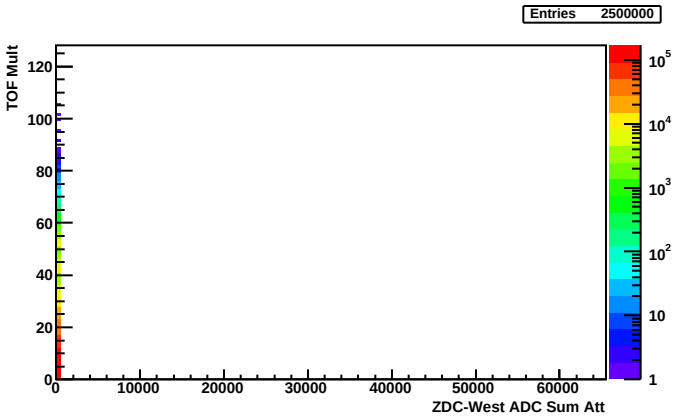

Entries 2500000
TOF Mult
BBC-S-East ADC Sum

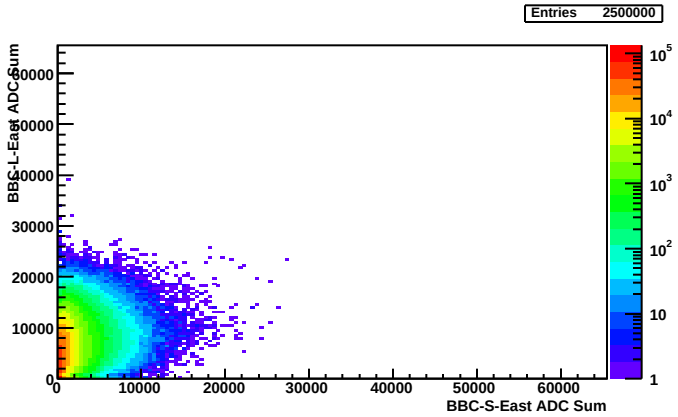

Entries 2500000
TOF Mult
BBC-S-West ADC Sum

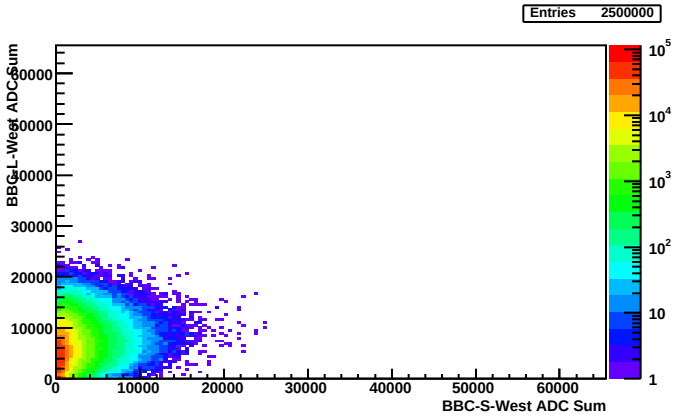

Entries 2500000
TOF Mult
VPD-East ADC Sum

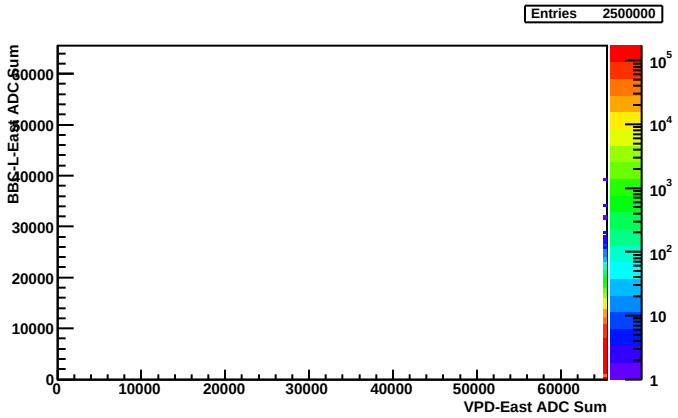

Entries 2500000
TOF Mult
VPD-West ADC Sum

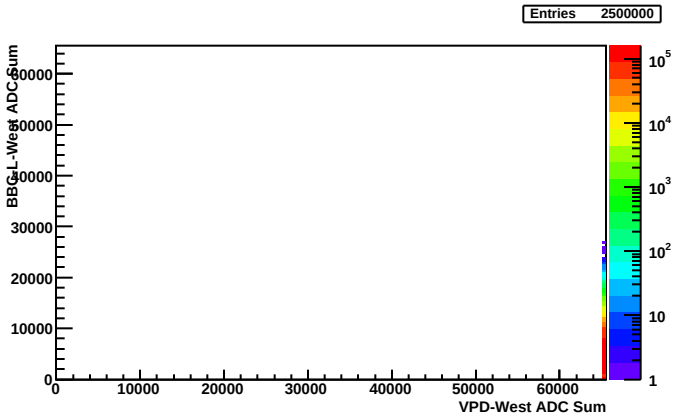

Entries 2500000
TOF Mult
ZDC-East ADC Sum Att

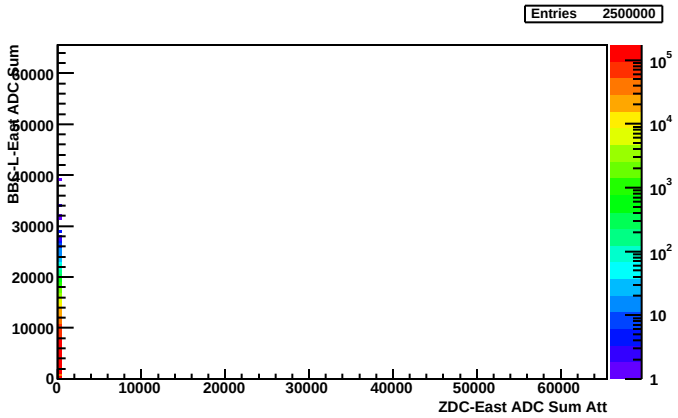

Entries 2500000
TOF Mult
ZDC-West ADC Sum Att

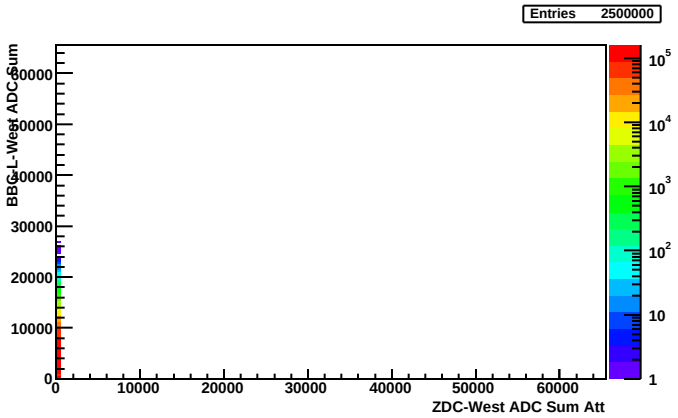

Entries 2500000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

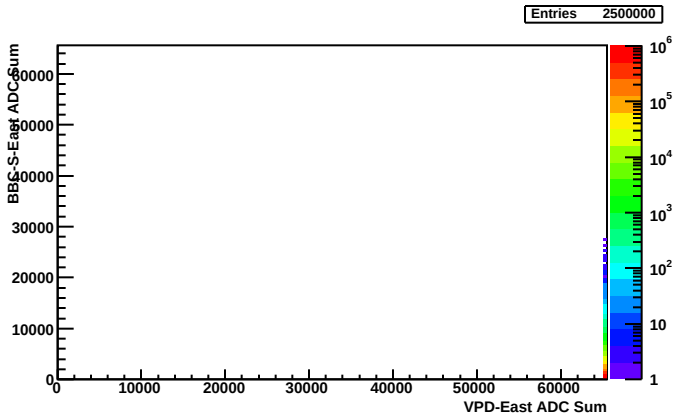

Entries 2500000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

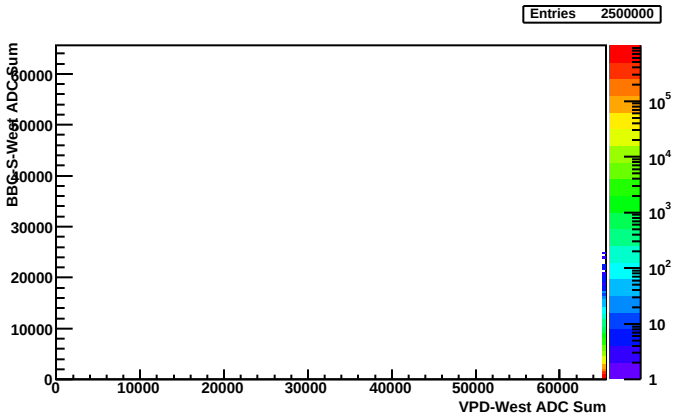

Entries 2500000
BBC-L-East ADC Sum
VPD-East ADC Sum

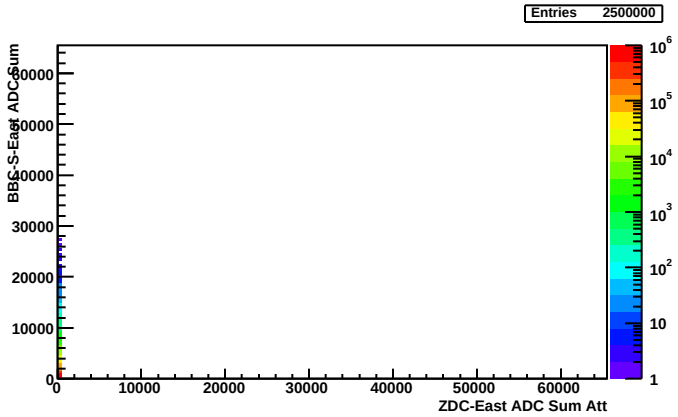

Entries 2500000
BBC-L-West ADC Sum
VPD-West ADC Sum

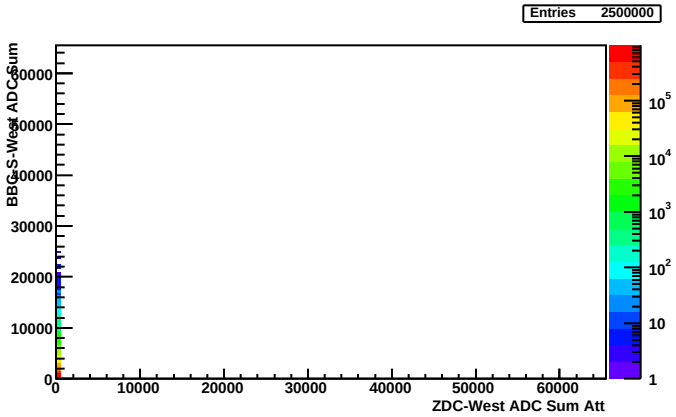

Entries 2500000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

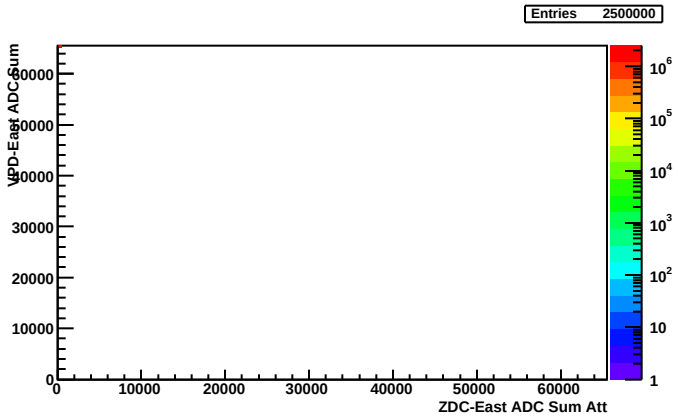

Entries 2500000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 2500000
BBC-S-East ADC Sum
VPD-East ADC Sum


Entries 2500000
BBC-S-West ADC Sum
VPD-West ADC Sum


Entries 2500000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 2500000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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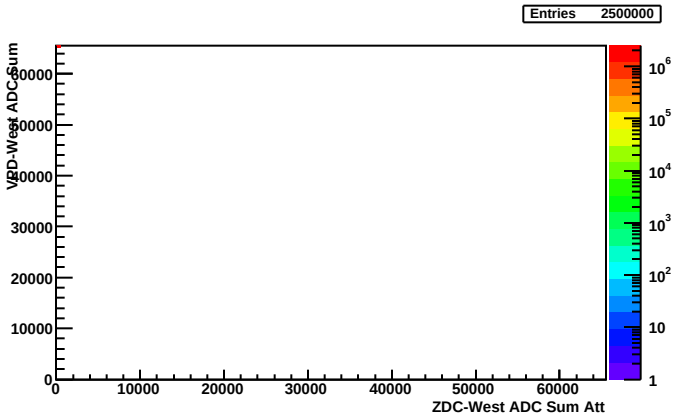
VPD-East ADC Sum
ZDC-East ADC Sum Att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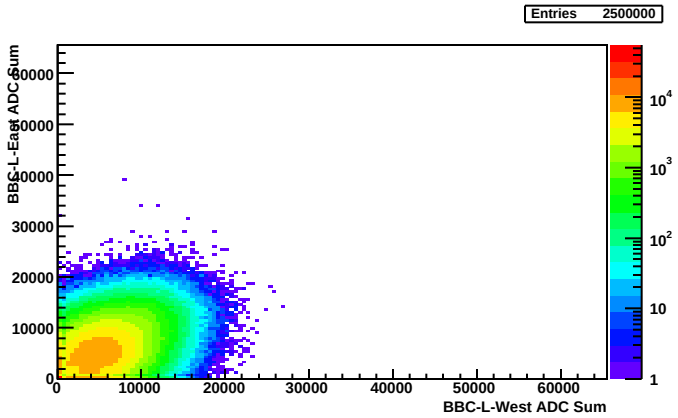
VPD-West ADC Sum
ZDC-West ADC Sum Att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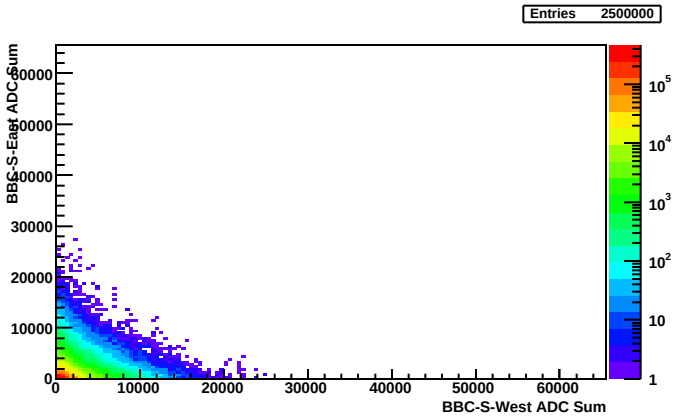
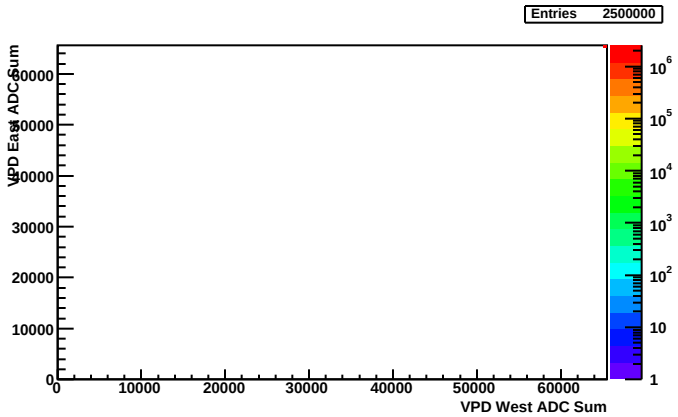
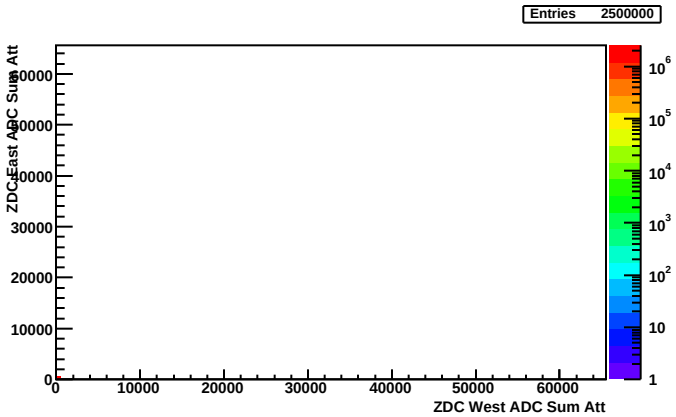
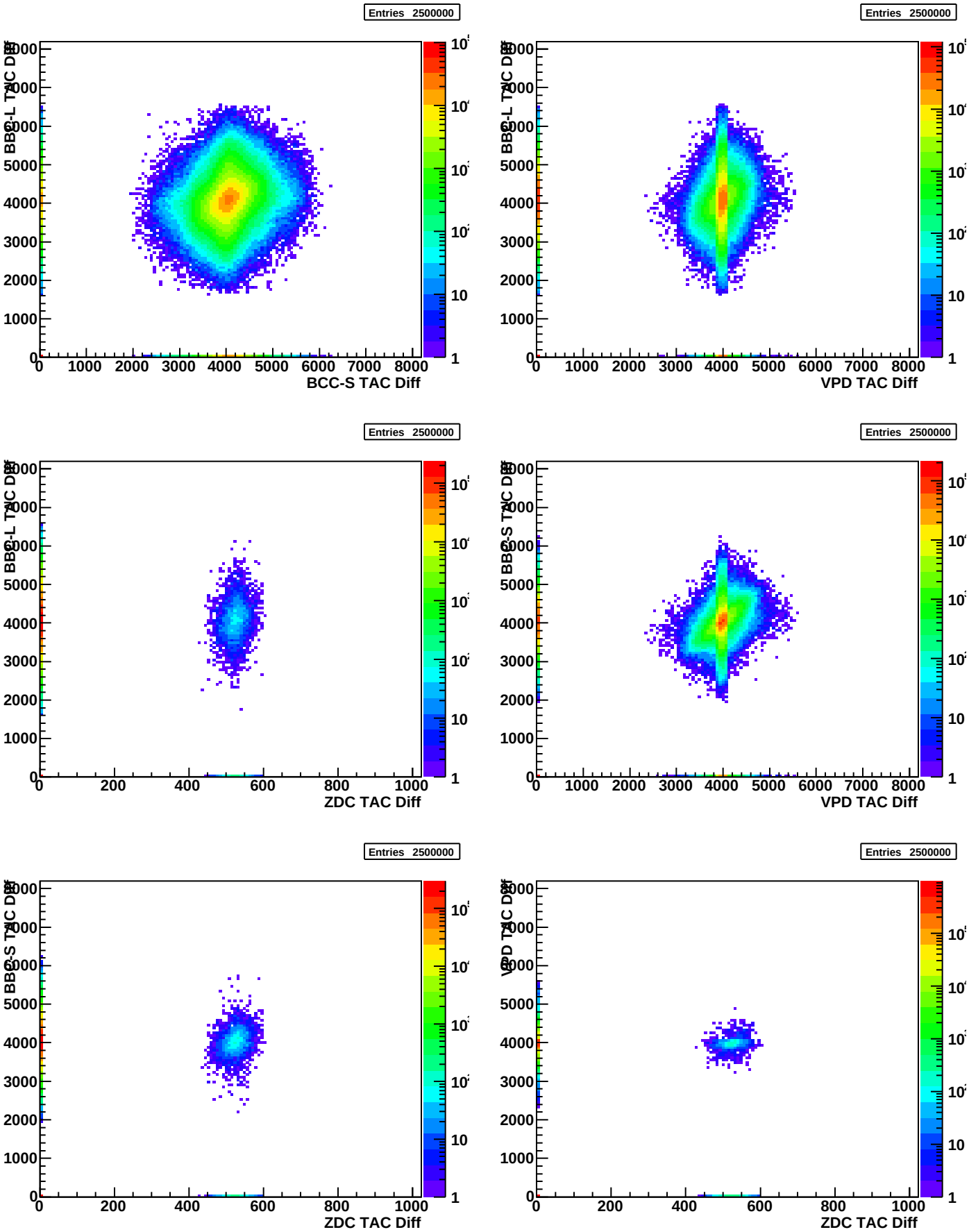
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 2500000
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 2500000
VPD East ADC Sum
VPD West ADC Sum


Entries 2500000
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 2500000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2500000
BBC-L TAC Diff
VPD TAC Diff
Entries 2500000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2500000
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VPD TAC Diff
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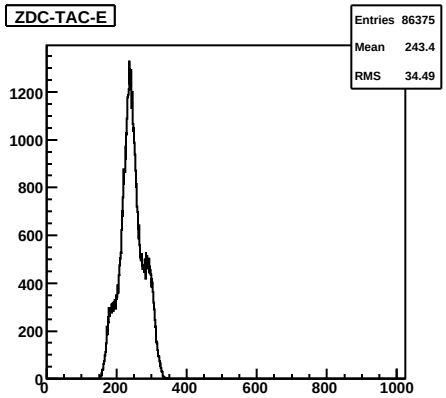
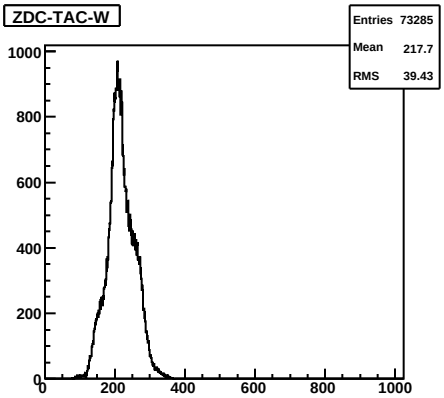
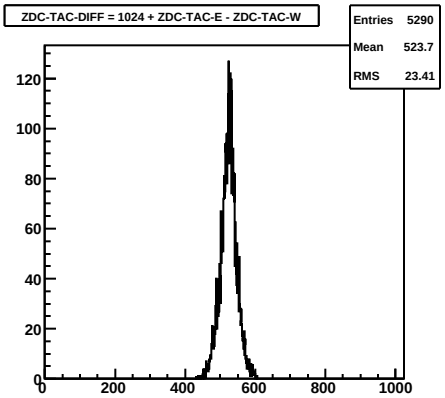
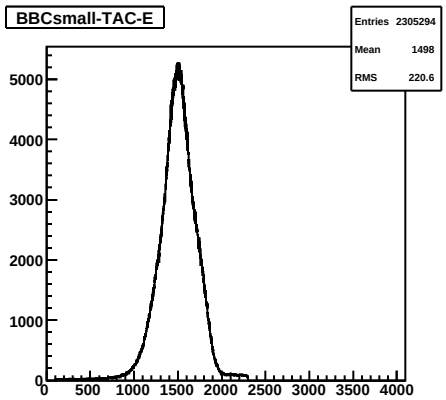
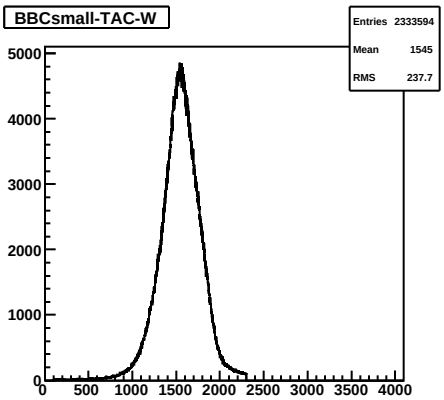
BBC-S TAC Diff
ZDC TAC Diff
Entries 2500000
VPD TAC Diff
ZDC TAC Diff

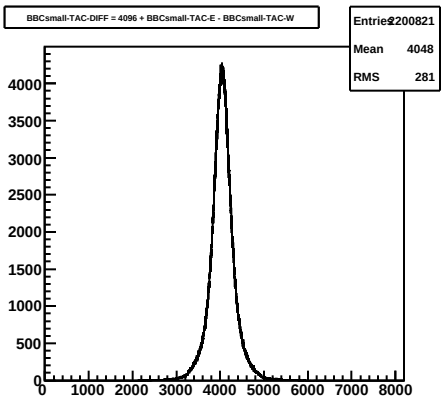

ZDC-TAC-E
Entries 86375
Mean 243.4
RMS 34.49

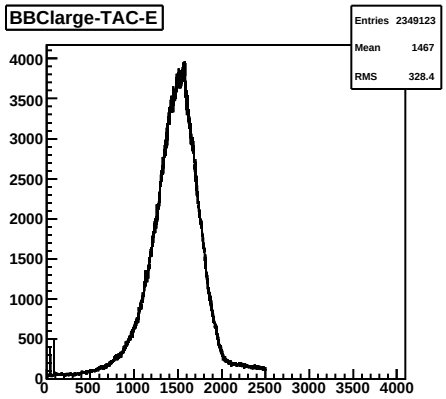

ZDC-TAC-W
Entries 73285
Mean 217.7
RMS 39.43

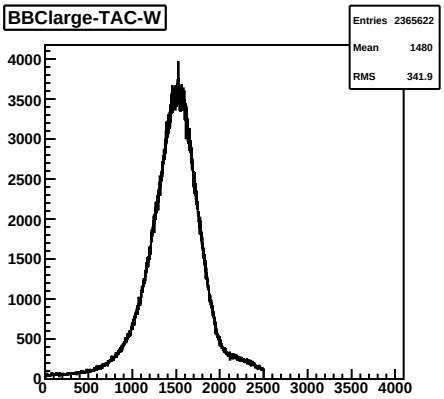

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 5290
Mean 523.7
RMS 23.41

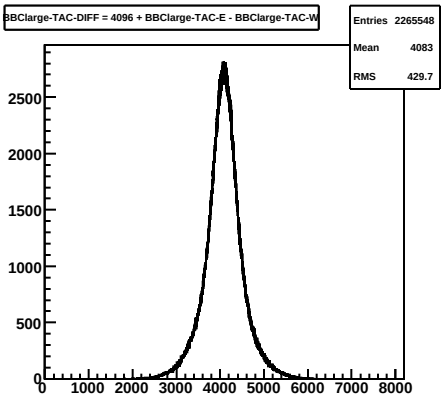

BBCsmall-TAC-E
Entries 2305294
Mean 1498
RMS 220.6

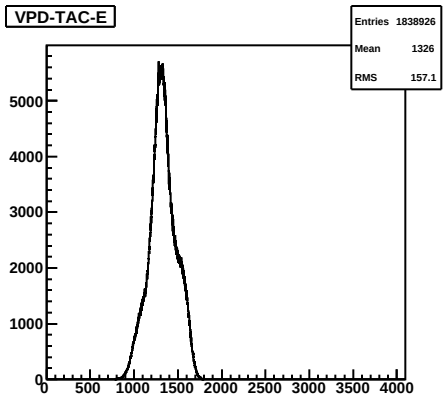

BBCsmall-TAC-W
Entries 2333594
Mean 1545
RMS 237.7

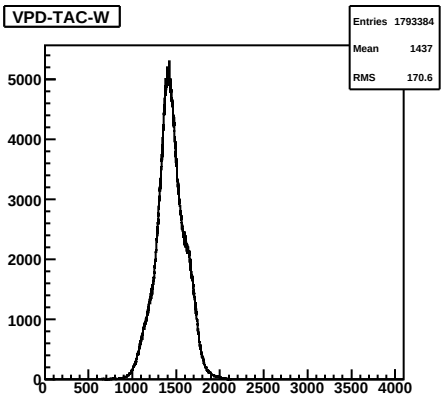

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 2200821
Mean 4048
RMS 281

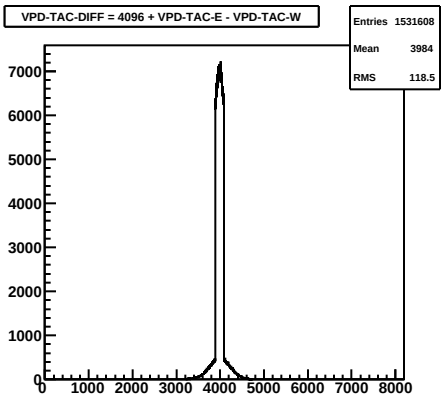

BBClarge-TAC-E
Entries 2349123
Mean 1467
RMS 328.4

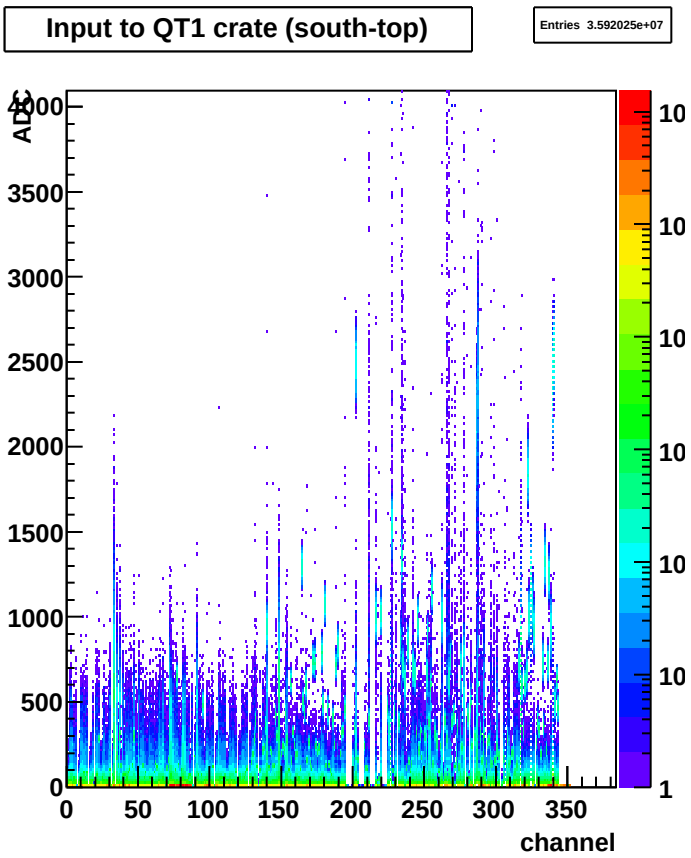

BBClarge-TAC-W
Entries 2365622
Mean 1480
RMS 341.9

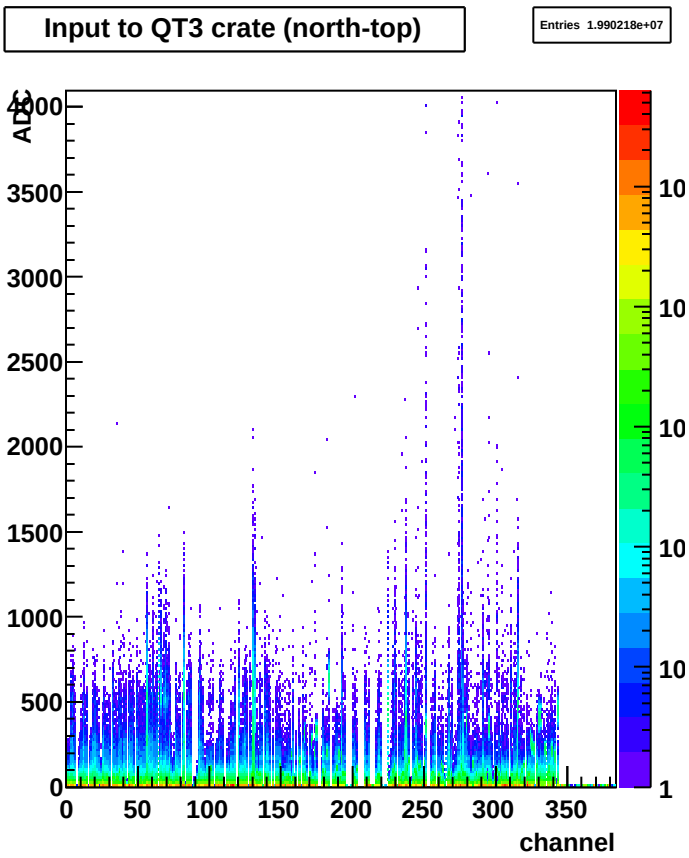

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 2265548
Mean 4083
RMS 429.7

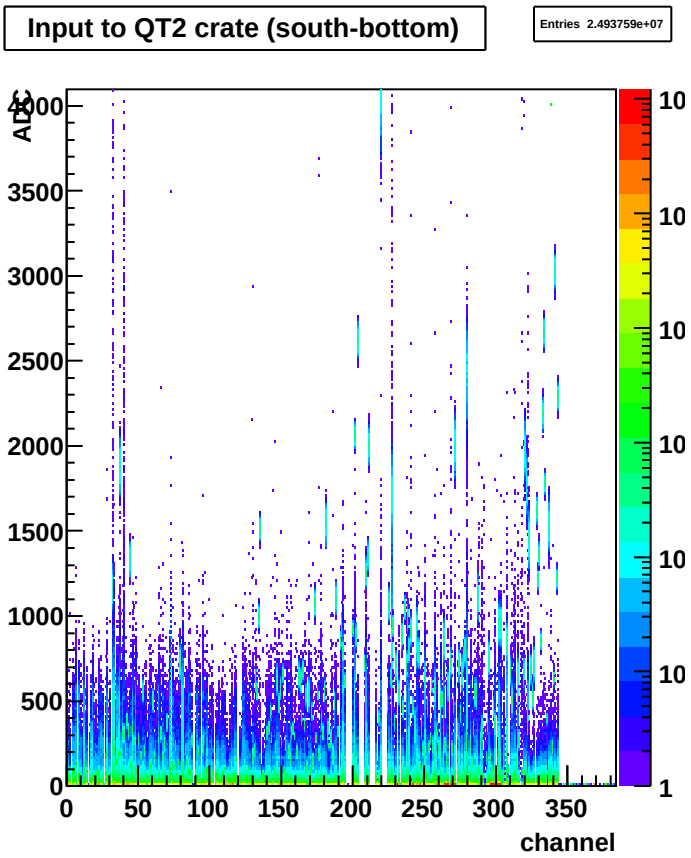

VPD-TAC-E
Entries 1838926
Mean 1326
RMS 157.1

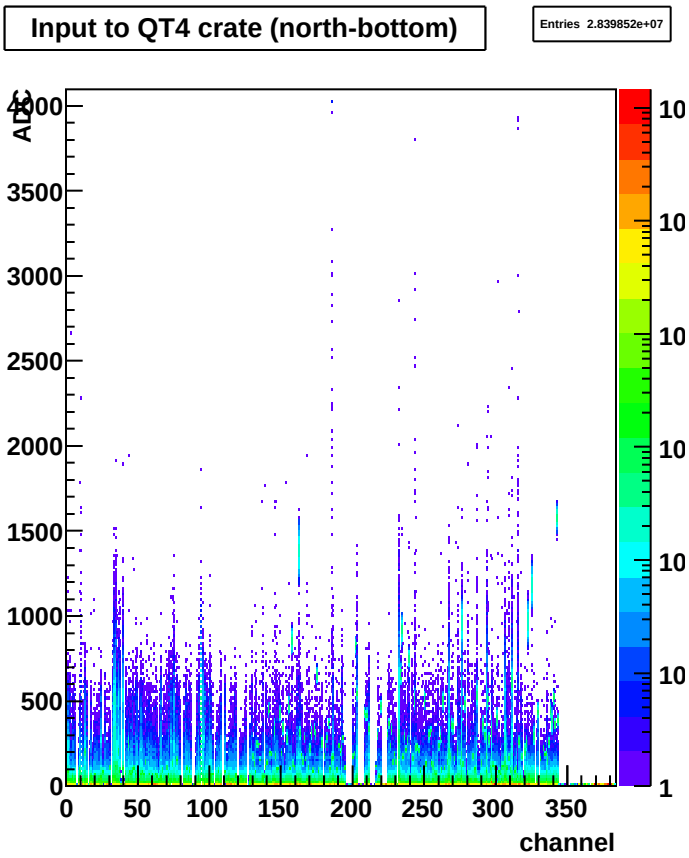

VPD-TAC-W
Entries 1793384
Mean 1437
RMS 170.6

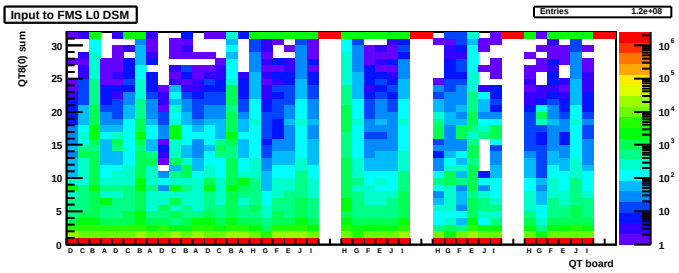

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 1531608
Mean 3984
RMS 118.5

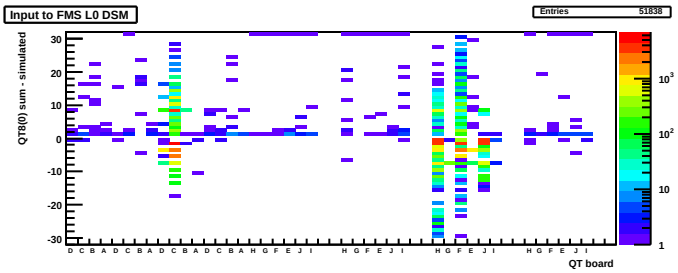

Input to QT1 crate (south-top)
Entries 3.592025e+07
ADC
channel

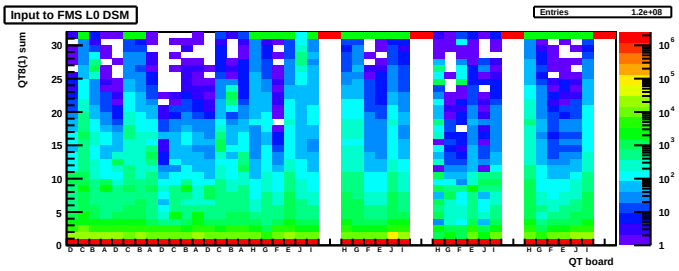

Input to QT3 crate (north-top)
Entries 1.990218e+07
ADC
channel

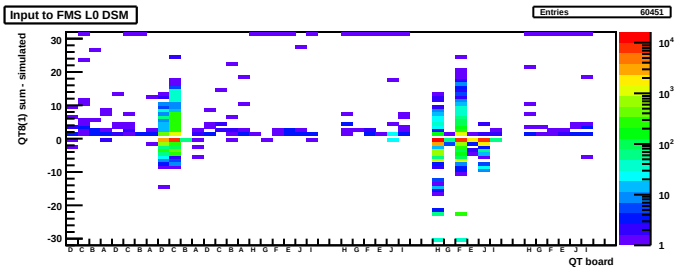

Input to QT2 crate (south-bottom)
Entries 2.493759e+07
ADC
channel

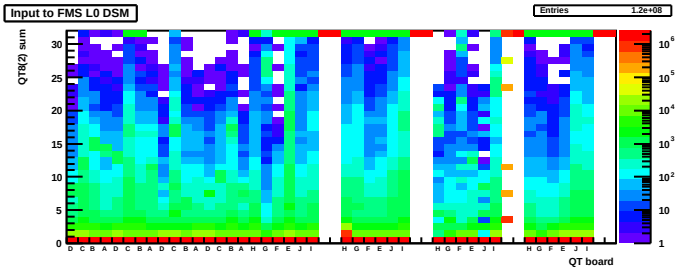

Input to QT4 crate (north-bottom)
Entries 2.839852e+07
ADC
channel

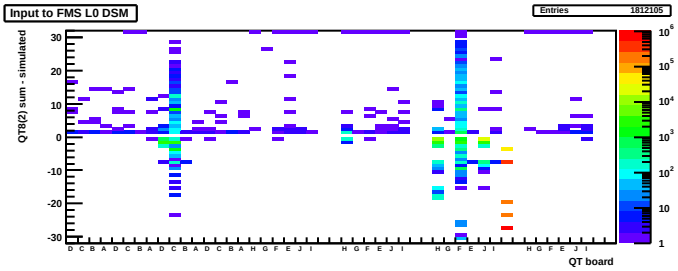

Input to FMS L0 DSM
Entries 1.2e+08
QT8(0) sum
QT board

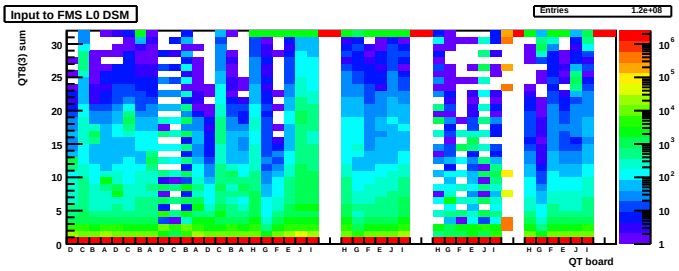

Input to FMS L0 DSM
Entries 51838
QT8(0) sum - simulated
QT board

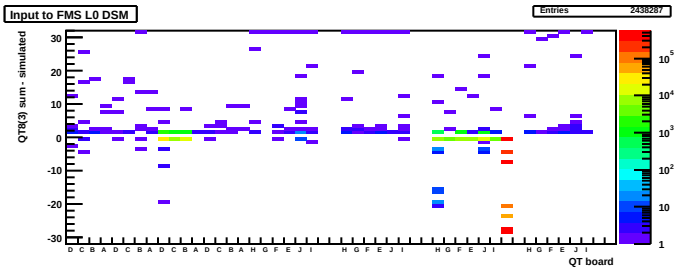

Input to FMS L0 DSM
Entries 1.2e+08
QT8(1) sum
QT board

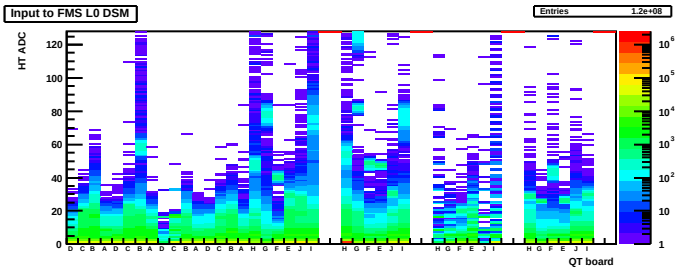

Input to FMS L0 DSM
Entries 60451
QT8(1) sum - simulated
QT board

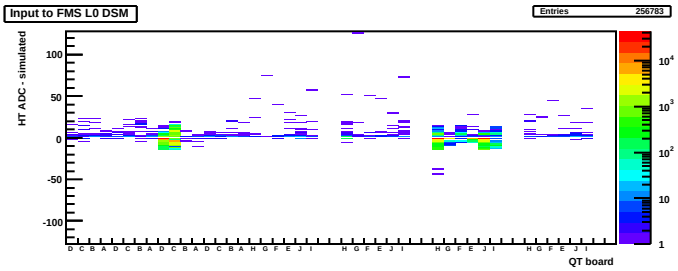

Input to FMS L0 DSM
Entries 1.2e+08
QT8(2) sum
QT board

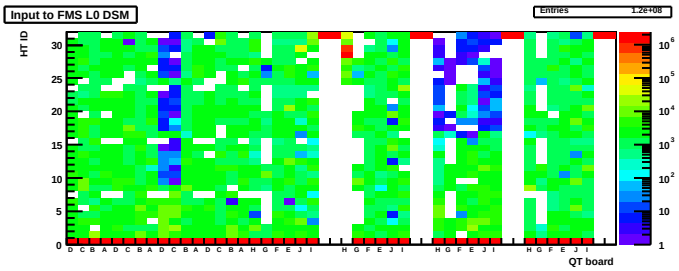

Input to FMS L0 DSM
Entries 1812105
QT8(2) sum - simulated
QT board

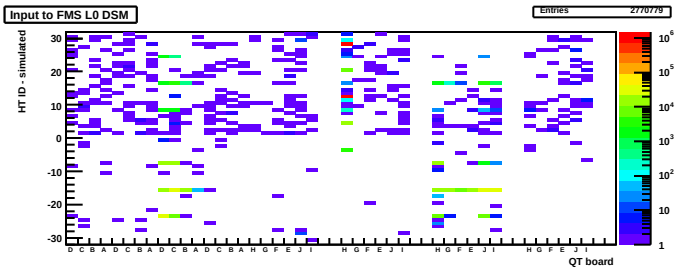

Input to FMS L0 DSM
Entries 1.2e+08
QT8(3) sum
QT board

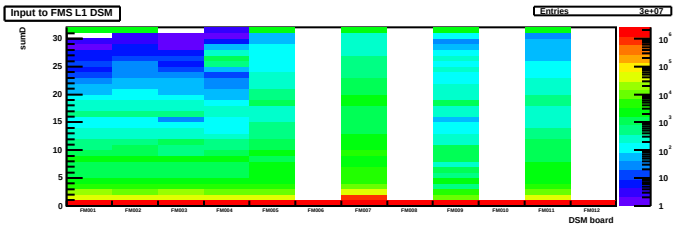

Input to FMS L0 DSM
Entries 2438287
QT8(3) sum - simulated
QT board

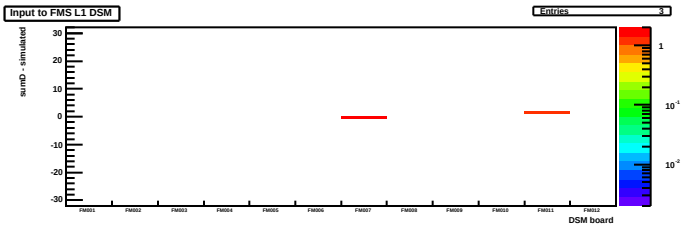

Input to FMS L0 DSM
Entries 1.2e+08
HT ADC
QT board

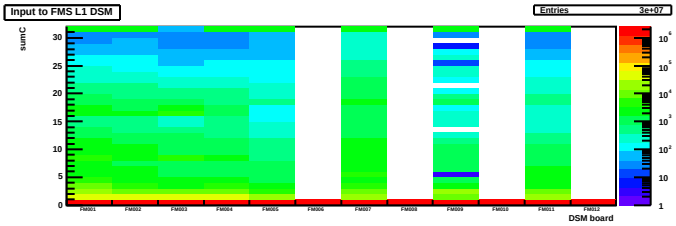

Input to FMS L0 DSM
Entries 256783
HT ADC - simulated
QT board

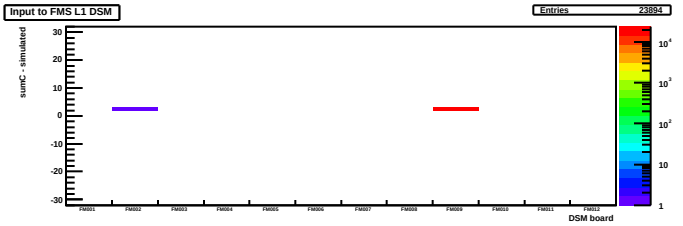

Input to FMS L0 DSM
Entries 1.2e+08
HT ID
QT board

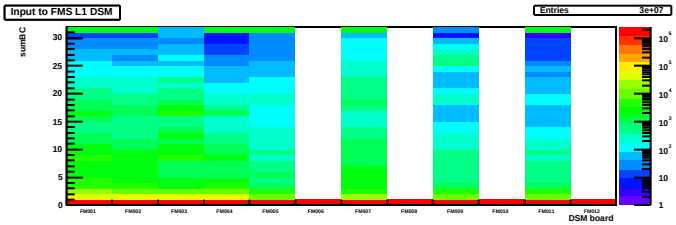

Input to FMS L0 DSM
Entries 2770779
HT ID - simulated
QT board

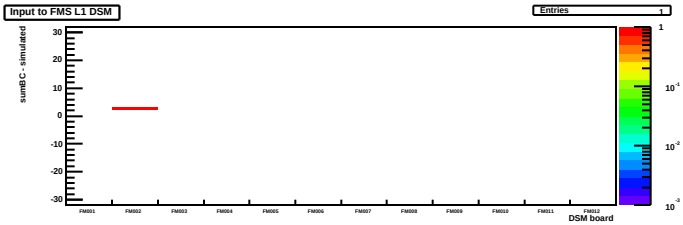

Input to FMS L1 DSM
Entries 3e+07
sumD
DSM board

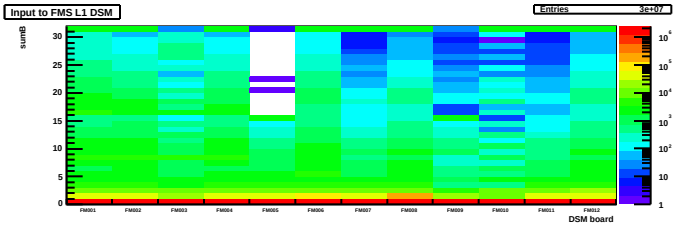

Input to FMS L1 DSM
Entries 3
sumD - simulated
DSM board

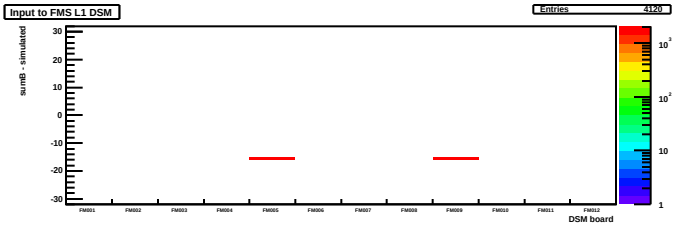

Input to FMS L1 DSM
Entries 3e+07
sumC
DSM board

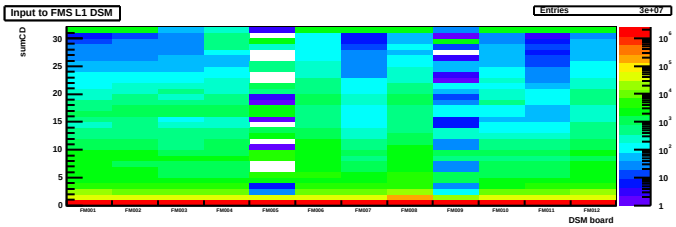

Input to FMS L1 DSM
Entries 23894
sumC - simulated
DSM board

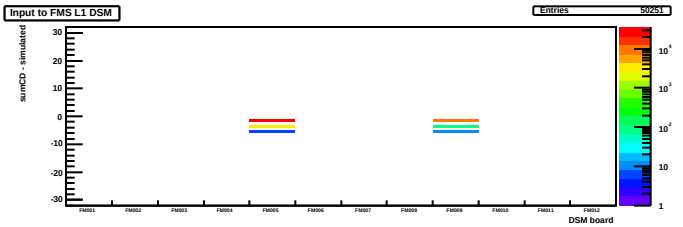

Input to FMS L1 DSM
Entries 3e+07
sumBC
DSM board

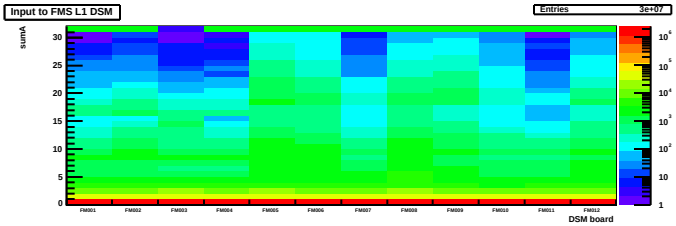

Input to FMS L1 DSM
Entries 1
sumBC - simulated
DSM board

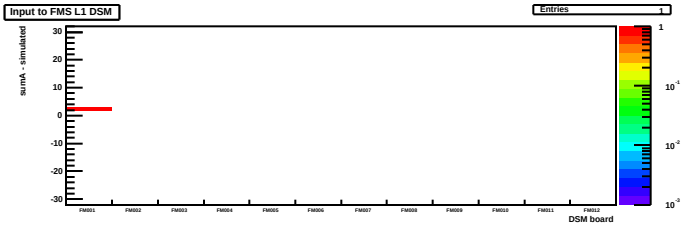

Input to FMS L1 DSM
Entries 3e+07
sumB
DSM board

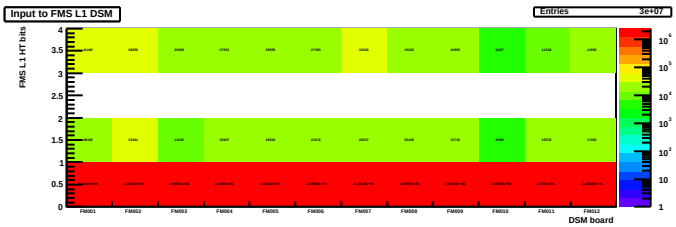

Input to FMS L1 DSM
Entries 4120
sumB - simulated
DSM board

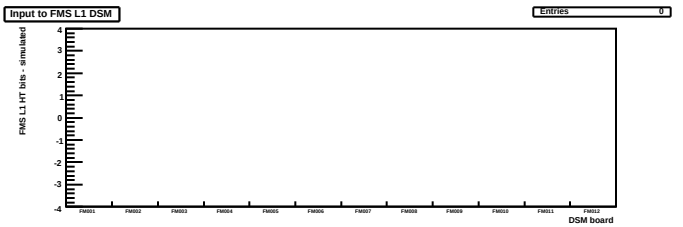

Input to FMS L1 DSM
Entries 3e+07
sumCD
DSM board

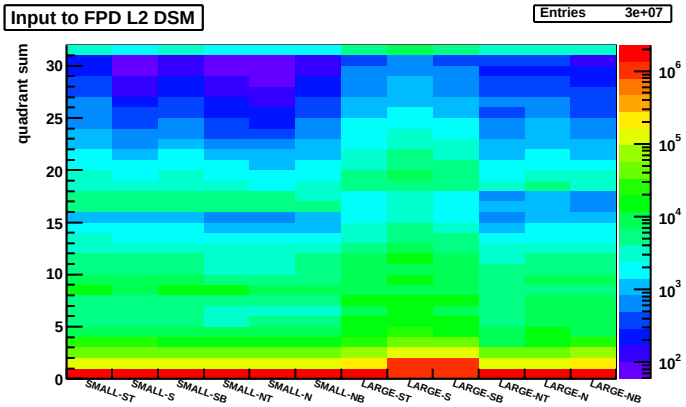

Input to FMS L1 DSM
Entries 50251
sumCD - simulated
DSM board

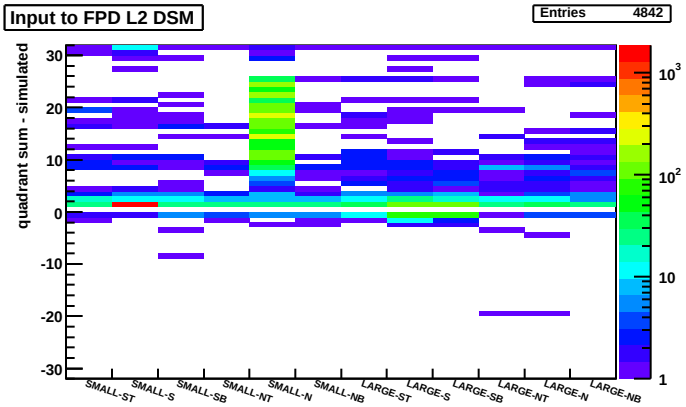

Input to FMS L1 DSM
Entries 3e+07
sumA
DSM board

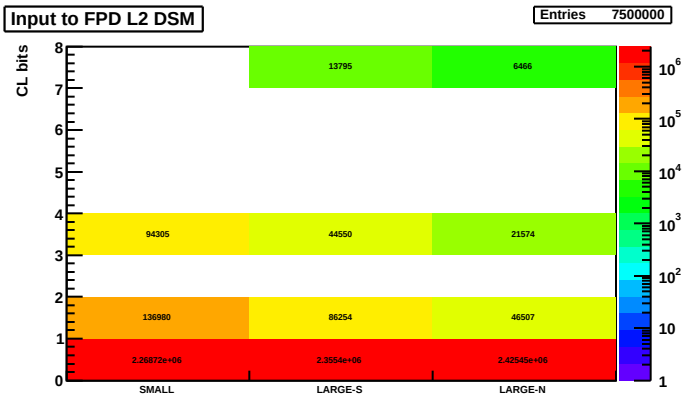

Input to FMS L1 DSM
Entries 1
sumA - simulated
DSM board

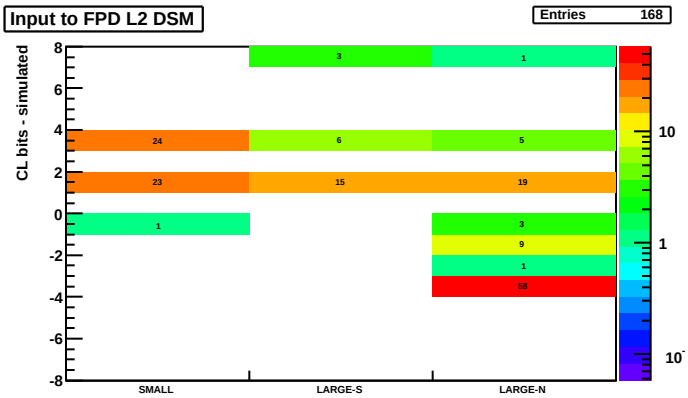

Input to FMS L1 DSM
Entries 3e+07
FMS L1 HT bits
DSM board

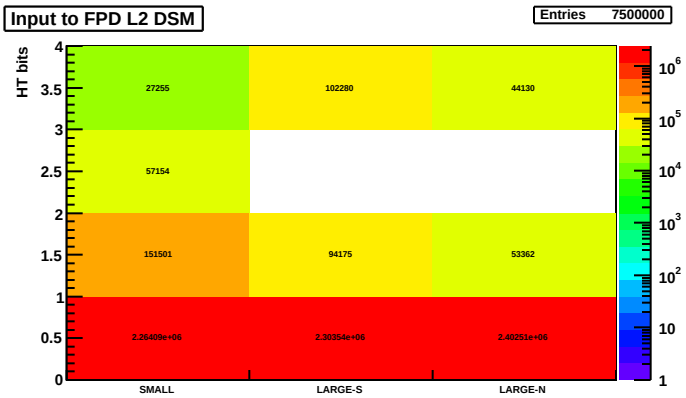

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

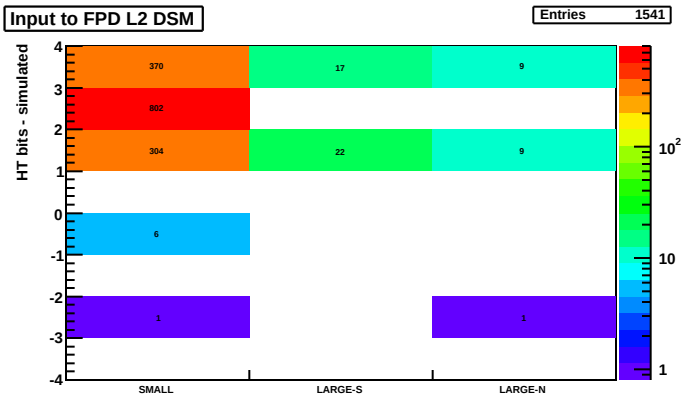

Input to FPD L2 DSM
Entries 3e+07
quadrant sum
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

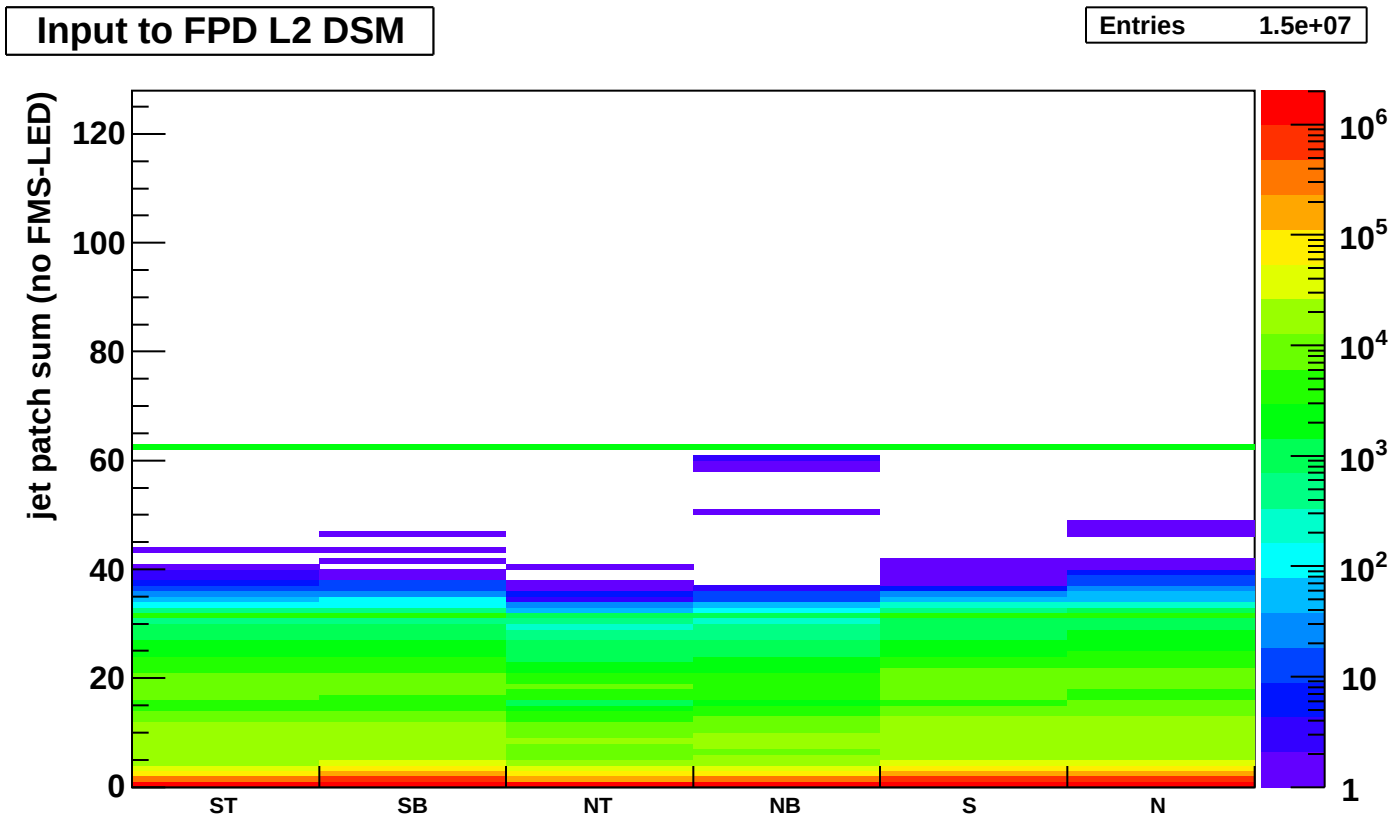

Input to FPD L2 DSM
Entries 4842
quadrant sum - simulated
SMALL-ST
SMALL-S
SMALL-SB
SMALL-NT
SMALL-N
SMALL-NB
LARGE-ST
LARGE-S
LARGE-SB
LARGE-NT
LARGE-N
LARGE-NB

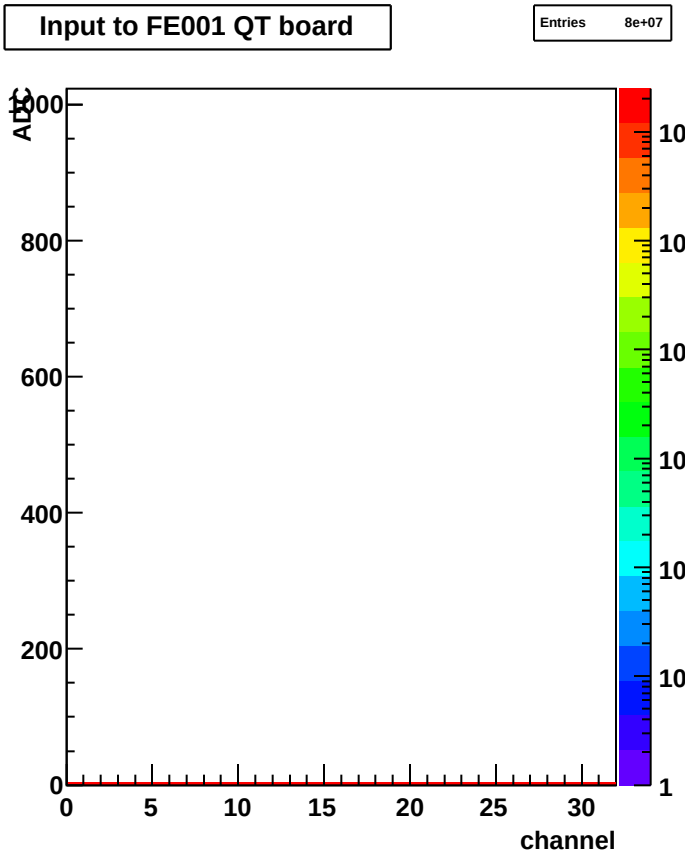

Input to FPD L2 DSM
Entries 7500000
CL bits
13795
6466
94305
44550
21574
136980
86254
46507
2.26872e+06
2.3554e+06
2.42545e+06
SMALL
LARGE-S
LARGE-N

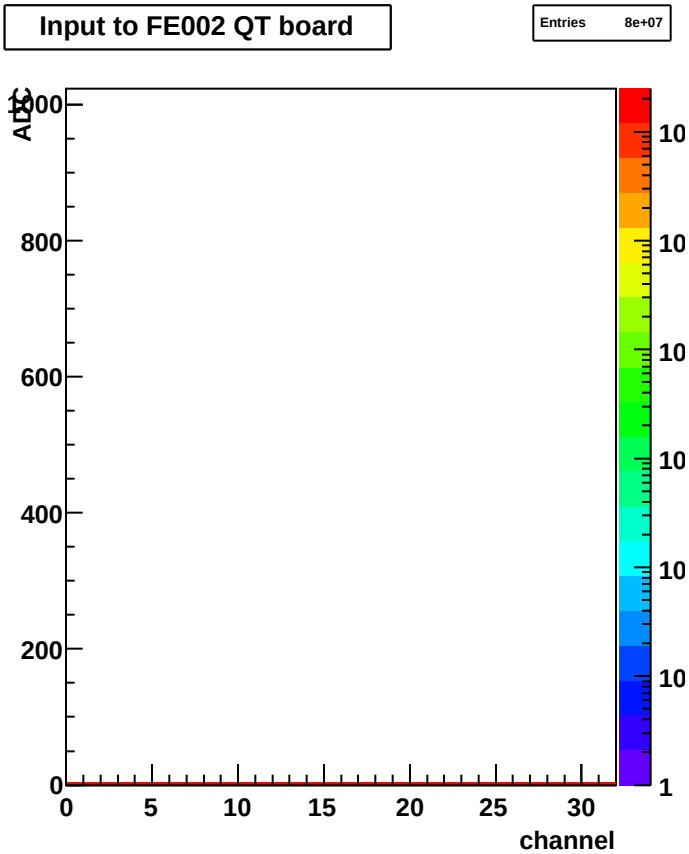

Input to FPD L2 DSM
Entries 168
CL bits - simulated
3
1
24
6
5
23
15
19
1
3
9
1
58
SMALL
LARGE-S
LARGE-N

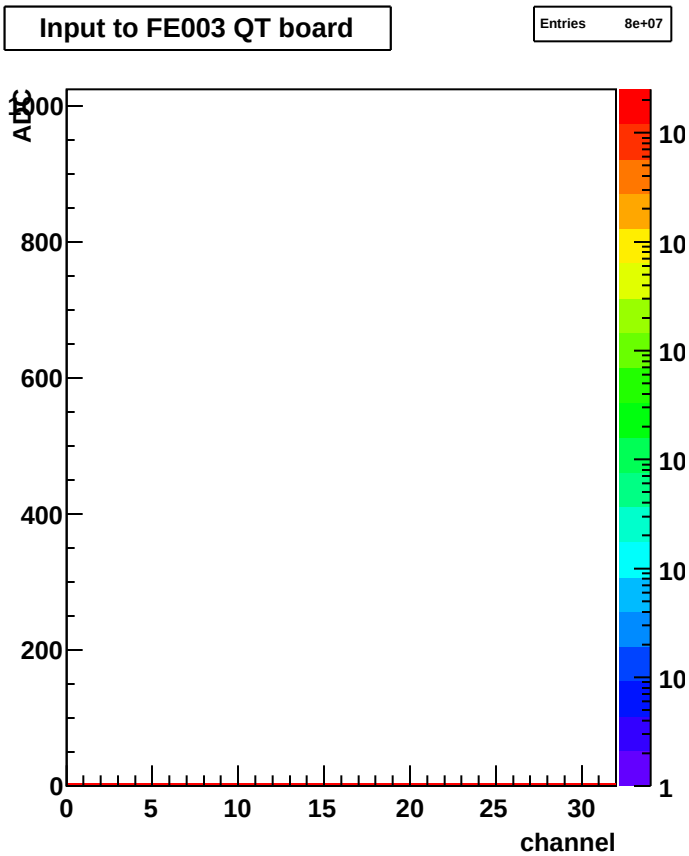

Input to FPD L2 DSM
Entries 7500000
HT bits
27255
102280
44130
57154
151501
94175
53362
2.26409e+06
2.30354e+06
2.40251e+06
SMALL
LARGE-S
LARGE-N

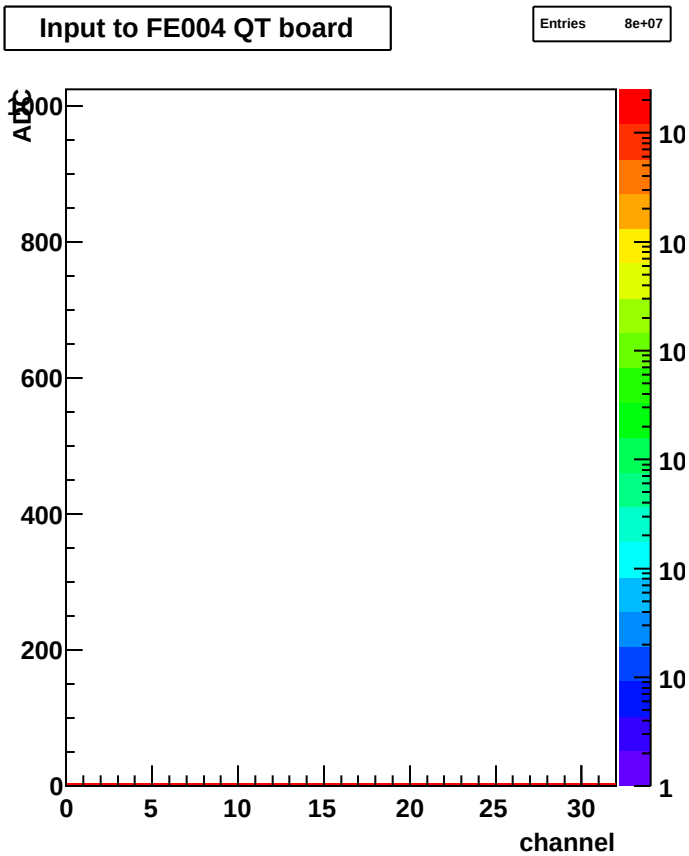

Input to FPD L2 DSM
Entries 1541
HT bits - simulated
370
17
9
802
304
22
9
6
1
1
SMALL
LARGE-S
LARGE-N

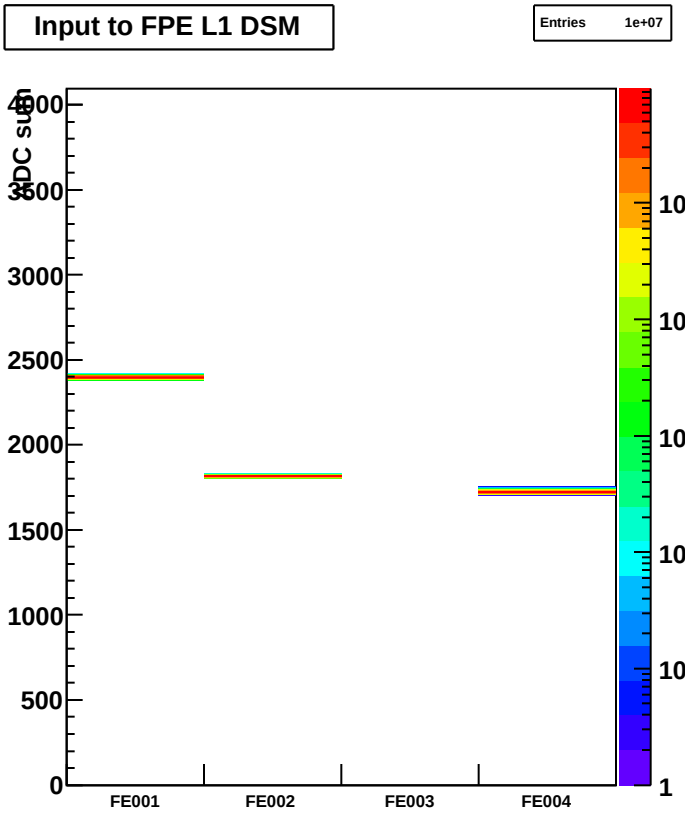

Input to FPD L2 DSM
Entries 1.5e+07
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5
10^6


Input to FE001 QT board
Entries 8e+07
ADC
channel

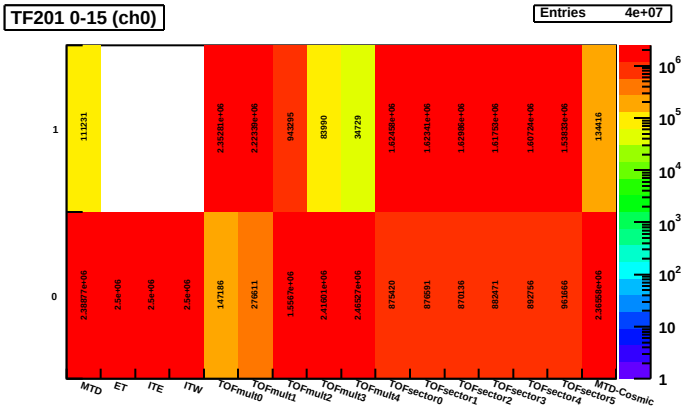

Input to FE002 QT board
Entries 8e+07
ADC
channel


Input to FE003 QT board
Entries 8e+07
ADC
channel

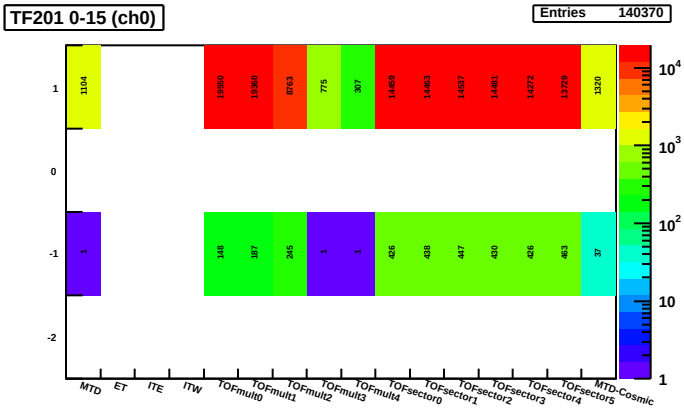

Input to FE004 QT board
Entries 8e+07
ADC
channel


Input to FPE L1 DSM
Entries 1e+07
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1

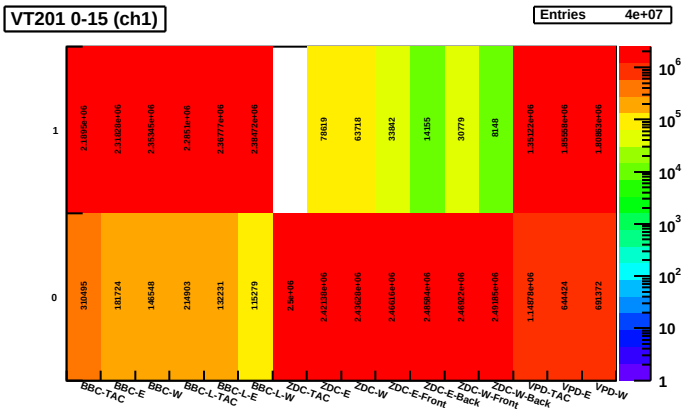
TF201 0-15 (ch0)



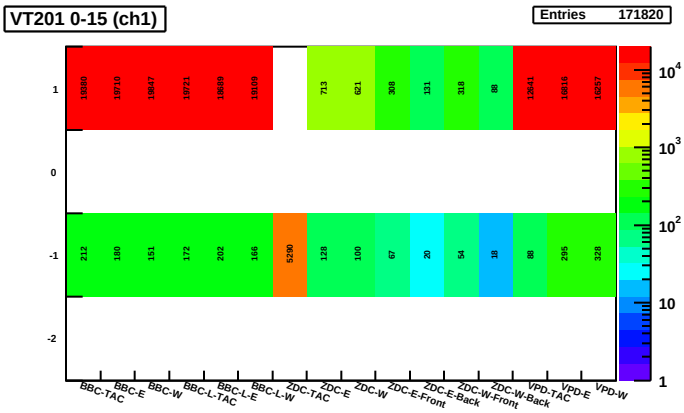
TF201 0-15 (ch0)



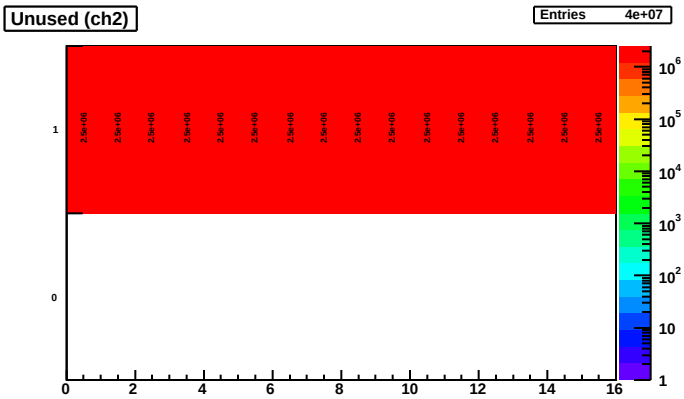
VT201 0-15 (ch1)

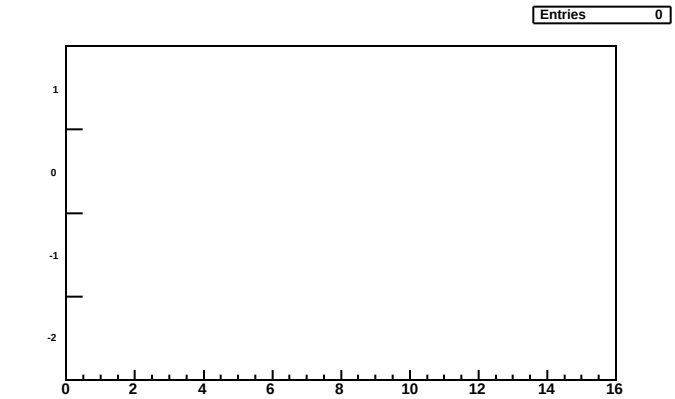


VT201 0-15 (ch1)

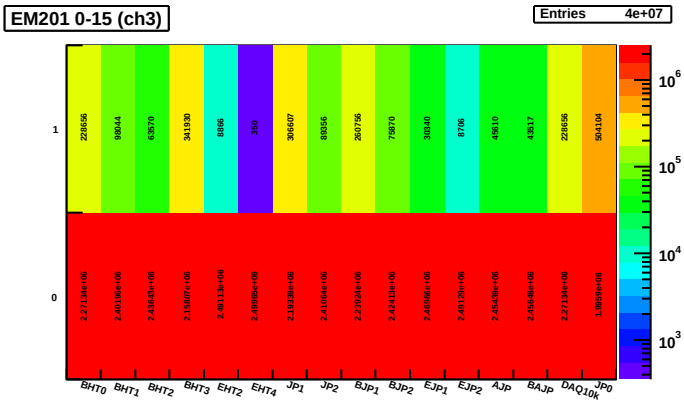


Unused (ch2)

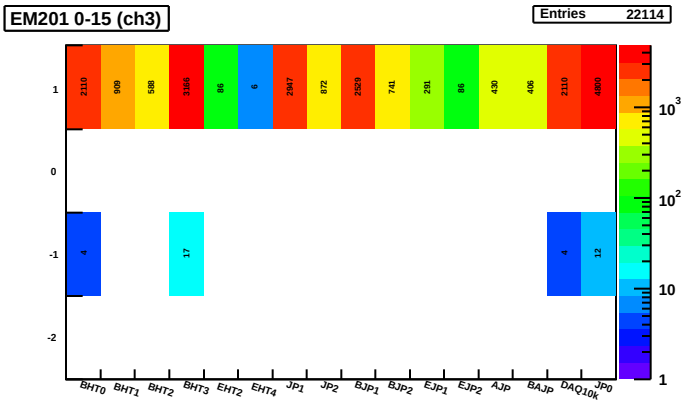




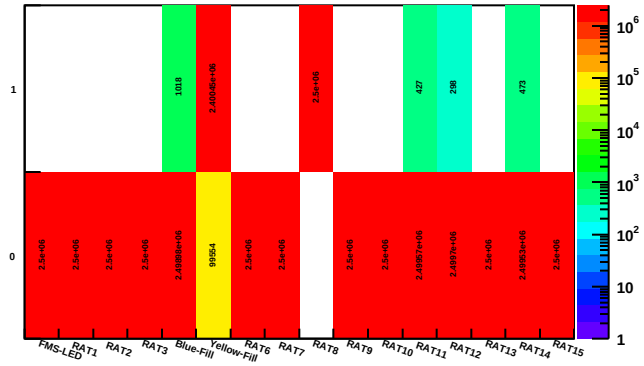
EM201 0-15 (ch3)

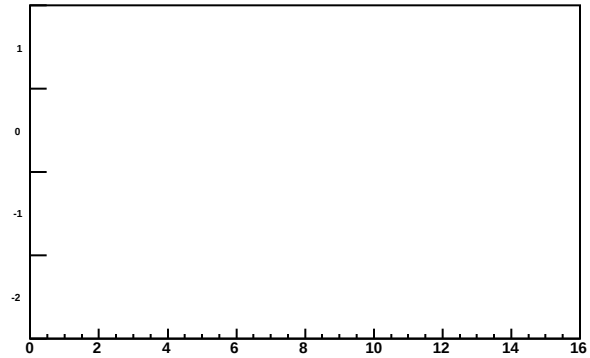


EM201 0-15 (ch3)

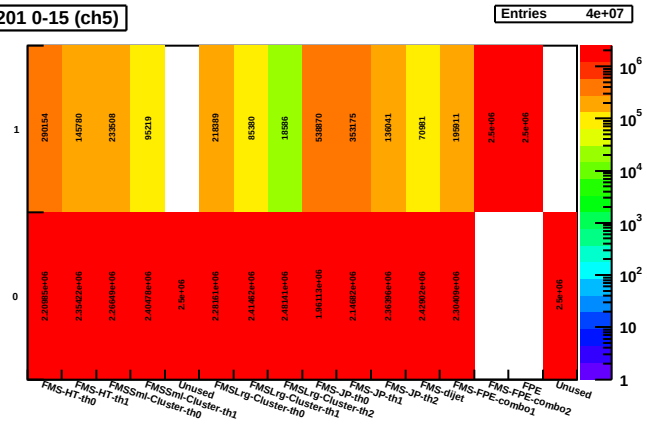


RAT board (ch4)





FP201 0-15 (ch5)



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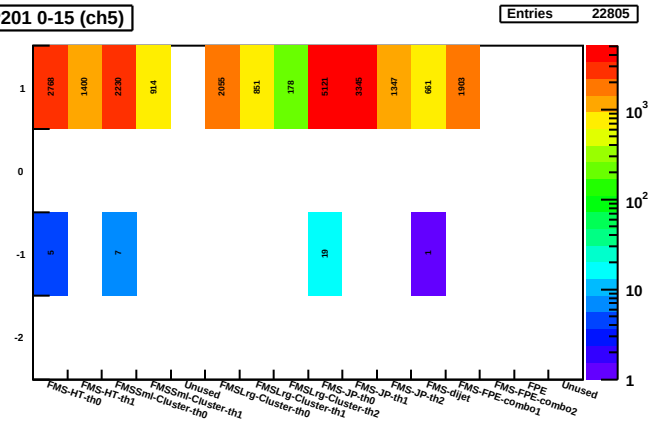
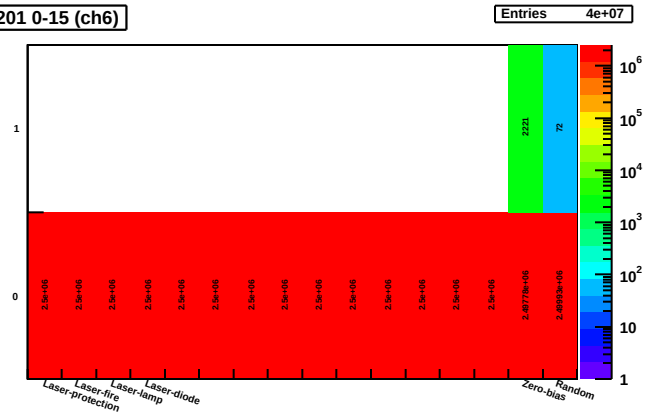
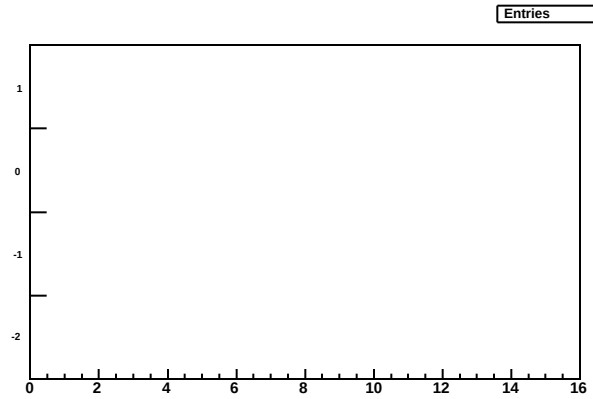
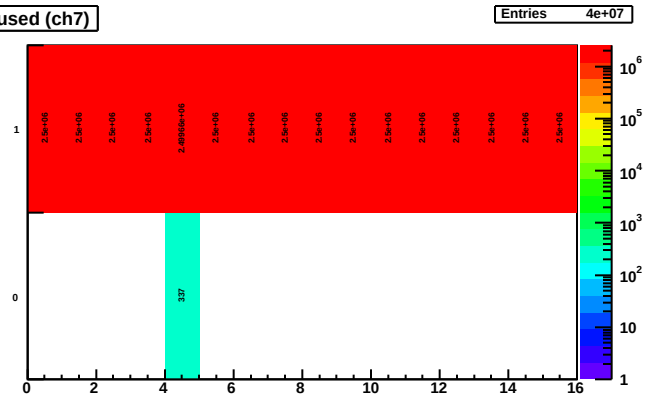
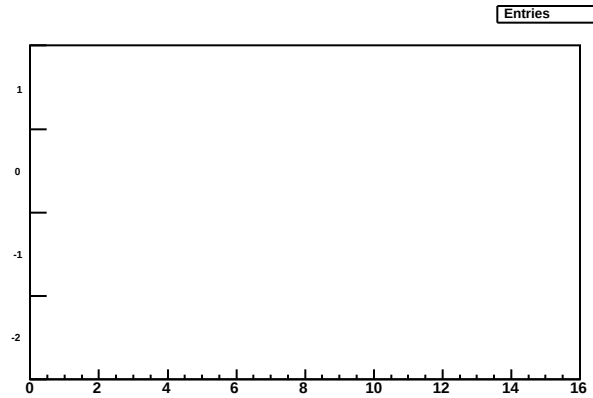


ST201 0-15 (ch6)





Unused (ch7)